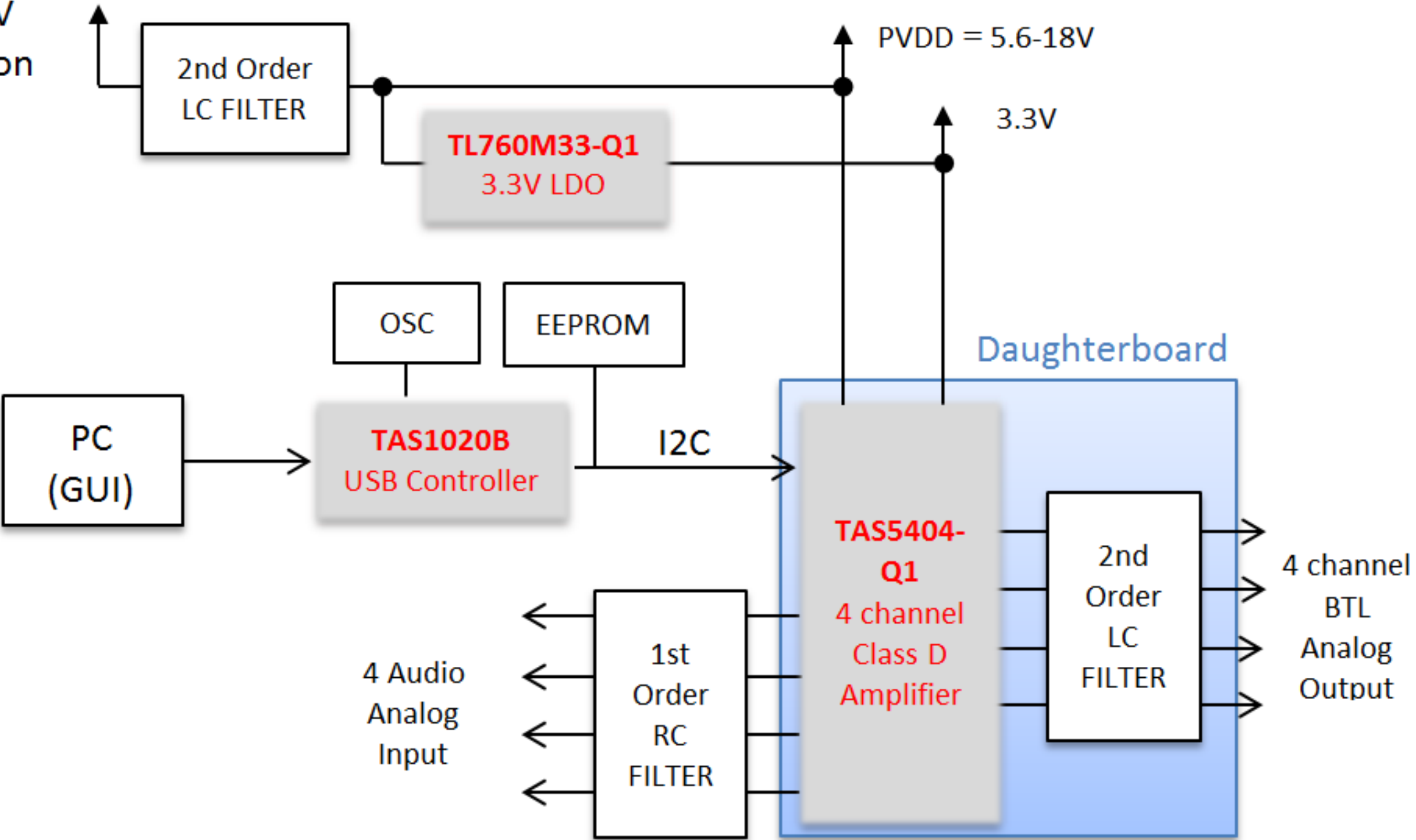
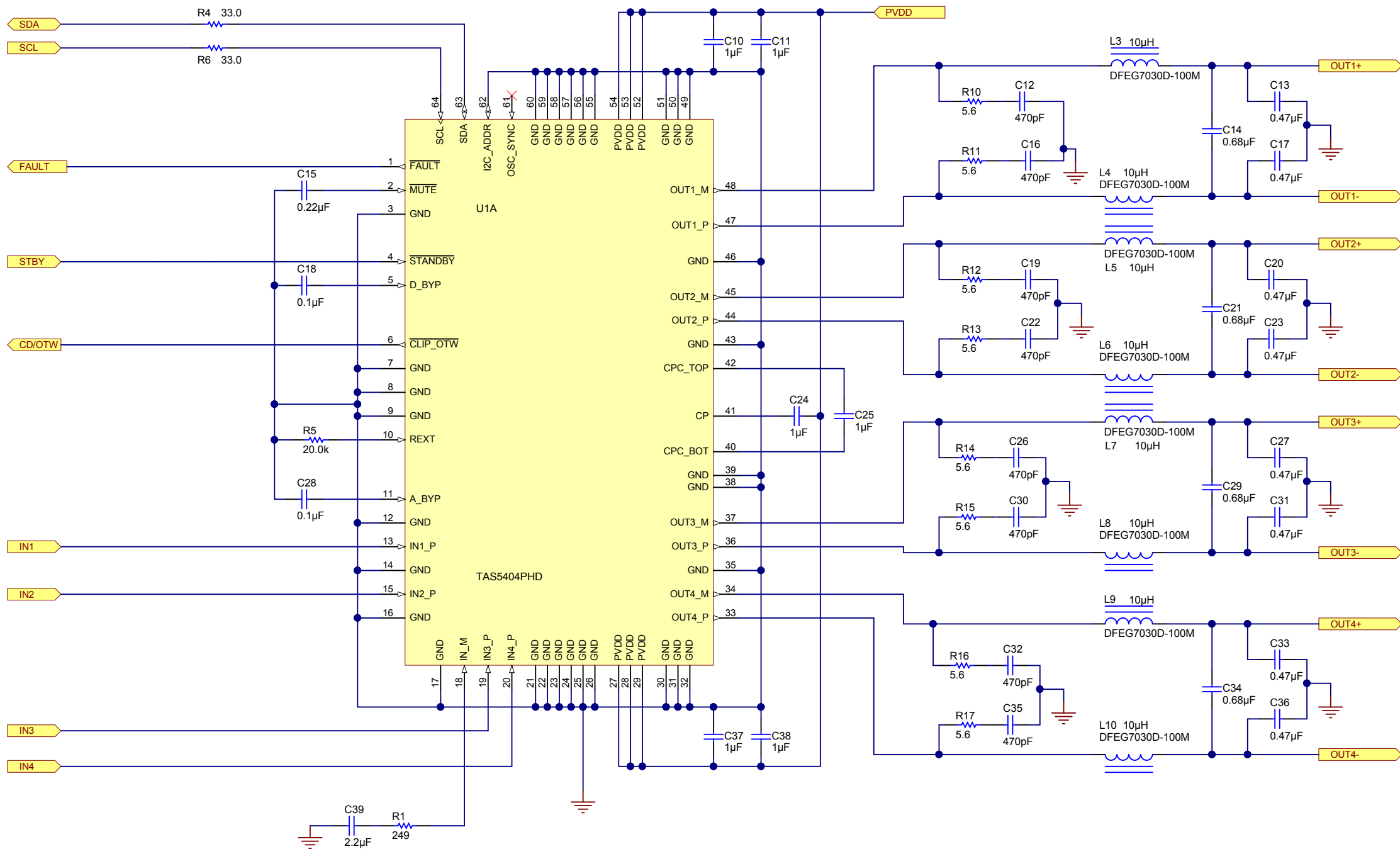
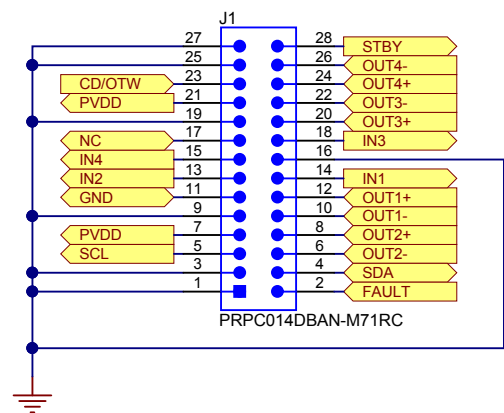


5.6-18V
Operation



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Orderable: N/A	Designed for: Public Release	Mod. Date: 10/9/2015
TID #: 00573	Project Title: 4-Channel Class D	
Number: TIDA-00573	Rev: E2	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 3
Drawn By:	File: Block Diagram.SchDoc	Size: B
Engineer: Albert Marco	Contact: http://www.ti.com/support	



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Orderable: N/A	Designed for: Public Release	Mod. Date: 7/29/2015
TID #: 00573	Project Title: 4-Channel Class D	
Number: TIDA-00573	Rev: E2	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By:	File: Main.SchDoc	Size: B
Engineer: Albert Marco	Contact: http://www.ti.com/support	



FID1

FID2

FID3

FID4

FID5

FID6

PCB Number: TIDA-00573

PCB Rev: E2

PCB

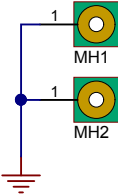
LOGO

Texas Instruments

PCB

LOGO

Pb-Free Symbol



Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: N/A		Designed for: Public Release	Mod. Date: 7/29/2015
TID #: 00573		Project Title: 4-Channel Class D	
Number: TIDA-00573	Rev: E2	Sheet Title:	
SVN Rev: Version control disabled		Assembly Variant: [No Variations]	Sheet: 3 of 3
Drawn By:		File: TID_Hardware.SchDoc	Size: B
Engineer: Albert Marco		Contact: http://www.ti.com/support	