

TPS62824 PSpice Transient Model Features and Limitations

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* Model Usage Notes:
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* A. Features have been modelled
* 1. Output Voltage Setting
* 2. Internal Fixed Soft-Start
* 3. Current Sense and Positive Overcurrent Protection(OCP)
* 4. Low-side FET Zero-Crossing
* 5. Output Voltage Discharge
* 6. Power Good
* 7. UVLO Protection
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* B. Features have not been modelled
* 1. Operating Quiescent Current
* 2. Shutdown Current
* 3. Temperature dependent characteristics
* 4. Ground pins have been tied to 0V internally and hence model does not support Inverting
*    topologies.
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* C. Application Notes
* 1. The parameter SS has been used to reach the steady state faster.
*    Keep SS = 0 to observe startup behaviour.
*    Keep SS = 1 and appropriate IC on Inductor and capacitor to observe for faster Steady state.
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