

4.1 Board Layouts

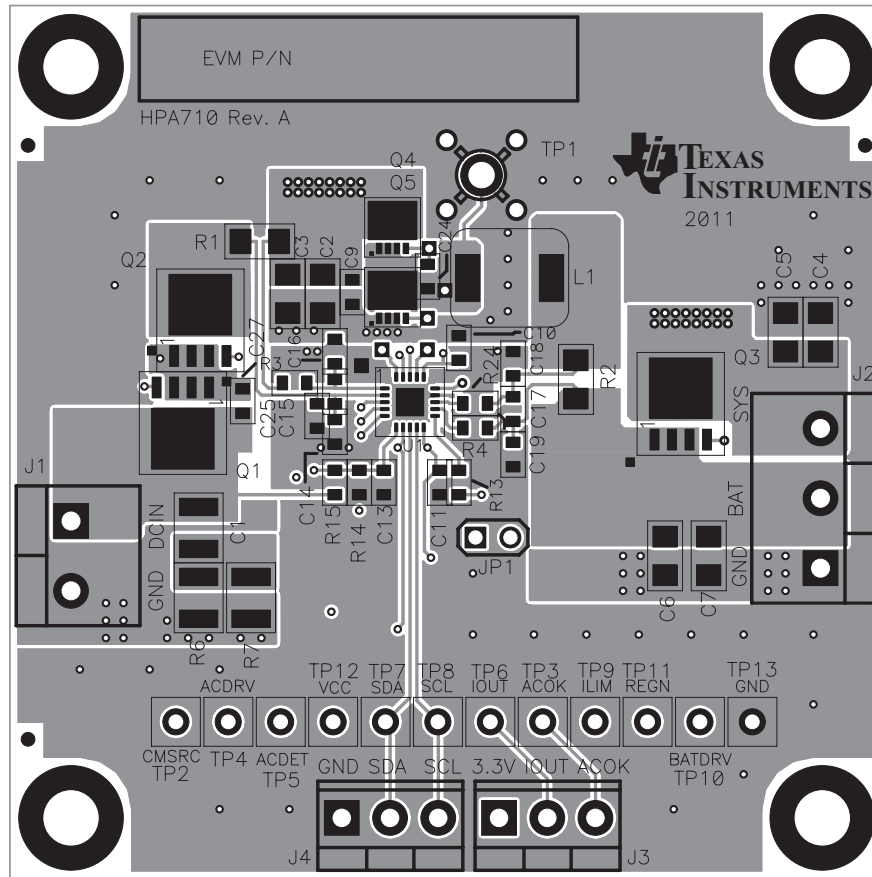


Figure 5. Top Assembly

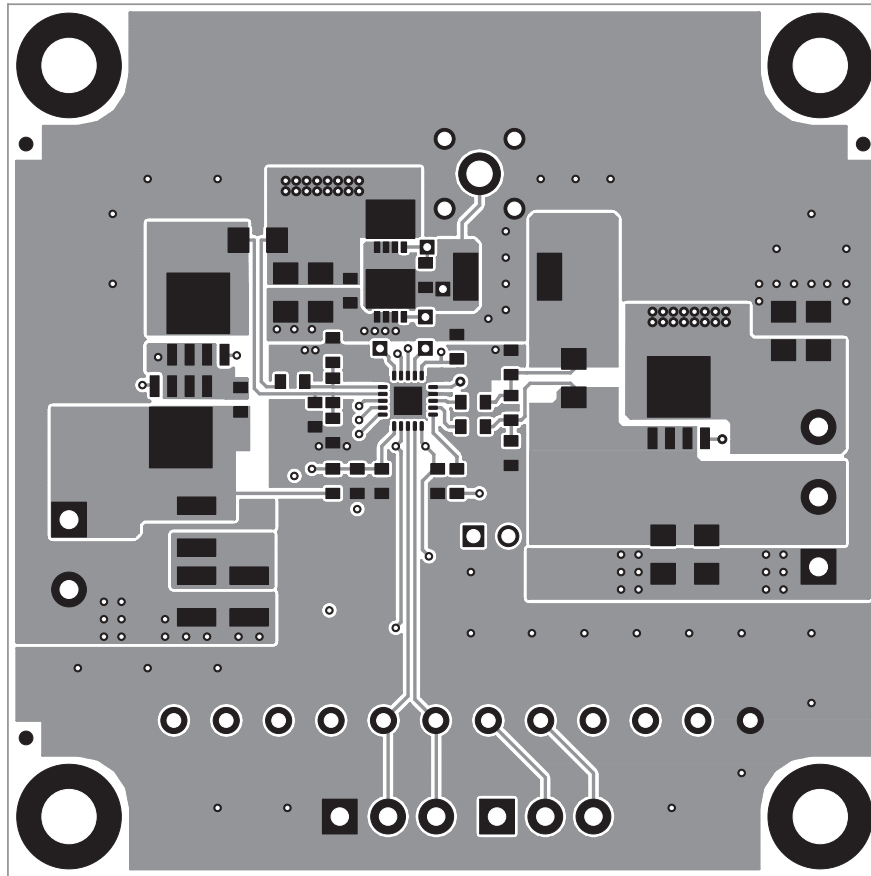


Figure 6. Top Layer

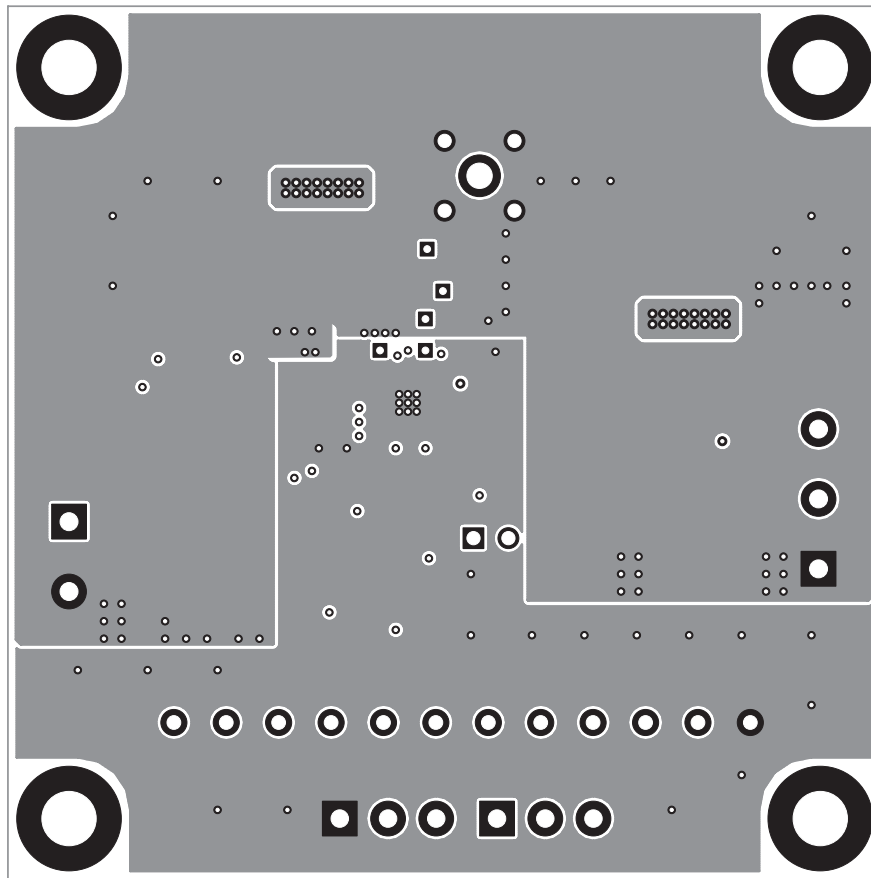


Figure 7. Second Layer

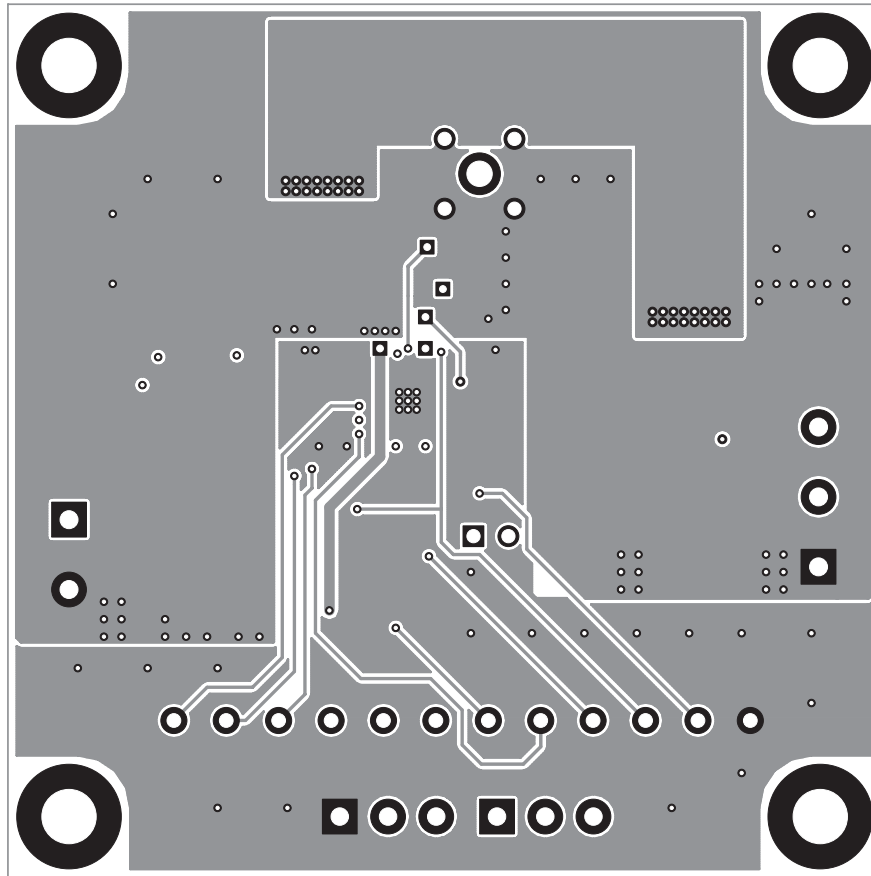


Figure 8. Third Layer

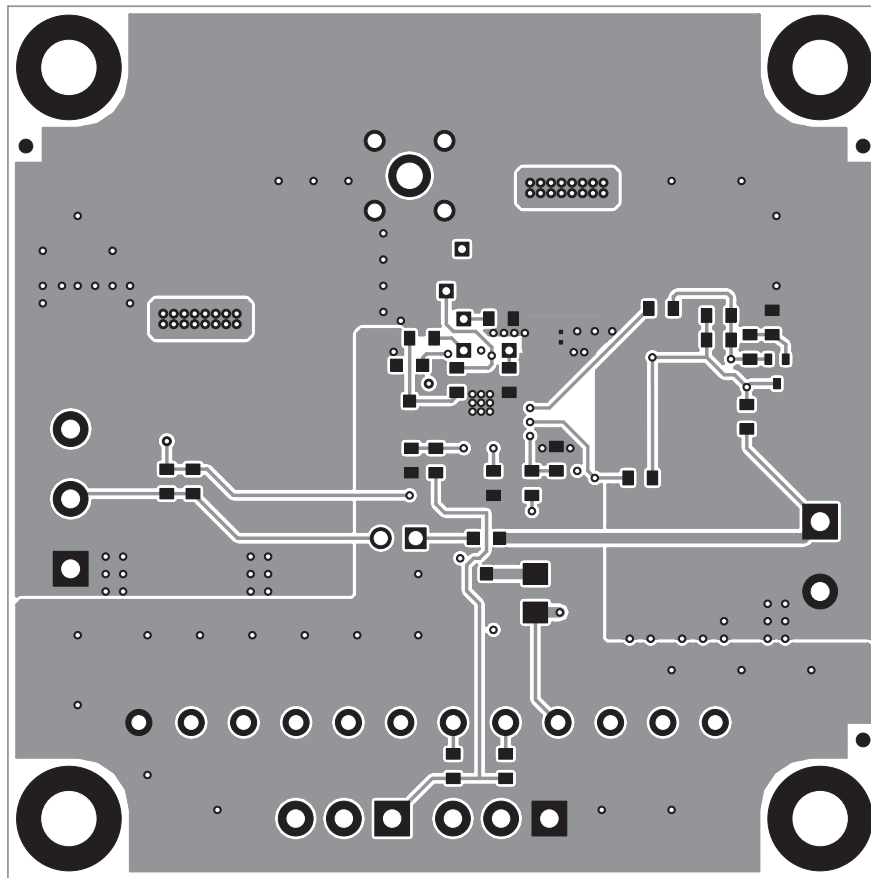


Figure 9. Bottom Layer

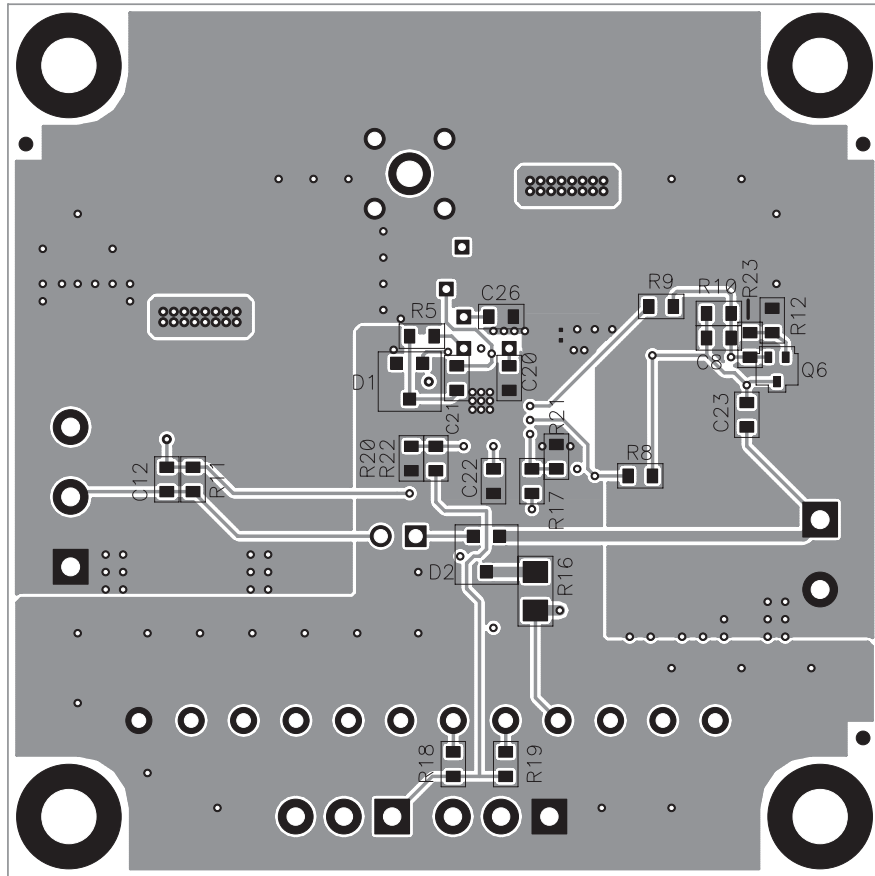


Figure 10. Bottom Assembly

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