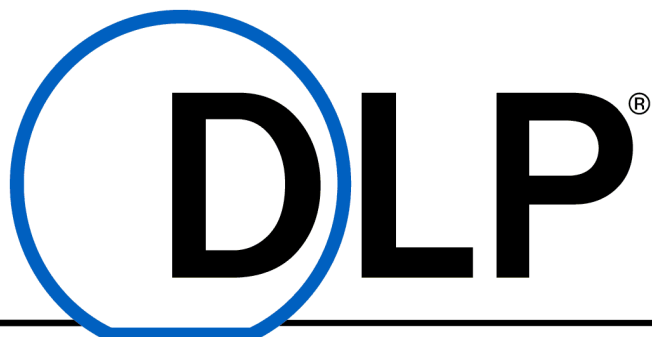


NOTES, UNLESS OTHERWISE SPECIFIED:

1. The netname "P1P1V" represents connection to the +1.1V power plane.
2. The netname "P1P2V" represents connection to the +1.2V power plane.
3. The netname "P1P8V" represents connection to the +1.8V power plane.
4. The netname "DSP\_P1P8V" represents connection to the +1.8V power plane for use with the DSP.
5. The netname "P2P5V" represents connection to the +2.5V power plane.
6. The netname "P3P3V" represents connection to the +3.3V power plane.
7. The netname "P3P3V\_LP" represents connection to the +3.3V power plane for use with ultra-low power mode.
8. The netname "P5V" represents connection to the +5.0V power plane.
9. The netname "P12V" represents connection to the +12.0V power plane.
10. The netname "GND" represents connection to the ground plane.
11. A "Z" suffix on a signal name indicates an active low signal.
12. All components with designators "U", "Q", and "D" are electrostatic discharge sensitive.
13. All components with designators above 500 are mounted solder side of the board.
14. All resistor values are in ohms.
15. All capacitor values in microfarads unless otherwise specified.

COMPUTER GENERATED DRAWING - DO NOT REVISE MANUALLY

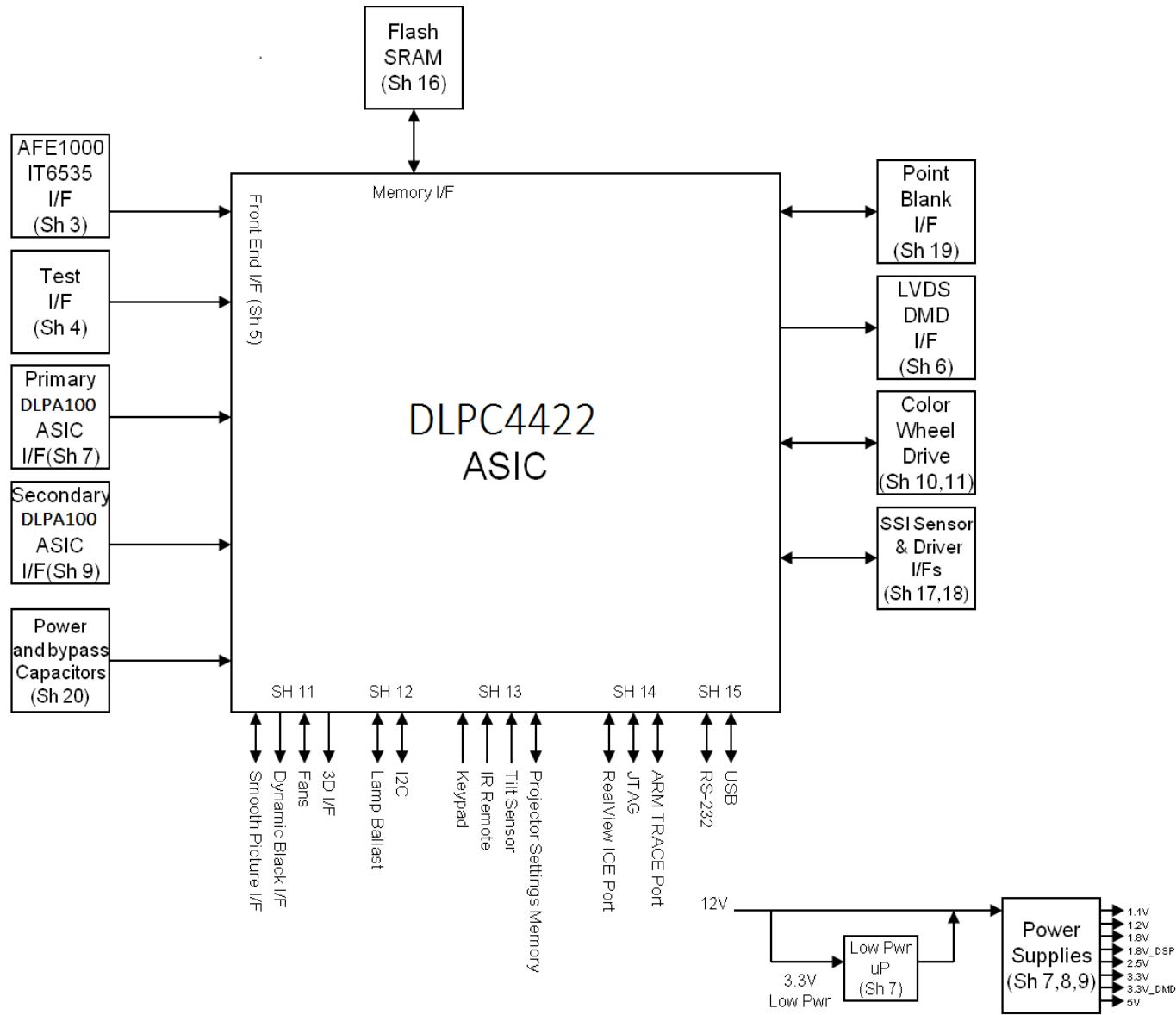
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	ECO 2135517: Initial Release	9/6/2013	DH
B	ECO 2138184: Revision B	12/3/2013	DH
C	ECO 2158403: Revision C	5/24/2016	DH



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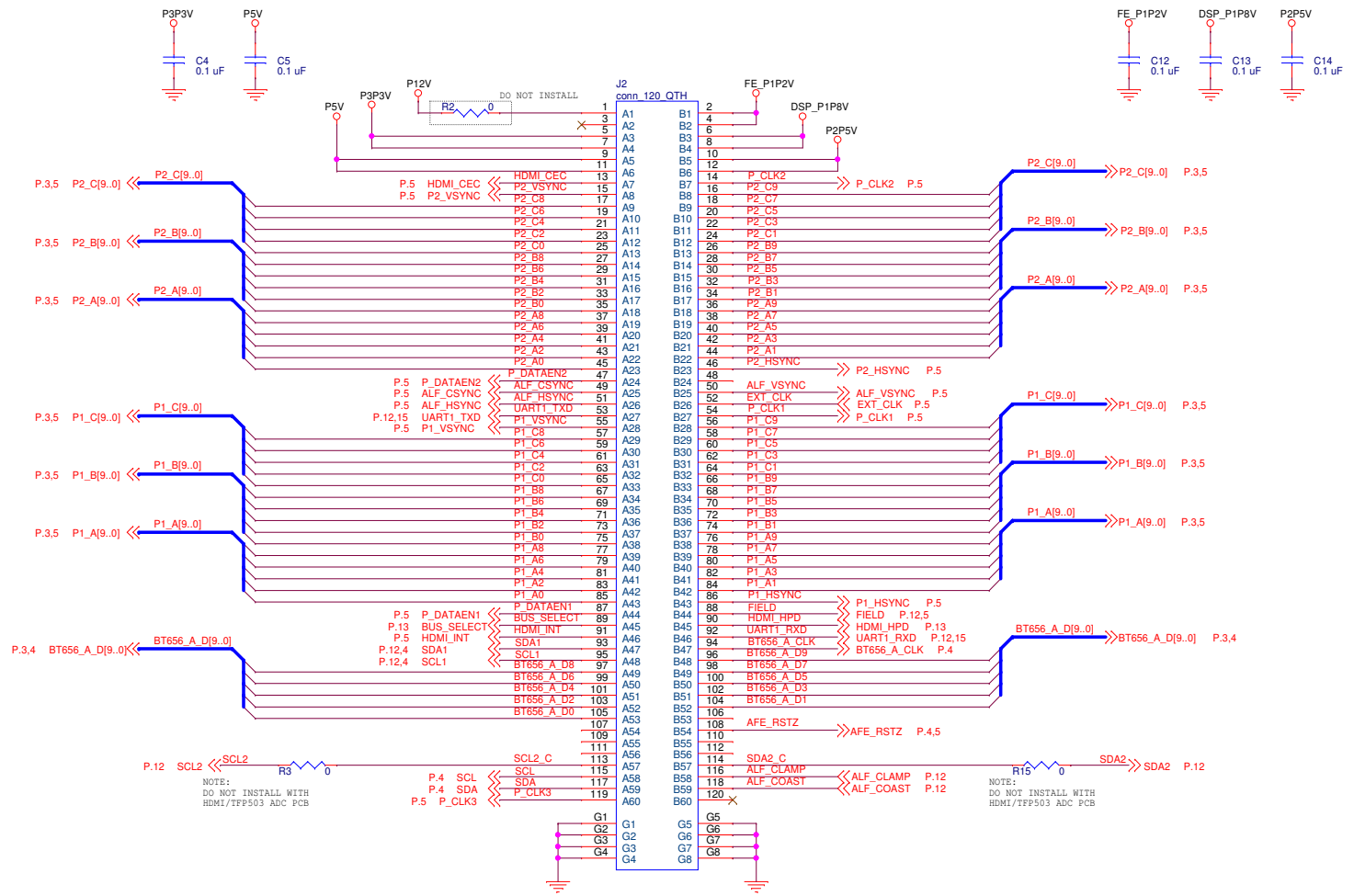
		DWN Amy White	DATE 9/5/2013	<b>TEXAS INSTRUMENTS</b> (C) COPYRIGHT 2016 TEXAS INSTRUMENTS ALL RIGHTS RESERVED	
		ENGR George Pawlowski	9/5/2013		
		SYST Nathan Buettner	9/5/2013		
		PRJ			
2513336	0314SS	QA Richard Gall	9/5/2013	TITLE TIDA-01474 Formatter Board Schematic	
NEXT ASSY	USED ON			<b>A3</b> DRAWING NO 2513334	REV C
APPLICATION		SW		SCALE	SHEET 1 of 22



I2C Slave Addresses		
I2C Device	I2C Slave Address	
Projector Settings Memory, 24C32	0xA8	Sheet 13
Temperature Sensor, TMP100	0x9C	Sheet 13
GPIO Expander, TCA8418	0x68	Sheet 13
TILT Sensor, MXC623550B	0x2A	Sheet 13
Non-volatile Pot (1.8V PLL), AD5252	0x5A	Sheet 8
Non-volatile Pot (1.1V & 1.8V), AD5252	0x5C	Sheet 8
Non-volatile Pot (1.2V), AD5252	0x5E	Sheet 9
Daughter Card I2C Device		
I2C Device	I2C Slave Address	
AFE1000	0xB8	
HDMI/DP Receiver, IT6535	0xB0	
GPIO Expander, PCA9539	0xEA	
VGA EDID #1, 24C02B	0xA0	
VGA EDID #2, 24C02B	0xA0	
HDMI EDID, 24C02B	0xA0	
DP EDID, 24C02B	0xA0	
Non-volatile Pot (3.3V), MAX5418	0x50	
Non-volatile Pot (1.9V), MAX5418	0x52	

### INDEX

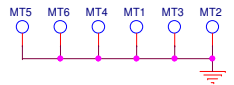
- Sheet 1: Cover
- Sheet 2: Block Diagram
- Sheet 3: AFE1000/IT6535 Daughter Card Interface
- Sheet 4: Test Interface Connector
- Sheet 5: Front End Interface
- Sheet 6: DMD Flex Interface
- Sheet 7: Input Power and PMD1000/DDP442x Interface
- Sheet 8: Primary PMD1000 Power Supplies
- Sheet 9: Secondary PMD1000 Power Supplies
- Sheet 10: Color Wheel Drive
- Sheet 11: Fan, Color Wheel Interface and Peripherals
- Sheet 12: GPIO, I2C, and Ballast Control
- Sheet 13: GPIO Expander and Keyboard
- Sheet 14: Test Points, ARM Trace JTAG and Reset
- Sheet 15: RS-232, SSP, and USB
- Sheet 16: Flash and SRAM Memory Interface
- Sheet 17: SSI Sensor Interface
- Sheet 18: SSI Driver Interface
- Sheet 19: PointBlank Interface
- Sheet 20: DDP442x Power and Bypass Capacitors
- Sheet 21: Revision History
- Sheet 22: Important Notice



NOTE:  
 DO NOT INSTALL WITH  
 HDMI/TFP503 ADC PCB

NOTE:  
 DO NOT INSTALL WITH  
 HDMI/TFP503 ADC PCB

CCA MOUNTING HOLES

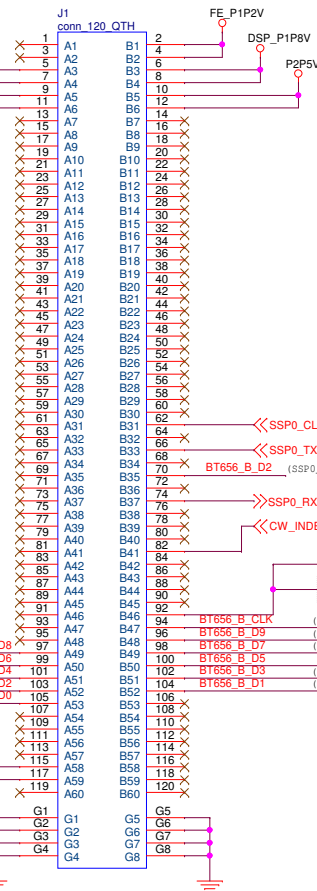
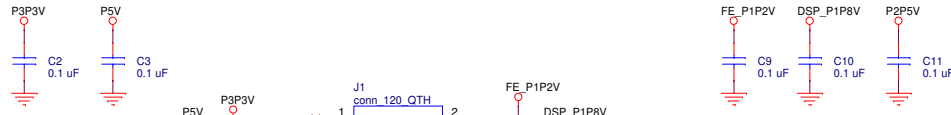


AFE1000/IT6535 Daughter Card Interface

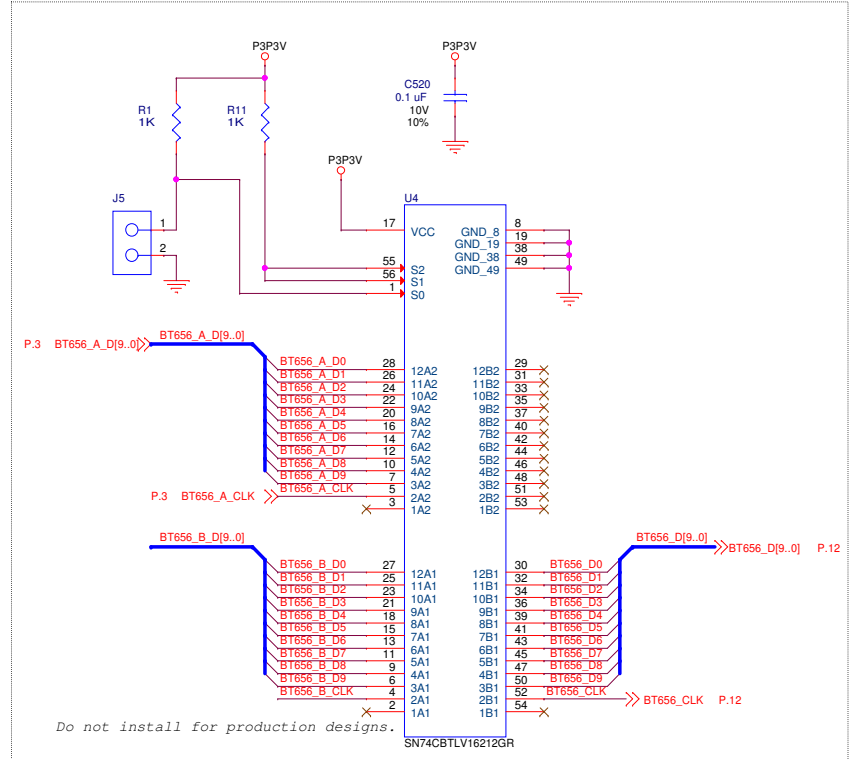
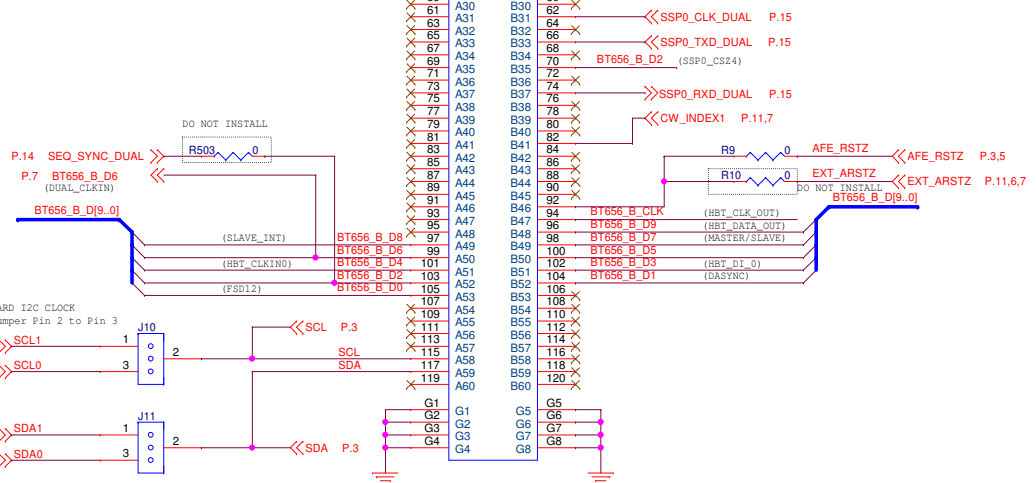
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	ISSUE DATE 9/5/2013	SCALE	SHEET 3 OF 22		



NOTE:  
 When jumper is un-installed, A2=B1  
 When jumper is installed, A1=B1  
 Default: Jumper is un-installed



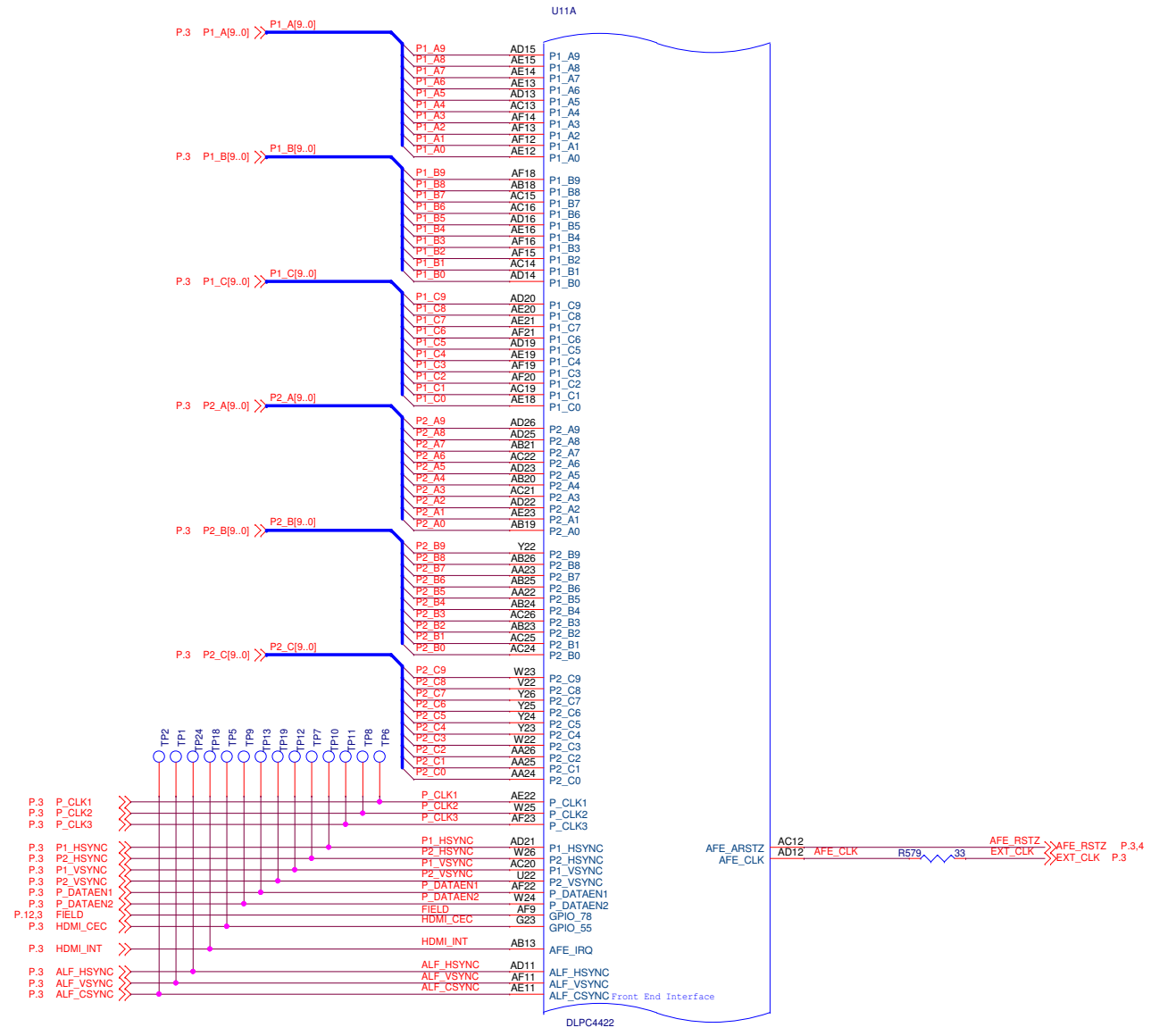
Do not install for production designs.

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Test Interface Connector

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U11A

DLPC442

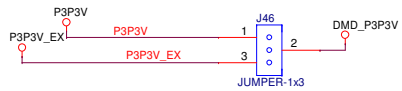


Front End Interface

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	SCALE	SHEET 5 OF 22	

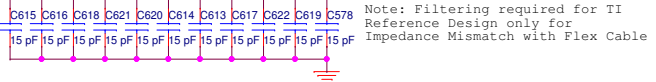


U11B

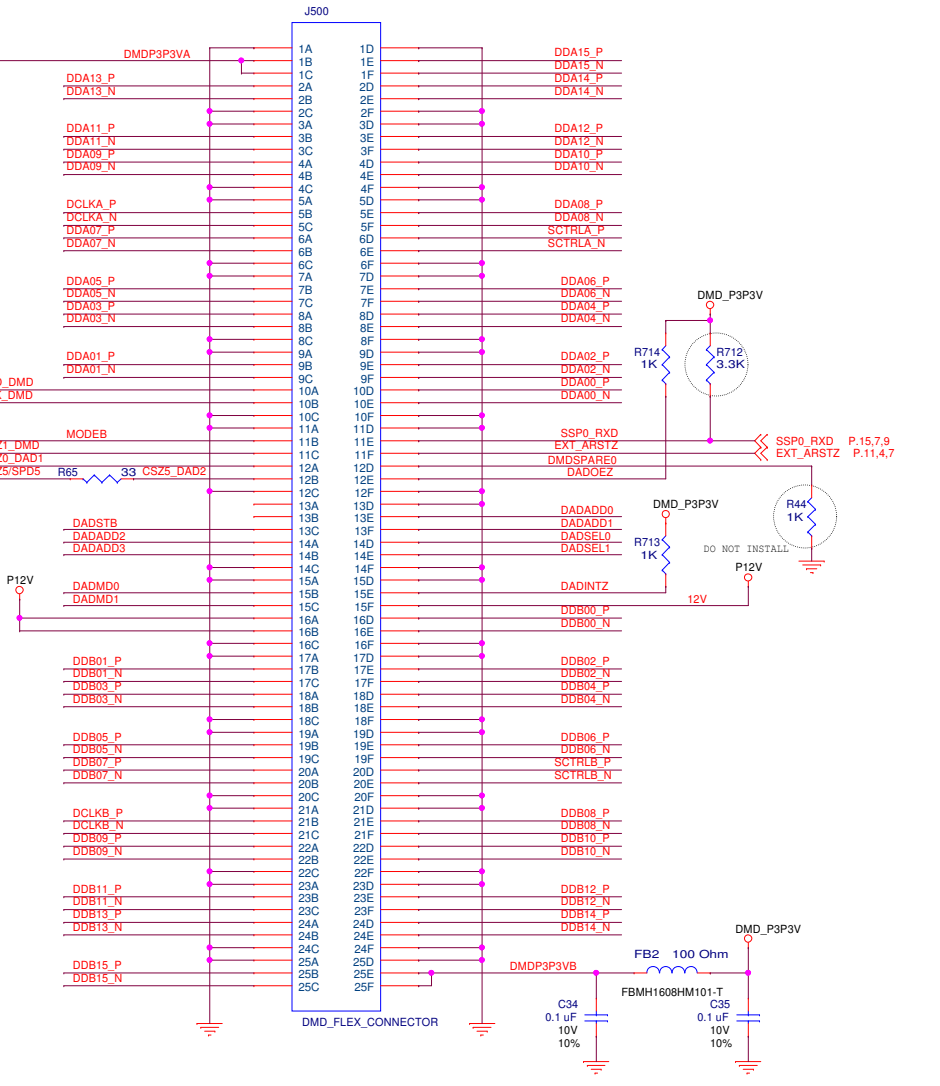
- N1 DBE00\_P
- N2 DBE00\_N
- N3 DBE01\_P
- N4 DBE01\_N
- M2 DBE02\_P
- M1 DBE02\_N
- N3 DBE03\_P
- N4 DBE03\_N
- L1 DBE04\_P
- L2 DBE04\_N
- L3 DBE05\_P
- L4 DBE05\_N
- K1 DBE06\_P
- K2 DBE06\_N
- K3 DBE07\_P
- K4 DBE07\_N
- H1 DBE08\_P
- H2 DBE08\_N
- H3 DBE09\_P
- H4 DBE09\_N
- G1 DBE10\_P
- G2 DBE10\_N
- G3 DBE11\_P
- G4 DBE11\_N
- F1 DBE12\_P
- F2 DBE12\_N
- F3 DBE13\_P
- F4 DBE13\_N
- E1 DBE14\_P
- E2 DBE14\_N
- D1 DBE15\_P
- D2 DBE15\_N
- P4 DDA00\_P
- P3 DDA00\_N
- P2 DDA01\_P
- P1 DDA01\_N
- R4 DDA02\_P
- R3 DDA02\_N
- R2 DDA03\_P
- R1 DDA03\_N
- T4 DDA04\_P
- T3 DDA04\_N
- T2 DDA05\_P
- T1 DDA05\_N
- U4 DDA06\_P
- U3 DDA06\_N
- U2 DDA07\_P
- U1 DDA07\_N
- W4 DDA08\_P
- W3 DDA08\_N
- W2 DDA09\_P
- W1 DDA09\_N
- Y2 DDA10\_P
- Y1 DDA10\_N
- Y4 DDA11\_P
- Y3 DDA11\_N
- AA2 DDA12\_P
- AA1 DDA12\_N
- AA4 DDA13\_P
- AA3 DDA13\_N
- AB2 DDA14\_P
- AB1 DDA14\_N
- AC2 DDA15\_P
- AC1 DDA15\_N
- J3 DCLKB\_P
- J4 DCLKB\_N
- V4 DCLKA\_P
- V3 DCLKA\_N
- J1 SCTRLB\_P
- J2 SCTRLB\_N
- V2 SCTRLA\_P
- V1 SCTRLA\_N
- AB8 DA0
- AF4 DA1
- AE5 DA2
- AD6 DA3
- AE6 DM0
- AD7 DM1
- AC7 DS0
- AE4 DS1
- AE5 DSTB
- AE7 DOEZ
- AC8 DINTZ
- DA0
- DA1
- DA2
- DA3
- DM0
- DM1
- DS0
- DS1
- DSTB
- DOEZ
- DADADD0
- DADADD1
- DADADD2
- DADADD3
- DADMD0
- DADMD1
- DADSEL0
- DADSEL1
- DADSTRB
- DADOEZ
- DAD\_INTZ

DLPC4422

J45 not installed - Half Bus Mode  
 J45 installed - Full Bus Mode



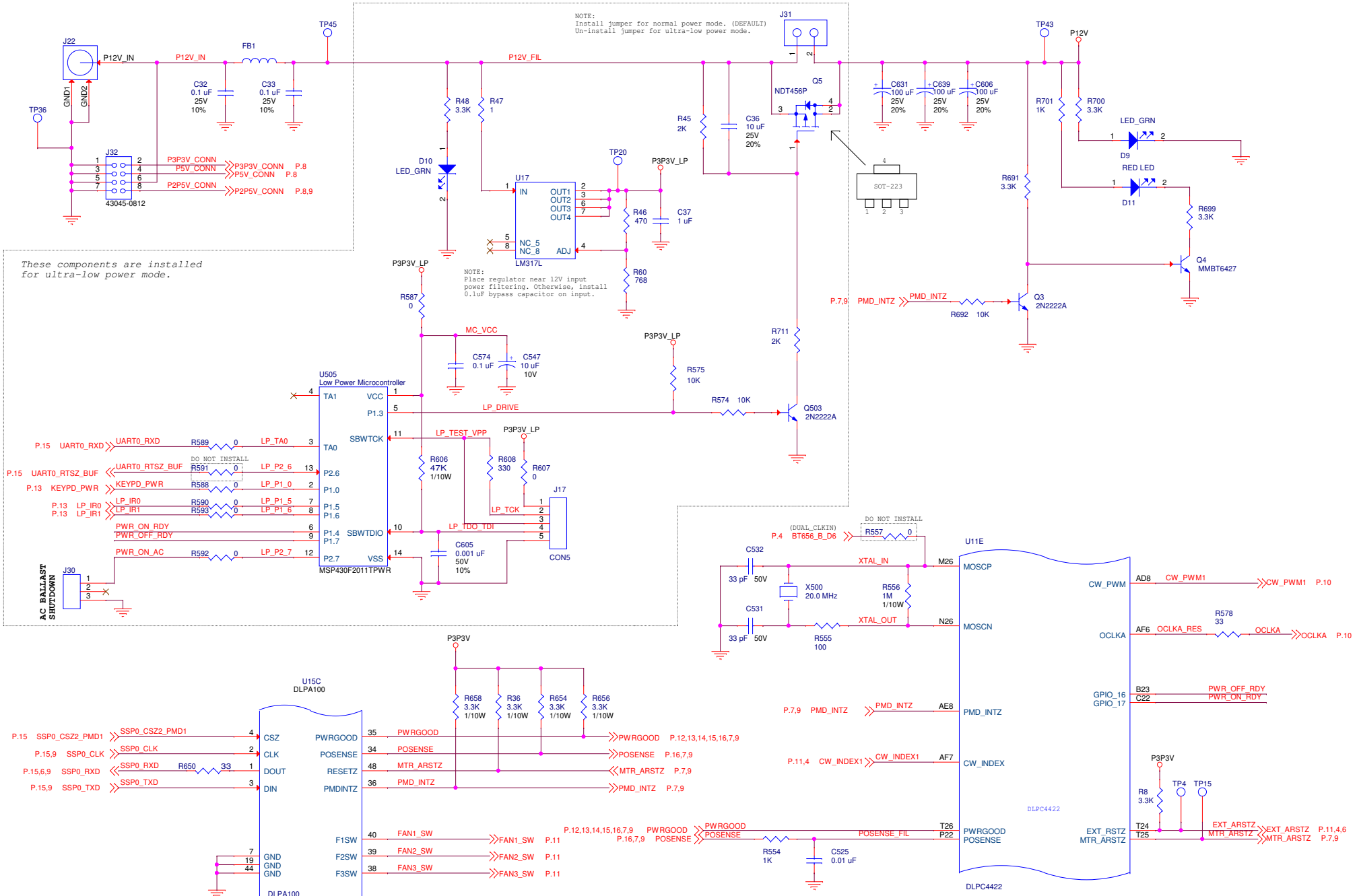
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DMD Flex Interface

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	ISSUE DATE 9/5/2013	SCALE		SHEET 6 OF 22	



NOTE:  
Install jumper for normal power mode. (DEFAULT)  
Un-install jumper for ultra-low power mode.

These components are installed for ultra-low power mode.

NOTE:  
Place regulator near 12V input power filtering. Otherwise, install 0.1uF bypass capacitor on input.

Input Power and PMD1000 / DDP442x Interface

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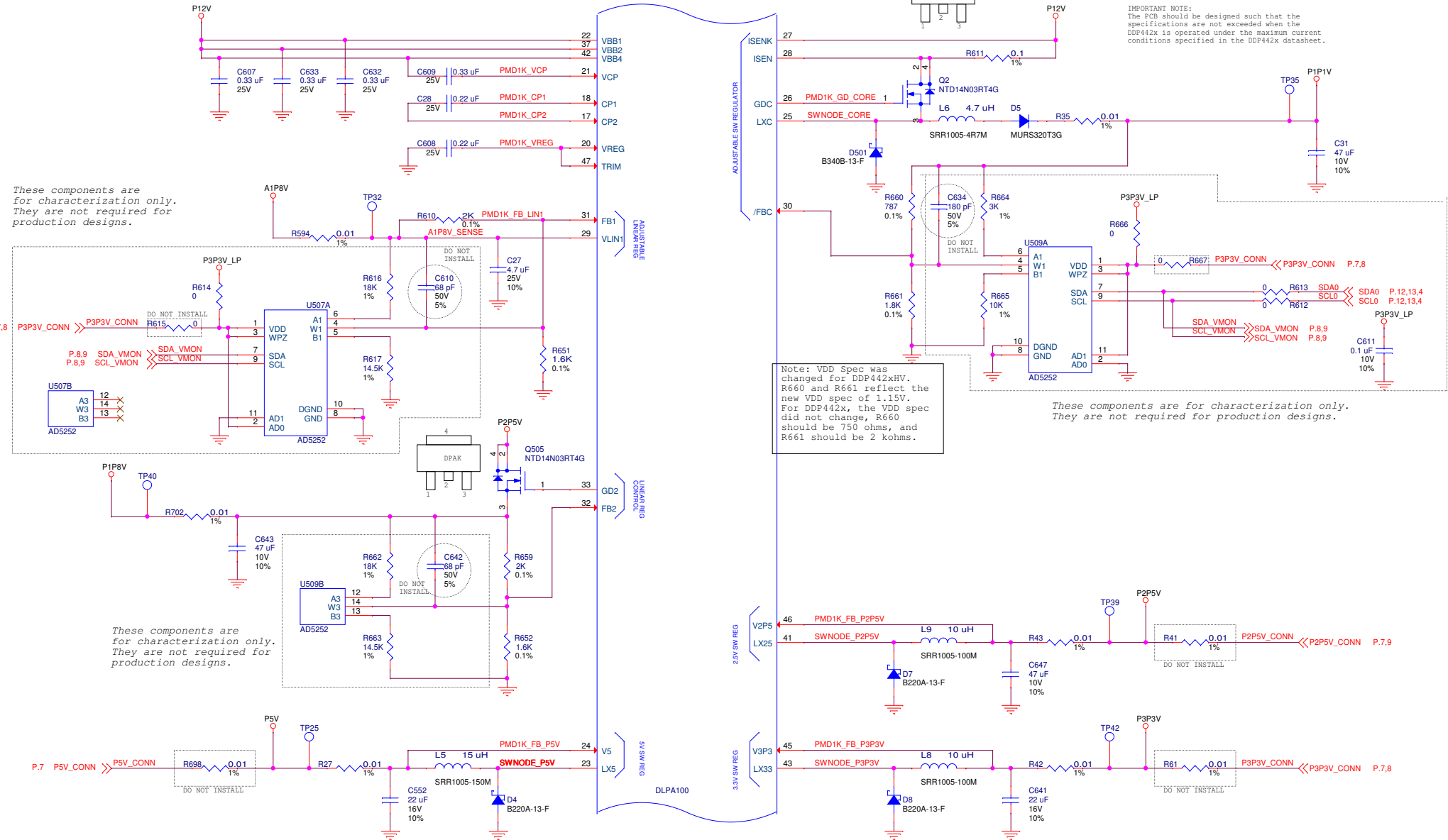
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	ISSUE DATE 9/5/2013			SCALE	SHEET 7 OF 22

NOTE:  
R27, R35, R42, R43, R594, R702 are used  
for current measurement and are not required for  
production units.

R61, R41, R696, R615 are used for alternate  
lab supplies and are not required for production units.

NOTE:  
Consult the PMD1000 Data Sheet for  
external component specifications.

IMPORTANT NOTE:  
The PCB should be designed such that the  
specifications are not exceeded when the  
DDP442x is operated under the maximum current  
conditions specified in the DDP442x datasheet.



These components are  
for characterization only.  
They are not required for  
production designs.

Note: VDD Spec was  
changed for DDP442xHV.  
R660 and R661 reflect the  
new VDD spec of 1.15V.  
For DDP442x, the VDD spec  
did not change, R660  
should be 750 ohms, and  
R661 should be 2 kohms.

These components are for characterization only.  
They are not required for production designs.

These components are  
for characterization only.  
They are not required for  
production designs.

Primary PMD1000 Power Supplies

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	ISSUE DATE 9/5/2013	SCALE		SHEET 8 OF 22	

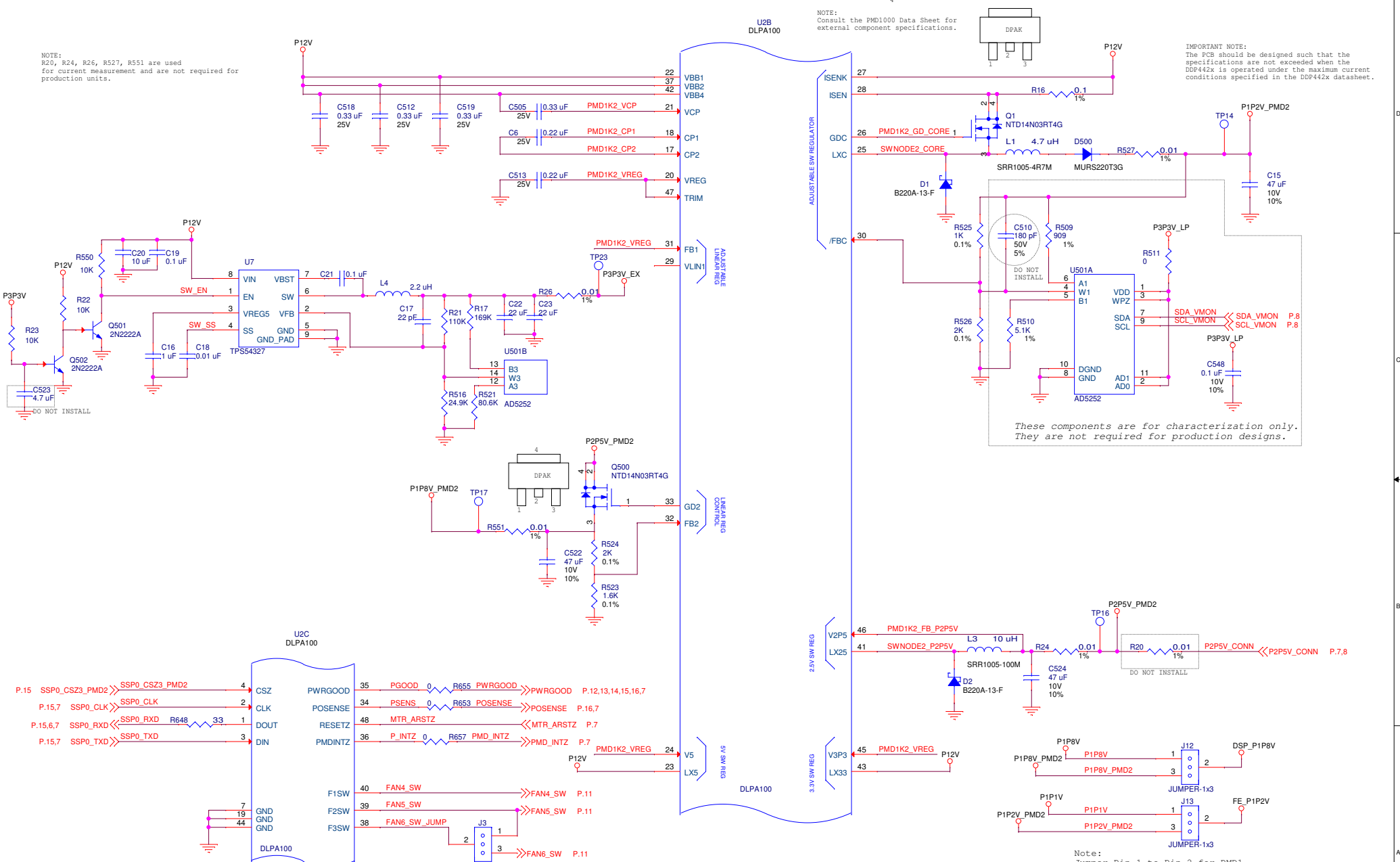
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NOTE:  
R20, R24, R26, R527, R551 are used  
for current measurement and are not required for  
production units.

NOTE:  
Consult the PMD1000 Data Sheet for  
external component specifications.

IMPORTANT NOTE:  
The PCB should be designed such that the  
specifications are not exceeded when the  
DDP442x is operated under the maximum current  
conditions specified in the DDP442x datasheet.



Note:  
Jumper Pin 1 to Pin 2 for High Current Fan 5 (Fan 6 Disabled)  
Jumper Pin 2 to Pin 3 for Fan 5 and Fan 6 normal current  
Default: Pin 2 to Pin 3

Note:  
Jumper Pin 1 to Pin 2 for PMD1  
Jumper Pin 2 to Pin 3 for PMD2  
Important: When using Advanced Connectivity Daughter  
Card, jumper configuration must match

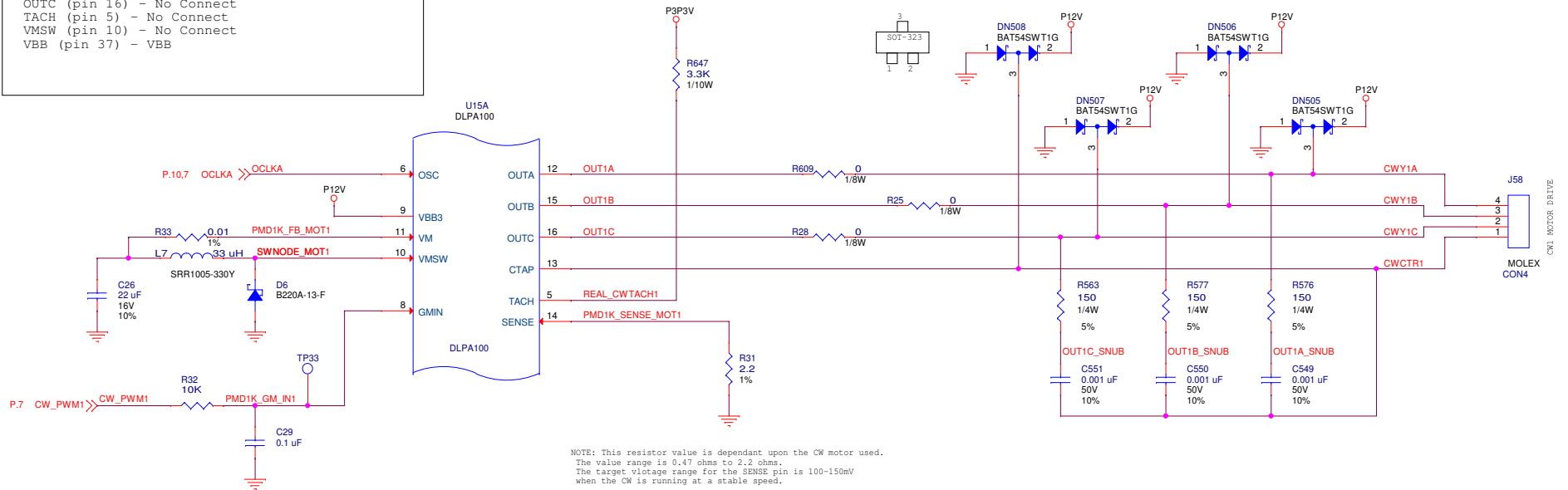
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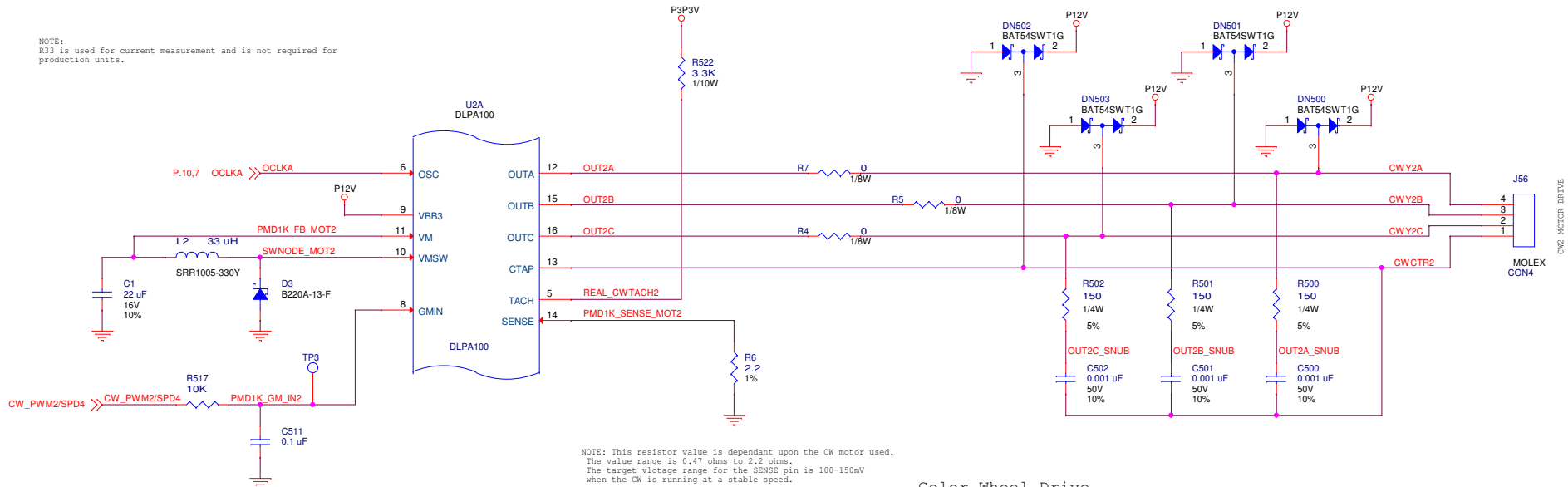
Secondary PMD1000 Supplies

TEXAS INSTRUMENTS	DWN Amy White	DATE 8/19/2013	A3	DRAWING NO 2513334	REV C
	ISSUE DATE 9/5/2013	SCALE		SHEET 9 OF 22	

Note: If not using the motor driver on the PMD1000 follow these guidelines for unused pins:  
 GMIN (pin 8) - Ground  
 OSC (pin 6) - Ground  
 SENSE (pin 14) - Ground  
 CTAP (pin 13) - VBB  
 VM (pin 11) - VBB  
 OUTA (pin 12) - No Connect  
 OUTB (pin 15) - No Connect  
 OUTC (pin 16) - No Connect  
 TACH (pin 5) - No Connect  
 VMSW (pin 10) - No Connect  
 VBB (pin 37) - VBB



NOTE:  
 R33 is used for current measurement and is not required for production units.

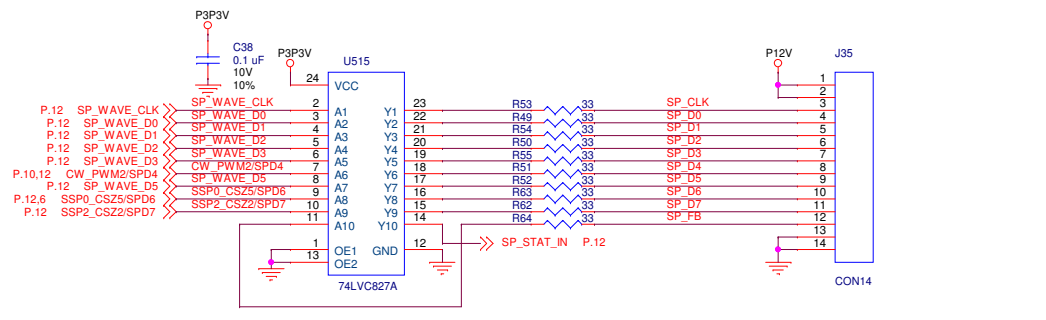
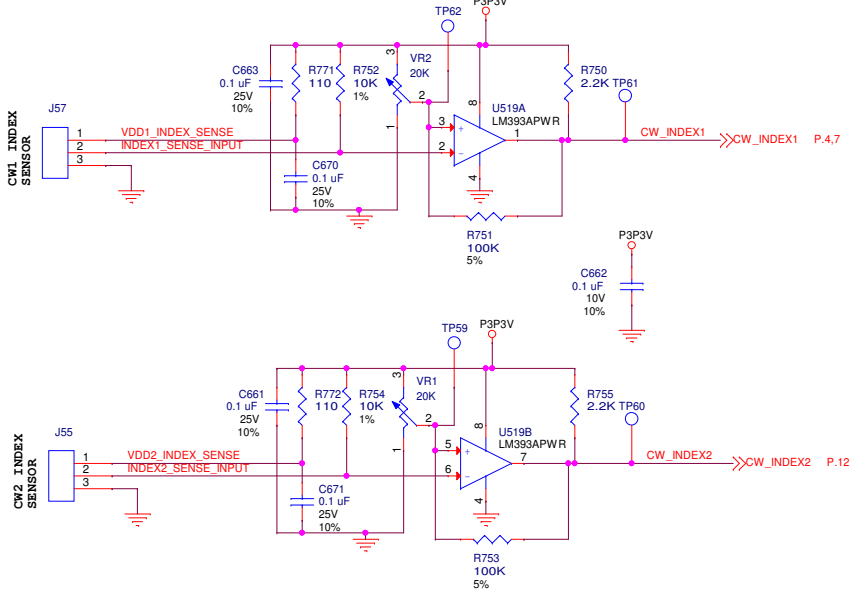
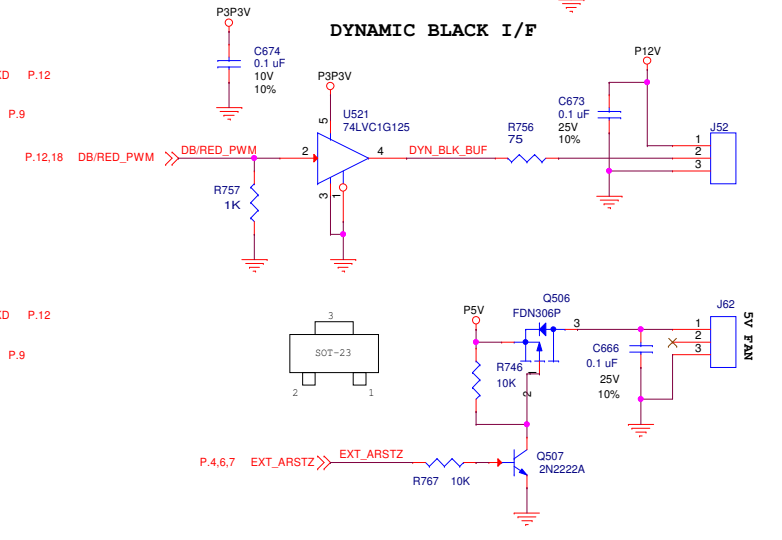
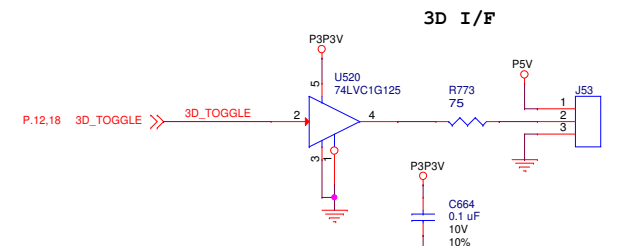
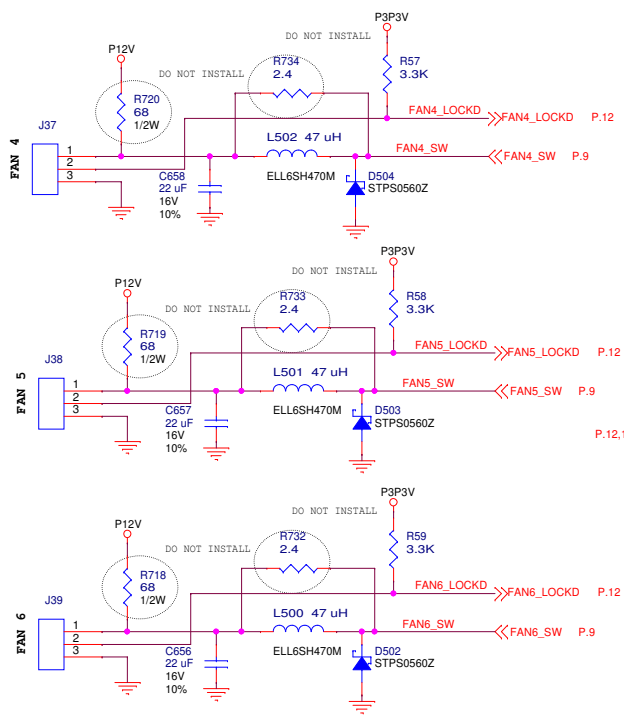
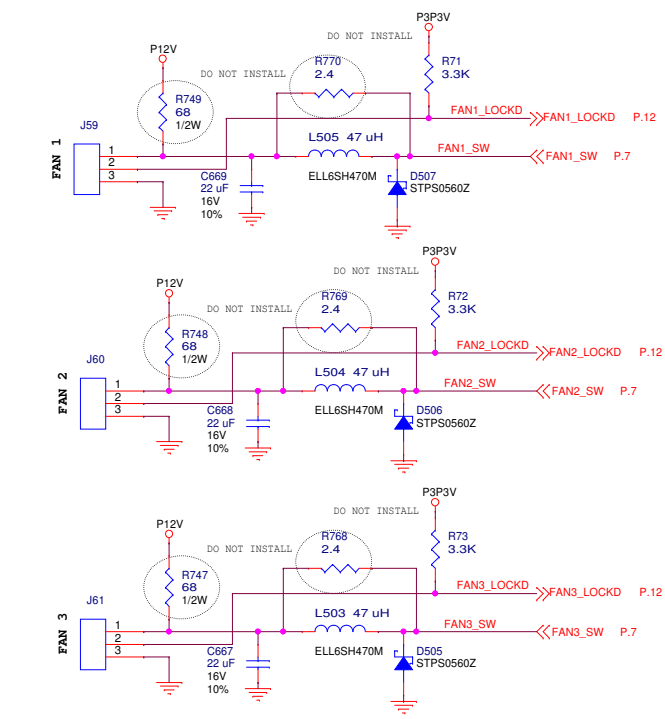


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Color Wheel Drive

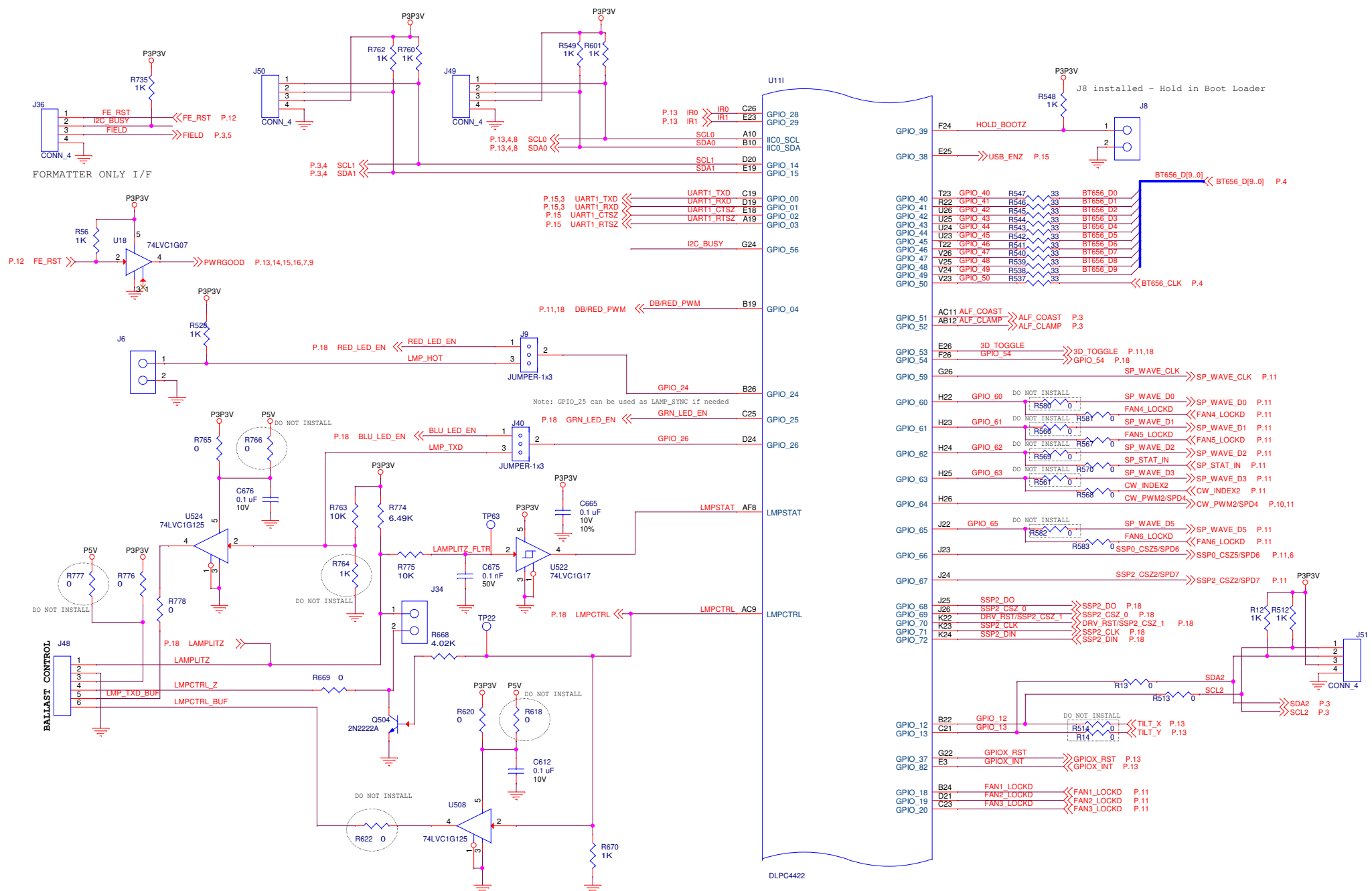
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	ISSUE DATE 9/5/2013	SCALE		SHEET 10 OF 22	



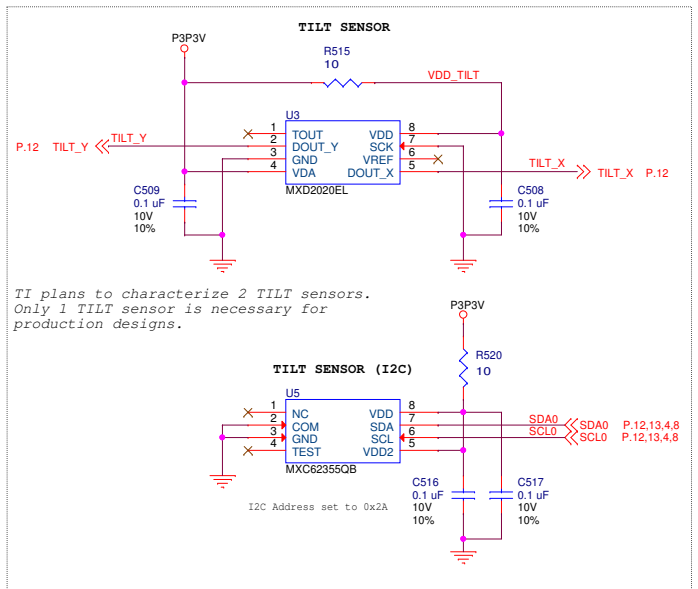
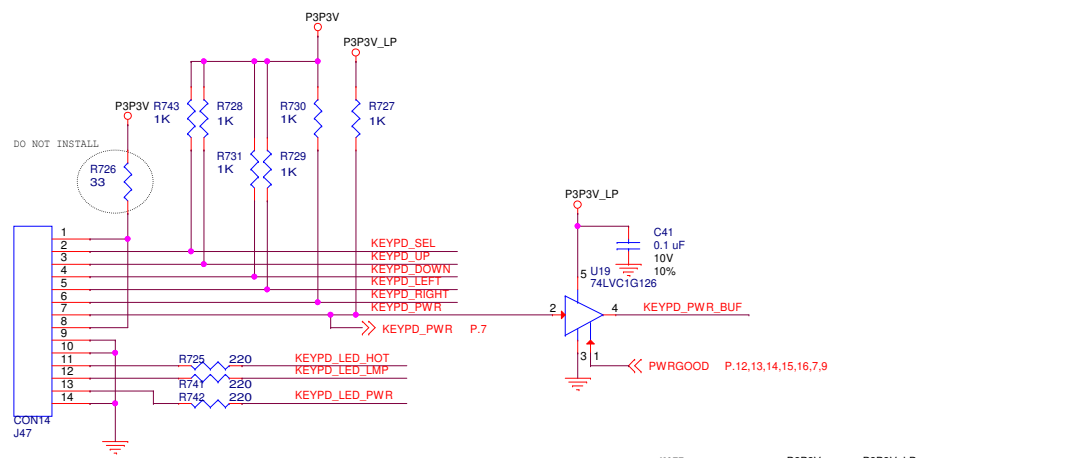
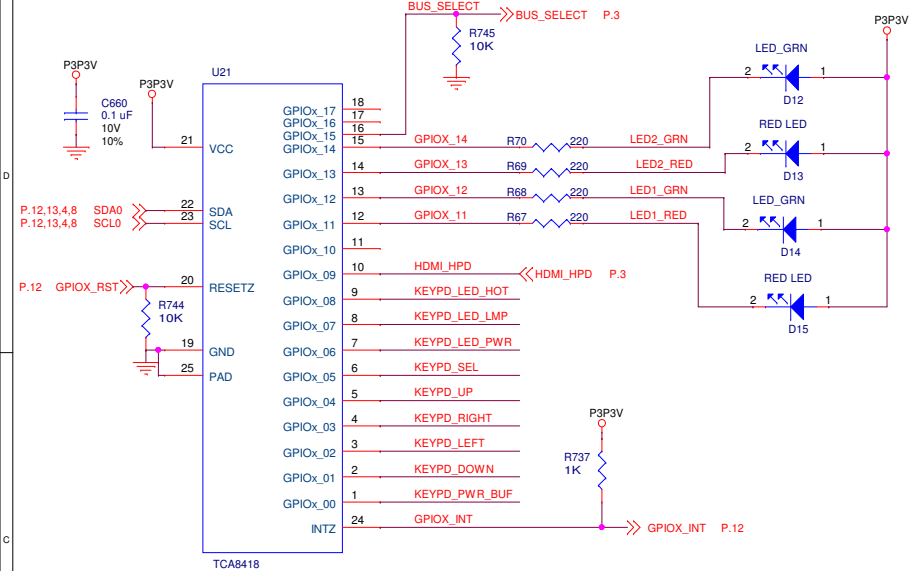
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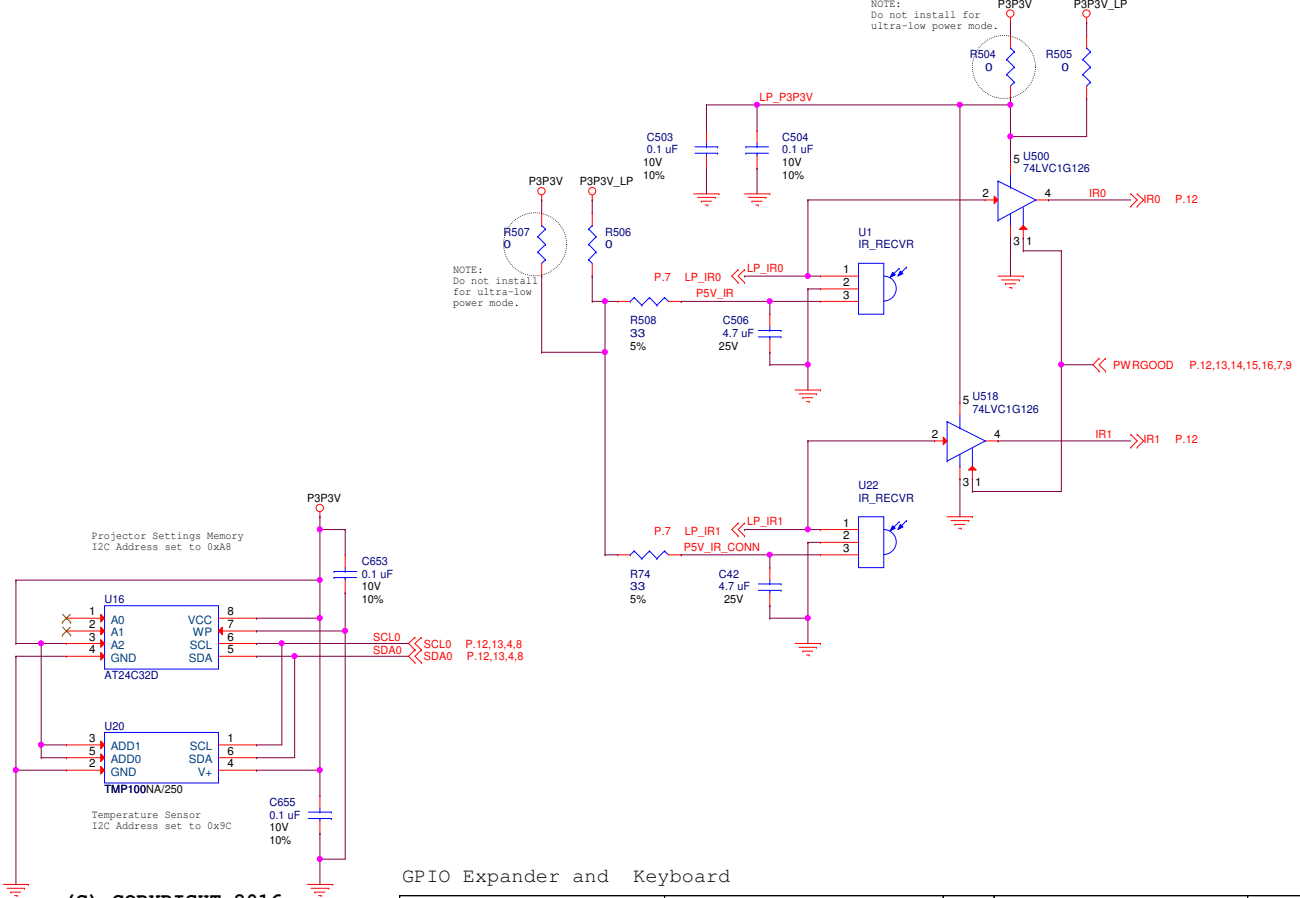
Fan, Color Wheel Interface and Peripherals		DWN Amy White	DATE 8/19/2013	A3	DRAWING NO 2513334	REV C
TEXAS INSTRUMENTS		ISSUE DATE 9/5/2013	SCALE	SHEET 11 OF 22		



**KEYBOARD INTERFACE**



TI plans to characterize 2 TILT sensors. Only 1 TILT sensor is necessary for production designs.



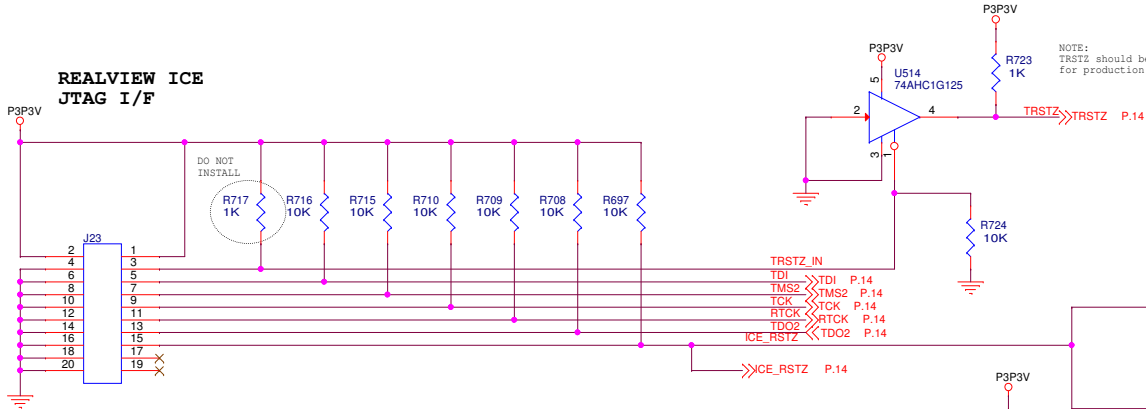
GPIO Expander and Keyboard

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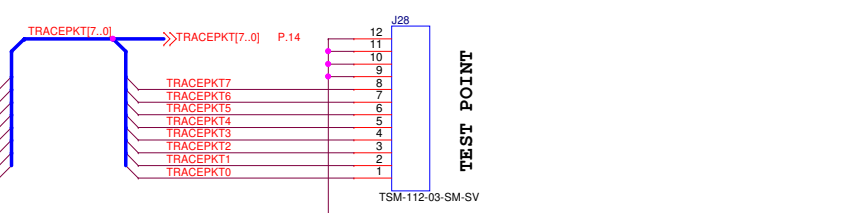
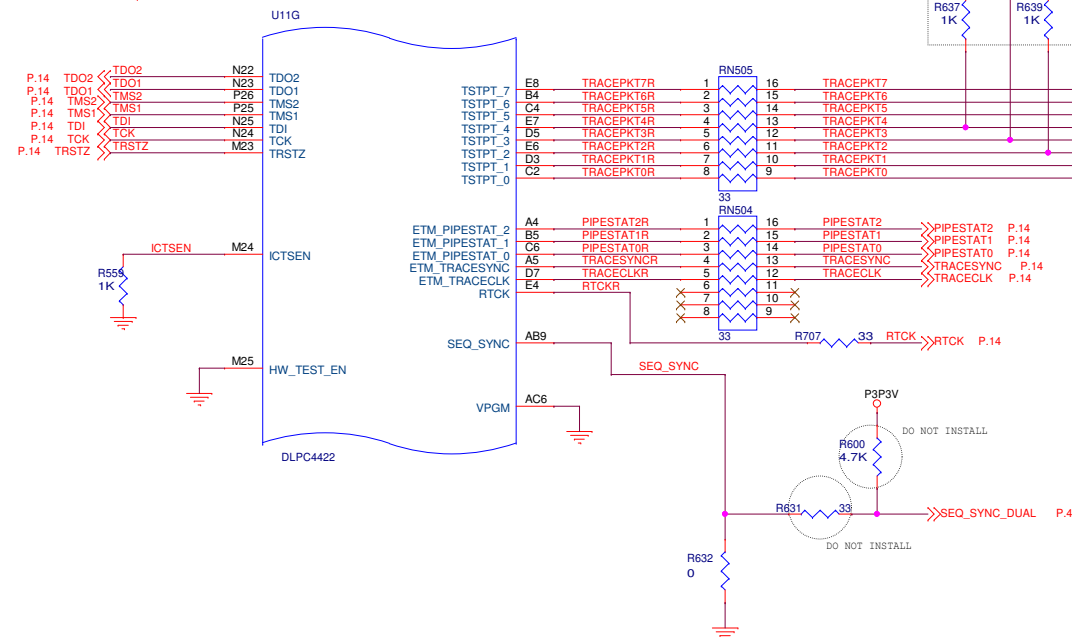
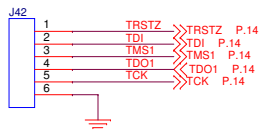
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	ISSUE DATE 9/5/2013	SCALE		SHEET 13 OF 22	

**REALVIEW ICE  
JTAG I/F**

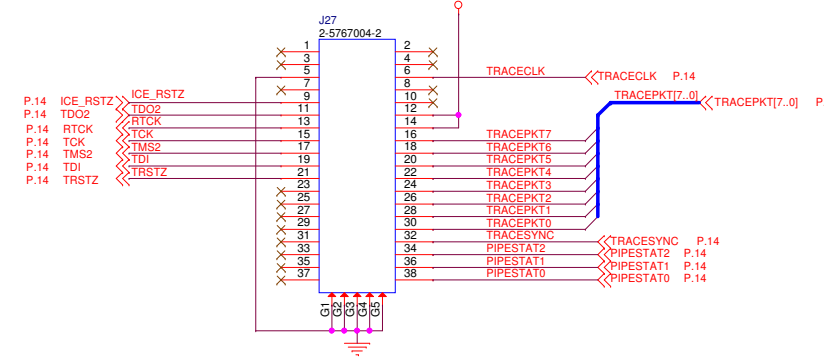


MANUAL RESET JUMPER	
INSTALLED	HOLD IN RESET
NOT INSTALLED	NORMAL OPERATION (DEFAULT)

**JTAG BOUNDARY SCAN**



**ARM TRACE PORT**

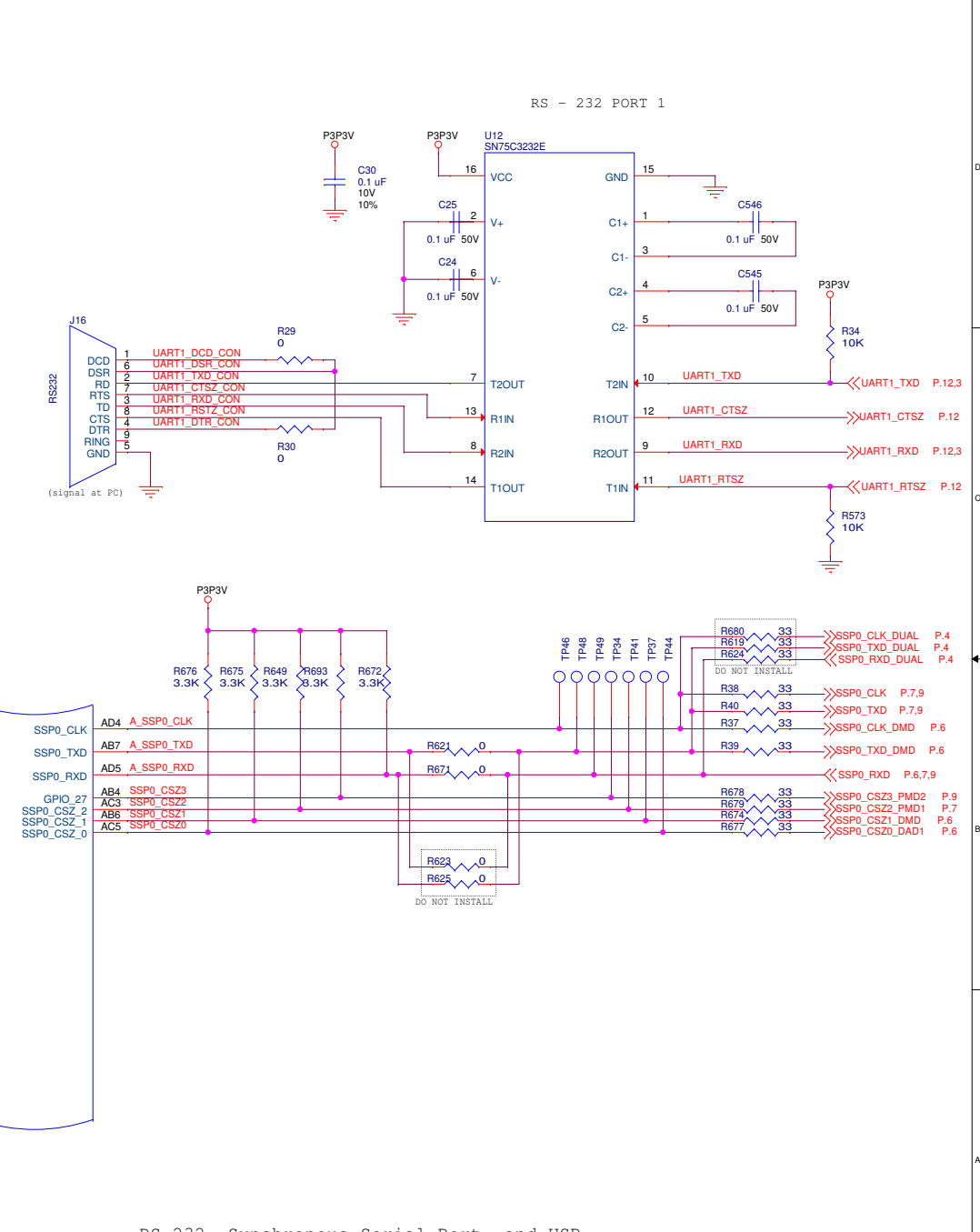
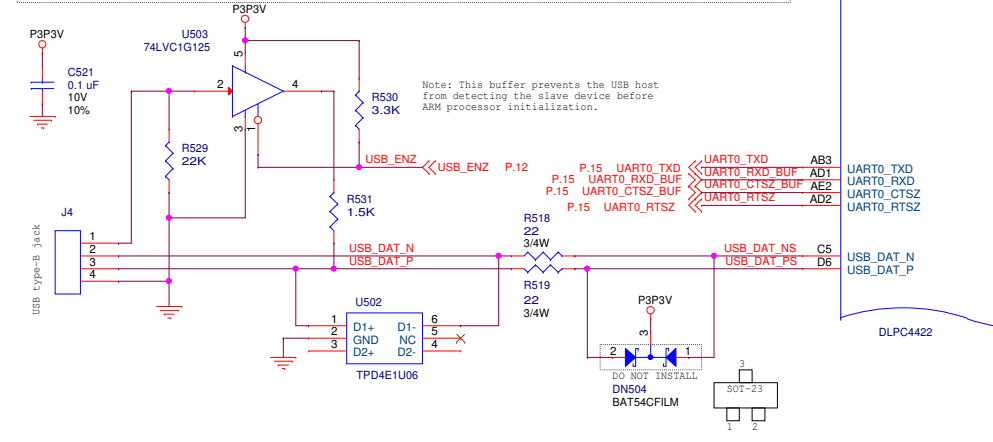
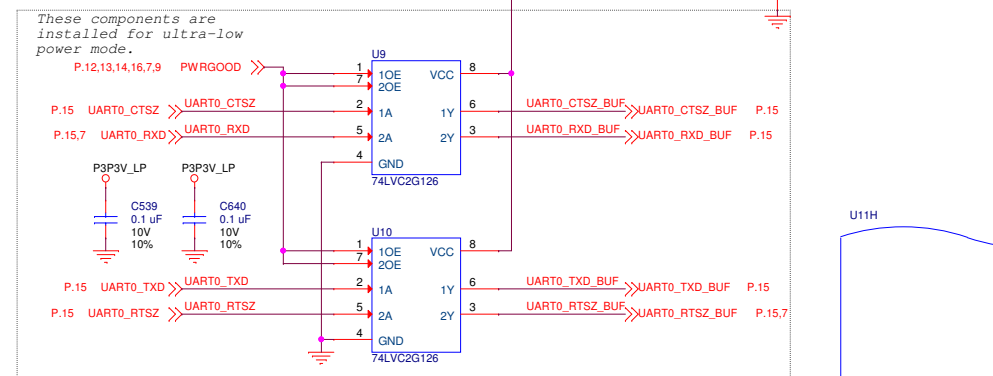
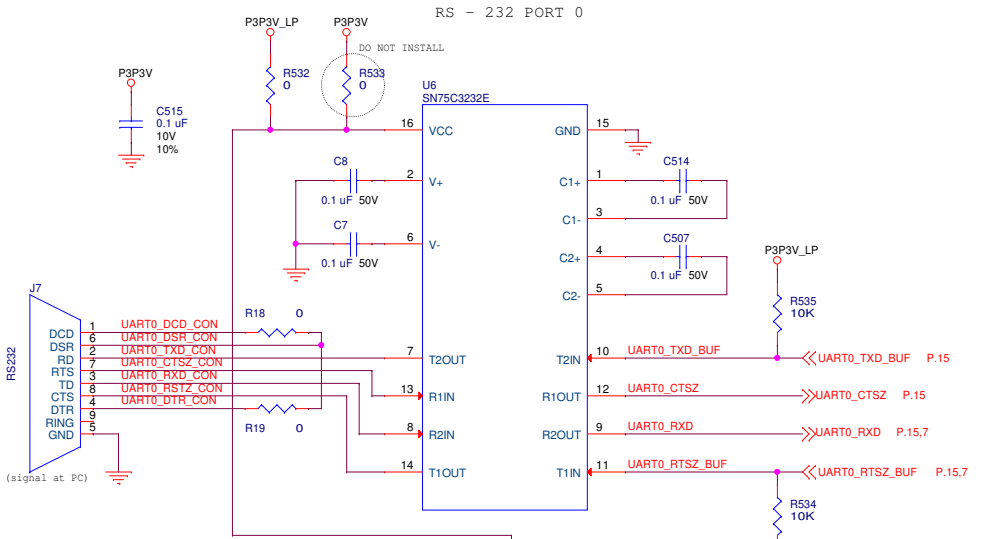


Test Points, ARM Trace, JTAG, and Reset

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	ISSUE DATE 9/5/2013	SCALE		SHEET 14 OF 22	

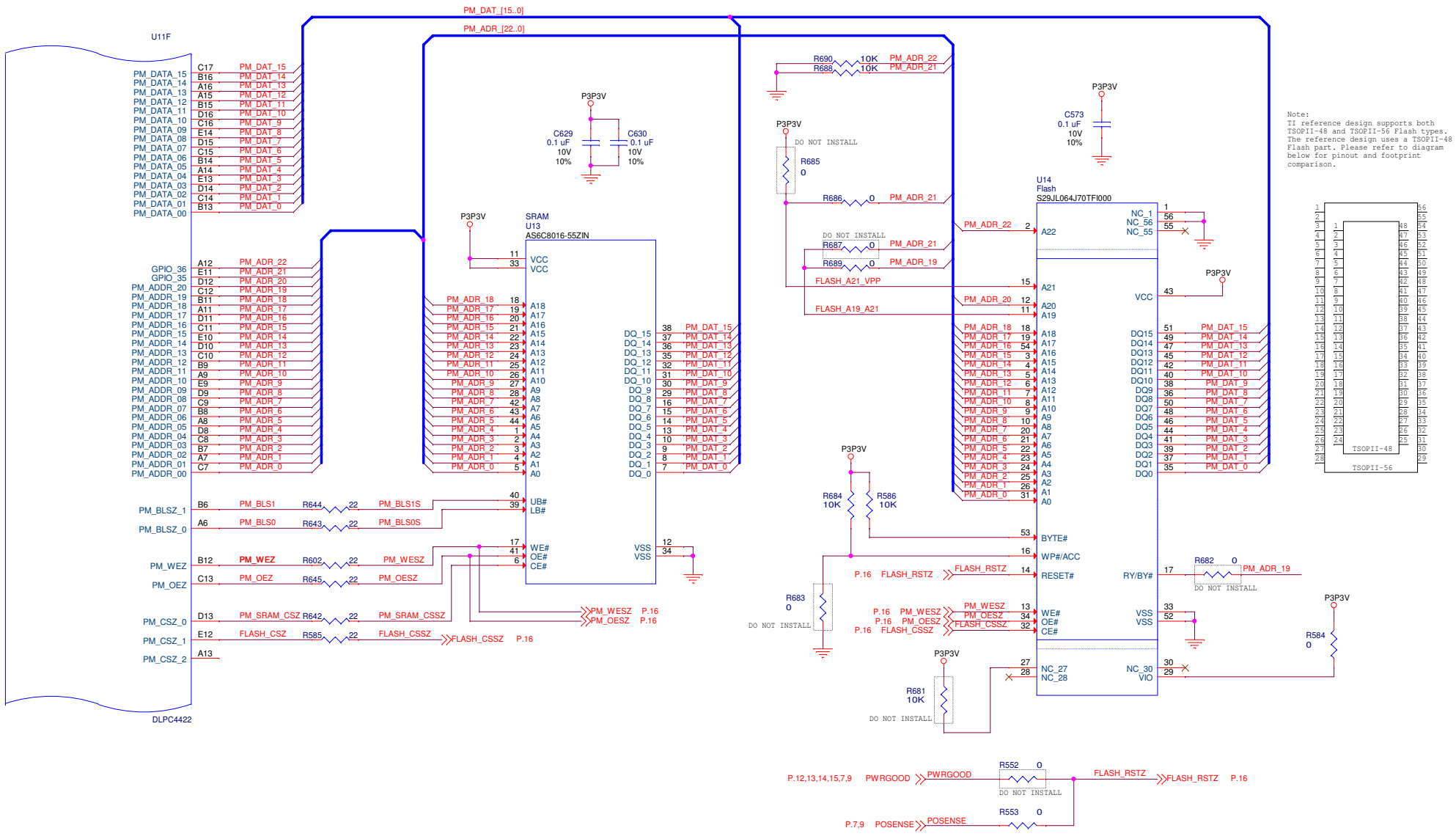


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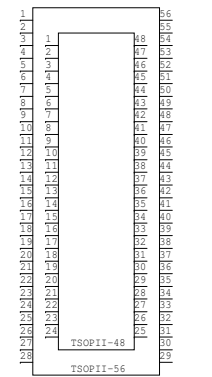
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RS-232, Synchronous Serial Port, and USB

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	ISSUE DATE 9/5/2013	SCALE			



Note:  
 TI reference design supports both  
 TSOP11-48 and TSOP11-56 Flash types.  
 The reference design uses a TSOP11-48  
 Flash part. Please refer to diagram  
 below for pinout and footprint  
 comparison.



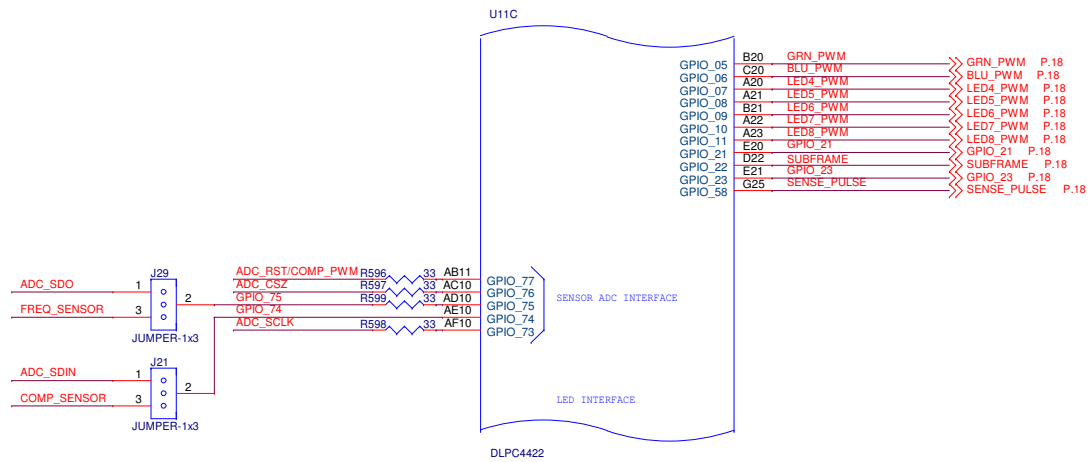
Flash and SRAM Memory Interface

TEXAS INSTRUMENTS	DWN Amy White	DATE 8/19/2013	A3	DRAWING NO 2513334	REV C
	ISSUE DATE 9/5/2013	SCALE		SHEET 16 OF 22	

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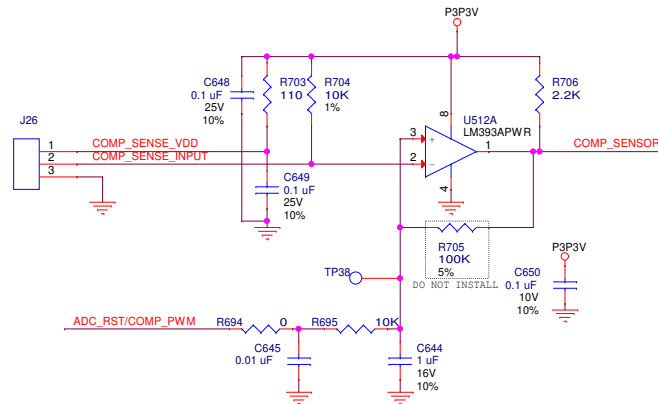
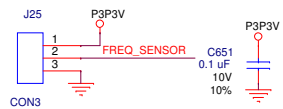
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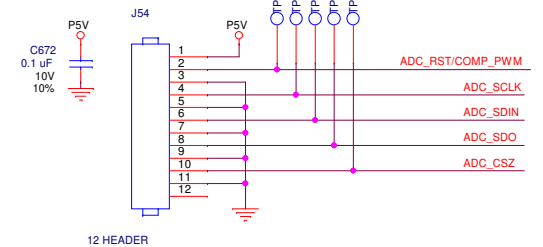


COMPARATOR SENSOR I/F

LIGHT TO FREQUENCY SENSOR I/F



ADC INTEGRATING SENSOR BOARD I/F



SSI SENSOR INTERFACE

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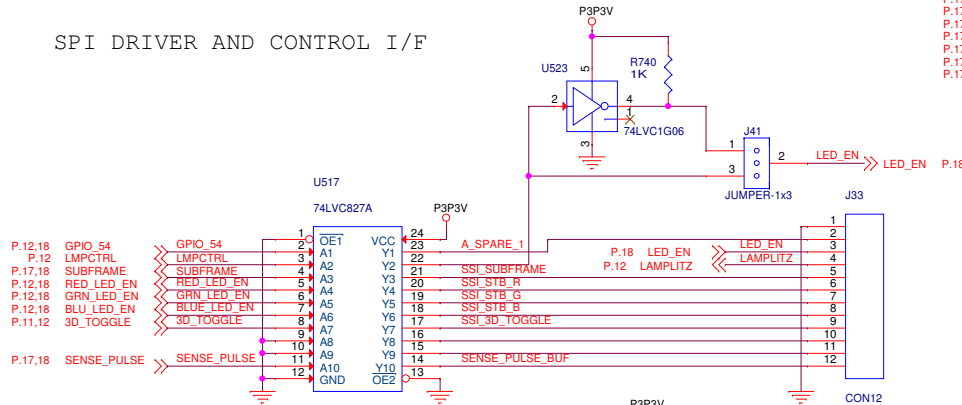
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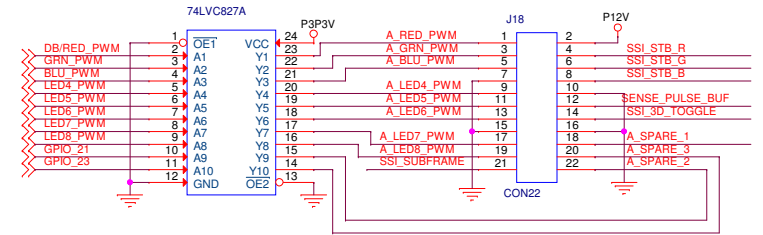
A3	DRAWING NO 2513334	REV C
SCALE	SHEET 17 OF 22	

### SPI DRIVER AND CONTROL I/F

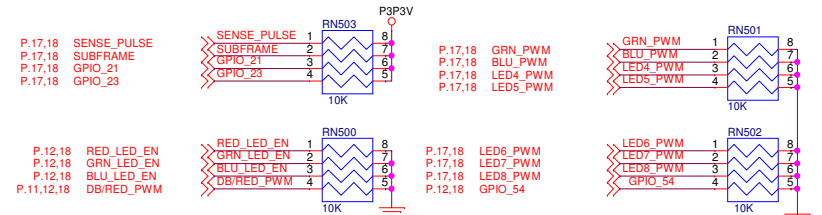


- P.11,12,18 DB/RED\_PWM
- P.17,18 GRN\_PWM
- P.17,18 BLU\_PWM
- P.17,18 LED4\_PWM
- P.17,18 LED5\_PWM
- P.17,18 LED6\_PWM
- P.17,18 LED7\_PWM
- P.17,18 LED8\_PWM
- P.17,18 GPIO\_21
- P.17,18 GPIO\_23

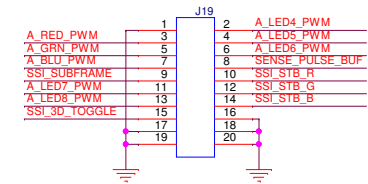
### PWM SSI DRIVER I/F



### PULL-UP/PULL-DOWN RESISTORS



### TESTPOINTS

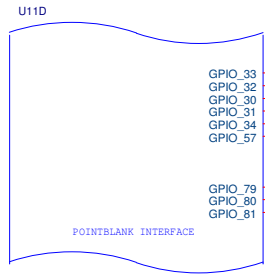


J48  
Jumper Pins 1 & 2: DLPC4422 with OSRAM Driver  
Jumper Pins 2 & 3: Default

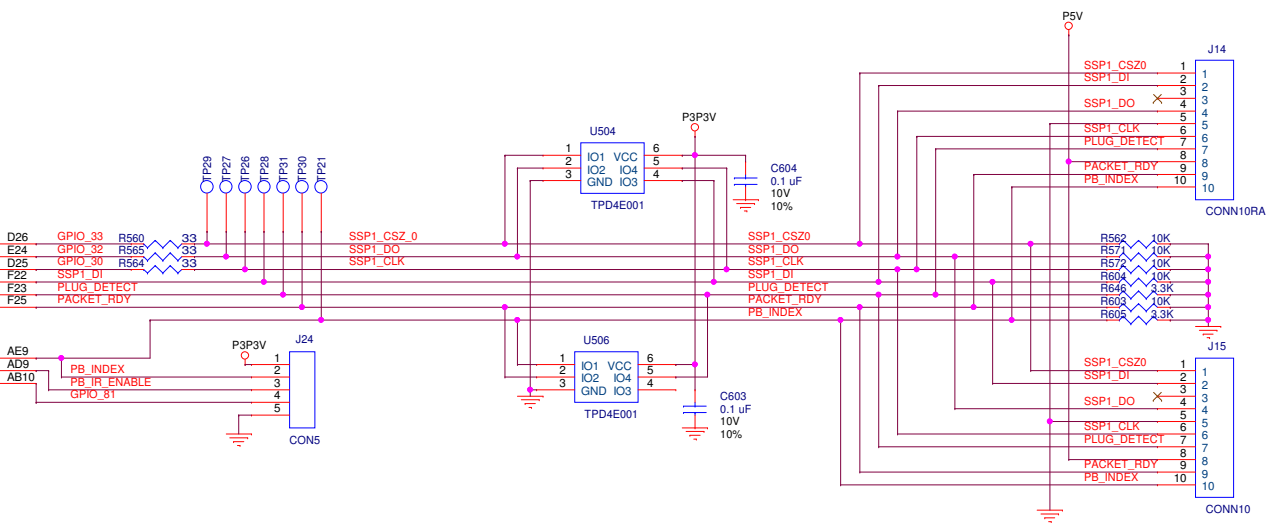
Note: U528 is needed to read from an OSRAM driver for DLPC4422

### SSI Driver Interfaces

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DLPC4422



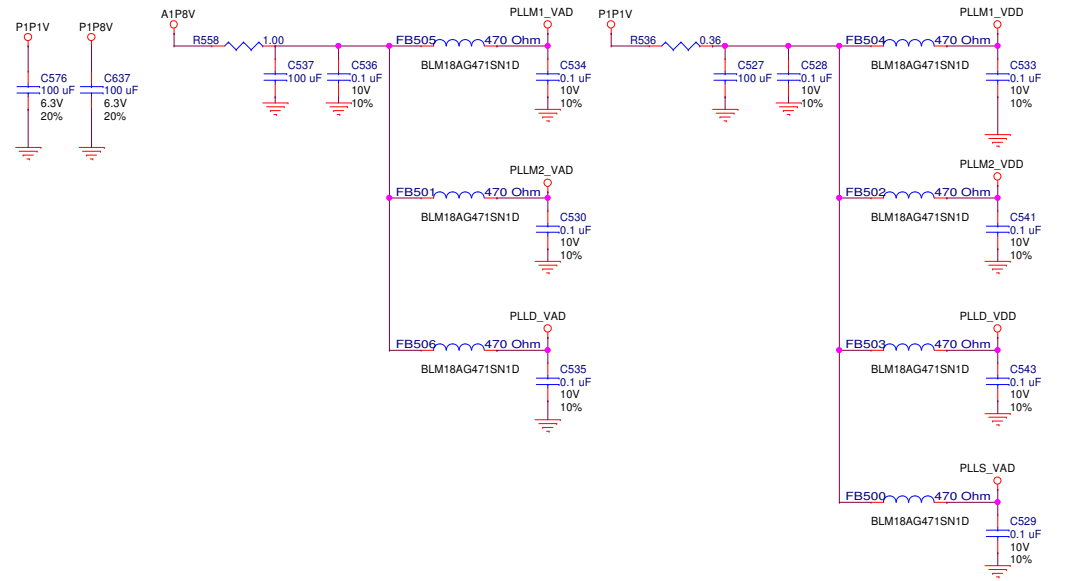
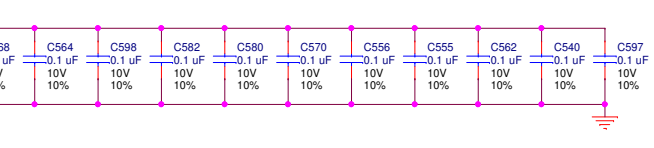
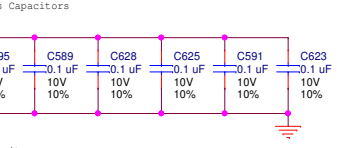
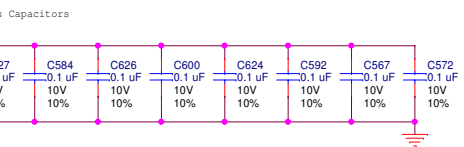
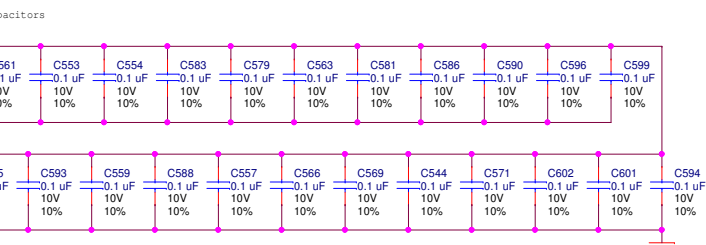
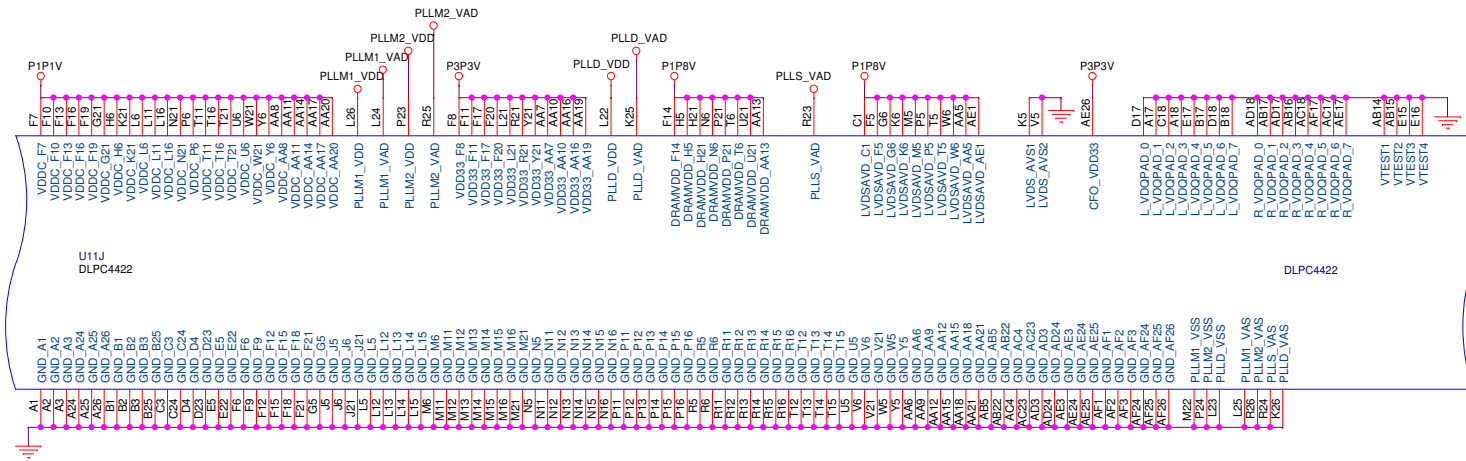
Note: Only one connector is needed for production designs (J68 or J20)

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PointBlank Interface

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DDP442x Power and Bypass Capacitors

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	ISSUE DATE 9/5/2013	SCALE		SHEET <b>20</b> OF <b>22</b>	

**Rev B**

Sheet 8: Changed R660 and R661 resistor values for new VDD Spec for DLPC4422  
Added note about R660 and R661 for DLPC4422

**Rev C**

Sheet 7: Deleted obsolete D9 and D10 dual package (Red/Green) LEDs. Added D9, D10, and D11 single package discrete Red and Green LEDs.  
Updated obsolete X500 crystal device with RoHS compliant replacement.  
Sheet 8: Updated obsolete Q2 and Q505 Devices with equivalent RoHS compliant replacement.  
Sheet 9: Updated obsolete Q1 and Q500 Devices with equivalent RoHS compliant replacement.  
Sheet 13: Deleted obsolete D11 and D12 dual package (Red/Green) LEDs. Added D12, D13, D14, and D15 single package discrete Red and Green LEDs.  
Updated U3 and U16 with equivalent RoHS compliant device.  
Sheet 16: Updated U14 with equivalent RoHS compliant device.  
Sheet 20: Updated obsolete R536 with equivalent replacement

REVISION HISTORY

<b>TEXAS INSTRUMENTS</b>	DWN Amy White	DATE 12/3/2013	<b>A3</b>	DRAWING NO <b>2513334</b>	REV <b>C</b>
	ISSUE DATE 12/3/2013	SCALE			SHEET 21 OF 22

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D

D

C

C

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B

A

A

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<b>TEXAS INSTRUMENTS</b>	DWN Amy White	DATE 8/19/2013	<b>A3</b>	DRAWING NO <b>2513334</b>	REV <b>C</b>
	ISSUE DATE 9/5/2013	SCALE		SHEET 22 OF 22	

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