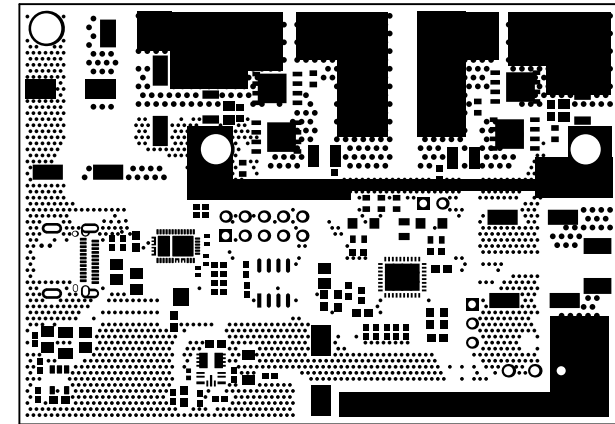
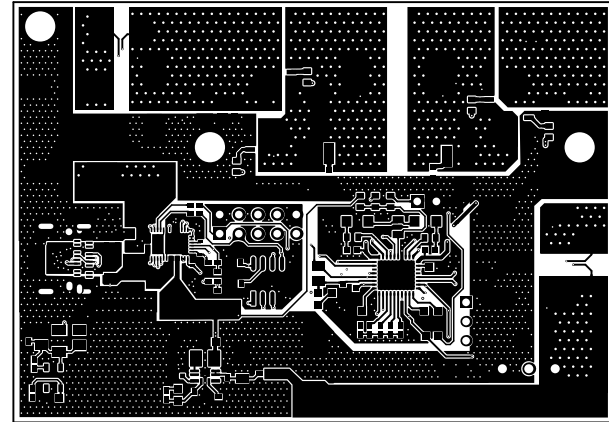


ALL DIMENSIONS UNLESS SPECIFIED OTHERWISE	DATE: 11/11/2011	REV: 01	SCALE: 1:1	DESIGNER: J. J. J.
PROJECT: PHP41062E1	PART: PHP41062E1			
DATE: 11/11/2011	DESIGNER: J. J. J.	DATE: 11/11/2011	DESIGNER: J. J. J.	DATE: 11/11/2011



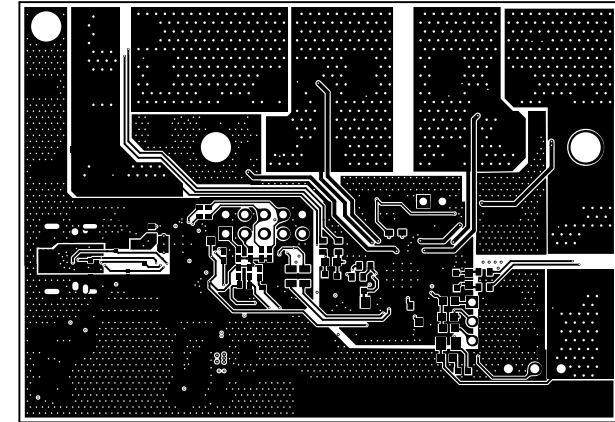
ALL RIGHTS RESERVED FOR THE BOARD	BOARD #	PROCESS	REV	DATE
LAYER 001 -	Top Layer			
LAYER 002 -	Top Layer - Pad	LAYER 003 -	LAYER 004 -	LAYER 005 -



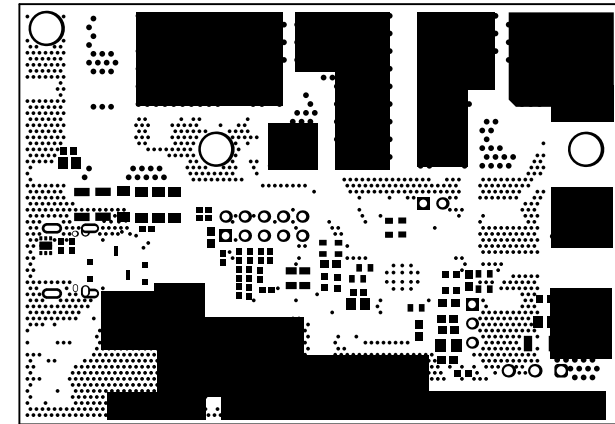
Layer	Name	Material	Thickness	Constant
	Top Overlay			
	Top Solder	Solder Resist	0.010mm	3.5
1	Top Layer		0.036mm	
	Dielectric 1	FR-4 High Tg	0.203mm	4.2
2	Signal Layer 1		0.036mm	
	Dielectric 2	FR-4 High Tg	1.016mm	4.2
3	Signal Layer 2		0.036mm	
	Dielectric 3	FR-4 High Tg	0.203mm	4.2
4	Bottom Layer		0.036mm	
	Bottom Solder	Solder Resist	0.010mm	3.5
	Bottom Overlay			

Total board thickness: 1.585mm

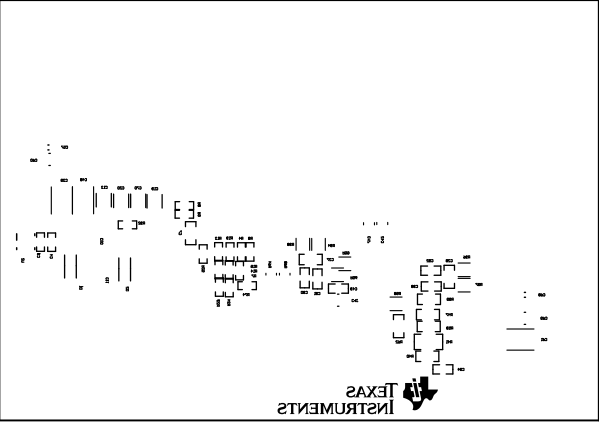
ALL DIMENSIONS UNLESS SPECIFIED OTHERWISE	DATE: 10/20/2023	REV: 02	SCALE: 1:1
DESIGNED BY: [Name]	CHECKED BY: [Name]		
APP'D BY: [Name]	DATE: 10/20/2023	DESIGNED BY: [Name]	CHECKED BY: [Name]



ALL RIGHTS RESERVED FOR THE BOARD	DATE: 01/15/2018	REV: 02	REV: 02	REV: 02	REV: 02
DATE: 01/15/2018	DATE: 01/15/2018	DATE: 01/15/2018	DATE: 01/15/2018	DATE: 01/15/2018	DATE: 01/15/2018
DATE: 01/15/2018	DATE: 01/15/2018	DATE: 01/15/2018	DATE: 01/15/2018	DATE: 01/15/2018	DATE: 01/15/2018



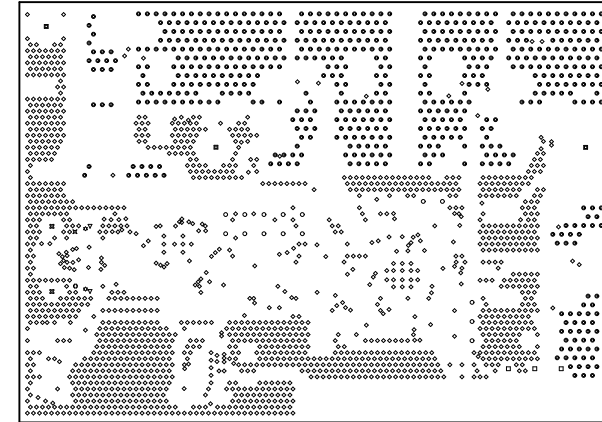
ALL RIGHTS RESERVED FOR THE BOARD	BOARD #	PROCESS	REV	DATE
LAYER #	LAYER NAME			
REF #	REF VALUE	REF DESC	REF COMMENT	



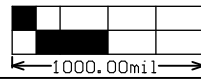
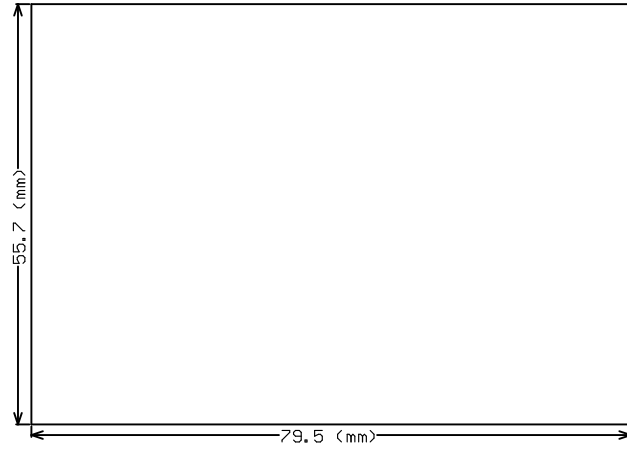
ALL RIGHTS RESERVED FOR THE STATE	STATE OF TEXAS	TEXAS INSTRUMENTS	STATE OF TEXAS	STATE OF TEXAS
STATE OF TEXAS	STATE OF TEXAS	STATE OF TEXAS	STATE OF TEXAS	STATE OF TEXAS
STATE OF TEXAS	STATE OF TEXAS	STATE OF TEXAS	STATE OF TEXAS	STATE OF TEXAS

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
◇	1548	0.200mm (7.87mil)	PTH	Round	Top Layer - Bottom Layer	
●	627	0.330mm (13.00mil)	PTH	Round	Top Layer - Bottom Layer	
⊗	1	0.500mm (19.69mil)	NPTH	Slot	Top Layer - Bottom Layer	+/-0.030mm
▽	1	0.600mm (23.62mil)	NPTH	Round	Top Layer - Bottom Layer	+/-0.030mm
∇	2	0.600mm (23.62mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
☆	2	0.700mm (27.56mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
⊗	2	0.700mm (27.56mil)	PTH	Slot	Top Layer - Bottom Layer	+/-0.050mm
○	15	1.016mm (40.00mil)	PTH	Round	Top Layer - Bottom Layer	
□	3	1.200mm (47.24mil)	PTH	Round	Top Layer - Bottom Layer	
■	3	4.000mm (157.48mil)	PTH	Round	Top Layer - Bottom Layer	
2204 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size + Slot length as defined in the PCB layout.



ALL DIMENSIONS UNLESS SPECIFIED OTHERWISE	DATE: 11/11/2011	DESIGNER: J. J. J.	DATE: 11/11/2011	DESIGNER: J. J. J.
UNIT: MM	SCALE: 1:1			
REF: 001	001	002	003	004



ALL RIGHTS RESERVED FOR THE USER	DATE	TIME	FILE	EXT	FILE	EXT	FILE	EXT
USER NAME	IP ADDRESS							
APP NAME	SERVER	IP	PORT	PROXY	USER	GROUP	ROLE	STATUS

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2024, Texas Instruments Incorporated