

SK-AM64B Design Package Folder and Files List



Table 1 lists names of the folders and file names in the folders along with the format for all the files that have been included in the SK-AM64B. The AM64B starter kit (SK) is a low-cost stand-alone test and development platform based on the Sitara™ AM6442 processor that is ideal for accelerating the prototype phase of your next design. The starter kit includes wired (Ethernet) and wireless (2.4GHz and 5GHz) connectivity, three expansion headers, multiple boot options and flexible debug. The product overview document is available on SK-AM64B product folder on TI.com for customers to review before downloading the single Zip folder.

Table 1. PROC100A

FOLDER (1st level)	FOLDER (2nd Level)	Files Inside	File Type
----	----	PROC100A_Folders_Files_List	XLS
		2024 Important Notice	PDF
1_SCHEMATIC	PDF	PROC100A_SCH_With_Design_Updates..Notes_V1.0	PDF
	PDF -Backup_SK_Schematic	PROC100A(004)_SCH	PDF
	----	PROC100A_Schematic_Revision_Readme	DOC
	ORCAD	PROC100A_SCH_With_Design_Updates..Notes_V1.0	DSN
	ORCAD - Backup_SK_Schematic	PROC100A_SCH	DSN
2_BOM	----	PROC100A_BOM_With_Design_Updates..Notes_V1.0	XLS
	Backup_SK_Schematic_BOM	PROC100A(004)_BOM	XLS
3_Board_File	Allegro	PROC100A_BRD	BRD
	Simulation Scorecard	AM64x_Simulations_Scorecard	PDF
	Altium_ASCII	PROC100A_BRD	ALG
4_Gerber	ODBGBR	PROC100A_ODBGBR	ZIP
	274X	PROC100A_RS274GBR	ZIP
	IPC-D-356_NETLIST	PROC100A_BRD	IPC
5_Gerber_PDF	FAB	PROC100A_FAB	PDF
	PCB LAYERS	PROC100A_ALL_LAYERS	PDF
	Gerber Layers	PROC100A_ALL_LAYERS	PDF
6_Assembly_Models_Packag e	2D	PROC100A_DXF_BASY	DXF
		PROC100A_DXF_TASY	DXF
	3D	PROC100A_3D.STEP	STP
		----	----
	IDF	PROC100A_BRD	EMP
		PROC100A_BRD	EMN
	Assembly_Drawing	PROC100A_ASSY	PDF
		PROC100A_TASY	PDF
		PROC100A_BASY	PDF
	STNL	art_aper + 8 x .ART files	ART
XY-REP	PROC100A_XYREP	XLS	
7_PCB_LAYER_STACKUP	---	SK-AM64_PROC100_Stack_up	PDF
8_Power_Supply_Sequencin g	---	SK-AM64B Power Sequencing_RevA	

References

- Texas Instruments, [\[FAQ\] AM6442 / AM6441 / AM6422 / AM6421 / AM6412 / AM6411 Custom board hardware design - Design and review notes for Reuse of SK-AM64B Schematics](#) article

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2024, Texas Instruments Incorporated