

# Complimentary Analog Products for the TMS320DM365 Digital Media Processor

	Best Performance	Best Value	Low Power
<b>Video Amplifier</b>	<b>THS7315</b> ❖SOIC • 3 SDTV Video Amplifiers for CVBS, S-Video, Y'U'V etc... • 5.2V/V Gain (14.3dB)	<b>OPA361</b> ❖SC70 • 3V Video Amp with Internal Gain and Filter • 2-Pole Reconstruction Filter • Integrated Level Shifter • Input Range Includes Ground – DC-Coupled Input	Rail-to-Rail Output Low Quiescent Current: 5.3 mA Shutdown Current: 1.5uA
<b>Class-D Amplifier</b>	<b>TPA2013D1</b> ❖DSBGA ❖QFN • Constant Output Power • 1.8-V to 5.5-V Operation • 2.2-W into an 8-Ω Load from a 3.6-V Supply	<b>TPA2010D1</b> ❖DSBGA • 2.5-W MONO Filter Free Class-D • Efficiency: 88% at 400mW, 80% at 100mW • Improved CMRR, PSRR	<b>TPA2006D1</b> ❖SON • 1.45-W MONO Class-D • 2.8-mA Quiescent Current • 0.5-uA Shutdown Current
<b>Low Power Voltage Ref</b>	<b>REF50xx</b> ❖SOIC ❖MSOP • Low Temperature Drift (3ppm/°C (max)) • High Accuracy: .05% max • Low Noise (3uVPP/V)	<b>REF33xx</b> ❖SOIC ❖MSOP • Low Supply Current: 3.9uA (typ) • Low Temperature Drift: 30ppm/°C (max) • High Initial Accuracy: ±0.15% (max)	High Output Current: ±5mA
<b>Audio Codec's Low-Power Stereo</b>	<b>AIC3107</b> ❖QFN • Stereo CODEC with Integrated MONO Class-D Amp • Audio ADC + Audio DAC • Seven Audio Input Pins	<b>AIC3104</b> ❖QFN • Stereo Audio DAC+ADC • Six Audio Output Drivers • Automatic Gain Control • 14mW Stereo 48-kHz PB	<b>AIC3254</b> ❖QFN • 4.1 mW Stereo 48ksps DAC Playback • 6.1 mW Stereo 48 ksps ADC Record • Low Power Bypass
<b>Video DAC (Decoder)</b>	<b>TVP5150</b> ❖TQFP • Ultralow-Power NTSC/PAL Video Decoder • Two Composite Inputs or One S-Video Input • VBI Modes Supported Include: Teletext, Wide Screen Signaling, etc...		Macrovision Copy Protection Detection Ultralow Power Consumption: 113 mw (typical) Power-Down Mode: <1 mW
<b>Low Power Touch Screen Controller</b>	<b>TSC2008</b> ❖QFN ❖DSBGA • 1.2V to 3.6V, 12-Bit, Nanopower, 4-Wire Micro TOUCH SCREEN CONTROLLER with SPI™ • Effective Throughput Rate: Up to 20kHz (8-Bit) or 10kHz (12-Bit) • Low Power (12-Bit, 8.2kHz Eq Rate): 30.4mA at 1.2V, fSCLK = 5MHz	1.5 x 2 WCSP-12 and 4 x 4 QFN-16 Packages 44.6mA at 2.7V, fSCLK = 10MHz	
<b>Low Power Digital Temp Sensor</b>	<b>TMP102</b> ❖SOT • Low Quiescent Current – 10 uA (MAX) • 10 uA (MAX) shutdown current • Accuracy: 0.5°C (-25°C to +85°C)		12-bit Resolution Supply Range: 1.4V to 3.6V

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Clocks Programmable PLL Synthesizer	<b>CDCE913</b> ♦TSSOP	<ul style="list-style-type: none"> <li>• Programmable 1-PLL VCXO Clock Synthesizer With 1.8-V and 3.3-V Outputs</li> <li>• In-System Programmability and EEPROM (Serial Programmable Volatile Register and Nonvolatile EEPROM)</li> <li>• Flexible Input Clocking Concept (External Crystal: 8 MHz to 32 MHz)</li> <li>• Low-Noise PLL Core (PLL Loop Filter Components Integrated and Low Period Jitter (typical 50 ps))</li> </ul>	
			Flexible Clock Separate Output Supply
Low Power Wireless 2.4 GHz RF Transceiver	<b>CC2525</b> ♦QFN	<ul style="list-style-type: none"> <li>• Wide Supply Range (2.0V – 3.8V)</li> <li>• Low Current Consumption (27 mA in RX, 31 mA in TX @ 0 dBm)</li> <li>• -87 dBm sensitivity (at 2 Mbps)</li> </ul>	<b>CC2500</b> ♦QFN
			<ul style="list-style-type: none"> <li>• Low Current Consumption (13.3 mA in RX, 250 kBaud)</li> <li>• Programmable data rate from 1.2 to 500 kBaud</li> </ul>
Analog Front End	<b>VSP01M01</b> ♦SOIC ♦MSOP	<b>VSP2582</b> ♦QFN	
		<ul style="list-style-type: none"> <li>• CCD Signal Processing</li> <li>• Programmable Gain Amp (PGA)</li> <li>• 85 mW at 3.0 V and 36 MHz, 1mW in Standby Mode</li> </ul>	
24/16/8-bit I/O Expander	<b>TCA6424</b> ♦QFN	<b>TCA6416</b> ♦TSSOP ♦QFN ♦BGA	<b>TCA6408</b> ♦TSSOP ♦QFN ♦BGA
		<ul style="list-style-type: none"> <li>• 10-bit Resolution</li> <li>• SNR 8 5dB @ 18 dB Gain</li> <li>• 2 channel 8-bit DAC</li> </ul>	<ul style="list-style-type: none"> <li>• 16-bit I/O Expander</li> <li>• Internal Power-On Reset</li> <li>• Noise Filter on SCL/SDA Inputs</li> </ul>
Integrated 1394a Phy and Link	<b>TSB43EA42</b> ♦BGA	<ul style="list-style-type: none"> <li>• Integrated 400/200/100 Mbps 2-port/3-port PHY</li> <li>• Supports bus manager functions and automatic 1394 self-ID verification</li> <li>• DTCP encryption support on IEEE 1394 bus</li> <li>• Support for up to two encrypted/decrypted streams at one time</li> <li>• Two configurable high-speed data ports for video data, one port serial, one serial or parallel</li> </ul>	
			2 x 4K-byte isochronous buffers for video data SRAM-like 16-bit asynchronous interface
ESD Protection ±15-kV Array	<b>TPD4E001</b> ♦SOT	<b>TPD2E001</b> ♦DRY ♦DRL ♦QFN	
		<ul style="list-style-type: none"> <li>• 4-Channel ESD Protection</li> <li>• Low 1.5-pF Input Capacitance</li> </ul>	<ul style="list-style-type: none"> <li>• 2-Channel ESD Protection</li> <li>• Low 1-nA (MAX) Leakage Current</li> <li>• 0.9-V to 5.5-V Supply-Voltage Range</li> <li>• DRY, DRL and QFN PKG</li> </ul>

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