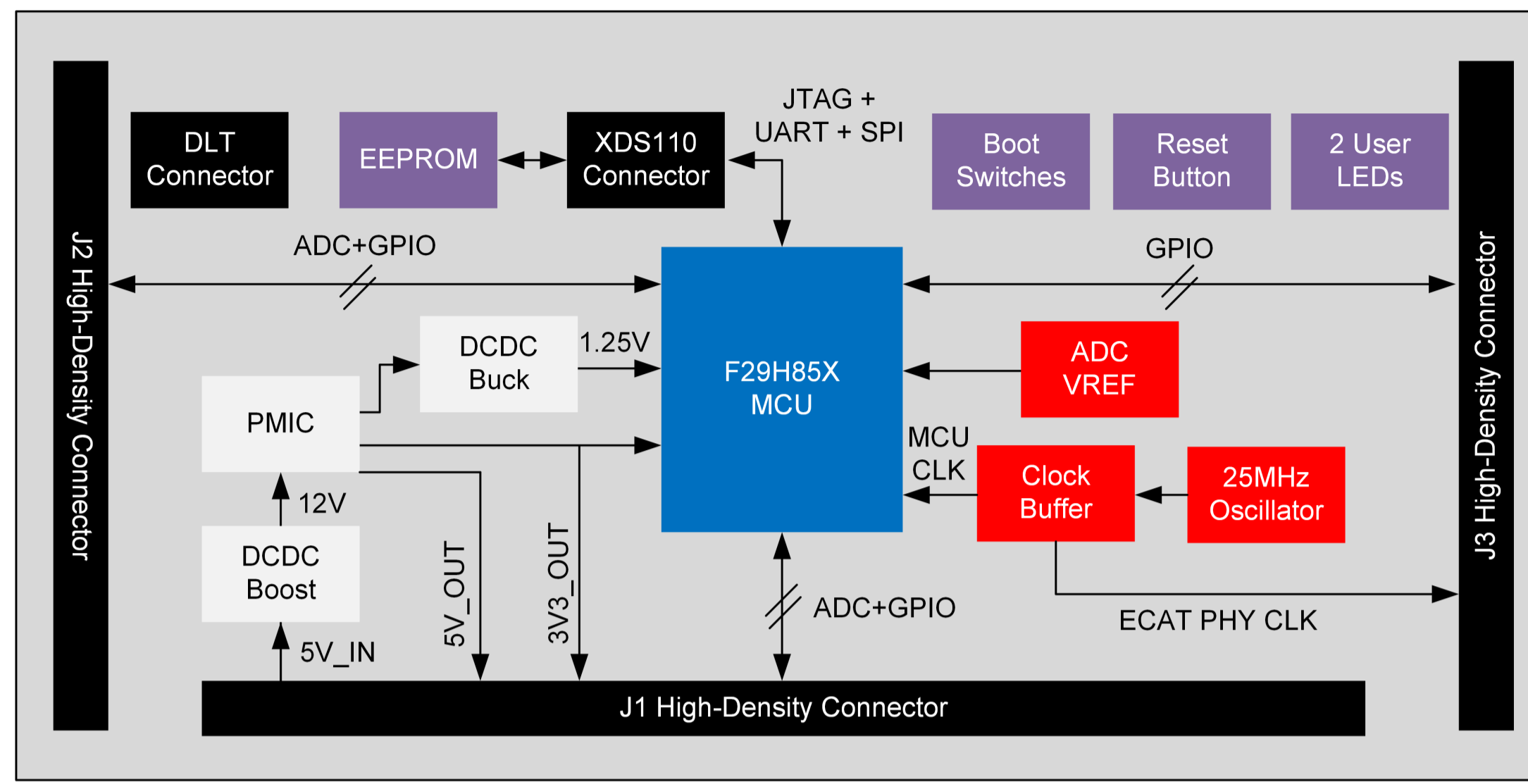
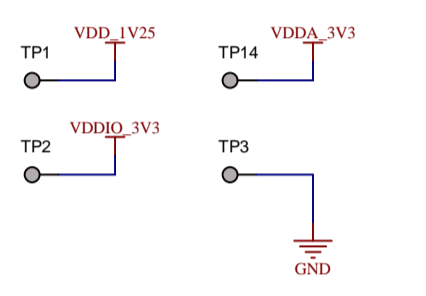
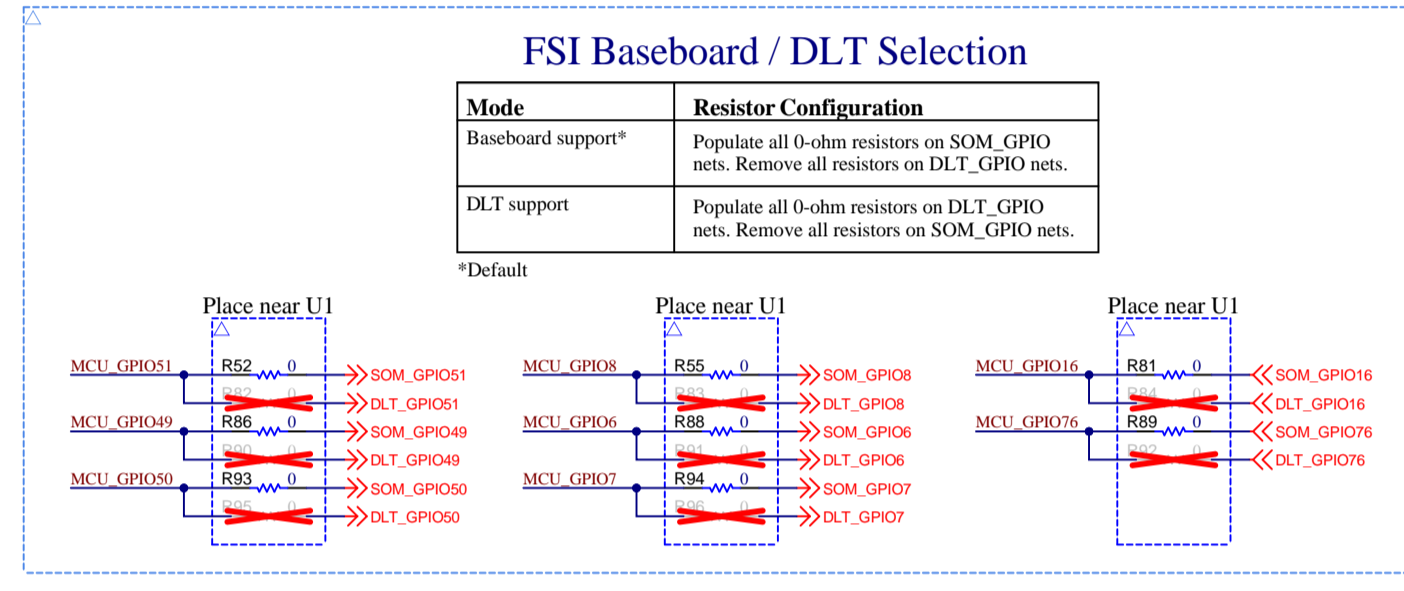
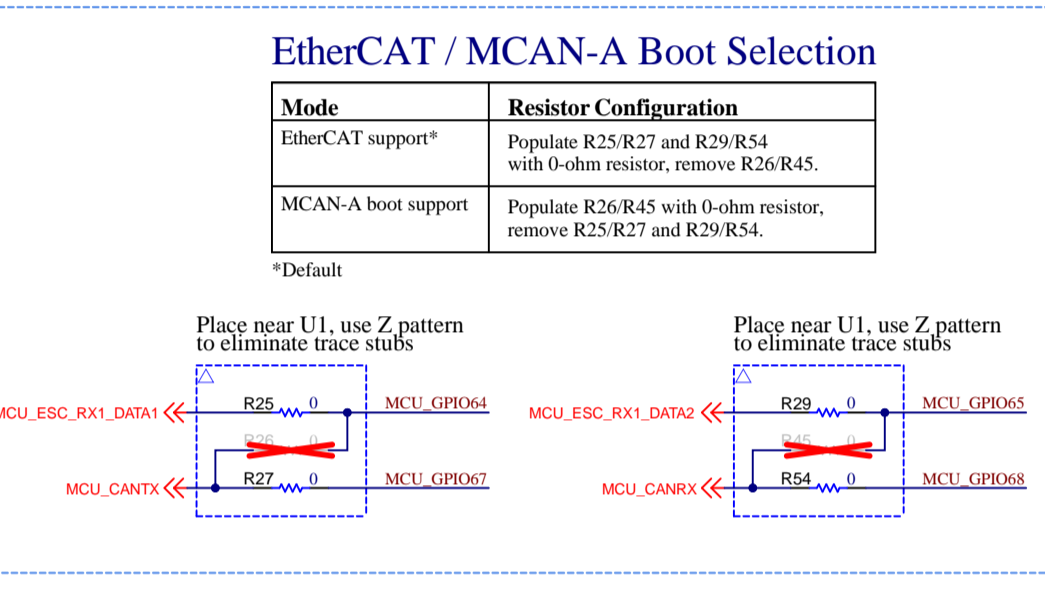
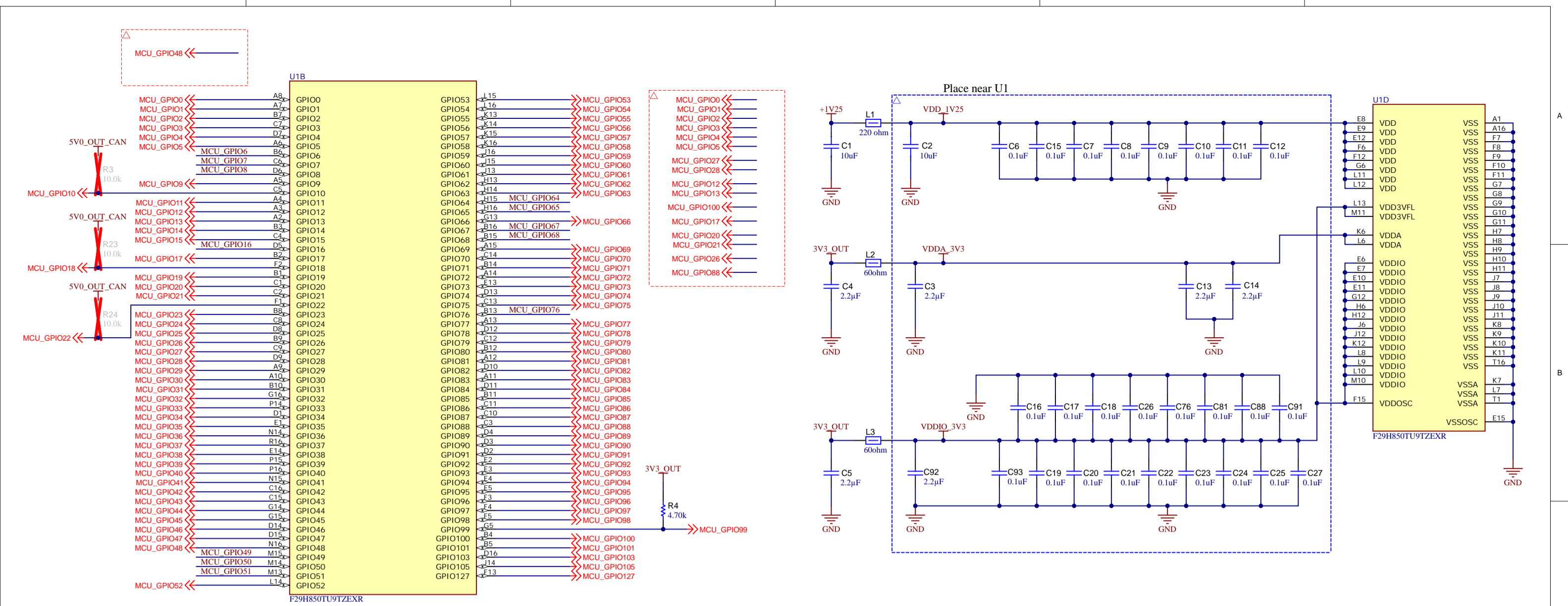


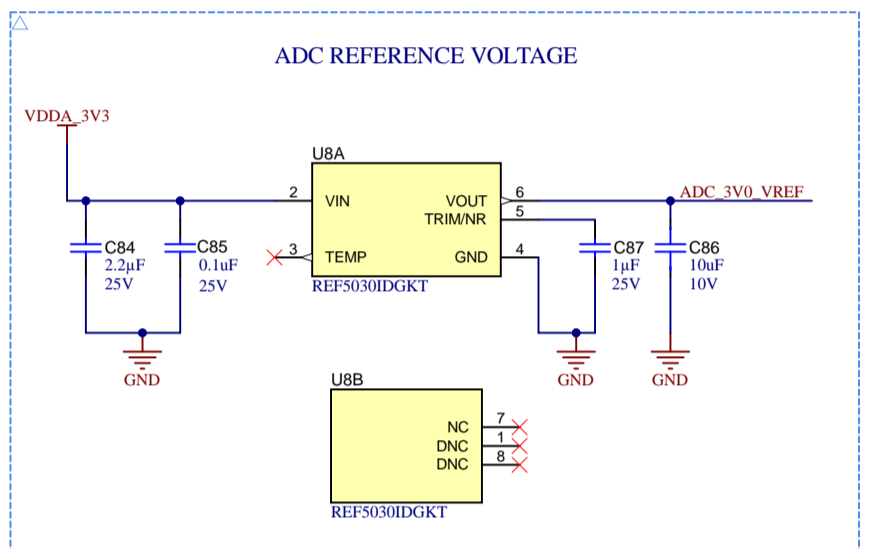
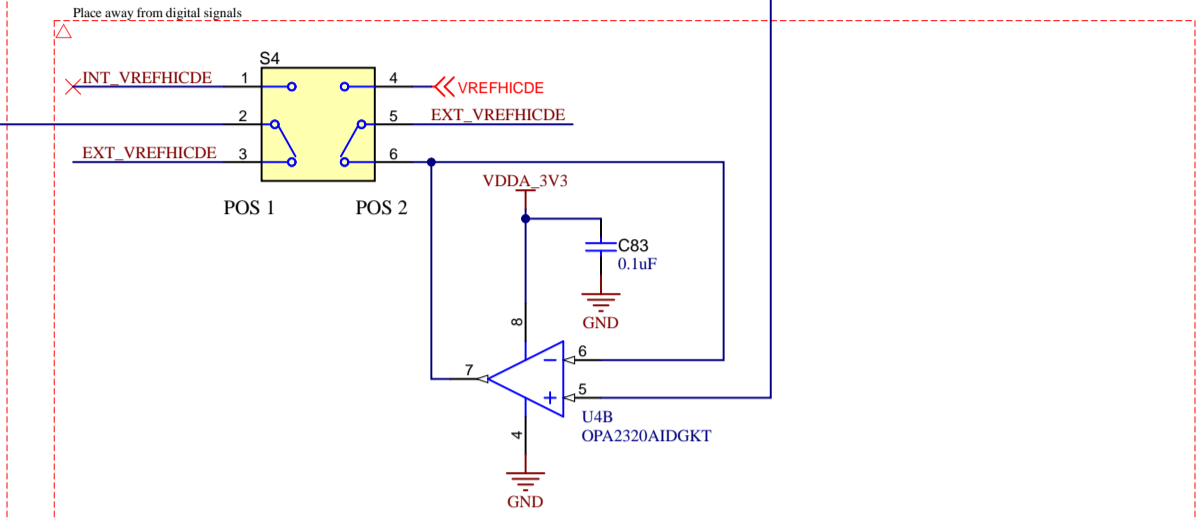
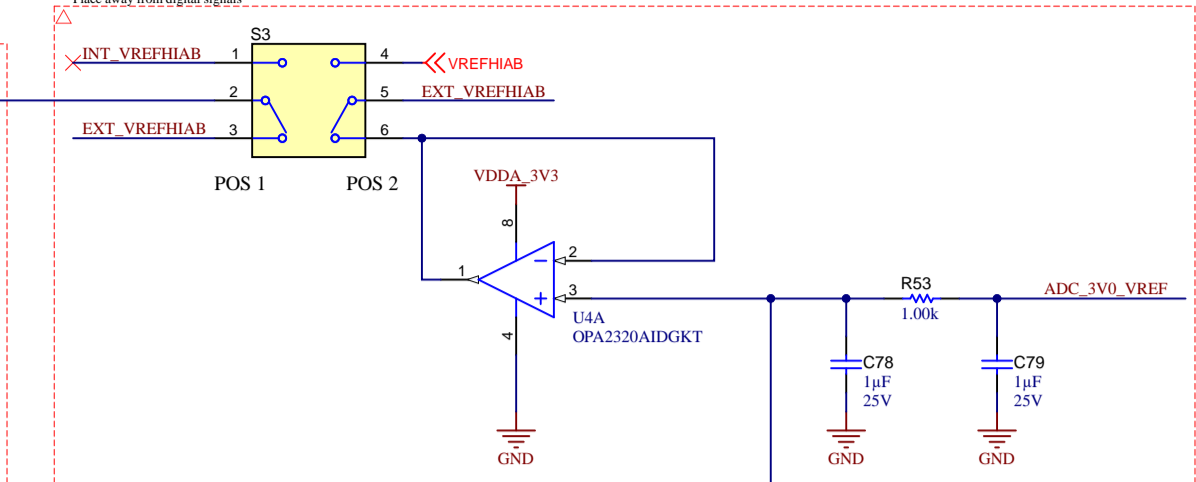
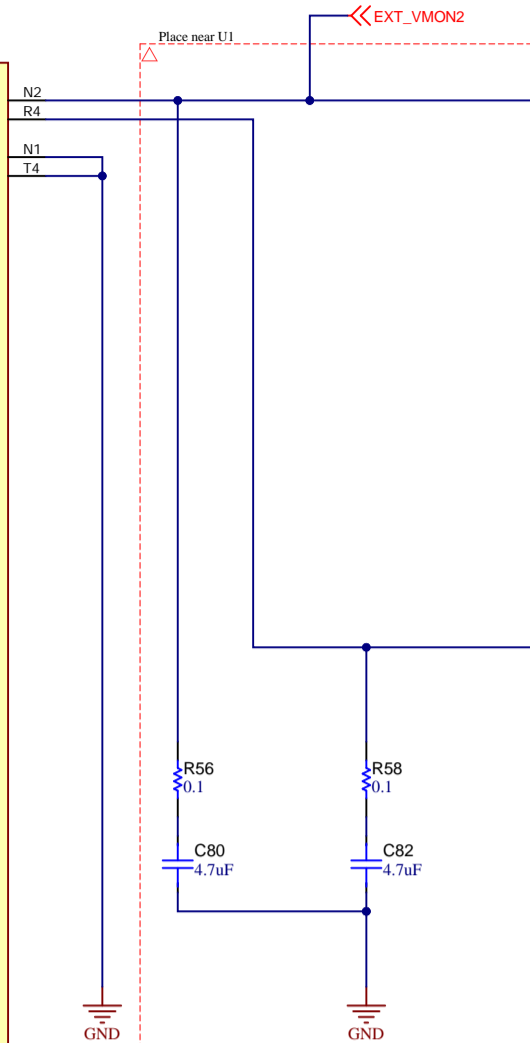
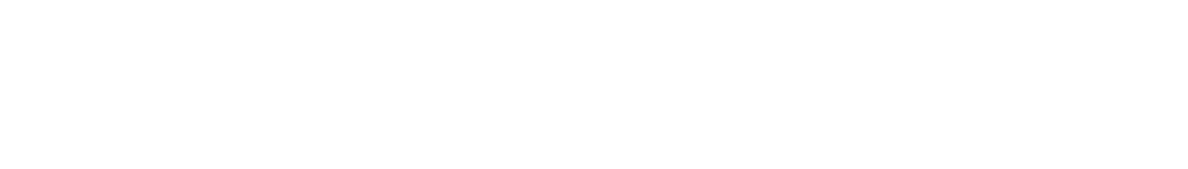
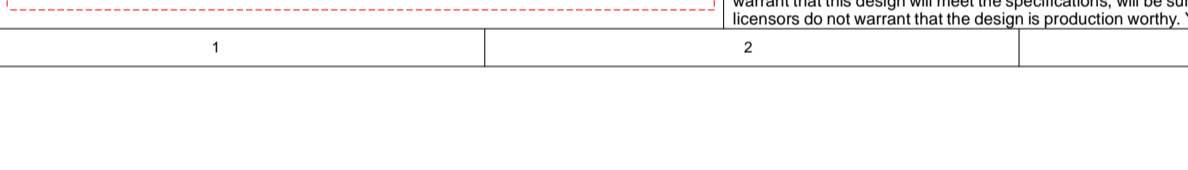
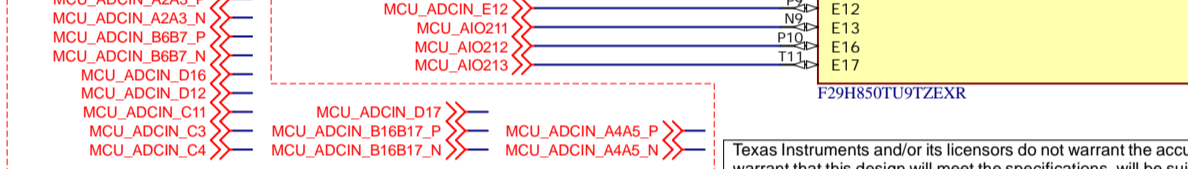
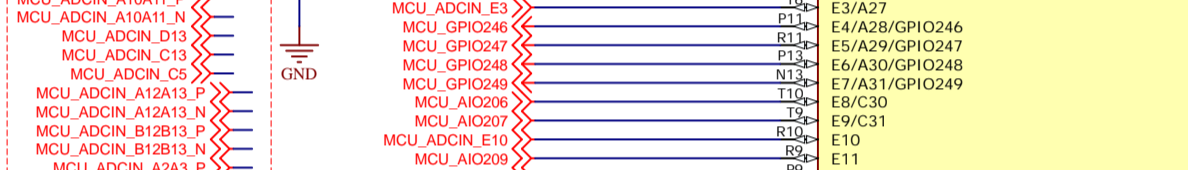
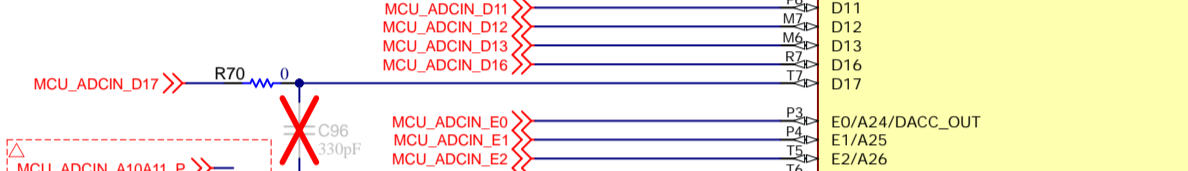
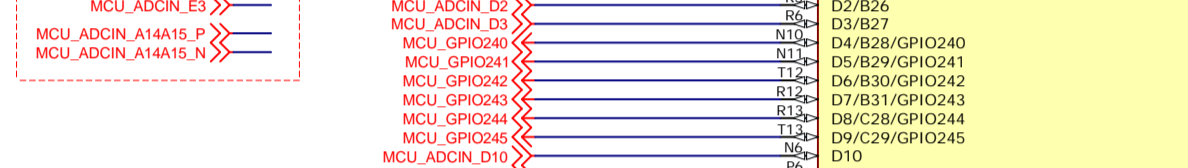
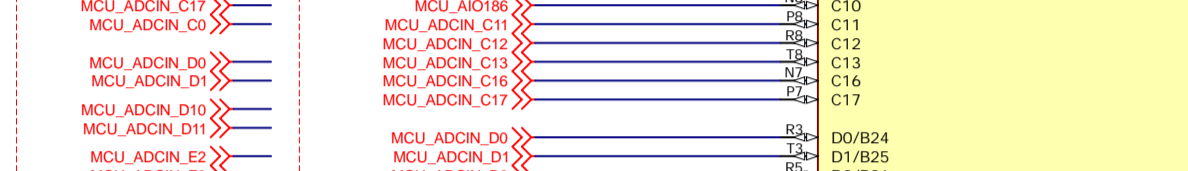
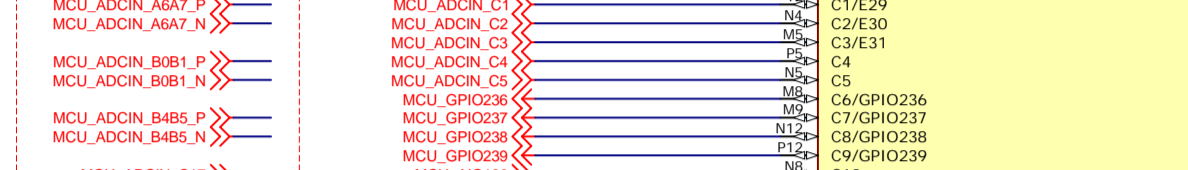
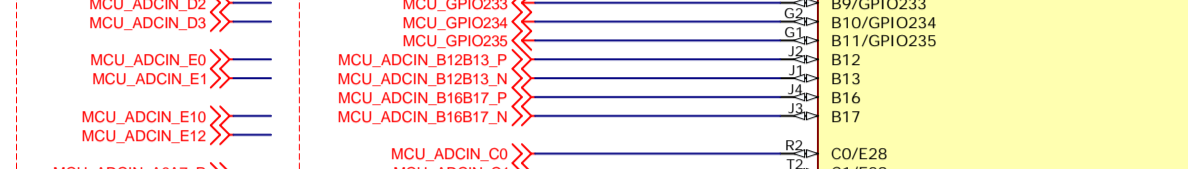
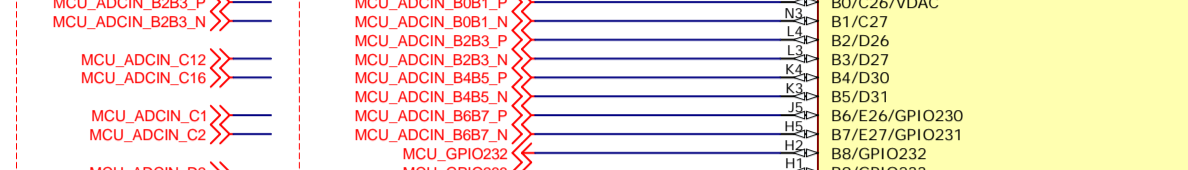
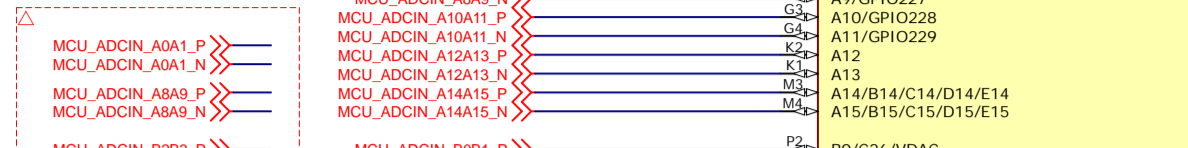
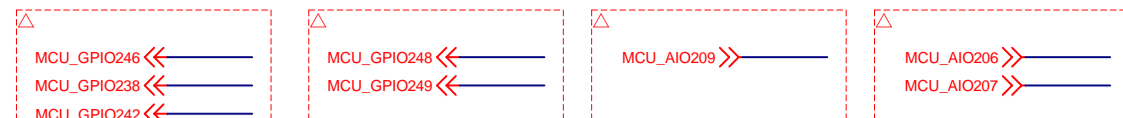
Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
E1	N/A	N/A	GM	Original engineering release.
A	N/A	Oct. 17, 2024	GM	- Changed default boot mode to 11b - Changed C97 part number - Changed internal VREF connection on S3S4 - Changed FBI connections on J5 - Removed tracking feature from U7 - Added soft start capacitor to U7 - Changed filtering scheme for ADC external reference



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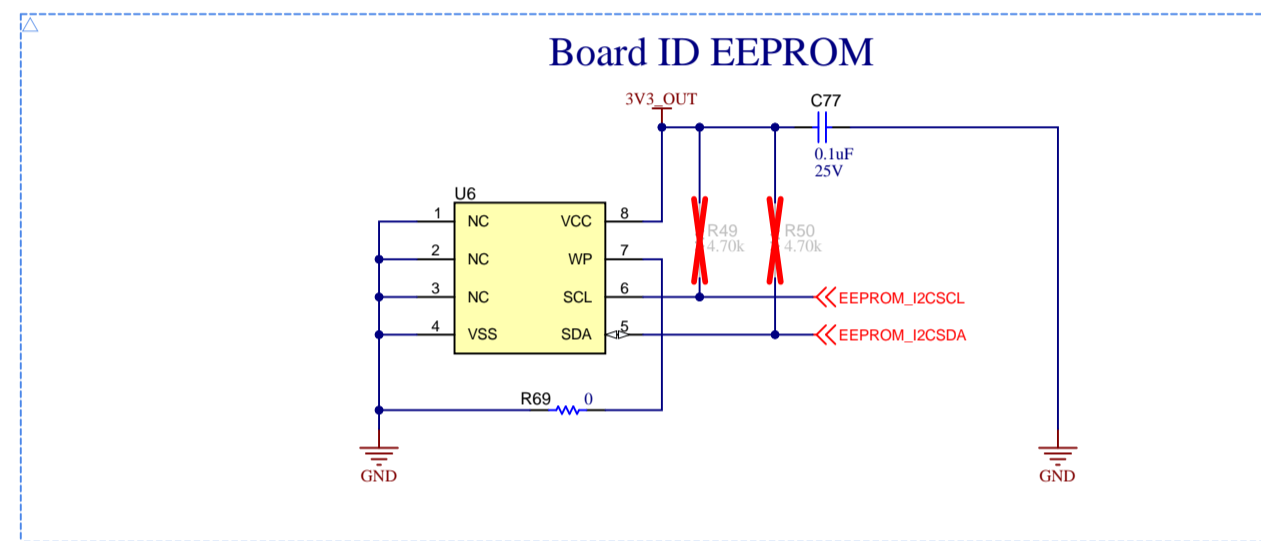
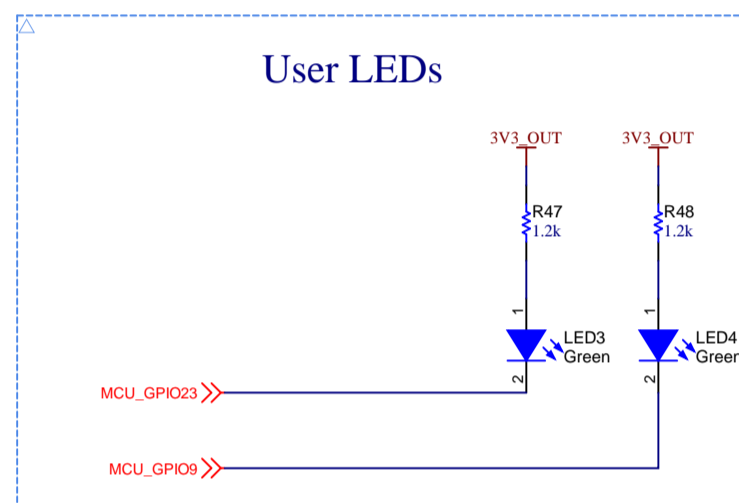
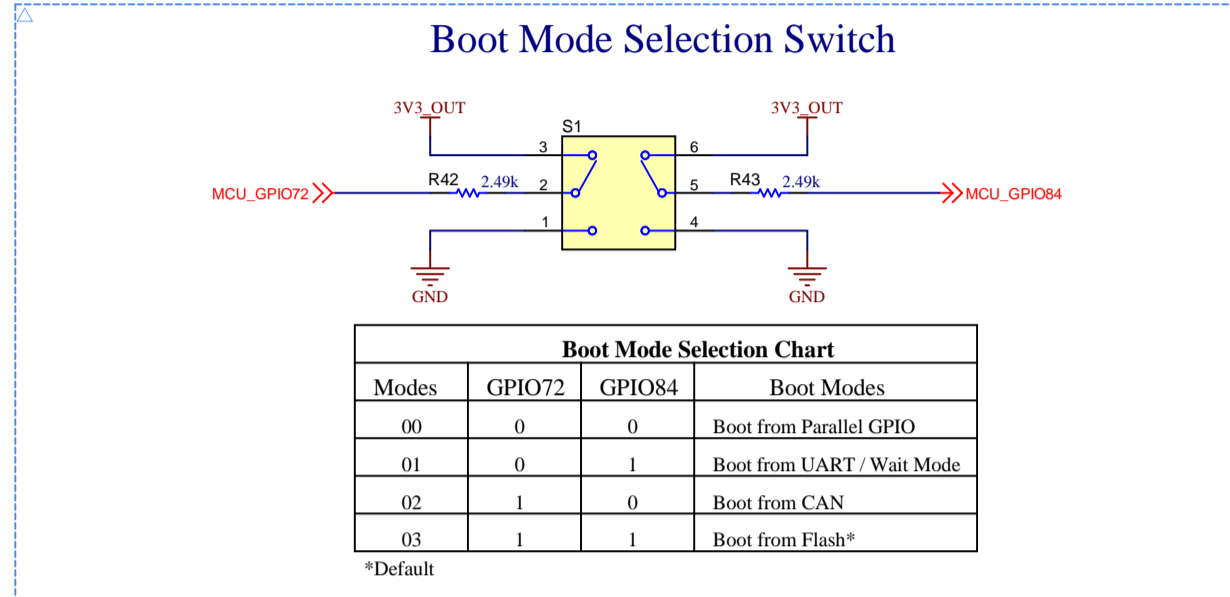
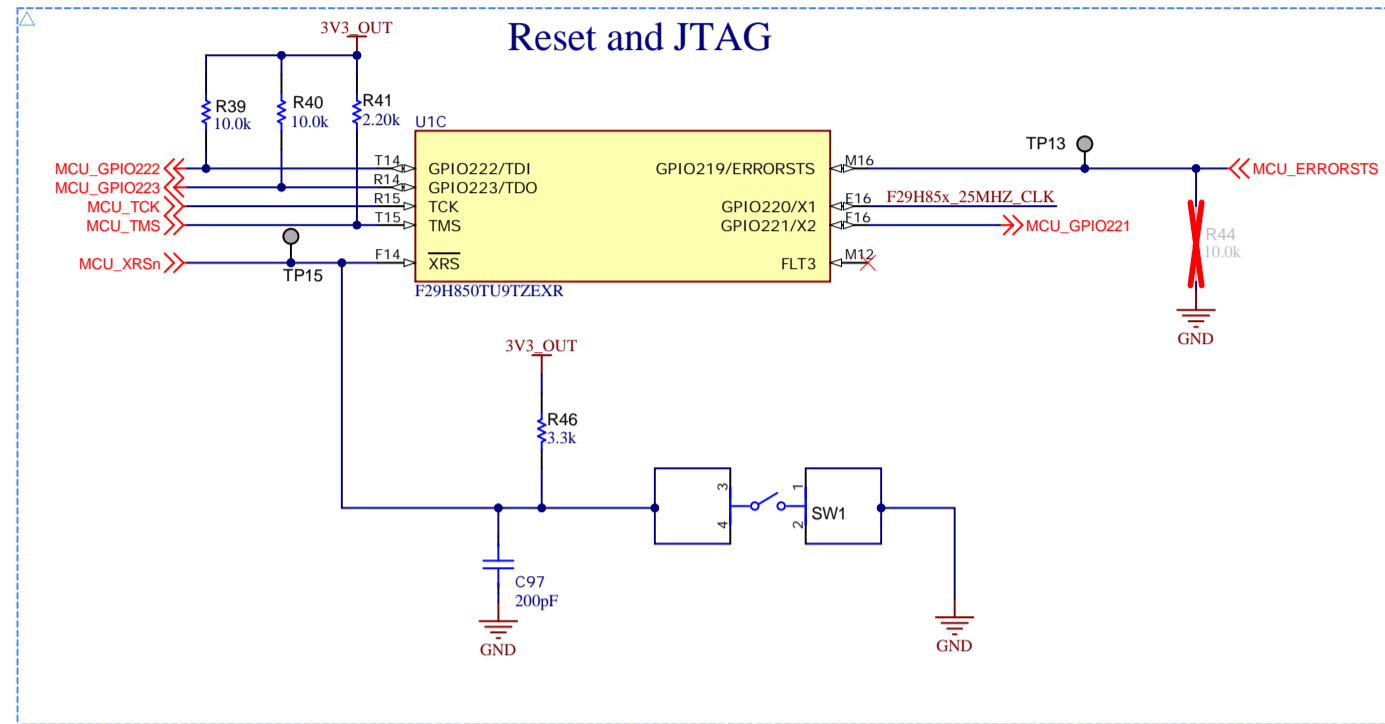
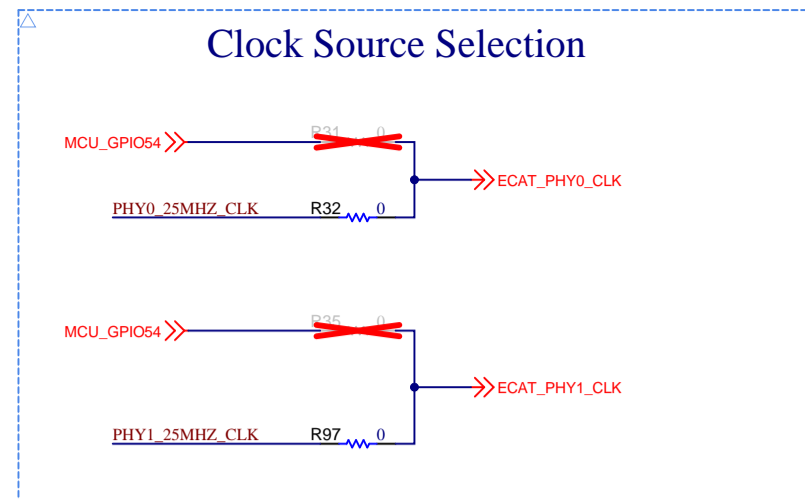
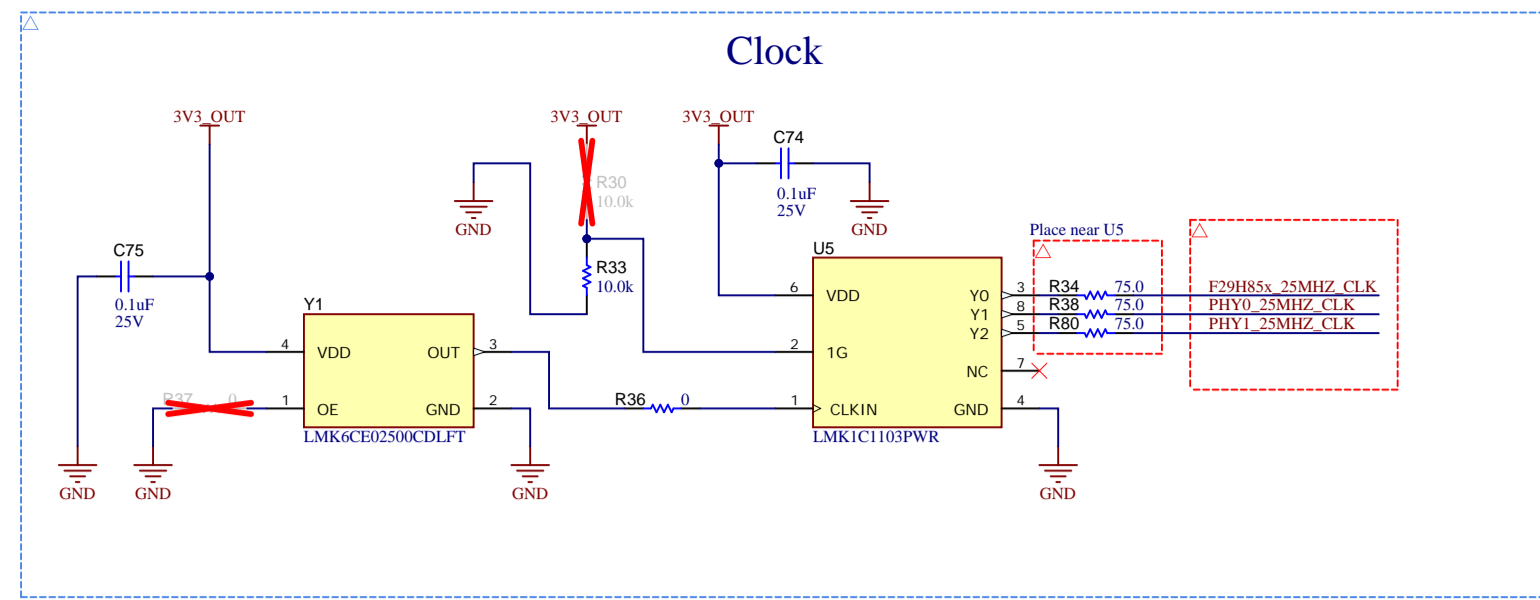
Orderable: F29H85X-SOM-EVM	Designed for: Public Release	Mod. Date: 10/23/2024	
TI Part: N/A	Project Title: F29H85X control SOM EVM		
Number: MCU144	Rev: A	Sheet Title: Block Diagram	
SW Rev: Version control disabled	Assembly Variant: 001	Sheet 1 of 8	
Drawn By: Gustavo Martinez	File: MCU144A_Coversheet_SchDoc	Size: B	http://www.ti.com
Engineer: Gustavo Martinez	Contact: http://www.ti.com/support		© Texas Instruments 2024



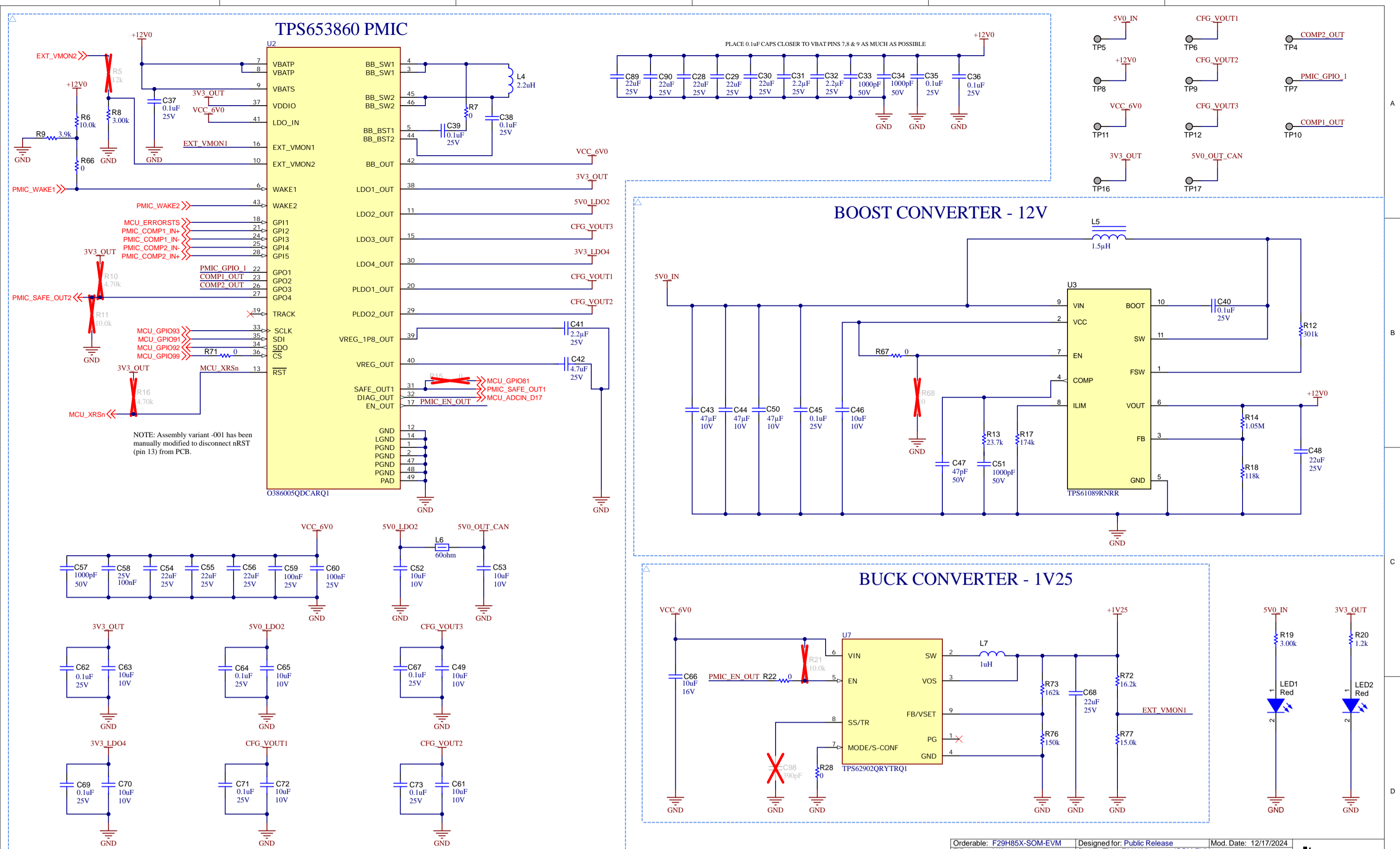


S3 (POS 1)	S3 (POS 2)	VREFHAB Source
1-2	X	Internal VREF
2-3	4-5	External VREF (J1.VREFHAB)
2-3	5-6	External VREF (3V0)*
*Default		

S4 (POS 1)	S4 (POS 2)	VREFHCDCE Source
1-2	X	Internal VREF
2-3	4-5	External VREF (J1.VREFHCDCE)
2-3	5-6	External VREF (3V0)*
*Default		

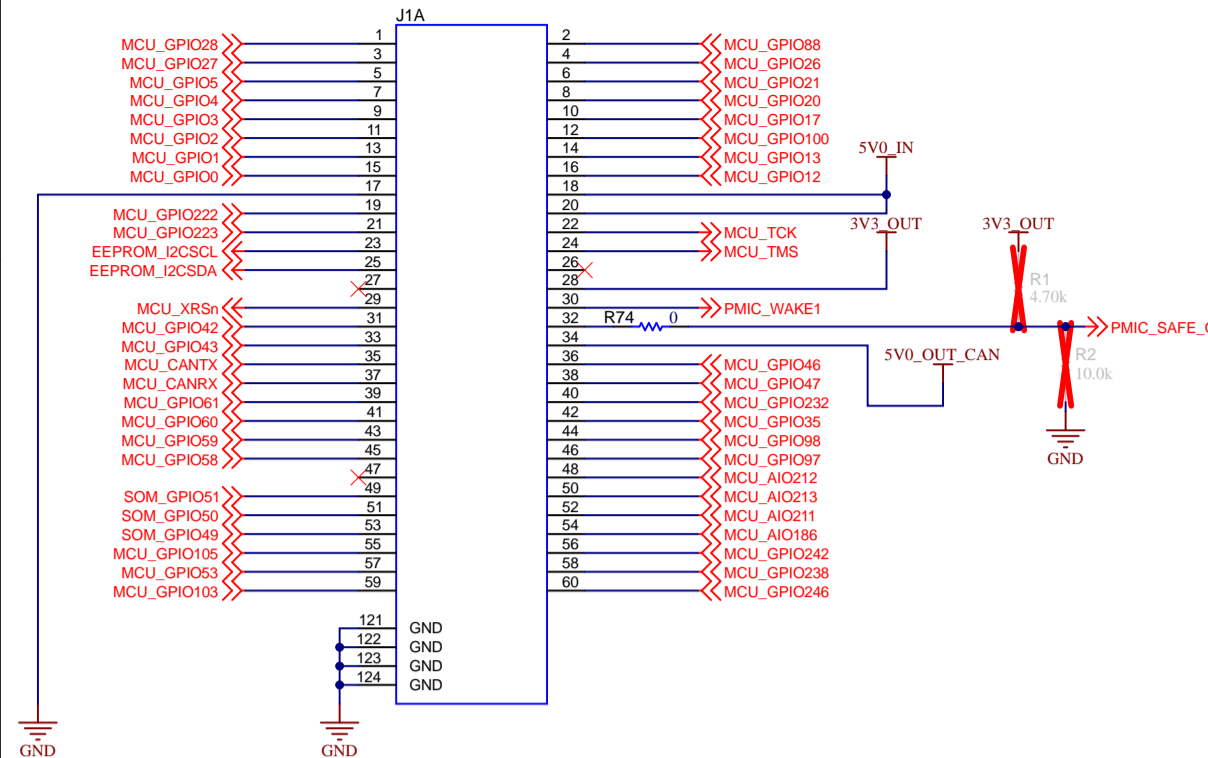


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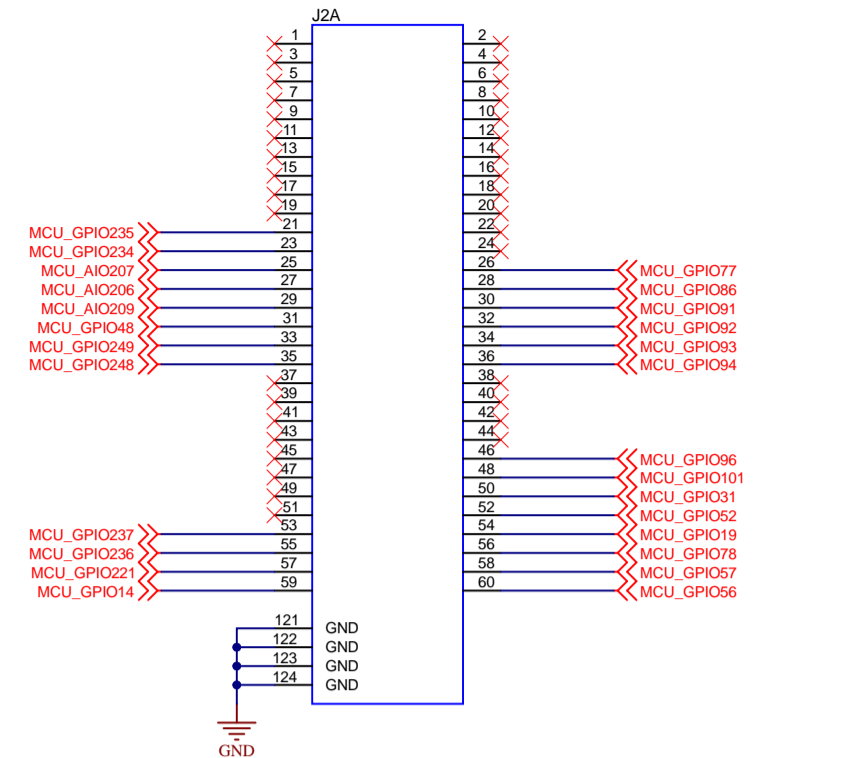


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 Part Number: MCUI44 Rev: A Project Title: F29H85X controlSOM EVM
 SVN Rev: Version control disabled Assembly Variant: 01 Sheet 5 of 8
 Drawn By: Gustavo Martinez File: MCUI44A_SOM_Power_SchDoc Size: B http://www.ti.com
 Engineer: Gustavo Martinez Contact: http://www.ti.com/support © Texas Instruments 2024

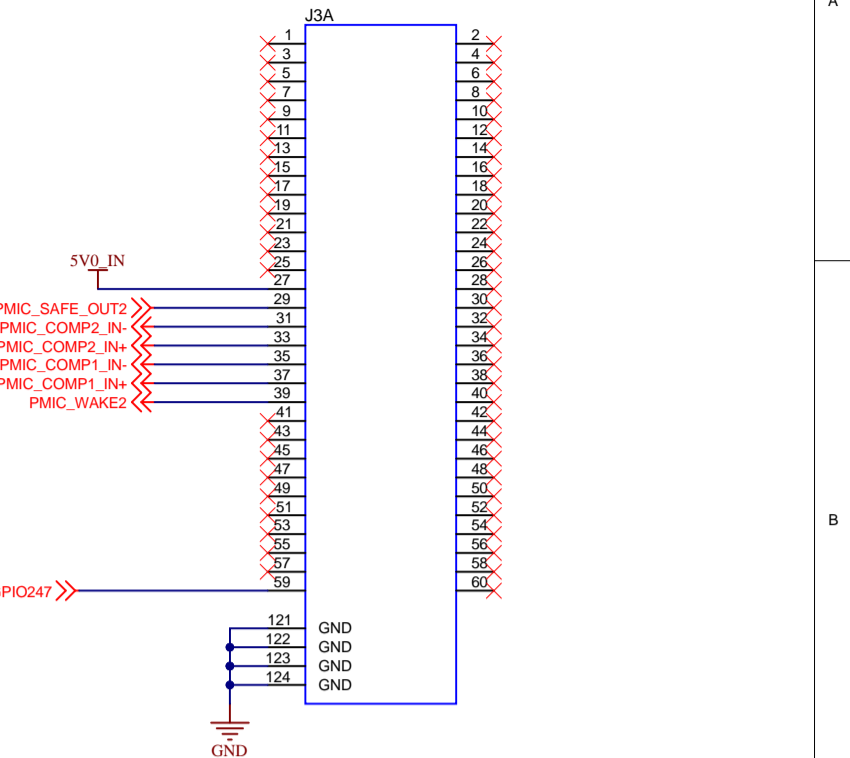
High Density Connector J1



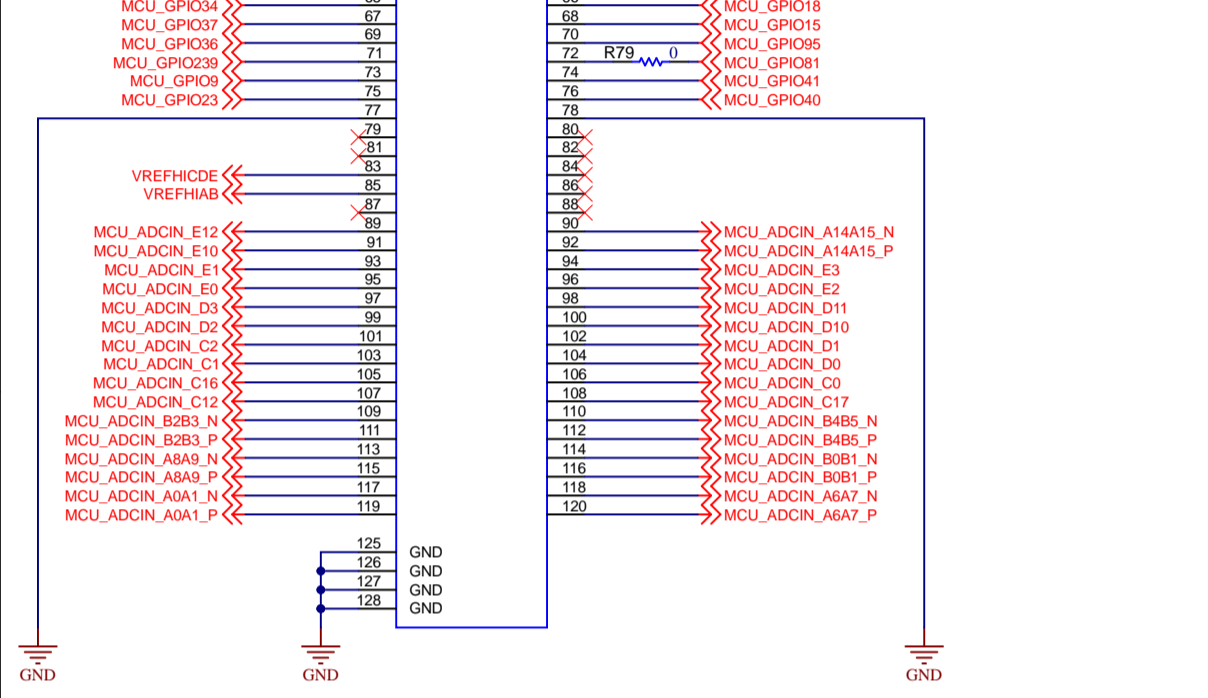
High Density Connector J2



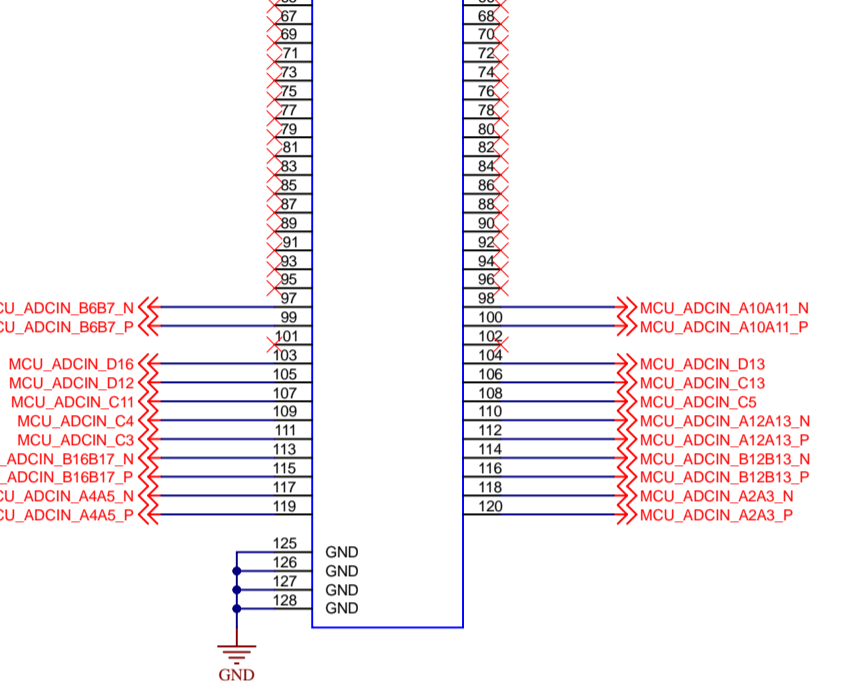
High Density Connector J3



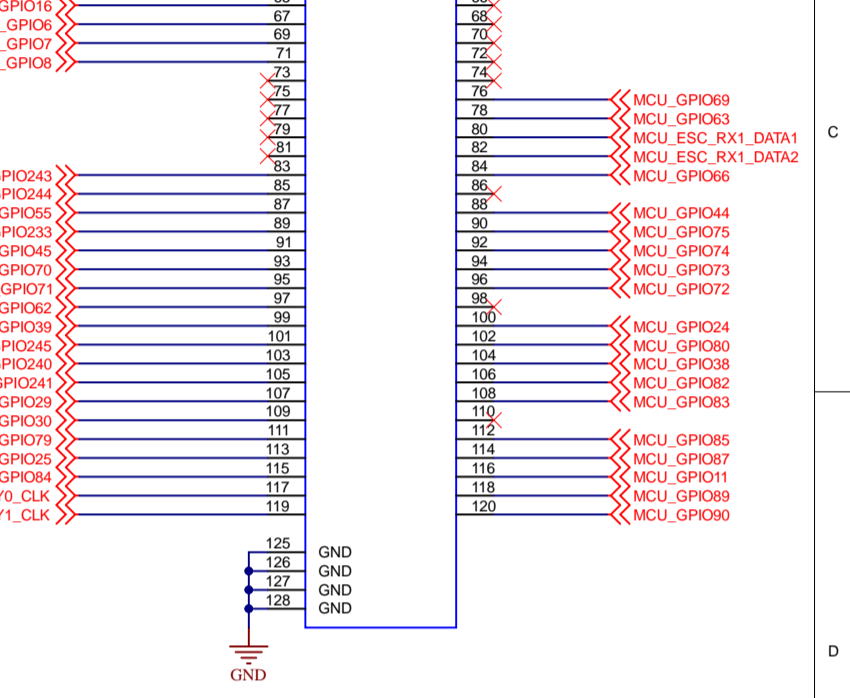
High Density Connector J1B



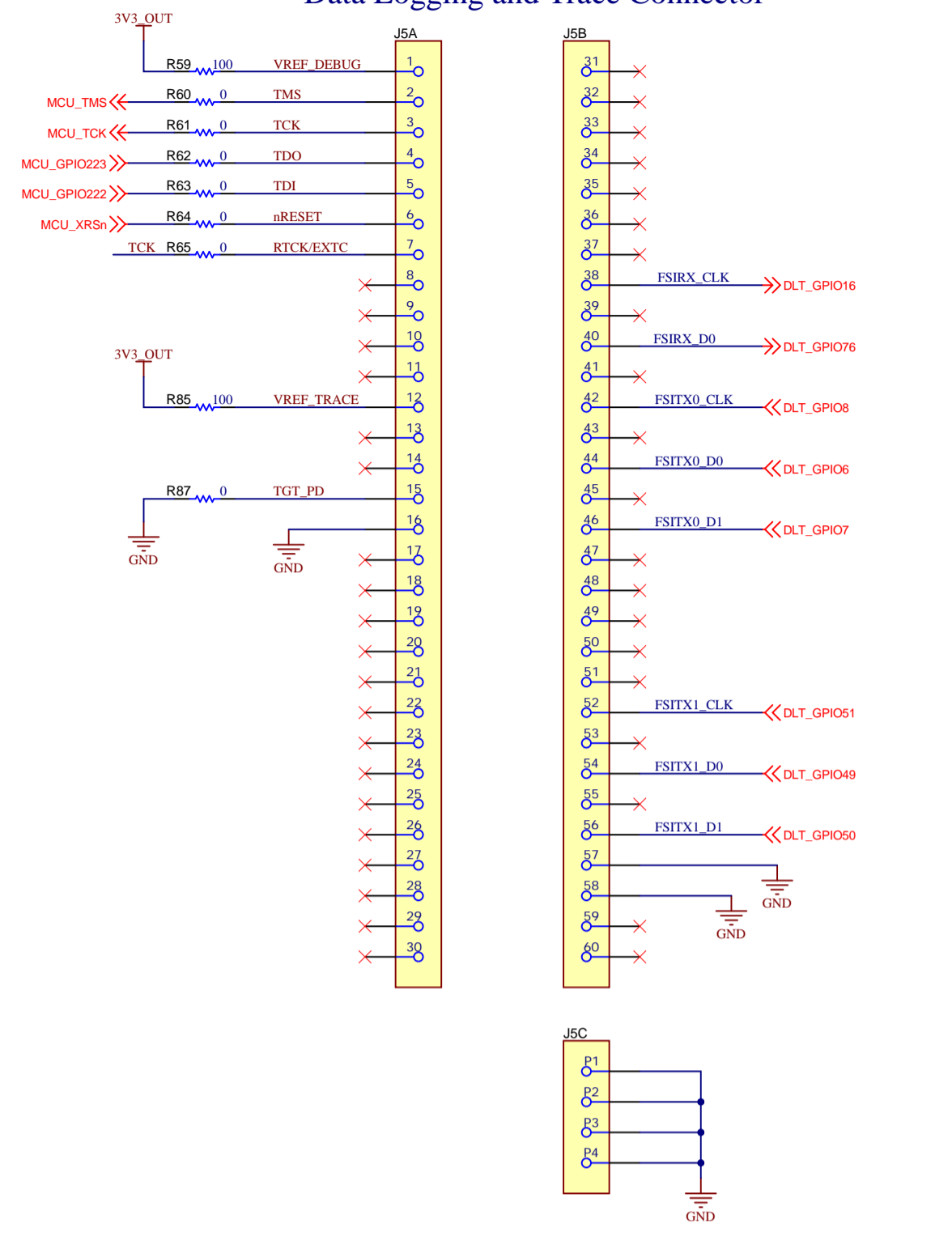
High Density Connector J2B



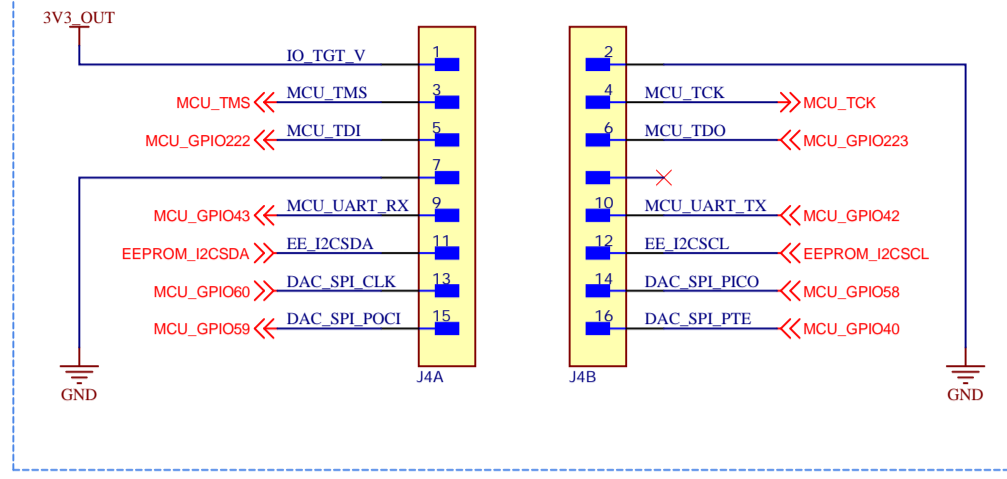
High Density Connector J3B



Data Logging and Trace Connector



Emulator Connector

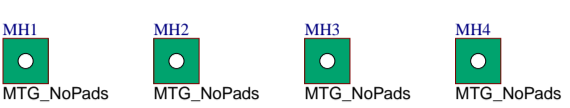
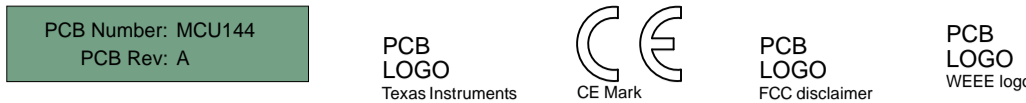


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Orderable: F29H85X-SOM-EVM	Designed for: Public Release	Mod. Date: 10/29/2024
TI Part: N/A	Project Title: F29H85X control SOM EVM	
Number: MCU144	Rev: A	Sheet Title: Emulation Connectors
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 7 of 8
Drawn By: Gustavo Martinez	File: MCU144A_Emulation_Connectors_SchDoc	Size: B
Engineer: Gustavo Martinez	Contact: http://www.ti.com/support	http://www.ti.com



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Variant/Label Table	
Variant	Variant Description
001	25-MHz clock disabled.
002	25-MHz clock enabled.

ZZ1
Label Assembly Note
 This Assembly Note is for PCB labels only

ZZ2
Assembly Note
 These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
 These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
 These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: F29H85X-SOM-EVM	Designed for: Public Release	Mod. Date: 12/9/2024	
TID #: N/A	Project Title: F29H85X control SOM EVM		
Number: MCU144	Rev: A	Sheet Title: EVM Hardware	
SW Rev: Version control disabled	Assembly Variant: 01	Sheet 8 of 8	
Drawn By: Gustavo Martinez	File: MCU144A_EVM_Hardware_SchDoc	Size: B	
Engineer: Gustavo Martinez	Contact: http://www.ti.com/support		© Texas Instruments 2024

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