# **TPA3255EVM**

## **Quick-Start Guide**





ti.com/tool/tpa3255evm



## **Evaluation Kit Contents**

- TPA3255 device
- · PCB, heat sink, and external components to evaluate at full power
- RCA input jacks
- · Banana output connectors

## Not included:

- Power supply: up to 53.5V, 12A for max power
- Speakers
- High-Resolution Audio Source

## **Supported Output Configurations**

See full User's Guide online for more information.



4-Channels-single-ended (SE)



2-Channels-bridge-tied load (BTL)



1-Channel-parallel bridge-tied load (PBTL)

## **Getting Started**

- 1. Ensure the RESET switch (S1) is in the RESET position.
- 2. Connect the power supply to the EVM using the PVDD and GND terminals.
- 3. Connect the first speaker to the output terminals OUTA and OUTB.
- 4. Connect the second speaker to the output terminals OUTC and OUTD.
- 5. Connect a high-resolution audio source to INA (J3) and INC (J18) for single-ended operation, which are default RCA input terminals.
- Apply power (14V-53.5V) and move the RESET switch (S1) to the NORMAL position.



## **Indicator Descriptions**

3.3V – indicates the 3.3V rail used for GPIO control is active 12V – indicates the 12V rail used for amplifier gate drive is active CLP\_OTWz – indicates when clipping or over-temperature warning occur FAULTz – indicates when a fault condition occurs (requires toggling reset to clear fault)

FAULTz	CLP_OTWz	Possible Faults
ON	ON	OTW, OTE, UVP, OLP
ON	0FF	UVP, OLP
OFF	ON	OTW (solid), Early Clipping (flickering)
OFF	0FF	No Fault

OTW - Over-temperature warning (> 125°C)

OTE - Over-temperature error / shutdown (> 150°C)

UVP - under-voltage protection

OLP - over-load or over-current protection

## **Default Jumper Configuration**

Jumper	Default	Comment	Jumper	Default	Comment	
J29	IN	PVDD to 15V BUCK	J24	IN	OUTC CAP SHUNT	
J31	IN	15V BUCK to 12V TERM	J25	IN	OUTD CAP SHUNT	
J32	IN	12V LDO to 12V TERM	J26	2 to 3	INC SELECT	
J33	IN	3.3V LDO to 3.3V TERM	J27	2 to 3	IND SELECT	
J21	OUT	CSTART SE	J7	OUT	PBTL SELECT INC	
J16	3 to 4	MASTER MODE	J8	OUT	PBTL SELECT IND	
J5	2 to 3	M1-BTL	J10	OUT	INA/B DIFF INPUT	
J6	2 to 3	M2-BTL	J12	OUT	INC/D DIFF INPUT	
J22	IN	OUTA CAP SHUNT	J4	1 to 2	INA/B SE INPUT	
J23	IN	OUTB CAP SHUNT	J19	1 to 2	INC/D SE INPUT	

## Features & Benefits

#### **High-Resolution**

Deliver audio as it was recorded all the way to the speaker. The TPA32xx family supports hi-res audio.



**H**R

## High-Bandwidth

**High-Power** 

The TPA32xx family of devices support up to 100 kHz audio bandwidth



#### Low-Distortion A new closed-loop design

A new closed-loop design enables utra-low THD across all frequencies



#### **Efficient Design**

Best power efficiency and idle losses enable low power consumption and smaller heat sink.

#### Easy to use

. 0

Devices with 35 W to 650 W of output power that deliver large sound in a compact size.



Simplify PCB design with fewer external components, integrated protection, and scalable power options.

## THD+N vs Output Power – 4Ω, BTL, 1kHz



## **TPA32xx Product Family**

Device	TPA3244	TPA3245	TPA3250	TPA3251	TPA3255	
Max Power to BTL/ Ch (W)	110	145	130	220	315	
Max Power to PBTL (W)	160	230	190	355	605	
Min Supported BTL Load ( $\Omega$ )	4	3	4	3	4	
Power Stage Supply Max (V)	31.5	31.5	38	38	53.5	
Thermal Pad Location	Bottom	Тор	Bottom	Тор	Тор	
Package	44HTSSOP <sup>2</sup>	44HTSSOP1	44HTSSOP <sup>2</sup>	44HTSSOP1	44HTSSOP1	
Dimensions	6.1 x 14mm					

<sup>1</sup>Pad-Up, pin-compatible package <sup>2</sup>Pad-Down, pin-compatible package

## **More Information**

#### **TPA3255 Product Webpage**

- TPA3255 Datasheet
- Complete TPA3255EVM User's Guide
- Schematics and layout

## Available on: ti.com/tpa3255

#### **High-Power Audio Portal**

- New Products
- Technical Documents
- Support and Training
- Product Selection Tool

Available on: ti.com/highpoweraudio

Engage in the<br/>Audio Amplifiers Support ForumTI E2E\*\*CommunityKnowledge with fellow engineers

## Available on: ti.com/audioamplifierse2e

## IMPORTANT NOTICE FOR TI DESIGN INFORMATION AND RESOURCES

Texas Instruments Incorporated ('TI") technical, application or other design advice, services or information, including, but not limited to, reference designs and materials relating to evaluation modules, (collectively, "TI Resources") are intended to assist designers who are developing applications that incorporate TI products; by downloading, accessing or using any particular TI Resource in any way, you (individually or, if you are acting on behalf of a company, your company) agree to use it solely for this purpose and subject to the terms of this Notice.

TI's provision of TI Resources does not expand or otherwise alter TI's applicable published warranties or warranty disclaimers for TI products, and no additional obligations or liabilities arise from TI providing such TI Resources. TI reserves the right to make corrections, enhancements, improvements and other changes to its TI Resources.

You understand and agree that you remain responsible for using your independent analysis, evaluation and judgment in designing your applications and that you have full and exclusive responsibility to assure the safety of your applications and compliance of your applications (and of all TI products used in or for your applications) with all applicable regulations, laws and other applicable requirements. You represent that, with respect to your applications, you have all the necessary expertise to create and implement safeguards that (1) anticipate dangerous consequences of failures, (2) monitor failures and their consequences, and (3) lessen the likelihood of failures that might cause harm and take appropriate actions. You agree that prior to using or distributing any applications. TI has not conducted any testing other than that specifically described in the published documentation for a particular TI Resource.

You are authorized to use, copy and modify any individual TI Resource only in connection with the development of applications that include the TI product(s) identified in such TI Resource. NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT OF TI OR ANY THIRD PARTY IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information regarding or referencing third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of TI Resources may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI RESOURCES ARE PROVIDED "AS IS" AND WITH ALL FAULTS. TI DISCLAIMS ALL OTHER WARRANTIES OR REPRESENTATIONS, EXPRESS OR IMPLIED, REGARDING TI RESOURCES OR USE THEREOF, INCLUDING BUT NOT LIMITED TO ACCURACY OR COMPLETENESS, TITLE, ANY EPIDEMIC FAILURE WARRANTY AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY YOU AGAINST ANY CLAIM, INCLUDING BUT NOT LIMITED TO ANY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON ANY COMBINATION OF PRODUCTS EVEN IF DESCRIBED IN TI RESOURCES OR OTHERWISE. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, DIRECT, SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF TI RESOURCES OR USE THEREOF, AND REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

You agree to fully indemnify TI and its representatives against any damages, costs, losses, and/or liabilities arising out of your noncompliance with the terms and provisions of this Notice.

This Notice applies to TI Resources. Additional terms apply to the use and purchase of certain types of materials, TI products and services. These include; without limitation, TI's standard terms for semiconductor products <a href="http://www.ti.com/sc/docs/stdterms.htm">http://www.ti.com/sc/docs/stdterms.htm</a>), evaluation modules, and samples (<a href="http://www.ti.com/sc/docs/stdterms.htm">http://www.ti.com/sc/docs/stdterms.htm</a>), evaluation

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2017, Texas Instruments Incorporated