



Abstract

This is the programmer's guide for the [DLPC3437](#) controller used with the [DLP3310](#) DMD. This guide primarily discusses the I²C interface of these controllers. For additional information please visit the desired device product folder on ti.com.

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1 Introduction

1.1 System Overview

A typical TI DLP® Pico™ chipset consists of the controller, the PMIC, and the DMD. The DMD and PMIC are controlled by the DLPC3437 controller. An example system is shown in Figure 1-1. The controller communicates with the outside world with I²C commands.

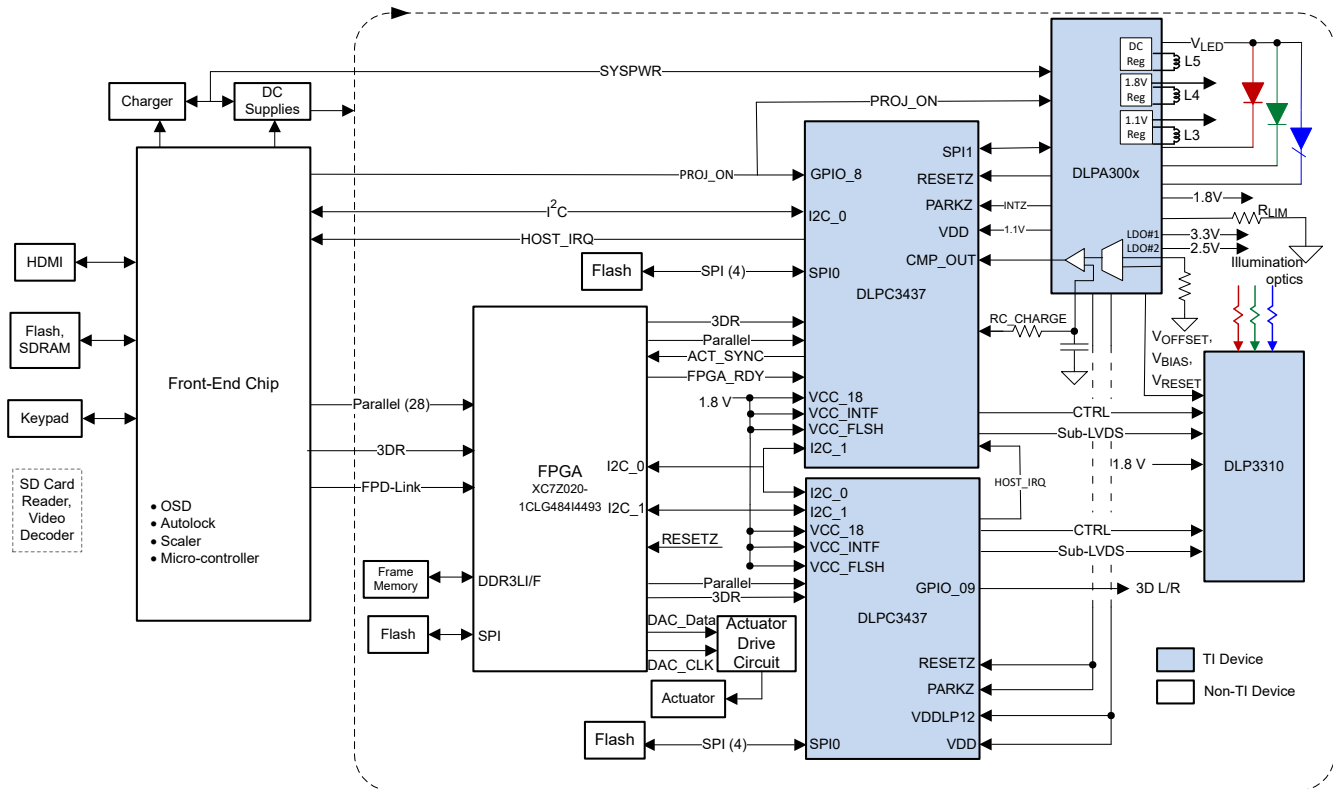


Figure 1-1. DLPC3437 Application Block Diagram

1.2 Software Overview

The DLPC343x controller contains an Arm® Cortex®-M3 processor with additional functional blocks to enable video processing and control. TI provides software as a firmware image. The firmware consists of the main application code (used by the Arm processor) along with other configuration and operational data required by the system for normal operation. The controller and its accompanying DLP chipset components require this proprietary software to operate.

The firmware must be programmed into the SPI flash memory. The DLPC343x controller loads the main application into the Arm processor which periodically accesses the operational data. The available controller functions depend on the firmware version installed. Different firmware is required for different chipset combinations (such as when using different PMIC devices). Visit the applicable controller product folder on ti.com, visit the [DLP Pico Firmware Selector](#), or contact TI for the latest firmware.

1.2.1 I²C Overview

The protocol used in communicating information to DLPC343x controller consist of a serial data bus conforming to the Philips I²C specification. The controller can be configured at runtime by using these I²C commands. The DLPC343x behaves as an I²C follower operating at up to 100kHz.

1.2.2 I²C Transactions

Since all I²C commands are processed by software, only one type of I²C transaction is supported. This transaction type is shown in Table 1-1 for both writes and reads. The I²C interface supports variably-sized

transactions (for example, a one byte transaction or a nine byte transaction) to match the commands discussed later in this document.

Table 1-1. I²C Write and Read Transactions

Transaction		Address ⁽¹⁾	Sub-Address ⁽²⁾	Remaining Data Bytes ⁽³⁾
Write	Size	8-bits	8-bits	8-bit parameter bytes (0 → N)
	Value	36h (or 3Ah)	Command value	Parameter values
Read Request	Size	8-bits	8-bits	8-bit parameter bytes (0 → N)
	Value	36h (or 3Ah)	Command value	Parameter values
Read Response	Size	8-bits		8-bit parameter bytes (0 → N)
	Value	37h (or 3Bh)		Parameter values

(1) The address corresponds to the chip address of the controller. The address is dependent upon the firmware image with the default value of 36h.

(2) The subaddress corresponds to a TI command.

(3) The data (if present) corresponds to any required command parameters.

1.2.3 Data Flow Control

While the I²C interface inherently supports flow control by holding the clock, this is not sufficient for all transactions (for example, sequence and some other updates). In this case, the host software will need to execute the *Read Short Status* command to determine if the system is busy.

1.3 Related Documentation

- [DLPC3437 Display Controller Data Sheet](#)
- [DLP3310 0.33 1080p DMD Data Sheet](#)
- [DLPA3000 Power Management and LED/Lamp Driver IC Data Sheet](#)
- [DLPA3005 Power Management and LED/Lamp Driver IC Data Sheet](#)

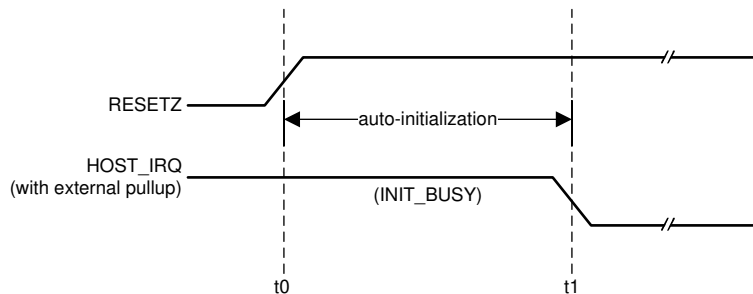
2 System Initialization

2.1 Boot ROM

The DLPC343x employs a boot ROM and associated boot software. This resident boot code consists of the minimum code necessary to load the software from flash to internal RAM for execution. For most DLPC343x product configurations, an external flash device can store the main application code, along with the other configuration and operational data required by the system for normal operation.

2.2 Device Startup

- The HOST_IRQ signal is provided to indicate when the system has completed auto-initialization.
- While reset is applied, HOST_IRQ is tri-stated (an external pullup resistor pulls the line high).
- HOST_IRQ remains tri-stated (pulled high externally) until the boot process completes. While the signal is pulled high, this indicates that the controller is performing boot-up and auto-initialization.
- As soon as possible after the controller boots-up, the controller drives HOST_IRQ to a logic high state to indicate that the controller is continuing to perform auto-initialization (no real state changes occur on the external signal).
- The software sets HOST_IRQ to a logic low state at the completion of the auto-initialization process. At the falling edge of the signal, the initialization is complete.
- The DLPC34xx controller is ready to receive commands through I²C or accept video over the DSI or the parallel interface only after auto-initialization is complete.
- The controller initialization typically completes (HOST_IRQ goes low) within 500 ms of RESETZ being asserted. However, this time may vary depending on the software version and the contents of the user configurable auto initialization file.



t0: rising edge of RESETZ; auto-initialization begins

t1: falling edge of HOST_IRQ; auto-initialization is complete

Figure 2-1. HOST_IRQ Timing

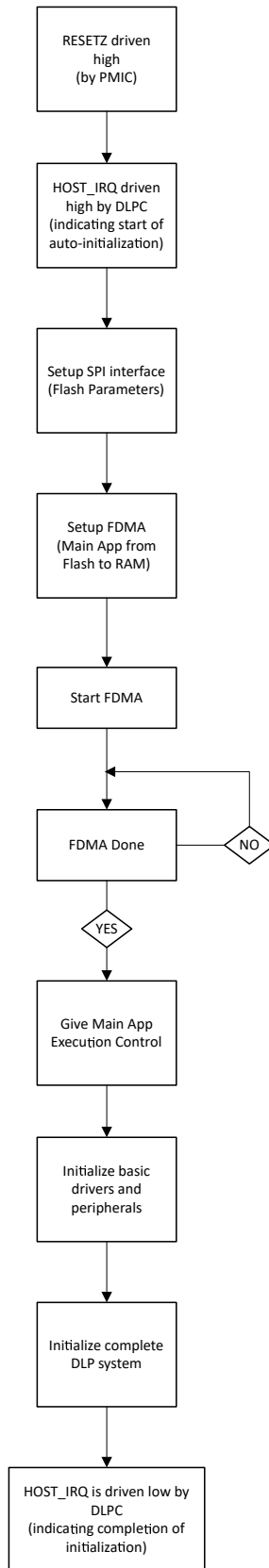


Figure 2-2. Startup Flow

3 I²C Commands

Table 3-1. List of System Write/Read Software Commands

Command Type	Command Description	OpCode (hex)	Reference
General Operation Commands			
Write	Write Input Source Select	05	Write Input Source Select (05h)
Read	Read Input Source Select	06	Read Input Source Select (06h)
Write	Write External Video Chroma Processing Select	09	Write External Video Chroma Processing Select (09h)
Read	Read External Video Chroma Processing Select	0A	Read External Video Chroma Processing Select (0Ah)
Write	Write Test Pattern Select	0B	Write Test Pattern Select (0Bh)
Read	Read Test Pattern Select	0C	Read Test Pattern Select (0Ch)
Write	Write Splash Screen Select	0D	Write Splash Screen Select (0Dh)
Read	Read Splash Screen Select	0E	Read Splash Screen Select (0Eh)
Read	Read Splash Screen Header	0F	Read Splash Screen Header (0Fh)
Write	Write Display Image Orientation	14	Write Display Image Orientation (14h)
Read	Read Display Image Orientation	15	Read Display Image Orientation (15h)
Write	Write Display Image Curtain	16	Write Display Image Curtain (16h)
Read	Read Display Image Curtain	17	Read Display Image Curtain (17h)
Write	Write Image Freeze	1A	Write Image Freeze (1Ah)
Read	Read Image Freeze	1B	Read Image Freeze (1Bh)
Write	Write 3-D Control	20	Write 3-D Control (20h)
Read	Read 3-D Control	21	Read 3-D Control (21h)
Write	Write Look Select	22	Write Look Select (22h)
Read	Read Look Select	23	Read Look Select (23h)
Read	Read Sequence Header Attributes	26	Read Sequence Header Attributes (26h)
Write	Write Degamma/CMT Select	27	Write Degamma/CMT Select (27h)
Read	Read Degamma/CMT Select	28	Read Degamma/CMT Select (28h)
Write	Write CCA Select	29	Write CCA Select (29h)
Read	Read CCA Select	2A	Read CCA Select (2Ah)
Read	Read DMD Sequencer Sync Mode	2C	Read DMD Sequencer Sync Mode (2Ch)
Write	Write Execute Flash Batch File	2D	Write Execute Flash Batch File (2Dh)
Write	Write Input Image Size	2E	Write Input Image Size (2Eh)
Read	Read Input Image Size	2F	Read Input Image Size (2Fh)
Write	Write 3-D Reference	30	Write 3-D Reference (30h)
Write	Write Splash Screen Execute	35	Write Splash Screen Execute (35h)
Write	Write Parallel Data Mask Control	37	Write Parallel Data Mask Control (37h)
Read	Read Parallel Data Mask Control	38	Read Parallel Data Mask Control (38h)
Write	Write Mirrors Lock Command	39	Write Mirrors Lock Command (39h)
Read	Read Mirrors Lock Command	3A	Read Mirrors Lock Command (3Ah)
Write	Write FPD Link Pixel Map Mode	4B	Write FPD Link Pixel Map Mode (4Bh)
Read	Read FPD Link Pixel Map Mode	4C	Read FPD Link Pixel Map Mode (4Ch)
Write	Write FPGA Input Video Chroma Processing Select	4D	Write FPGA Input Video Chroma Processing Select (4Dh)
Read	Read FPGA Input Video Chroma Processing Select	4E	Read FPGA Input Video Chroma Processing Select (4Eh)
Illumination Control Commands			
Write	Write LED Output Control Method	50	Write LED Output Control Method (50h)

Table 3-1. List of System Write/Read Software Commands (continued)

Command Type	Command Description	OpCode (hex)	Reference
Read	Read LED Output Control Method	51	Read LED Output Control Method (51h)
Write	Write RGB LED Enable	52	Write RGB LED Enable (52h)
Read	Read RGB LED Enable	53	Read RGB LED Enable (53h)
Write	Write RGB LED Current	54	Write RGB LED Current (54h)
Read	Read RGB LED Current	55	Read RGB LED Current (55h)
Read	Read CAIC LED Max Available Power	57	Read CAIC LED Max Available Power (57h)
Write	Write RGB LED Max Current	5C	Write RGB LED Max Current (5Ch)
Read	Read RGB LED Max Current	5D	Read RGB LED Max Current (5Dh)
Read	Read CAIC RGB LED Current	5F	Read CAIC RGB LED Current (5Fh)
Write	Write XPR FPGA Input Image Size	60	Write XPR FPGA Input Image Size (60h)
Read	Read XPR FPGA Input Image Size	61	Read XPR FPGA Input Image Size (61h)
Write	Write XPR FPGA Source Select	62	Write XPR FPGA Source Select (62h)
Read	Read XPR FPGA Source Select	63	Read XPR FPGA Source Select (63h)
Read	Read XPR FPGA Version	64	Read XPR FPGA Version (64h)
Write	Write XPR FPGA Test Pattern Select	67	Write XPR FPGA Test Pattern Select (67h)
Read	Read XPR FPGA Test Pattern Select	68	Read XPR FPGA Test Pattern Select (68h)
Write	Write XPR FPGA Parallel Video Control	6B	Write XPR FPGA Parallel Video Control (6Bh)
Read	Read XPR FPGA Parallel Video Control	6C	Read XPR FPGA Parallel Video Control (6Ch)
Write	Write XPR FPGA Video Format Select	6D	Write XPR FPGA Video Format Select (6Dh)
Read	Read XPR FPGA Video Format Select	6E	Read XPR FPGA Video Format Select (6Eh)
Read	Read XPR FPGA Status	6F	Read XPR FPGA Status (6Fh)
Write	Write Actuator Latency	70	Write Actuator Latency (70h)
Read	Read Actuator Latency	71	Read Actuator Latency (71h)
Write	Write Actuator Gain	72	Write Actuator Gain (72h)
Read	Read Actuator Gain	73	Read Actuator Gain (73h)
Write	Write Segment Length	74	Write Segment Length (74h)
Read	Read Segment Length	75	Read Segment Length (75h)
Write	Write Manual Actuator Sync Delay	76	Write Manual Actuator Sync Delay (76h)
Read	Read Manual Actuator Sync Delay	77	Read Manual Actuator Sync Delay (77h)
Write	Write Manual Actuator Offset	78	Write Manual Actuator Offset (78h)
Read	Read Manual Actuator Offset	79	Read Manual Actuator Offset (79h)
Image Processing Control Commands			
Write	Write Local Area Brightness Boost Control	80	Write Local Area Brightness Boost Control (80h)
Read	Read Local Area Brightness Boost Control	81	Read Local Area Brightness Boost Control (81h)
Write	Write CAIC Image Processing Control	84	Write CAIC Image Processing Control (84h)
Read	Read CAIC Image Processing Control	85	Read CAIC Image Processing Control (85h)
Write	Write Color Coordinate Adjustment Control	86	Write Color Coordinate Adjustment Control (86h)

Table 3-1. List of System Write/Read Software Commands (continued)

Command Type	Command Description	OpCode (hex)	Reference
Read	Read Color Coordinate Adjustment Control	87	Read Color Coordinate Adjustment Control (87h)
Write	Write Keystone Correction Control	88	Write Keystone Correction Control (88h)
Read	Read Keystone Correction Control	89	Read Keystone Correction Control (89h)
Write	Write Keystone Projection Pitch	BB	Write Keystone Projection Pitch Angle (BBh)
Read	Read Keystone Projection Pitch	BC	Read Keystone Projection Pitch Angle (BCh)
General Setup Commands			
Write	Write Border Color	B2	Write Border Color (B2h)
Read	Read Border Color	B3	Read Border Color (B3h)
Write	Write Parallel Interface Sync Polarity	B6	Write Parallel Interface Sync Polarity (B6h)
Read	Read Parallel Interface Sync Polarity	B7	Read Parallel Interface Sync Polarity (B7h)
Read	Read Auto Framing Information	BA	Read Auto Framing Information (BAh)
Administrative Commands			
Read	Read Short Status	D0	Read Short Status (D0h)
Read	Read System Status	D1	Read System Status (D1h)
Read	Read System Software Version	D2	Read System Software Version (D2h)
Read	Read Communication Status	D3	Read Communication Status (D3h)
Read	Read Controller Device ID	D4	Read Controller Device ID (D4h)
Read	Read DMD Device ID	D5	Read DMD Device ID (D5h)
Read	Read System Temperature	D6	Read System Temperature (D6h)
Read	Read Flash Build Version	D9	Read Flash Build Version (D9h)
Write	Write Flash Batch File Delay	DB	Write Flash Batch File Delay (DBh)
Read	Read DMD I/F Training Data	DC	Read DMD I/F Training Data (DCh)
Flash Update Commands			
Read	Read Flash Update PreCheck	DD	Read Flash Update PreCheck (DDh)
Write	Write Flash Data Type Select	DE	Write Flash Data Type Select (DEh)
Write	Write Flash Data Length	DF	Write Flash Data Length (DFh)
Write	Write Erase Flash Data	E0	Write Erase Flash Data (E0h)
Write	Write Flash Start	E1	Write Flash Start (E1h)
Write	Write Flash Continue	E2	Write Flash Continue (E2h)
Read	Read Flash Start	E3	Read Flash Start (E3h)
Read	Read Flash Continue	E4	Read Flash Continue (E4h)

The following sections describe each of the above listed commands in detail.

3.1 General Operation Commands

3.1.1 Write Input Source Select (05h)

This command selects the input source of the system.

3.1.1.1 Write Parameters

Table 3-2 describes the write parameters.

Table 3-2. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Byte 1	Operating Mode
00h	External Video Mode
01h	Test Pattern Generator Mode
02h	Splash Screen Mode
03h - FFh	Reserved

The other operating modes have associated commands which are only applicable to that mode and must be run to properly configure the selected mode. The associated commands are:

- External Video Port:
 - [Write Input Image Size \(2Eh\)](#)
 - [Write External Video Chroma Processing Select \(09h\)](#)
- Test Pattern Generator:
 - [Write Test Pattern Select \(0Bh\)](#)
- Splash Screen:
 - [Write Splash Screen Select \(0Dh\)](#)
 - [Write Splash Screen Execute \(35h\)](#)

Note

Only RGB (and not YCbCr) format is supported in splash pattern mode.

The table below show the source specific associated commands, where ‘Y’ represents a valid source selection and ‘N’ implies that the command is not supported by the selected source.

Table 3-3. Source Specific Associated Commands

Source Specific Associated Commands	Display Modes		
	External Video Port	Test Pattern Generator	Splash Screen
Write External Video Chroma Processing Select	Y	N	N
Write External Input Image Size	Y	N	N
Write Test Pattern Select	N	Y	N
Write Splash Screen Select	N	N	Y
Write Splash Screen Execute	N	N	Y ⁽¹⁾

(1) The Write Splash Screen Execute command is special in that there is no maintained state or history. Thus, this command has no “settings” to be stored and reused by the system.

It is recommended that the source associated commands be sent prior to sending the *Write Operating Mode Select* command. These commands (except for *Write Splash Screen Execute*) describe the unique characteristics of their associated source, and once these settings have been defined, they are stored in a volatile manner. When source associated commands are sent when that source is not active, the controller software saves the new settings, but does not execute these commands. When that source becomes active (via the *Write Operating Mode Select* command), the controller applies these settings. Each time an operating mode selection is made, the system retrieves the settings defined previously and automatically applies them. As such, the user only needs to send these associated commands when the source first needs to be defined, or when the

source characteristics for that port need to be changed. It is important to note that the appropriate associated commands must be updated when source characteristics change. See below for examples to configure different operating modes.

The rest of the commands that apply to image setup are those commands whose settings are applicable across all source selections, and indeed, these command settings would typically remain the same across the different Operating Mode selections. Sometimes the values for these commands are the same across the different input source types, but this does not indicate that hardware settings have not changed. For example, if the display image size is set to 720p and the external port input source size is set to VGA, the input scales up to the display size of 720p. When splash screen is selected, the controller modifies the settings specified by Write Image Crop (10h). Therefore, the scale settings must be changed. The controller software manages the underlying hardware settings. An example is shown below:

1. User sends the following commands (active Input Source = Test Pattern Generator)
 - a. Write Image Freeze = Freeze
 - b. Write External Video Chroma Processing Select (settings stored, command not executed)
 - c. Write External Input Image Size (settings stored, command not executed)
 - d. Write Operating Mode Select = External Port (see the next step)
 - e. Write Image Freeze = Unfreeze
2. When the Write Operating Mode Select command is received, software applies the settings from these External Video Port associated commands.
 - a. External Video Chroma Processing Select
 - b. External Input Image Size
 - c. External Parallel Manual Image Framing (as appropriate – that is, if parallel port selected)
 - d. External CPU Video Sync Mode (as appropriate – that is, if CPU port selected)

If source associated commands are sent for a source that is already active, the controller software will execute these commands when received. An example is shown below:

1. User sends the following commands (active Input Source = External Video Port)
 - a. Write Image Freeze = Freeze
 - b. Write External Video Chroma Processing Select (command executed)
 - c. Write Image Freeze = Unfreeze

Refer to [Write Image Freeze \(1Ah\)](#) for information on hiding on-screen artifacts when selecting an input source.

3.1.2 Read Input Source Select (06h)

This command reads the input source of the system.

3.1.2.1 Read Parameters

This command has no read parameters.

3.1.2.2 Return Parameters

The following table describes the return parameters.

Byte 1	Operating Mode
00h	External Video Mode
01h	Test Pattern Generator Mode
02h	Splash Screen Mode
03h - FFh	<i>Reserved</i>

This command works in all operating modes and has no effect on the current system configuration.

3.1.3 Write External Video Chroma Processing Select (09h)

This command specifies the active external video chroma processing type for the display module.

3.1.3.1 Write Parameters

Table 3-4 describes the write parameters.

Table 3-4. Write Parameters

Parameter Bytes		Description	
Byte 1		Chroma	
Byte 2		CSC coefficient set	

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:5)	Reserved						
b(4)	Chroma Interpolation Method: <ul style="list-style-type: none"> 0: Chroma Interpolation 1: Chroma Copy 						
b(3)	Reserved						
b(2)	Chroma Channel Swap: <ul style="list-style-type: none"> 0: CbCr 1: CrCb 						
b(0:1)	Reserved						

MSB	Byte 2						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:2)	Reserved						
b(1:0)	CSC Coefficient Set (color space)						

Note

CSC coefficient sets are specified in Byte 2 by an enumerated value (0, 1, 2, or 3). The set stored in '0' is ITU-R BT. Rec. 601. The other three sets are user definable.

3.1.4 Read External Video Chroma Processing Select (0Ah)

This command is used to specify the characteristics of the selected YCbCr source, as well as specifying the type of chroma processing to be used for this YCbCr source by the display module.

3.1.4.1 Read Parameters

This command has no read parameters.

3.1.4.2 Return Parameters

Table 3-5 describes the return parameters.

Table 3-5. Return Parameters

Parameter Bytes		Description	
Byte 1		Chroma	
Byte 2		CSC coefficient set	

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:5)	Reserved						
b(4)	Chroma Interpolation Method: <ul style="list-style-type: none"> 0: Chroma Interpolation 1: Chroma Copy 						

b(3)	Reserved
b(2)	Chroma Channel Swap: <ul style="list-style-type: none"> • 0: CbCr • 1: CrCb
b(0:1)	Reserved

MSB	Byte 2						LSB
b7	b6	b5	b4	b3	b2	b1	b0

b(7:2)	Reserved
b(1:0)	CSC Coefficient Set (color space)

Note

CSC coefficient sets are specified in Byte 2 by an enumerated value (0, 1, 2, or 3). The set stored in '0' is ITU-R BT. Rec. 601. The other three sets are user definable.

3.1.5 Write Test Pattern Select (0Bh)

This command specifies an internal test pattern for display on the display module.

3.1.5.1 Write Parameters

Table 3-6 describes the write parameters.

Table 3-6. Write Parameters

Parameter Bytes	Description
Byte 1	TPG pattern select
Byte 2	Foreground and background color (see Table 3-9)
Byte 3	Parameter 1 (see Table 3-10)
Byte 4	Parameter 2 (see Table 3-10)
Byte 5	Parameter 3 (see Table 3-10)
Byte 6	Parameter 4 (see Table 3-10)

Table 3-7. Byte 1 Write Parameter

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7)	Test pattern border: <ul style="list-style-type: none"> • 00h: Disabled • 01h: Enabled 						
b(6:4)	Reserved						
b(3:0)	Left pattern select: <ul style="list-style-type: none"> • 00h: Solid field • 01h: Fixed step horizontal ramp • 02h: Fixed step vertical ramp • 03h: Horizontal lines • 04h: Diagonal lines • 05h: Vertical lines • 06h: Horizontal and vertical grid • 07h: Checkerboard • 08h: Color bars • 09h-0Fh: Reserved 						

Table 3-8. Byte 2 Write Parameter

MSB	Byte 2						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7)	Reserved						
b(6:4)	Foreground color: <ul style="list-style-type: none"> • 0h: Black • 1h: Red • 2h: Green • 3h: Blue • 4h: Cyan • 5h: Magenta • 6h: Yellow • 7h: White 						
b(3)	Reserved						
b(2:0)	Background color: <ul style="list-style-type: none"> • 0h: Black • 1h: Red • 2h: Green • 3h: Blue • 4h: Cyan • 5h: Magenta • 6h: Yellow • 7h: White 						

Table 3-9. Foreground and Background Color Use

Pattern	Byte 2	
	Foreground Color	Background Color
Solid field	Yes	No
Fixed step horizontal ramp	Yes	No
Fixed step vertical ramp	Yes	No
Horizontal lines	Yes	Yes
Vertical lines	Yes	Yes
Diagonal lines	Yes	Yes
Grid lines	Yes	Yes
Checkerboard	Yes	Yes
Color bars	No	No

Table 3-10. Descriptions and Bit Assignments for Parameters 1-4

Pattern	Byte 6 (Parameter 4)		Byte 5 (Parameter 3)		Byte 4 (Parameter 2)		Byte 3 (Parameter 1)	
	Description	Bits	Description	Bits	Description	Bits	Description	Bits
Solid field	N/A		N/A		N/A		N/A	
Fixed step horizontal ramp	N/A		N/A		End value	8	Start value	8
Fixed step vertical ramp	N/A		N/A		End value	8	Start value	8
Horizontal lines	N/A		N/A		Background line width	8	Foreground line width	8
Vertical lines	N/A		N/A		Background line width	8	Foreground line width	8
Diagonal lines	N/A		N/A		Vertical spacing	8	Horizontal spacing	8

Table 3-10. Descriptions and Bit Assignments for Parameters 1-4 (continued)

Pattern	Byte 6 (Parameter 4)		Byte 5 (Parameter 3)		Byte 4 (Parameter 2)		Byte 3 (Parameter 1)	
	Description	Bits	Description	Bits	Description	Bits	Description	Bits
Grid lines	Vertical background line width	8	Vertical foreground line width	8	Horizontal background line width	8	Horizontal foreground line width	8
Checkerboard	Number of vertical checkers	3	Number of vertical checkers	8	Number of horizontal checkers	3	Number of horizontal checkers	8
Color bars	N/A		N/A		N/A		N/A	

This command is used in conjunction with the *Write Input Source Select* command. This command specifies which test pattern displays when the *Write Input Source Select* command selects test pattern generator as the image source. The settings for this command are retained until changed using this command. These settings automatically apply each time the test pattern generator is selected.

Batch files are created and stored in flash, and recall the settings for predefined test patterns.

Test patterns are created at the resolution of the display (DMD), are modified by the *Write Image Crop* command, and display at the DMD resolution.

Test patterns display at the default frame rate 60 Hz.

The *Test Pattern Border Selection* creates a white border, a single pixel wide and tall, around the specified test pattern.

The user must review the notes for the *Write Input Source Select* command to understand the concept of source-associated commands. This concept determines when source-associated commands are executed by the system. This command is a source-associated command.

When a foreground or background color is not used, the bit values are ignored (see [Table 3-9](#)). If both foreground and background color are not used, or when a parameter byte (bytes 3 thru 6) is not used, the byte must not be sent. [Table 3-11](#) shows the number of bytes required, based on the specified pattern.

Table 3-11. Number of Bytes Required based on Pattern Selection

Specified Pattern	Number of Bytes Required
Solid field	2
Fixed step horizontal ramp	4
Fixed step vertical ramp	4
Horizontal lines	4
Vertical lines	4
Diagonal lines	4
Grid lines	6
Checkerboard	6
Color bars	1

As noted in [Table 3-9](#), the color for the solid field pattern is specified using the foreground color. An example of a solid field pattern is shown in [Figure 3-1](#).

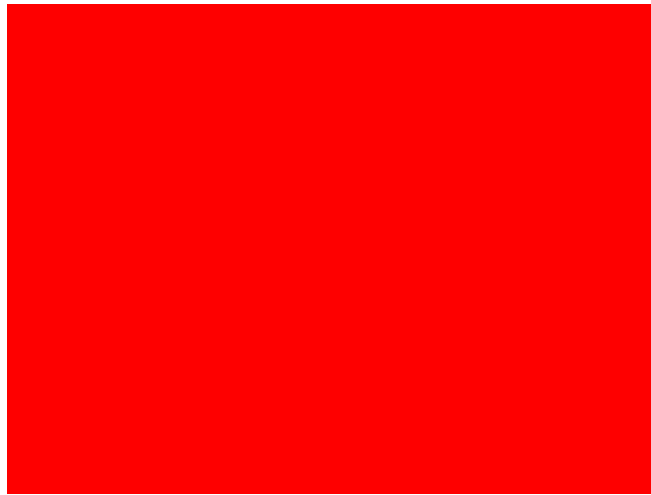


Figure 3-1. Example of Solid Field Test Pattern (Red)

As noted in [Table 3-9](#), the color for the fixed step horizontal ramp pattern is specified using the foreground color. As noted in [Table 3-10](#), the user specifies the start value and the stop value for the ramp. For this pattern, the system automatically determines the step size based on the start and stop values and the size of the display (DMD). The minimum start value is 0, the maximum stop value is 255, and the start value must always be smaller than the stop value. For example, if the start value = 0, the stop value = 255, and the DMD resolution is 1280 wide, the step size is 5 (1280 pixels / 256 values = 5). Thus, every gray shade value from 0 to 255 has a step size of 5 pixels (such that each step has 5 columns of pixels with the same gray scale value). The gray scale value always increments by 1 for each step between the start and stop values. An example of a fixed step horizontal ramp pattern is shown in [Figure 3-2](#).



Figure 3-2. Example of Fixed Step Horizontal Ramp Test Pattern

As noted in [Table 3-9](#), the color for the fixed step vertical ramp pattern is specified using the foreground color. As noted in [Table 3-10](#), the user specifies the start value and the stop value for the ramp. For this pattern, the system automatically determines the step size based on the start and stop values and the size of the display (DMD). The minimum start value = 0, the maximum stop value = 255, and the start value must always be smaller than the stop value. For example, if the start value = 0, the stop value = 255, and the DMD resolution is 768 tall, then the step size is 3 (768 pixels / 256 values = 3). Thus, every value from 0 to 255 has a step size of 3 pixels (such that each step has 3 rows of pixels with the same gray scale value). The gray scale value always increments by 1 for each step between the start and stop values. An example of a fixed step vertical ramp pattern is shown in [Figure 3-3](#).



Figure 3-3. Example of Fixed Step Vertical Ramp Test Pattern

As noted in [Table 3-9](#), the colors for the horizontal lines pattern are specified using both the foreground and background colors. The foreground color is used for the horizontal lines, and the background color is used for the space between the lines. As noted in [Table 3-10](#), the user specifies the foreground line width, as well as the background line width. The user must determine the line spacing for each resolution display. For example, if the foreground line width = 1, and the background line width = 9, there is a single pixel horizontal line on every tenth line. An example of a horizontal lines pattern is shown in [Figure 3-4](#).

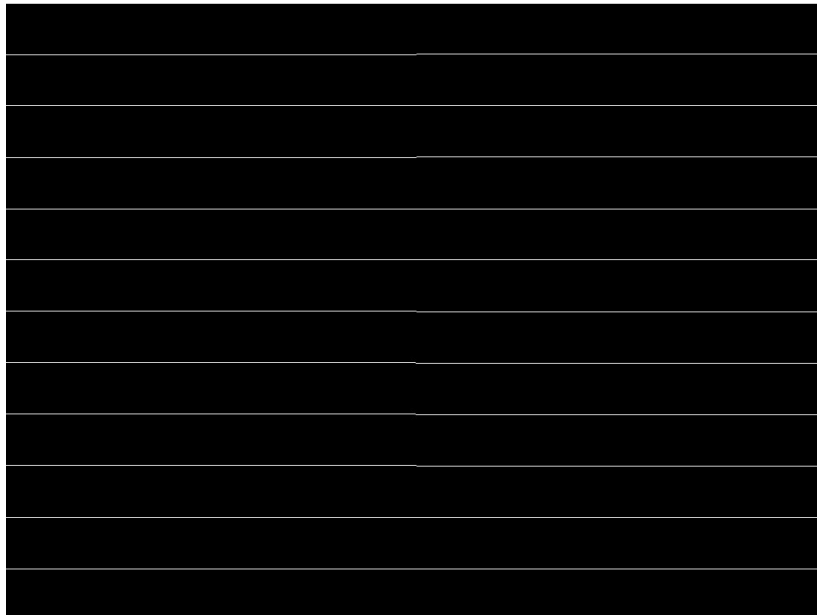


Figure 3-4. Example of Horizontal Lines Test Pattern

As noted in [Table 3-9](#), the colors for the vertical lines pattern are specified using both the foreground and background colors. The foreground color is used for the vertical lines, and the background color is used for the space between the lines. As noted in [Table 3-10](#), the user specifies the foreground line width, as well as the background line width. The user must determine the line spacing for each resolution display. For example, if the foreground line width = 1, and the background line width = 9, there is a single pixel vertical line on every tenth line. An example of a vertical lines pattern is shown in [Figure 3-5](#).

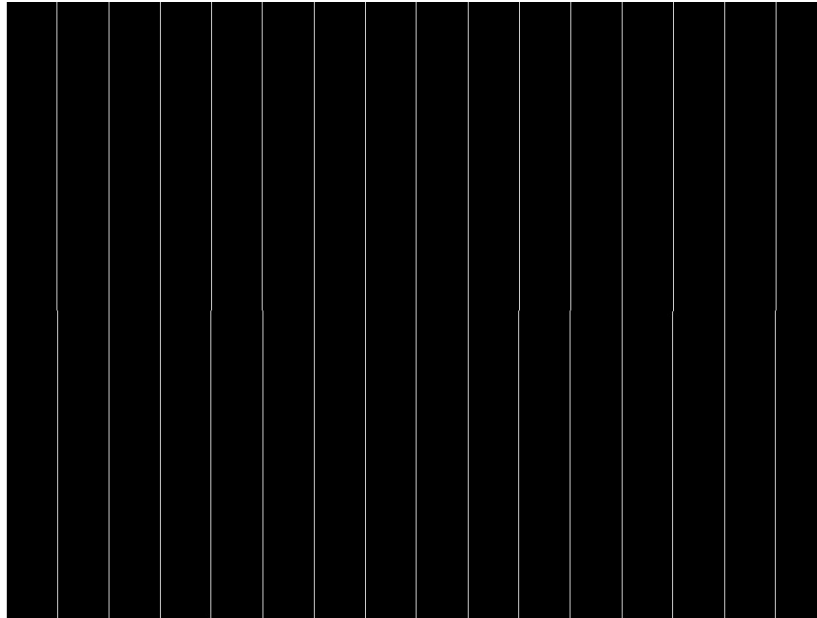


Figure 3-5. Example of Vertical Lines Test Pattern

As noted in [Table 3-9](#), the colors for the diagonal lines pattern are specified using both the foreground and background colors. The foreground color is used for the diagonal lines, and the background color is used for the space between the lines. As noted in [Table 3-10](#), the user specifies the horizontal and vertical line spacing. The line width is always one pixel. The user determines the line spacing for each resolution display. Both horizontal and vertical line spacing must use the same value, and are limited to values of 3, 7, 15, 31, 63, 127, and 255. Invalid values result in a communication error (invalid command parameter). An example of a diagonal lines pattern is shown in [Figure 3-6](#).

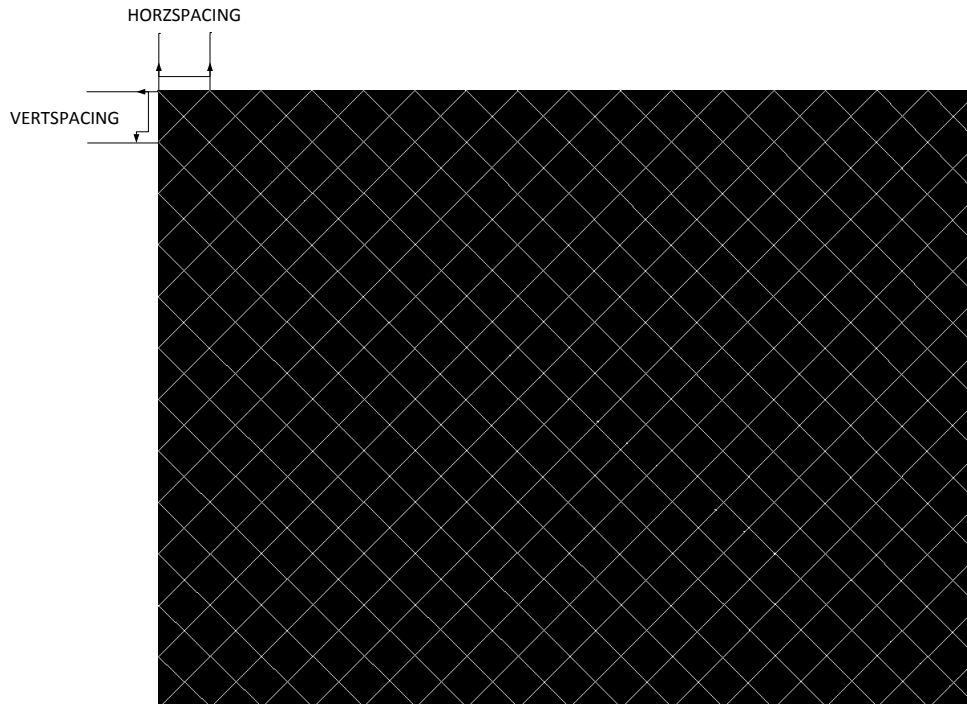


Figure 3-6. Example of Diagonal Lines Test Pattern

As noted in [Table 3-9](#), the colors for the grid lines pattern are specified using both the foreground and background colors. The foreground color is used for the grid lines, and the background color is used for the space between the lines. As noted in [Table 3-10](#), the user specifies the horizontal foreground and background line width, as well as the vertical foreground and background line width. The user determines the line spacing for each resolution display. For example, if the horizontal foreground line width = 1, and background line width = 9, there is a single pixel horizontal line on every tenth line. If the vertical foreground line width = 1, and background line width = 9, there is a single pixel vertical line on every tenth line. An example of a grid lines pattern is shown in [Figure 3-7](#).

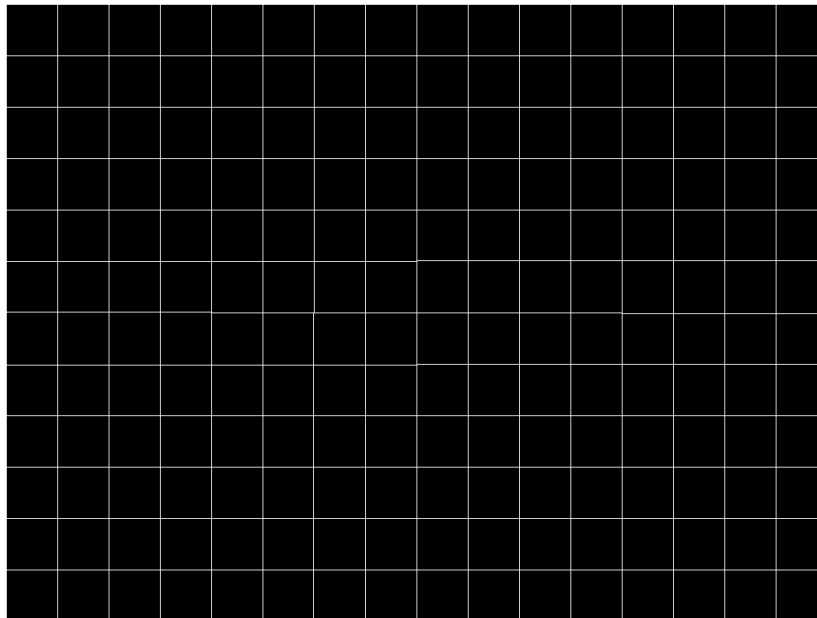


Figure 3-7. Example of Grid Lines Test Pattern

As noted in [Table 3-9](#), the colors for the checkerboard pattern are specified using both the foreground and background colors. The foreground color is used for one of the checkers, and the background color is used for the alternating checker. As noted in [Table 3-10](#), the user specifies the number of horizontal checkers and the number of vertical checkers. For this pattern, the system automatically determines the checker size in each direction based on the number of checkers and the size of the display (DMD). For example, if the number of horizontal checkers = 4, the number of vertical checkers = 4, and the DMD resolution is 1280x720, the size of the horizontal checkers is 320 pixels, and the size of the vertical checkers is 180 pixels (1280 pixels / 4 checkers = 320 pixels; 720 pixels / 4 checkers = 180 pixels). An example of a checkerboard pattern (16 checkers by 12 checkers) is shown in [Figure 3-8](#).

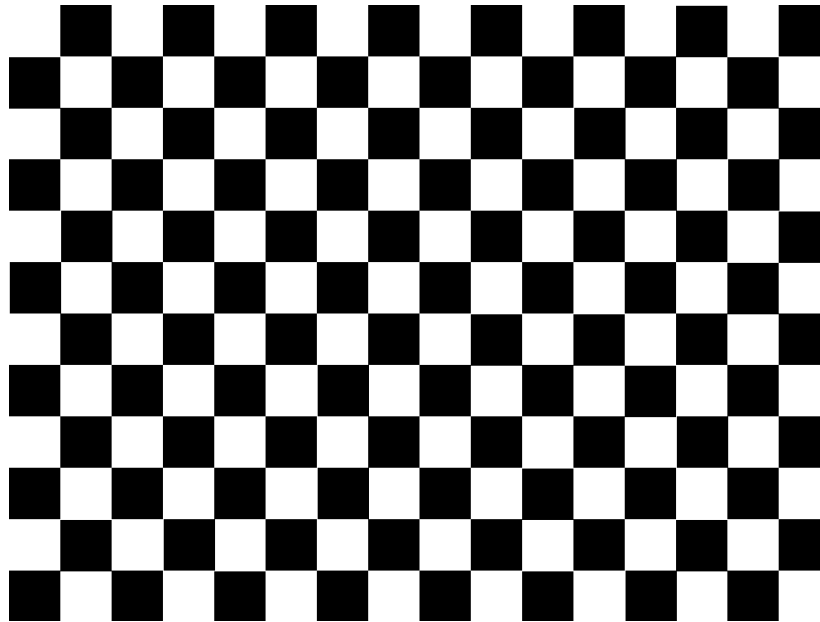


Figure 3-8. Example of Checkerboard Test Pattern

As noted in [Table 3-9](#) and [Table 3-10](#), there is no user programmability associated the color bars test pattern. This pattern is made up of eight vertical color bars: white, yellow, cyan, green, magenta, red, blue, and black. For this pattern, the system automatically determines the width for each color bar based on the size of the display (DMD). An example of the color bars pattern is shown in [Figure 3-9](#).

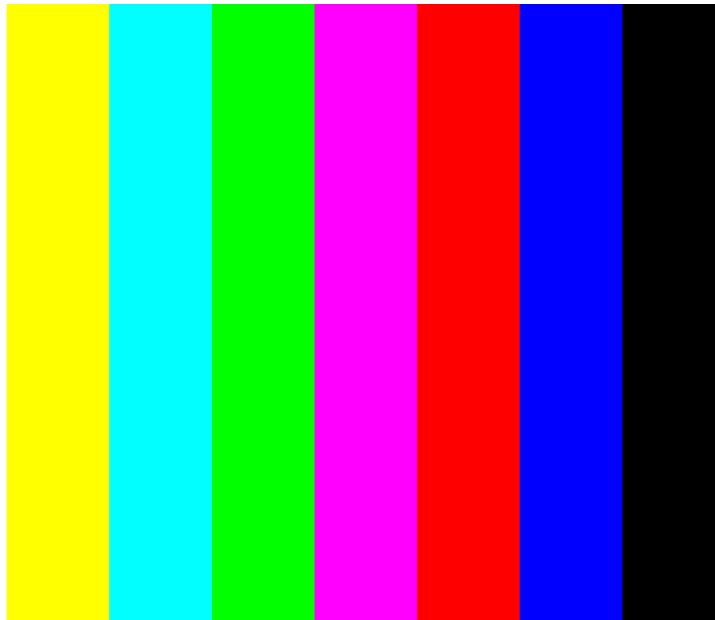


Figure 3-9. Example of Color Bars Test Pattern

3.1.6 Read Test Pattern Select (0Ch)

This command reads the state of the test pattern selected for the display module.

3.1.6.1 Read Parameters

This command has no read parameters.

3.1.6.2 Return Parameters

[Table 3-12](#) describes the return parameters.

Table 3-12. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Table 3-13. Parameter Bytes

Parameter Bytes	Description
Byte 1	TPG pattern select
Byte 2	Foreground and background color (see Table 3-9)
Byte 3	Parameter 1 (see Table 3-10)
Byte 4	Parameter 2 (see Table 3-10)
Byte 5	Parameter 3 (see Table 3-10)
Byte 6	Parameter 4 (see Table 3-10)

This command always returns six bytes, since the host does not know how many bytes are valid until the pattern is selected. All unnecessary bytes (see [Table 3-11](#)) are set to 0.

If a batch file is used to specify the parameters of the test pattern generator, those parameters are returned by this command.

3.1.7 Write Splash Screen Select (0Dh)

This command selects a stored splash screen to be displayed on the display module.

3.1.7.1 Write Parameters

[Table 3-14](#) describes the write parameters.

Table 3-14. Write Parameters

Parameter Bytes	Description
Byte 1	Splash screen reference number (integer)

This command is used in conjunction with the *Write Input Source Select* and the *Write Splash Screen Execute* commands, and specifies which splash screen is selected by the *Input Source Select* command. The settings for this command are retained until changed using this command.

The steps required to display a splash screen are:

1. Select the desired splash screen (using this command)
2. Change the input source to splash screen (using *Write Input Source Select*)
3. Start the splash screen retrieval process (using *Write Splash Screen Execute*).

The splash screen is read from flash and sent down the processing path of the controller once, to be stored in memory for display at the end of the processing path. As such, all image processing settings (such as image crop, image orientation, display size, splash screen select, splash screen as input source, and so forth) must be set by the user before executing the *Write Splash Screen Execute* command.

The user must review the notes for the *Write Input Source Select* command to understand the concept of source-associated commands. This concept determines when source-associated commands are executed by the system. This command is a source-associated command.

The availability of the splash screen is limited by the available space in flash memory. All splash screens must be landscape oriented.

For single controller applications which support DMD resolutions up to 1280 x 720, the minimum splash image size allowed for flash storage is 427 x 240, with the maximum being the resolution of the DMD. Typical splash image sizes for flash are 427 x 240 and 640 x 360. The full resolution size is typically used to support an “Optical Test” splash screen.

The user must specify how the splash image is displayed on the screen. A key command for this is *Write Image Crop*.

When this command is received while splash screen is the active source, other than storing the specified splash screen value, the only action taken by the controller software is to obtain the header information from the selected splash screen and store this in internal memory. When the *Write Splash Screen Execute* command is received, the controller software uses this stored information to set up the processing path prior to pulling the splash data from flash.

3.1.8 Read Splash Screen Select (0Eh)

This command reads the state of the *Splash Screen Select* command of the display module.

3.1.8.1 Read Parameters

This command has no read parameters.

3.1.8.2 Return Parameters

[Table 3-15](#) describes the return parameters.

Table 3-15. Return Parameters

Parameter Bytes	Description
Byte 1	Splash screen selected (integer)

3.1.9 Read Splash Screen Header (0Fh)

This command reads the splash screen header information for the selected splash screen of the display module.

3.1.9.1 Read Parameters

The read parameter specifies the splash screen for which the header parameters are returned. If a splash screen value is provided for an unavailable splash screen, this is considered an error (invalid command parameter value – communication status).

Table 3-16. Read Parameters

Parameter Bytes	Description
Byte 1	Splash screen reference number (integer)

3.1.9.2 Return Parameters

Table 3-17 describes the return parameters.

Table 3-17. Return Parameters

Parameter Bytes	Description
Byte 1	Splash image width in pixels (LSByte)
Byte 2	Splash image width in pixels (MSByte)
Byte 3	Splash image height in pixels (LSByte)
Byte 4	Splash image height in pixels (MSByte)
Byte 5	Splash image size in bytes (LSByte)
Byte 6	Splash image size in bytes
Byte 7	Splash image size in bytes
Byte 8	Splash image size in bytes (MSByte)
Byte 9	Pixel format
Byte 10	Compression type
Byte 11	Color order
Byte 12	Chroma order
Byte 13	Byte order

Parameter definitions are referenced in Table 3-18.

Table 3-18. Splash Screen Header Definitions

Parameter	Values
Pixel format	0h = 24-bit RGB unpacked (not used) 1h = 24-bit RGB packed (not used) 2h = 16-bit RGB 5-6-5 3h = 16-bit YCbCr 4:2:2
Compression type	0h = Uncompressed 1h = RGB RLE compressed 2h = User-defined (not used) 3h = YUV RLE compressed
Color order	0h = 00RRGGBB 1h = 00GGRRBB
Chroma order	0h = Cr is first pixel 1h = Cb is first pixel
Byte order	0h = Little endian 1h = Big endian

3.1.10 Write Display Image Orientation (14h)

This command specifies the image orientation of the displayed image.

3.1.10.1 Write Parameters

Table 3-19 describes the write parameters.

Table 3-19. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
Parameter Byte							
b(7:3)		Reserved					
b(2)		Short axis image flip: • 0: Image not flipped. • 1: Image flipped.					
b(1)		Long axis image flip: • 0: Image not flipped. • 1: Image flipped.					
b(0)		Reserved					



Figure 3-10. Long-Axis Flip

Figure 3-11 shows the short-axis flip.



Figure 3-11. Short-Axis Flip

3.1.11 Read Display Image Orientation (15h)

This command reads the state of the displayed image orientation function for the display module.

3.1.11.1 Read Parameters

This command has no read parameters.

3.1.11.2 Return Parameters

Table 3-20 describes the return parameters.

Table 3-20. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:3)		Reserved					

b(2)	Short-axis image flip: <ul style="list-style-type: none"> • 0: Image not flipped. • 1: Image flipped.
b(1)	Long-axis image flip: <ul style="list-style-type: none"> • 0: Image not flipped. • 1: Image flipped.
b(0)	Reserved

3.1.12 Write Display Image Curtain (16h)

This command controls the display image curtain for the display module.

3.1.12.1 Write Parameters

Table 3-21 shows the write parameters.

Table 3-21. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:4)	Reserved						
b(3:1)	Select curtain color: <ul style="list-style-type: none"> • 0h: Black • 1h: Red • 2h: Green • 3h: Blue • 4h: Cyan • 5h: Magenta • 6h: Yellow • 7h: White 						
b(0)	Curtain enable: <ul style="list-style-type: none"> • 0: Curtain disabled • 1: Curtain enabled 						

The image curtain fills the entire display with a user-specified color. The curtain color specified by this command is separate from the border color defined in the *Write Border Color* command, though both are displayed using the curtain capability.

3.1.13 Read Display Image Curtain (17h)

This command reads the state of the image curtain control function for the display module.

3.1.13.1 Read Parameters

This command has no read parameters.

3.1.13.2 Return Parameters

Table 3-22 describes the return parameters.

Table 3-22. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:4)	Reserved						

b(3:1)	Select curtain color: <ul style="list-style-type: none"> • 0h: Black • 1h: Red • 2h: Green • 3h: Blue • 4h: Cyan • 5h: Magenta • 6h: Yellow • 7h: White
b(0)	Curtain enable: <ul style="list-style-type: none"> • 0: Curtain disabled • 1: Curtain enabled

3.1.14 Write Image Freeze (1Ah)

This command enables or disables the image freeze function for the display module.

3.1.14.1 Write Parameters

Table 3-23 describes the write parameters.

Table 3-23. Write Parameters

MSB	Byte 1						LSB
	b7	b6	b5	b4	b3	b2	
b(7:1)	Reserved						
b(0)	Image freeze: <ul style="list-style-type: none"> • 0: Image freeze disabled • 1: Image freeze enabled 						

The image freeze capability has two main uses. The first use is to simply freeze the current image on the screen. The second use is to assist the user in reducing display artifacts during system changes. In this second case, the image is frozen, system changes are made, and the image is unfrozen when complete. In all cases, when the image is unfrozen, the display shows the most resent input image. Input data between the freeze point and the unfreeze point is lost.

The controller software does not freeze or unfreeze the image except when explicitly commanded by the *Write Image Freeze* command. The controller software may execute the *Write Image Freeze* when the software is making updates to the system on its own volition, and for any operation commanded via the I²C interface.

The user must review the notes for the *Write Input Source Select* command to understand the concept of source-associated commands. This concept determines when source-associated commands are executed by the system.

If the user chooses not to make use of image freeze, they must change the source before changing the image parameters, to minimize transition artifacts.

3.1.14.2 Use of Image Freeze to Reduce On-Screen Artifacts

Commands that take a long time to process, require a lot a data to be loaded from flash, or change the frame timing of the system may create on-screen artifacts. The *Write Image Freeze* command can try and minimize, if not eliminate, these artifacts. The process is:

1. Send a *Write Image Freeze* command to enable freeze.
2. Send commands with the potential to create image artifacts.
3. Embedded software executes *Write Image Freeze* command to disable freeze.

Because commands to the controller process serially, no special timing or delay is required between these commands. The number of commands placed between the freeze and unfreeze must be small, as it is not

desirable for the image to be frozen for a long period of time. A list of commands that may produce image artifacts is listed in [Table 3-24](#), which is not an all-inclusive list. The user is responsible for determining the correct use of the image freeze command.

Table 3-24. Partial List of Commands that May Benefit from the Use of Image Freeze

Command	Command OpCode	Notes
Write Input Source Select	05h	
Write Test Pattern Select	0Bh	If changed while this source is the active source
Write Splash Screen Select	0Dh	If changed while this source is the active source, embedded software disables image freeze after this command is executed
Write Look Select	22h	

[Table 3-25](#) and [Table 3-26](#) show a few examples of how to use the image freeze command.

Table 3-25. Splash Screen Example Using Image Freeze

Command	Notes
Write Display Image Curtain = enable	May want to apply curtain if already displaying an unwanted image (such as a broken source)
Write Image Freeze = freeze	
Write Image Crop, Write Display Image Orientation	Potential data processing commands that may be required for proper display of splash image. These must be set prior to write splash screen execute command to affect the splash screen image.
Write Splash Screen Select Write Input Source Select = splash	These must be set prior to write splash screen execute
Write Splash Screen Execute	Retrieves the desired splash screen image for display

The new splash image displays when the *Write Splash Screen Execute* command executes, regardless of the state of the *Write Image Freeze* command (due to the one time nature of the splash image). Embedded software disables image freeze after this command is executed.

Table 3-26. Test Pattern Generator Example Using Image Freeze

Command	Notes
Write Image Freeze = freeze	
Write Image Crop, Write Display Image Orientation, Write Test Pattern Select	Potential data processing commands that may be required for proper display of test pattern image. These must be set before the Write Input Source Select command.
Write Input Source Select = test pattern generator	
Write Image Freeze = unfreeze	

3.1.15 Read Image Freeze (1Bh)

This command reads the state of the image freeze function for the display module.

3.1.15.1 Read Parameters

This command has no read parameters.

3.1.15.2 Return Parameters

[Table 3-27](#) describes the return parameters.

Table 3-27. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:1)	Reserved						

b(0)	Image freeze: <ul style="list-style-type: none"> • 0: Image freeze disabled • 1: Image freeze enabled
------	---

3.1.16 Write 3-D Control (20h)

This command controls 3-D functionality for the display module.

3.1.16.1 Write Parameters

Table 3-28 describes the write parameters.

Table 3-28. Write Parameters

<i>msb</i>	<i>Byte 1</i>						<i>lsb</i>
b7	b6	b5	b4	b3	b2	b1	b0
b(7)	<i>Reserved</i>						
b(6)	Polarity of 3-D Reference (External Only) <ul style="list-style-type: none"> • 0h: Correct – No Inversion Required • 1h: Incorrect – Inversion Required 						
b(5)	Frame Dominance <ul style="list-style-type: none"> • 0h: Left Dom. (Data sent left eye first) • 1h: Right Dom. (Data sent right eye first) 						
b(4:2)	<i>Reserved</i>						
b(1)	Source of 3-D Reference <ul style="list-style-type: none"> • 0h: Internal Reference Generator NOT supported • 1h: External (SLT_3DR Pin) 						
b(0)	<i>Reserved</i>						
Default: 00h							

When appropriate, the system will automatically enable 3-D operation based on source frame rate and whether 3-D sequences are loaded in flash. The 3-D parameters specified by this command will take effect following the next VSYNC.

3-D image data must always be sent frame sequential. Syncs and blanking are sent between every eye frame at frame rates greater than approximately 94Hz. DLPC3437 does not support frame rate multiplication.

Internal reference generator is not supported on dual controller DLPC3437.

The 3-D Reference is used to specify whether a frame of data contains left eye data or right eye data. This 3-D reference can be provided to the display by an external hardware signal. Table 3-29 shows which 3-D Reference source can be used with which image data port.

When using the external hardware signal as the reference, it must be provided for every frame of data. If the external 3-D Reference is misaligned with the data, it can be corrected using the *Polarity of 3-D Reference (External Only)* parameter. As noted, the *Polarity of 3-D Reference* parameter is only applicable when the External Signal is selected as the 3-D Reference source.

Table 3-29. 3-D Reference Source Applicability for Display Data Ports

Display Data Port	3-D Reference Source	Applicable	Notes
Parallel	External Hardware Signal	Yes	Recommended
Parallel	Internal Reference Generator	No	

The [Write 3-D Reference \(30h\)](#) command should be use with this selection.

For frame sequential 3-D, Frame Dominance determines which eye frames in the data stream go together to create a single 3-D image. Left Dominance indicates that the first eye frame of a pair is Left and the second eye

frame is Right. Right Dominance indicates that the first eye frame of a pair is Right and the second eye frame is Left. Frame dominance is essential for correct operation of display histograms, which span both eye frames of a single image. When the image is frozen, proper Frame Dominance ensures the correct display of two eye frames together. Frame dominance control must not be used to attempt correction for misalignment of the 3-D reference signal to the image data.

3.1.17 Read 3-D Control (21h)

This command reads the state of the 3-D control function for the display module.

3.1.17.1 Read Parameters

This command has no read parameters.

3.1.17.2 Return Parameters

Table 3-30 describes the return parameters.

Table 3-30. Return Parameters

msb	Byte 1						lsb
	b7	b6	b5	b4	b3	b2	
b(7)	Reserved						
b(6)	Polarity of 3-D Reference (External Only) <ul style="list-style-type: none"> 0h: Correct – No Inversion Required 1h: Incorrect – Inversion Required 						
b(5)	Frame Dominance <ul style="list-style-type: none"> 0h: Left Dom. (Data sent left eye first) 1h: Right Dom. (Data sent right eye first) 						
b(4:2)	Reserved						
b(1)	Source of 3-D Reference <ul style="list-style-type: none"> 0h: Internal Reference Generator NOT supported 1h: External (SLT_3DR Pin) 						
b(0)	3-D Mode Control <ul style="list-style-type: none"> 0h: 2-D Operation 1h: 3-D Operation 						

The system automatically enables and disables 3-D operation. Bit(0) will indicate the state of 2-D/3-D operation.

3.1.18 Write Look Select (22h)

This command specifies the Look for the image on the display module.

3.1.18.1 Write Parameters

Table 3-31 describes the write parameters.

Table 3-31. Write Parameters

MSB	Byte 1						LSB
	b7	b6	b5	b4	b3	b2	
b(7:0)	Look number						

In this product, a Look typically specifies a target white point. The number of Looks available may be limited by the available space in flash memory.

This command allows the host to select a Look (target white point) from a number of Looks stored in flash.

Looks are specified in this byte by an enumerated value (such as 0,1,2,3). There must always be at least one Look, with an enumerated value of 0.

There are two other items that the host must specify when determining a white point. These are:

- A desired degamma curve, achieved by selecting the appropriate degamma/CMT, which has the desired degamma curve and correct bit weights for the sequence selected.
- The desired color points, achieved by selecting the appropriate CCA parameters using the CCA select command.

3.1.19 Read Look Select (23h)

This command reads the state of the Look select command for the display module.

3.1.19.1 Read Parameters

This command has no read parameters.

3.1.19.2 Return Parameters

Table 3-32 describes the return parameters.

Table 3-32. Return Parameters

Parameter Bytes	Description
Byte 1	Look Number. See the following notes.
Byte 2	Sequence number. See the following notes.
Byte 3	Current Sequence Frame Rate (LSB). See the following notes.
Byte 4	Current Sequence Frame Rate.
Byte 5	Current Sequence Frame Rate.
Byte 6	Current Sequence Frame Rate (MSB).

MSB	Byte 1 and 2						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Table 3-33. Byte 1 Read Look Select Register Field Descriptions

Bit	Type	Description
7-0	R	Look number

Table 3-34. Byte 2 Read Look Select Register Field Descriptions

Bit	Type	Description
7-0	R	Sequence number

Looks are specified by an enumerated value (such as 0, 1, 2, 3).

Sequences are specified by an enumerated value (such as 0, 1, 2, 3). The value returned by this command is the sequence currently selected by the Look algorithm when the command is received.

The current sequence frame rate is returned as a count specified in units of 66.67ns. This is based on the internal 15-MHz clock used to time between input frame syncs. The frame rate is specified in this manner to enable the software to make quick and simple comparisons to the frame count. Either the controller software or the user can make sequence frame rate and duty cycle selections.

3.1.20 Read Sequence Header Attributes (26h)

This command reads sequence header information for the active sequence of the display module.

3.1.20.1 Read Parameters

This command has no read parameters.

3.1.20.2 Return Parameters

Table 3-35 describes the return parameters.

Table 3-35. Return Parameters

Parameter Bytes	Description
Byte 1	Red duty cycle (LSByte) (Look structure)
Byte 2	Red duty cycle (MSByte) (Look structure)
Byte 3	Green duty cycle (LSByte)(Look structure)
Byte 4	Green duty cycle (MSByte) (Look structure)
Byte 5	Blue duty cycle (LSByte) (Look structure)
Byte 6	Blue duty cycle (MSByte) (Look structure)
Byte 7	Maximum frame count (LSByte) (Look structure)
Byte 8	Maximum frame count (Look structure)
Byte 9	Maximum frame count (Look structure)
Byte 10	Maximum frame count (MSByte) (Look structure)
Byte 11	Minimum frame count (LSByte) (Look structure)
Byte 12	Minimum frame count (Look structure)
Byte 13	Minimum frame count (Look structure)
Byte 14	Minimum frame count (MSByte) (Look structure)
Byte 15	Maximum number of sequence vectors (Look structure)
Byte 16	Red duty cycle (LSByte) (Sequence structure)
Byte 17	Red duty cycle (MSByte) (Sequence structure)
Byte 18	Green duty cycle (LSByte) (Sequence structure)
Byte 19	Green duty cycle (MSByte) (Sequence structure)
Byte 20	Blue duty cycle (LSByte) (Sequence structure)
Byte 21	Blue duty cycle (MSByte) (Sequence structure)
Byte 22	Maximum frame count (LSByte) (Sequence structure)
Byte 23	Maximum frame count (Sequence structure)
Byte 24	Maximum frame count (Sequence structure)
Byte 25	Maximum frame count (MSByte) (Sequence structure)
Byte 26	Minimum frame count (LSByte) (Sequence structure)
Byte 27	Minimum frame count (Sequence structure)
Byte 28	Minimum frame count (Sequence structure)
Byte 29	Minimum frame count (MSByte) (Sequence structure)
Byte 30	Maximum number of sequence vectors (Sequence structure)

The sequence header data is stored in two separate flash data structures (the Look structure and the sequence structure), and the values from each must match.

The bit weight and bit order for the duty cycle data is shown in [Table 3-36](#).

Table 3-36. Bit Weight and Bit Order for Duty Cycle Data

MSB	Byte 2						LSB	MSB	Byte 1						LSB
b15 2 ⁷	b14 2 ⁶	b13 2 ⁵	b12 2 ⁴	b11 2 ³	b10 2 ²	b9 2 ¹	b8 2 ⁰	b7 2 ⁻¹	b6 2 ⁻²	b5 2 ⁻³	b4 2 ⁻⁴	b3 2 ⁻⁵	b2 2 ⁻⁶	b1 2 ⁻⁷	b0 2 ⁻⁸

The duty cycle data is specified as each color percent of the frame time. The sum of the three duty cycles must add up to 100. An example possibility is, R = 30.5 = 1E80h, G = 50 = 3200h, and B = 19.5 = 1380h)

The sequence maximum and minimum frame counts are specified in units of 66.67ns. This is based on the internal 15-MHz clock used to time between input frame syncs. The frame rate is specified in this manner to enable the software to make quick and simple comparisons to the frame count.

The maximum number of sequence vectors byte is defined in [Table 3-37](#).

Table 3-37. Maximum Number of Sequence Vectors

MSB	Byte 15 and 30						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:4)	Reserved						
b(3:0)	Maximum number of sequence vectors						

3.1.21 Write Degamma/CMT Select (27h)

This command is used to select a specific Degamma/CMT LUT for the display module.

3.1.21.1 Write Parameters

[Table 3-38](#) describes the write parameters.

Table 3-38. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:0)	Degamma/CMT LUT index number (0-255)						

3.1.22 Read Degamma/CMT Select (28h)

This command is used to read the Degamma/CMT LUT index for the display module.

3.1.22.1 Read Parameters

This command has no read parameters.

3.1.22.2 Return Parameters

[Table 3-39](#) describes the return parameters.

Table 3-39. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:0)	Degamma/CMT LUT index number						

3.1.23 Write CCA Select (29h)

This command specifies which set of CCA (color coordinate adjustment) parameters to use.

3.1.23.1 Write Parameters

[Table 3-40](#) describes the write parameters.

Table 3-40. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:0)	CCA Parameter Set Index						

One or more CCA parameter sets may be included in a firmware image. Each CCA parameter set is intended to specify a target color point in the system. This command enables selection from multiple CCA sets by an enumerated value (such as 0,1,2,3). A CCA parameter set must exist for it to be selected. This command may be used in conjunction with *Write Color Coordinate Adjustment Control*.

3.1.24 Read CCA Select (2Ah)

This command reads the state of the CCA Parameter Set Index.

3.1.24.1 Read Parameters

This command has no read parameters.

3.1.24.2 Return Parameters

Table 3-41 describes the return parameters.

Table 3-41. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:0)	CCA Parameter Set Index						

3.1.25 Read DMD Sequencer Sync Mode (2Ch)

This command reads the state of the DMD sequencer sync mode function of the display module.

3.1.25.1 Read Parameters

This command has no read parameters.

3.1.25.2 Return Parameters

Table 3-42 describes the return parameters.

Table 3-42. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:2)	Reserved						
b(1)	System auto-sync setting: <ul style="list-style-type: none"> 0h: Lock to external VSYNC (auto-sync) 1h: Lock to internal VSYNC (auto-sync) 						
b(0)	DMD sequencer sync mode: <ul style="list-style-type: none"> 0h: Auto-sync 1h: Force lock to internal VSYNC 						

The DMD sequencer sync mode response indicates the setting specified by the *Write DMD Mode* command.

System auto-sync setting response is only valid when the DMD sequencer sync mode is set to auto-sync (otherwise set to 0). The lock to the external VSYNC (auto-sync) option indicates that the system is using the externally provided VSYNC to drive the display module. The lock to the internal VSYNC option indicates that the system is using the internal VSYNC generator to drive the display module.

3.1.26 Write Execute Flash Batch File (2Dh)

This command executes a flash batch file for the display module.

3.1.26.1 Write Parameters

Table 3-43 describes the write parameters.

Table 3-43. Write Parameters

Parameter Bytes	Description
Byte 1	Batch file number

This command executes a batch file stored in the flash of the display module. Any system write command that can be sent by itself can be grouped together with other system commands or write parameters into a flash batch file, with the exception of all read commands.

The flash batch file numbers specified in this byte are enumerated values (such as 0,1,2,3). Flash batch file 0 is a special auto-initialization batch file that runs automatically by the DLPC34xx software immediately after system initialization is complete. The flash batch file 0 is typically not called using the *Write Execute Batch File* command (although the system allows it). This special flash batch file specifies the source to be used (such as splash screen or data port) once the system initializes.

Embedding flash batch file calls within a flash batch file is not allowed (for example, calling another batch file from within a batch file is not allowed). To execute two batch files back to back, use back to back execute batch file commands.

The system allows adding an execution delay between commands within a flash batch file, which is done using the [Write Flash Batch File Delay \(DBh\)](#) command.

The order of command execution for commands within a flash batch file is the same as if the commands are received over the I²C port.

3.1.27 Write Input Image Size (2Eh)

This command specifies the active data size of the external input image to the display module.

3.1.27.1 Write Parameters

[Table 3-44](#) describes the write parameters.

Table 3-44. Write Parameters

Parameter Bytes	Description
Byte 1	Pixels per line (LSByte)
Byte 2	Pixels per line (MSByte)
Byte 3	Lines per frame (LSByte)
Byte 4	Lines per frame (MSByte)

This command is used in conjunction with the [Write Input Source Select \(05h\)](#) command. When external video is selected as the image source, this command specifies the active data size of the input image to the system for all external video interfaces. The settings for this command are retained until changed with subsequent Write Input Image Size commands. The settings for this command are retained until changed using this command. These settings automatically apply each time the external video port is selected.

The parameter values are 1-based (for example, a value of 1280 pixels specifies 1280 pixels per line).

The user must review the notes for the [Write Input Source Select \(05h\)](#) command to understand the concept of source-associated commands. This concept determines when source-associated commands are executed by the system. This command is a source-associated command.

The maximum and minimum input values are shown in [Table 3-45](#). The controller flags values outside of these ranges as an error (invalid write parameter), and does not execute the command.

Table 3-45. Input Image Size

Parameter	Minimum Value	Maximum Value
Input source active pixels per line	320	1280
Input source active lines per frame	200	800

3.1.28 Read Input Image Size (2Fh)

This command reads the specified data size of the external input image to the display module.

3.1.28.1 Read Parameters

This command has no read parameters.

3.1.28.2 Return Parameters

Table 3-46 describes the return parameters.

Table 3-46. Return Parameters

Parameter Bytes	Description
Byte 1	Pixels per line (LSByte)
Byte 2	Pixels per line (MSByte)
Byte 3	Lines per frame (LSByte)
Byte 4	Lines per frame (MSByte)

The parameter values are 1-based (for example, a value of 1280 pixels specifies 1280 pixels per line).

This command returns the value specified by the *Write External Input Image Size* command.

3.1.29 Write 3-D Reference (30h)

This command selects a 3-D reference for the display module.

3.1.29.1 Write Parameters

Table 3-47 describes the write parameters.

Table 3-47. Write Parameters

<i>msb</i>	<i>Byte 1</i>						<i>lsb</i>
b7	b6	b5	b4	b3	b2	b1	b0
b(7:1)	<i>Reserved</i>						
b(0)	3-D Reference						
	<ul style="list-style-type: none"> • 0h: Next Frame Left • 1h: Next Frame Right 						
Default: 00h							

The 3-D Reference is used to specify whether a frame of data contains left eye data or right eye data. The 3-D reference is provided to the display as either a GPIO hardware signal or through sending this command. The selection is made using the Write 3-D Control command. When using this command as the reference, it is recommended that the command be sent every frame, or at the minimum, at the start of each eye pair. For example, the command can be sent before each left eye frame. At a minimum, it must be sent once at the start of 3-D operation. If the 3-D Reference is misaligned with the data, it can be corrected using this command or by using the *Polarity of 3-D Reference* parameter in the [Write 3-D Control \(20h\)](#) command.

When the Write 3-D Reference command is received, its parameter value will be applied to the image data following the next VSYNC or Start of Frame command.

When this command is received, software must set up the internal 3-D reference generator. If the command is sent every frame, software keeps the output of the internal 3-D reference generator accurate.

3.1.30 Write Splash Screen Execute (35h)

This command starts the process of retrieving a splash screen from flash for display on the display module.

3.1.30.1 Write Parameters

This command has no write parameters.

This command is used in conjunction with the *Write Input Source Select* and the *Write Splash Screen Select* commands to start the process of retrieving a splash screen from flash for display.

The splash screen is read from flash and sent down the processing path of the controller to be stored in memory for display at the end of the processing path. All image processing settings, such as image crop, image orientation, display size, splash screen select, and splash screen as input source, must be set by the user

before executing this command. Any data path processing changed after the splash screen has been executed requires this command to be re-executed before the result is seen on the display. Thus, the splash screen retrieval process repeats each time this command is received. See the *Write Image Freeze* command for more information on hiding on-screen artifacts when selecting and retrieving a splash image.

The user must review the notes for the *Write Input Source Select* command to understand the concept of source-associated commands. This concept determines when source-associated commands are executed by the system. This command is a source-associated command; however, this command has no maintained state or history and has no settings to be stored or reused by the system.

When this command is processed the system automatically initializes the system color processing based on the splash header information prior to sending the splash image down the data path.

Note

If an incorrect image size is set for the splash image prior to splash screen execute, then the system requires power cycle to recover

3.1.31 Write Parallel Data Mask Control (37h)

This command is used to control the masking function for the external parallel port I/F of the DLPC343x.

3.1.31.1 Write Parameters

Table 3-48 describes the write parameters.

Table 3-48. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:2)	Reserved						
b(1)	Polarity Select for Data Mask Control: <ul style="list-style-type: none"> • 0: Unmasked = 0, Masked = 1 • 1: Unmasked = 1, Masked = 0 						
b(0)	Data Mask Enable: <ul style="list-style-type: none"> • 0: Mask Disable • 1: Mask Enable 						

When the parallel data mask is enabled, the DLPC343x input PDM_CVS_TE pin functions as a data mask control for the video data on the parallel port interface. Therefore, when this functionality is enabled and the mask control is active, input image frames will be ignored and the source image will not be propagated to the display. During image frames that are masked, the last unmasked image frame received will continue to be displayed. The mask control signal (PDM_CVS_TE) should only be updated during vertical blanking.

The Polarity Select specifies the active state for the mask control signal. The polarity should only be updated when the mask function is disabled (via this command).

3.1.32 Read Parallel Data Mask Control (38h)

This command reads the state of the parallel data mask control.

3.1.32.1 Read Parameters

This command has no read parameters.

3.1.32.2 Return Parameters

Table 3-49 describes the return parameters.

Table 3-49. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:2)	Reserved						
b(1)	Polarity Select for Data Mask Control: <ul style="list-style-type: none"> 0: Unmasked = 0, Masked = 1 1: Unmasked = 1, Masked = 0 						
b(0)	Data Mask Enable: <ul style="list-style-type: none"> 0: Mask Disabled 1: Mask Enabled 						

3.1.33 Write Mirrors Lock Command (39h)

This command writes the Mirrors Lock command to lock/unlock the DMD interface for Optical Alignment.

3.1.33.1 Write Parameters

[Table 3-50](#) describes the write parameters.

Table 3-50. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
7-2	Reserved						
1-0	Mirror Lock State Selection 0h = Reserved 1h = DMD Interface Lock 2h = DMD Interface Unlock 3h = DMD Interface Unlock, Delay 100ms, DMD Interface Lock						

This command is only used in factory to prevent DMD damage.

3.1.34 Read Mirrors Lock Command (3Ah)

This command reads the status of the Mirrors Lock.

3.1.34.1 Read Parameters

This command has no read parameters.

3.1.34.2 Return Parameters

[Table 3-51](#) describes the return parameters.

Table 3-51. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
7-2	Reserved						
1-0	Mirror Lock State Selection 0h = Reserved 1h = DMD Interface Lock 2h = DMD Interface Unlock 3h = DMD Interface Unlock, Delay 100ms, DMD Interface Lock						

3.1.35 Write FPD Link Pixel Map Mode (4Bh)

This command is used to configure the FPD link display bit rate and Map mode.

Write Parameters

Table 3-52 describes the write parameters.

Table 3-52. Write Parameters

Parameter Bytes		Description	
Byte 1		Reserved	
Byte 2		Reserved	
Byte 3		See below	

MSB	Byte 3						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7-4	R	Reserved
3-0	W	Pixel Map Mode 1h = Mode #1 2h = Mode #2 3h = Mode #3 4h = Mode #4 5h = Mode #5 6h = Mode #6 7h = Mode #7 8h = Mode #8

Input video data is encoded into the FPD data buses as indicated in the following tables.

Table 3-53. FPD LVDS Data Bus Encoding

	Mode 1	Mode 2	Mode 3	Mode 4	Mode 5	Mode 6
FPD Bus A - Data_A Channel						
FPD_A_DATA_A_6	Green_4	Green_2	Green_0	Green_4	Green_0	Green_2
FPD_A_DATA_A_5	Red_9	Red_7	Red_5	Red_9	Red_5	Red_7
FPD_A_DATA_A_4	Red_8	Red_6	Red_4	Red_8	Red_4	Red_6
FPD_A_DATA_A_3	Red_7	Red_5	Red_3	Red_7	Red_3	Red_5
FPD_A_DATA_A_2	Red_6	Red_4	Red_2	Red_6	Red_2	Red_4
FPD_A_DATA_A_1	Red_5	Red_3	Red_1	Red_5	Red_1	Red_3
FPD_A_DATA_A_0	Red_4	Red_2	Red_0	Red_4	Red_0	Red_2
FPD Bus A - Data_B Channel						
FPD_A_DATA_B_6	Blue_5	Blue_3	Blue_1	Blue_5	Blue_1	Blue_3
FPD_A_DATA_B_5	Blue_4	Blue_2	Blue_0	Blue_4	Blue_0	Blue_2
FPD_A_DATA_B_4	Green_9	Green_7	Green_5	Green_9	Green_5	Green_7
FPD_A_DATA_B_3	Green_8	Green_6	Green_4	Green_8	Green_4	Green_6
FPD_A_DATA_B_2	Green_7	Green_5	Green_3	Green_7	Green_3	Green_5
FPD_A_DATA_B_1	Green_6	Green_4	Green_2	Green_6	Green_2	Green_4
FPD_A_DATA_B_0	Green_5	Green_3	Green_1	Green_5	Green_1	Green_3

Table 3-53. FPD LVDS Data Bus Encoding (continued)

	Mode 1	Mode 2	Mode 3	Mode 4	Mode 5	Mode 6
FPD Bus A - Data_C Channel						
FPD_A_DATA_C_6	DEN	DEN	DEN	DEN	DEN	DEN
FPD_A_DATA_C_5	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC
FPD_A_DATA_C_4	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC
FPD_A_DATA_C_3	Blue_9	Blue_7	Blue_5	Blue_9	Blue_5	Blue_7
FPD_A_DATA_C_2	Blue_8	Blue_6	Blue_4	Blue_8	Blue_4	Blue_6
FPD_A_DATA_C_1	Blue_7	Blue_5	Blue_3	Blue_7	Blue_3	Blue_5
FPD_A_DATA_C_0	Blue_6	Blue_4	Blue_2	Blue_6	Blue_2	Blue_4
FPD Bus A - Data_D Channel						
FPD_A_DATA_D_6	Map to Field	Map to Field	Map to Field	Map to Field	Map to Field	Map to Field
FPD_A_DATA_D_5	Blue_3	Blue_9	Blue_7	not used	Blue_7	Blue_1
FPD_A_DATA_D_4	Blue_2	Blue_8	Blue_6	not used	Blue_6	Blue_0
FPD_A_DATA_D_3	Green_3	Green_9	Green_7	not used	Green_7	Green_1
FPD_A_DATA_D_2	Green_2	Green_8	Green_6	not used	Green_6	Green_0
FPD_A_DATA_D_1	Red_3	Red_9	Red_7	not used	Red_7	Red_1
FPD_A_DATA_D_0	Red_2	Red_8	Red_6	not used	Red_6	Red_0
FPD Bus A - Data_E Channel						
FPD_A_DATA_E_6	Map to Field	Map to Field	Map to Field	Map to Field	not used	not used
FPD_A_DATA_E_5	Blue_1	Blue_1	Blue_9	not used	not used	not used
FPD_A_DATA_E_4	Blue_0	Blue_0	Blue_8	not used	not used	not used
FPD_A_DATA_E_3	Green_1	Green_1	Green_9	not used	not used	not used
FPD_A_DATA_E_2	Green_0	Green_0	Green_8	not used	not used	not used
FPD_A_DATA_E_1	Red_1	Red_1	Red_9	not used	not used	not used
FPD_A_DATA_E_0	Red_0	Red_0	Red_8	not used	not used	not used
FPD Bus B						
FPD Bus B is unused in Modes 1 through 6						

Mode 7 (AM8280 Mode 9-1)

FPD Bus A - Data_A Channel		FPD Bus B - Data_A Channel	
FPD_A_DATA_A_6	Odd_Green_0	FPD_B_DATA_A_6	Even_Green_0
FPD_A_DATA_A_5	Odd_Red_5	FPD_B_DATA_A_5	Even_Red_5
FPD_A_DATA_A_4	Odd_Red_4	FPD_B_DATA_A_4	Even_Red_4
FPD_A_DATA_A_3	Odd_Red_3	FPD_B_DATA_A_3	Even_Red_3
FPD_A_DATA_A_2	Odd_Red_2	FPD_B_DATA_A_2	Even_Red_2
FPD_A_DATA_A_1	Odd_Red_1	FPD_B_DATA_A_1	Even_Red_1
FPD_A_DATA_A_0	Odd_Red_0	FPD_B_DATA_A_0	Even_Red_0
FPD Bus A - Data_B Channel		FPD Bus B - Data_B Channel	
FPD_A_DATA_B_6	Odd_Blue_1	FPD_B_DATA_B_6	Even_Blue_1
FPD_A_DATA_B_5	Odd_Blue_0	FPD_B_DATA_B_5	Even_Blue_0
FPD_A_DATA_B_4	Odd_Green_5	FPD_B_DATA_B_4	Even_Green_5
FPD_A_DATA_B_3	Odd_Green_4	FPD_B_DATA_B_3	Even_Green_4
FPD_A_DATA_B_2	Odd_Green_3	FPD_B_DATA_B_2	Even_Green_3
FPD_A_DATA_B_1	Odd_Green_2	FPD_B_DATA_B_1	Even_Green_2
FPD_A_DATA_B_0	Odd_Green_1	FPD_B_DATA_B_0	Even_Green_1

Mode 7 (AM8280 Mode 9-1)			
FPD Bus A - Data_C Channel		FPD Bus B - Data_C Channel	
FPD_A_DATA_C_6	DEN	FPD_B_DATA_C_6	DEN
FPD_A_DATA_C_5	VSYNC	FPD_B_DATA_C_5	VSYNC
FPD_A_DATA_C_4	HSYNC	FPD_B_DATA_C_4	HSYNC
FPD_A_DATA_C_3	Odd_Blue_5	FPD_B_DATA_C_3	Even_Blue_5
FPD_A_DATA_C_2	Odd_Blue_4	FPD_B_DATA_C_2	Even_Blue_4
FPD_A_DATA_C_1	Odd_Blue_3	FPD_B_DATA_C_1	Even_Blue_3
FPD_A_DATA_C_0	Odd_Blue_2	FPD_B_DATA_C_0	Even_Blue_2
FPD Bus A - Data_D Channel		FPD Bus B - Data_D Channel	
FPD_A_DATA_D_6	Map to Field	FPD_B_DATA_D_6	Map to Field
FPD_A_DATA_D_5	Odd_Blue_7	FPD_B_DATA_D_5	Even_Blue_7
FPD_A_DATA_D_4	Odd_Blue_6	FPD_B_DATA_D_4	Even_Blue_6
FPD_A_DATA_D_3	Odd_Green_7	FPD_B_DATA_D_3	Even_Green_7
FPD_A_DATA_D_2	Odd_Green_6	FPD_B_DATA_D_2	Even_Green_6
FPD_A_DATA_D_1	Odd_Red_7	FPD_B_DATA_D_1	Even_Red_7
FPD_A_DATA_D_0	Odd_Red_6	FPD_B_DATA_D_0	Even_Red_6
FPD Bus A - Data_E Channel		FPD Bus B - Data_E Channel	
FPD_A_DATA_E_6	not used	FPD_B_DATA_E_6	not used
FPD_A_DATA_E_5	not used	FPD_B_DATA_E_5	not used
FPD_A_DATA_E_4	not used	FPD_B_DATA_E_4	not used
FPD_A_DATA_E_3	not used	FPD_B_DATA_E_3	not used
FPD_A_DATA_E_2	not used	FPD_B_DATA_E_2	not used
FPD_A_DATA_E_1	not used	FPD_B_DATA_E_1	not used
FPD_A_DATA_E_0	not used	FPD_B_DATA_E_0	not used

Mode 8 (AM8280 Mode 9-2)			
FPD Bus A - Data_A Channel		FPD Bus B - Data_A Channel	
FPD_A_DATA_A_6	Odd_Green_2	FPD_B_DATA_A_6	Even_Green_2
FPD_A_DATA_A_5	Odd_Red_7	FPD_B_DATA_A_5	Even_Red_7
FPD_A_DATA_A_4	Odd_Red_6	FPD_B_DATA_A_4	Even_Red_6
FPD_A_DATA_A_3	Odd_Red_5	FPD_B_DATA_A_3	Even_Red_5
FPD_A_DATA_A_2	Odd_Red_4	FPD_B_DATA_A_2	Even_Red_4
FPD_A_DATA_A_1	Odd_Red_3	FPD_B_DATA_A_1	Even_Red_3
FPD_A_DATA_A_0	Odd_Red_2	FPD_B_DATA_A_0	Even_Red_2
FPD Bus A - Data_B Channel		FPD Bus B - Data_B Channel	
FPD_A_DATA_B_6	Odd_Blue_3	FPD_B_DATA_B_6	Even_Blue_3
FPD_A_DATA_B_5	Odd_Blue_2	FPD_B_DATA_B_5	Even_Blue_2
FPD_A_DATA_B_4	Odd_Green_7	FPD_B_DATA_B_4	Even_Green_7
FPD_A_DATA_B_3	Odd_Green_6	FPD_B_DATA_B_3	Even_Green_6
FPD_A_DATA_B_2	Odd_Green_5	FPD_B_DATA_B_2	Even_Green_5
FPD_A_DATA_B_1	Odd_Green_4	FPD_B_DATA_B_1	Even_Green_4
FPD_A_DATA_B_0	Odd_Green_3	FPD_B_DATA_B_0	Even_Green_3

Mode 8 (AM8280 Mode 9-2)			
FPD Bus A - Data_C Channel		FPD Bus B - Data_C Channel	
FPD_A_DATA_C_6	DEN	FPD_B_DATA_C_6	DEN
FPD_A_DATA_C_5	VSYNC	FPD_B_DATA_C_5	VSYNC
FPD_A_DATA_C_4	HSYNC	FPD_B_DATA_C_4	HSYNC
FPD_A_DATA_C_3	Odd_Blue_7	FPD_B_DATA_C_3	Even_Blue_7
FPD_A_DATA_C_2	Odd_Blue_6	FPD_B_DATA_C_2	Even_Blue_6
FPD_A_DATA_C_1	Odd_Blue_5	FPD_B_DATA_C_1	Even_Blue_5
FPD_A_DATA_C_0	Odd_Blue_4	FPD_B_DATA_C_0	Even_Blue_4
FPD Bus A - Data_D Channel		FPD Bus B - Data_D Channel	
FPD_A_DATA_D_6	Map to Field	FPD_B_DATA_D_6	Map to Field
FPD_A_DATA_D_5	Odd_Blue_1	FPD_B_DATA_D_5	Even_Blue_1
FPD_A_DATA_D_4	Odd_Blue_0	FPD_B_DATA_D_4	Even_Blue_0
FPD_A_DATA_D_3	Odd_Green_1	FPD_B_DATA_D_3	Even_Green_1
FPD_A_DATA_D_2	Odd_Green_0	FPD_B_DATA_D_2	Even_Green_0
FPD_A_DATA_D_1	Odd_Red_1	FPD_B_DATA_D_1	Even_Red_1
FPD_A_DATA_D_0	Odd_Red_0	FPD_B_DATA_D_0	Even_Red_0
FPD Bus A - Data_E Channel		FPD Bus B - Data_E Channel	
FPD_A_DATA_E_6	not used	FPD_B_DATA_E_6	not used
FPD_A_DATA_E_5	not used	FPD_B_DATA_E_5	not used
FPD_A_DATA_E_4	not used	FPD_B_DATA_E_4	not used
FPD_A_DATA_E_3	not used	FPD_B_DATA_E_3	not used
FPD_A_DATA_E_2	not used	FPD_B_DATA_E_2	not used
FPD_A_DATA_E_1	not used	FPD_B_DATA_E_1	not used
FPD_A_DATA_E_0	not used	FPD_B_DATA_E_0	not used

3.1.36 Read FPD Link Pixel Map Mode (4Ch)

This command is used to read the FPD link display Pixel Map mode.

3.1.36.1 Read Parameters

This command has no read parameters.

Return Parameters

Table 3-54 describes the return parameters.

Table 3-54. Return Parameters

Parameter Bytes		Description	
Byte 1		Reserved	
Byte 2		Reserved	
Byte 3		See below	

MSB	Byte 3						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7-4	R	Reserved

Bit	Type	Description
3-0	R	Pixel Map Mode 1h = Mode #1 2h = Mode #2 3h = Mode #3 4h = Mode #4 5h = Mode #5 6h = Mode #6 7h = Mode #7 8h = Mode #8

3.1.37 Write FPGA Input Video Chroma Processing Select (4Dh)

This command is used to specify Chroma processing select for the YUV422 source input to the FPGA.

Write Parameters

Table 3-55 describes the write parameters.

Table 3-55. Write Parameters

MSB	Byte 2						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7-4	R	Reserved
3	W	Chroma Channel Swap 0h = CbCr 1h = CrCb
2-0	R	Reserved

3.1.38 Read FPGA Input Video Chroma Processing Select (4Eh)

This command is used to read the Chroma processing select for the YUV422 source input to the FPGA.

3.1.38.1 Read Parameters

This command has no read parameters.

Return Parameters

Table 3-56 describes the return parameters.

Table 3-56. Return Parameters

MSB	Byte 2						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7-4	R	Reserved
3	R	Chroma Channel Swap 0h = CbCr 1h = CrCb
2-0	R	Reserved

3.2 Illumination Control Commands

3.2.1 Write LED Output Control Method (50h)

This command specifies the method for controlling the LED outputs for the display module.

3.2.1.1 Write Parameters

[Table 3-57](#) describes the write parameters.

Table 3-57. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:2)	Reserved						
b(1:0)	LED control method: <ul style="list-style-type: none"> • 00: Manual RGB LED currents (disables CAIC algorithm) • 01: CAIC (automatic) RGB LED power (enables CAIC algorithm) • 10: Reserved • 11: Reserved 						

This command selects the method used to control the output of the red, green, and blue LEDs. Based on the method chosen, a specific set of commands are available for controlling the LED outputs. These are shown in [Table 3-58](#).

The manual RGB LED currents method provides for manual control of the LED currents, and disables the CAIC algorithm. The CAIC (automatic) RGB LED current control method provides automatic control of the LED currents using the CAIC algorithm.

Table 3-58. Available Commands Based on LED Control Method

LED Control Method	Available Commands
Manual RGB LED current control	Write RGB LED Enable (52h) Read RGB LED Enable (53h) Write RGB LED Current (54h) Read RGB LED Current (55h) Write RGB LED Max Current (5Ch) Read RGB LED Max Current (5Dh)
CAIC (automatic) RGB LED current control	Write RGB LED Enable (52h) Read RGB LED Enable (53h) Write RGB LED Current (54h) Read RGB LED Current (55h) Read CAIC LED Max Available Power (57h) Read CAIC RGB LED Current (5Fh)

3.2.2 Read LED Output Control Method (51h)

This command reads the selected LED output control method for the display module.

3.2.2.1 Read Parameters

This command has no read parameters.

3.2.2.2 Return Parameters

[Table 3-59](#) describes the return parameters.

Table 3-59. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0

b(7:2)	Reserved
b(1:0)	LED control method: <ul style="list-style-type: none"> • 00: Manual RGB LED currents (CAIC algorithm disabled) • 01: CAIC (automatic) RGB LED current control (CAIC algorithm enabled) • 10: Reserved • 11: Reserved

3.2.3 Write RGB LED Enable (52h)

This command enables the LEDs for the display module.

3.2.3.1 Write Parameters

Table 3-60 describes the write parameters.

Table 3-60. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:3)	Reserved						
b(2)	Blue LED enable: <ul style="list-style-type: none"> • 0: Blue LED disabled • 1: Blue LED enabled 						
b(1)	Green LED enable: <ul style="list-style-type: none"> • 0: Green LED disabled • 1: Green LED enabled 						
b(0)	Red LED enable: <ul style="list-style-type: none"> • 0: Red LED disabled • 1: Red LED enabled 						

3.2.4 Read RGB LED Enable (53h)

This command reads the state of the LED enables for the display module.

3.2.4.1 Read Parameters

This command has no read parameters.

3.2.4.2 Return Parameters

Table 3-61 describes the return parameters.

Table 3-61. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:3)	Reserved						
b(2)	Blue LED enable: <ul style="list-style-type: none"> • 0: Blue LED disabled • 1: Blue LED enabled 						
b(1)	Green LED enable: <ul style="list-style-type: none"> • 0: Green LED disabled • 1: Green LED enabled 						

b(0)	Red LED enable: <ul style="list-style-type: none"> • 0: Red LED disabled • 1: Red LED enabled
------	---

3.2.5 Write RGB LED Current (54h)

This command sets the current for the red, green, and blue LEDs of the display module.

3.2.5.1 Write Parameters

Table 3-62 describes the write parameters.

Table 3-62. Write Parameters

Parameter Bytes	Description
Byte 1	Red LED current parameter (LSByte)
Byte 2	Red LED current parameter (MSByte)
Byte 3	Green LED current parameter (LSByte)
Byte 4	Green LED current parameter (MSByte)
Byte 5	Blue LED current parameter (LSByte)
Byte 6	Blue LED current parameter (MSByte)

When an all-white image is displayed, this command allows the system white point to be adjusted while establishing the total LED power whether the CAIC algorithm is enabled or disabled.

The parameters specified by this command have a resolution of 10 bits, and are defined by the appropriate DLPA200x specification.

When the CAIC algorithm is disabled, this command directly sets the LED currents (the R, G, and B values provided are sent directly to the DLPA200x device) regardless of the image being displayed.

When the CAIC algorithm is enabled:

- This command directly sets the LED currents when an all-white image is displayed. If the image is changed from an all-white image, depending on the image the CAIC algorithm may alter one or more of the LED currents from those specified by this command, and the total LED power may drop. The *Read CAIC RGB LED Current* command can read the actual LED currents for the image currently displayed.
- In the case of an all-white image, the values read by the *Read CAIC RGB LED Current* command closely match, but may not exactly match, those requested using the *Write RGB LED Current* command. For an all-white image, the *Read CAIC RGB LED Current* command gives currents within +/-4 DLPA200x device current steps for each LED color relative to those requested by the *Write RGB LED Current* command.
- When the *Write RGB LED Current* command changes the LED currents, the LED current for any color must not be changed by more than +/-25% from the nominal current used for that color when the CAIC LUTs were created. No LED current can be set to a current value beyond the maximum value supported in the CAIC intensity-to-current LUT for the corresponding color.
- The maximum total LED power for any displayed image occurs for an all-white image, since the CAIC algorithm requests the CAIC LED maximum available power. The maximum available LED power for the CAIC is controlled by the *Write RGB LED Current* command, as this command controls currents for an all-white image. After the currents are adjusted, the *Read CAIC LED Max Available Power* command is used to see the maximum power in watts derived from the CAIC.

3.2.6 Read RGB LED Current (55h)

This command reads the state of the current for the red, green, and blue LEDs of the display module.

3.2.6.1 Read Parameters

This command has no read parameters.

3.2.6.2 Return Parameters

Table 3-63 describes the return parameters.

Table 3-63. Return Parameters

Parameter Bytes	Description
Byte 1	Red LED current parameter (LSByte)
Byte 2	Red LED current parameter (MSByte)
Byte 3	Green LED current parameter (LSByte)
Byte 4	Green LED current parameter (MSByte)
Byte 5	Blue LED current parameter (LSByte)
Byte 6	Blue LED current parameter (MSByte)

See [Write RGB LED Current \(54h\)](#) for a detailed description of the return parameters.

Unused most significant bits are set to 0.

3.2.7 Read CAIC LED Max Available Power (57h)

This command reads the specified maximum LED power allowed for the display module.

3.2.7.1 Read Parameters

This command has no read parameters.

3.2.7.2 Return Parameters

[Table 3-64](#) describes the return parameters.

Table 3-64. Return Parameters

Parameter Bytes	Description
Byte 1	Maximum LED power (LSByte)
Byte 2	Maximum LED power (MSByte)

The value is specified in watts × 100 (for example: 25.75W = A0Fh). This command is only applicable when CAIC is enabled.

The CAIC maximum available LED power pertains if an all-white image is displayed where LED currents are set by the *Write RGB LED Current* command. The calculation is:

R duty cycle × R LED current × R LED voltage + G duty cycle × G LED current × G LED voltage + B duty cycle × B LED current × B LED voltage

For example: (.30 × .49A × 2.0V) + (.50 × .39 A × 3.1V) + (.20 × .39 A × 3.1V) = (.30 × .980W) + (.50 × 1.209W) + (.20 × 1.209W) = 1.140W

3.2.8 Write RGB LED Max Current (5Ch)

This command specifies the maximum LED current allowed for each LED in the display module.

3.2.8.1 Write Parameters

[Table 3-65](#) describes the write parameters.

Table 3-65. Write Parameters

Parameter Bytes	Description
Byte 1	Maximum red LED current (LSByte)
Byte 2	Maximum red LED current (MSByte)
Byte 3	Maximum green LED current (LSByte)
Byte 4	Maximum green LED current (MSByte)
Byte 5	Maximum blue LED current (LSByte)
Byte 6	Maximum blue LED current (MSByte)

This command sets the maximum LED currents that can be used when CAIC is enabled or disabled. When CAIC is enabled, the maximum LED currents may be further limited by the CAIC LUTs stored in the flash. The parameters specified by this commands have a resolution of 10 bits. The unused most significant bits should be set to 0.

3.2.9 Read RGB LED Max Current (5Dh)

This command reads the specified maximum LED current allowed for each LED in the display module.

3.2.9.1 Read Parameters

This command has no read parameters.

3.2.9.2 Return Parameters

Table 3-66 describes the return parameters.

Table 3-66. Return Parameters

Parameter Bytes	Description
Byte 1	Maximum red LED current (LSByte)
Byte 2	Maximum red LED current (MSByte)
Byte 3	Maximum green LED current (LSByte)
Byte 4	Maximum green LED current (MSByte)
Byte 5	Maximum blue LED current (LSByte)
Byte 6	Maximum blue LED current (MSByte)

See the [Write RGB LED Current \(54h\)](#) for a detailed description of the return parameters.

Unused most significant bits are set to 0.

3.2.10 Read CAIC RGB LED Current (5Fh)

This command reads the state of the current for the red, green, and blue LEDs of the display module.

3.2.10.1 Read Parameters

This command has no read parameters.

3.2.10.2 Return Parameters

Table 3-67 describes the return parameters.

Table 3-67. Return Parameters

Parameter Bytes	Description
Byte 1	Red LED current parameter (LSByte)
Byte 2	Red LED current parameter (MSByte)
Byte 3	Green LED current parameter (LSByte)
Byte 4	Green LED current parameter (MSByte)
Byte 5	Blue LED current parameter (LSByte)
Byte 6	Blue LED current parameter (MSByte)

The parameters returned by this command have a resolution of 10 bits, and are defined by the appropriate DLPA200x specification.

When the CAIC algorithm is enabled using the *LED Output Control Method* command:

- The *Write RGB LED Current* command directly sets the LED currents when an all-white image is displayed. If the image changes from an all-white image, depending on the image, the CAIC algorithm may alter one or more of the LED currents from those specified by the *Write RGB LED Current* command, and the total LED power may drop. The actual LED currents for the image currently displayed are read using the *Read CAIC RGB LED Current* command.

- In the case of an all-white image, the values returned by this command closely match, but may not exactly match, those specified using the *Write RGB LED Current* command. For an all-white image, this command provides values within +/- 4 DLPA200x device current steps for each LED color relative to those specified with the *Write RGB LED Current* command.

Use of this command is only appropriate when the LED output control method is set to CAIC (automatic) RGB LED current control.

Unused most significant bits are set to 0.

3.2.11 Write XPR FPGA Input Image Size (60h)

This command is used to specify the active data size of the external input image that goes to the XPR FPGA. Supported resolutions are 1280×720, 1366×768, and 1920×1080.

Write Parameters

[Table 3-68](#) describes the write parameters.

Table 3-68. Write Parameters

Parameter Bytes	Description
Byte 1	Pixels per line (LSByte)
Byte 2	Pixels per line (MSByte)
Byte 3	Lines per frame (LSByte)
Byte 4	Lines per frame (MSByte)

3.2.12 Read XPR FPGA Input Image Size (61h)

This command is used to read specified data size of the external input image to the display module.

3.2.12.1 Read Parameters

This command has no read parameters.

3.2.12.2 Return Parameters

[Table 3-69](#) describes the return parameters.

Table 3-69. Return Parameters

Parameter Bytes	Description
Byte 1	Pixels per line (LSByte)
Byte 2	Pixels per line (MSByte)
Byte 3	Lines per frame (LSByte)
Byte 4	Lines per frame (MSByte)

3.2.13 Write XPR FPGA Source Select (62h)

This command specifies the XPR FPGA input source.

Write Parameters

Table 3-70 describes the write parameters.

Table 3-70. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
Bit	Type	Description					
7-2	R	Reserved					
1-0	W	Input source 0h = TPG 1h = External Parallel Video 2h = FPD-Link or LVDS 3h = Reserved					

3.2.14 Read XPR FPGA Source Select (63h)

This command reads the selected XPR FPGA input source.

3.2.14.1 Read Parameters

This command has no read parameters.

3.2.14.2 Return Parameters

Table 3-71 describes the return parameters.

Table 3-71. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
Bit	Type	Description					
7-2	R	Reserved					
1-0	W	Input source 0h = TPG 1h = External Parallel Video 2h = FPD-Link or LVDS 3h = Reserved					

3.2.15 Read XPR FPGA Version (64h)

This command is used to read the XPR FPGA software and bitstream version.

3.2.15.1 Read Parameters

This command has no read parameters.

Return Parameters

Table 3-72 describes the return parameters.

Table 3-72. Return Parameters

Parameter Bytes	Description
Byte 1	b(7:0) = FPGA ECO Revision
Byte 2	• b(7:0) = FPGA ARM Software Version - Major

Table 3-72. Return Parameters (continued)

Parameter Bytes	Description
Byte 3:6	<ul style="list-style-type: none"> b(31:28) = FPGA Firmware Version – Build Level b(27:20) = FPGA Firmware Version – Minor b(19:12) = FPGA Firmware Version – Major b(11:0) = FPGA Firmware Version – Build Number

3.2.16 Write XPR FPGA Test Pattern Select (67h)

This command is used to specify an internal test pattern from XPR FPGA for display on the display module.

Write Parameters

Table 3-73 describes the write parameters.

Table 3-73. Write Parameters

Parameter Bytes	Description
Byte 1	TPG pattern select (LSByte)
Byte 2	TPG pattern options (MSByte)

MSB	Byte 1 and 2						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7	W	Test Pattern Boarder 0h = Disabled (default) 1h = Enabled
6-4	W	Color 0h = Black 1h = Blue 2h = Red 3h = Magenta 4h = Green 5h = Cyan 6h = Yellow 7h = White
3-0	W	Pattern Select 0h = Solid Field 1h = Grids 2h = Horizontal Ramp 4h = Checkerboard 5h = Horizontal Lines 6h = Vertical Lines 7h = Diagonal Lines 8h = Actuator Calibration Pattern 9h = 3D Test Pattern Ah = Color Bars Bh = Frame & Cross Ch - Fh = Reserved

Byte 2: Varies depending on configuration selected. It is ignored for Solid Field, Grids, Horizontal, Vertical and Diagonal lines, 3D test patterns and Color Bars.

For Horizontal and Vertical Ramps, Byte 2 represents the pixel level intensity at the brightest part of the ramp ranging from 0-255.

For Checkerboards, Byte 2 specifies the size of each checker in 4 pixel resolution so a value of 10 would generate 40 pixel checkers.

For Actuator Calibration test patterns, Byte 2 specifies the sub-frames to be displayed:

- Byte 2 (7:0) = 0: Actuator Calibration Pattern - Sub-Frame 0 and 1 Only (HD only)
- Byte 2 (7:0) = 3: Actuator Calibration Pattern - Sub-Frames 0, 1, 2, 3 (Full HD only)

For Frame & Cross, Byte 2 is divided into two nibbles. Each nibble is a pixel position from the upper left corner of the image with a resolution of (Pixel Count / 16). So 720p has 80 pixel increments horizontally and 45 pixel increments vertically. 1080p has 120 pixel increments horizontally and 68 pixel increments vertically.

- MS-Nibble (7:4): Horizontal position
- LS-Nibble (3:0): Vertical position

3.2.17 Read XPR FPGA Test Pattern Select (68h)

This command is used to an internal test pattern from XPR FPGA.

3.2.17.1 Read Parameters

This command has no read parameters.

Return Parameters

[Table 3-74](#) describes the return parameters.

Table 3-74. Return Parameters

Parameter Bytes		Description	
Byte 1		TPG pattern select (LSByte)	
Byte 2		TPG pattern options (MSByte)	

MSB	Byte 1 and 2						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7	R	Test Pattern Boarder 0h = Disabled (default) 1h = Enabled
6-4	R	Color 0h = Black 1h = Blue 2h = Red 3h = Magenta 4h = Green 5h = Cyan 6h = Yellow 7h = White

Bit	Type	Description
3-0	R	Pattern Select 0h = Solid Field 1h = Grids 2h = Horizontal Ramp 4h = Checkerboard 5h = Horizontal Lines 6h = Vertical Lines 7h = Diagonal Lines 8h = Actuator Calibration Pattern 9h = 3D Test Pattern Ah = Color Bars Bh = Frame & Cross Ch - Fh = Reserved

Byte 2: Varies depending on configuration selected. It is ignored for Solid Field, Grids, Horizontal, Vertical and Diagonal lines, 3D test patterns and Color Bars.

For Horizontal and Vertical Ramps, Byte 2 represents the pixel level intensity at the brightest part of the ramp ranging from 0-255.

For Checkerboards, Byte 2 specifies the size of each checker in 4 pixel resolution so a value of 10 would generate 40 pixel checkers.

For Actuator Calibration test patterns, Byte 2 specifies the sub-frame(s) to be displayed:

- Byte 2 (7:0) = 0: Actuator Calibration Pattern - Sub-Frame 0 and 1 Only (HD only)
- Byte 2 (7:0) = 3: Actuator Calibration Pattern - Sub-Frames 0, 1, 2, 3 (Full HD only)

For Frame & Cross, Byte 2 is divided into two nibbles. Each nibble is a pixel position from the upper left corner of the image with a resolution of (Pixel Count / 16). So 720p has 80 pixel increments horizontally and 45 pixel increments vertically. 1080p has 120 pixel increments horizontally and 68 pixel increments vertically.

- MS-Nibble (7:4): Horizontal position
- LS-Nibble (3:0): Vertical position

3.2.18 Write XPR FPGA Parallel Video Control (6Bh)

This command is used to configure polarity of syncs and sampling edge of the pixel clock in XPR FPGA.

Write Parameters

Table 3-75 describes the write parameters.

Table 3-75. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
Bit	Type	Description					
7-4	R	Reserved					
3	W	VSync Polarity 0h = Active Low 1h = Active High					
2	W	HSync Polarity 0h = Active Low 1h = Active High					
1	W	IValid Polarity 0h = Active Low 1h = Active High					

Bit	Type	Description
0	W	Pixel Clock Sampling Edge 0h = Falling Edge 1h = Rising Edge

3.2.19 Read XPR FPGA Parallel Video Control (6Ch)

This command is used to read XPR FPGA video format.

3.2.19.1 Read Parameters

This command has no read parameters.

Return Parameters

[Table 3-76](#) describes the return parameters.

Table 3-76. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7-4	R	Reserved
3	R	VSynC Polarity 0h = Active Low 1h = Active High
2	R	HSynC Polarity 0h = Active Low 1h = Active High
1	R	IValid Polarity 0h = Active Low 1h = Active High
0	R	Pixel Clock Sampling Edge 0h = Falling Edge 1h = Rising Edge

3.2.20 Write XPR FPGA Video Format Select (6Dh)

This command is used to specify XPR FPGA video format.

Write Parameters

[Table 3-77](#) describes the write parameters.

Table 3-77. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7:3	R	Reserved

Bit	Type	Description
2:0	W	Input source format <ul style="list-style-type: none"> • 0h = RGB888 • 1h = RGB565 • 2h = RGB666 • 3h = YCbCr422 • 4h = YCbCr444 • 5h = YCbCr565 • 6h = YCbCr666

When adjusting the XPR FPGA Video Format selection, the parallel video data input must be properly aligned with the 24-bit bus of the FPGA. The appropriate data encoding format is provided in [Figure 3-12](#).

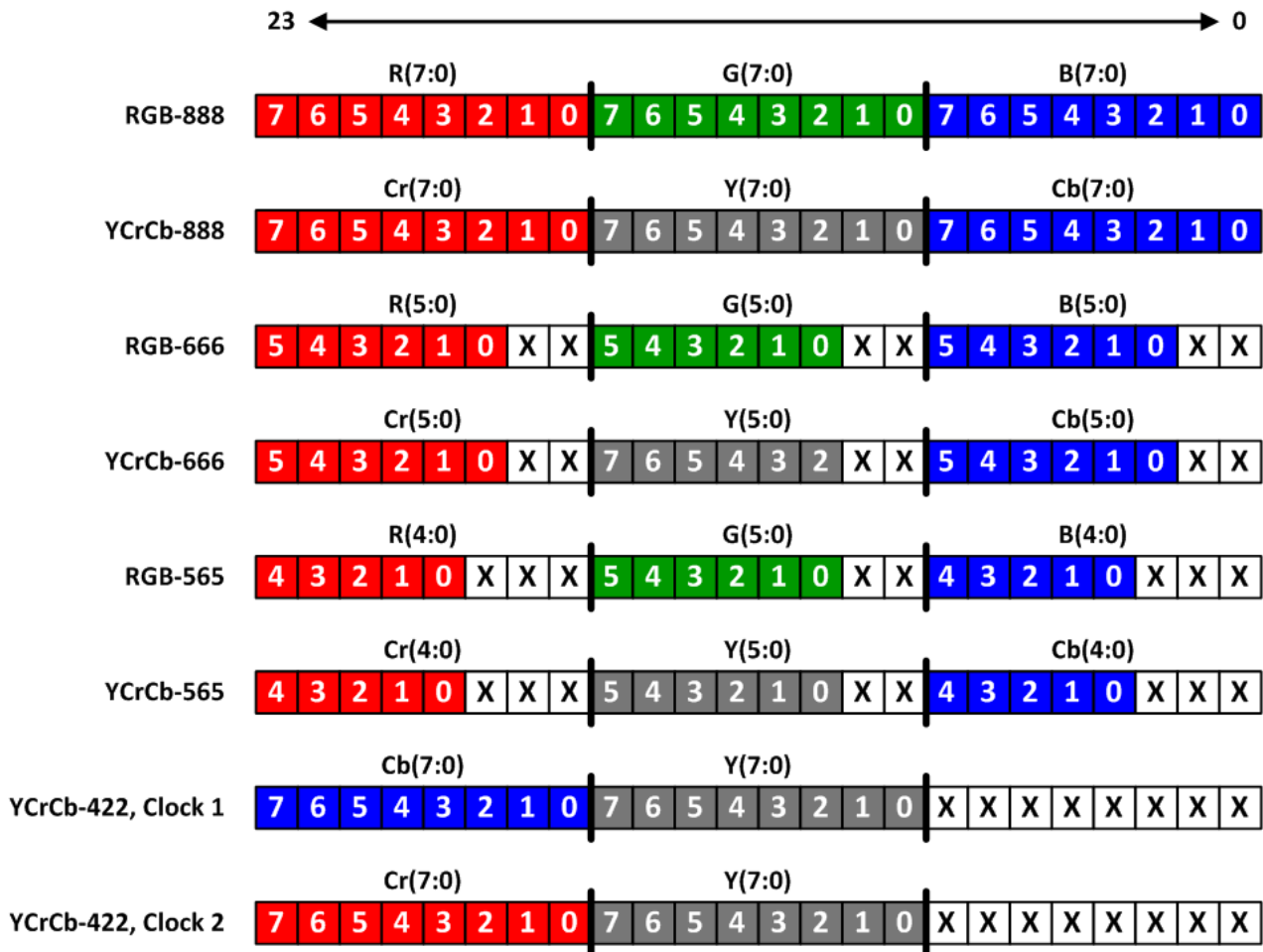


Figure 3-12. Parallel Data Bus (23:0) Encoding Options

3.2.21 Read XPR FPGA Video Format Select (6Eh)

This command is used to read XPR FPGA video format.

3.2.21.1 Read Parameters

This command has no read parameters.

Return Parameters

[Table 3-78](#) describes the return parameters.

Table 3-78. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
Bit	Type	Description					
7:3	R	Reserved					
2:0	W	Input source format <ul style="list-style-type: none"> • 0h = RGB888 • 1h = RGB565 • 2h = RGB666 • 3h = YCbCr422 • 4h = YCbCr444 • 5h = YCbCr565 • 6h = YCbCr666 					

3.2.22 Read XPR FPGA Status (6Fh)

This command is used to read XPR FPGA status.

3.2.22.1 Read Parameters

This command has no read parameters.

Return Parameters

[Table 3-79](#) describes the return parameters.

Table 3-79. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
Bit	Type	Description					
7-2	R	Reserved					
1	R	Display Mode 0h = Non-XPR Mode 1h = XPR Mode					
0	R	FPGA Keying Status 0h = Failed 1h = Passed					

3.2.23 Write Actuator Latency (70h)

This command is used to specify the Actuator Latency. This command is required for Actuator calibration.

The reset value is the latency value in the sequence header.

3.2.23.1 Write Parameters

[Table 3-80](#) describes the write parameters.

Table 3-80. Write Parameters

Parameter Bytes	Description
Byte 1	Latency (LSByte)
Byte 2	Latency
Byte 3	Latency (MSByte)
Byte 4	See below

Note

Valid latency input values range from 000h to 3FFh, with a step size of 133.333ns. This yields a minimum latency of 0ns and a maximum latency of 34952312.619ns.

MSB	Byte 4						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7:1	R	Reserved
0	W	Manual Latency Enable 0h = Manual Latency disabled – Latency value stored in the sequence header is used. 1h = Latency value provided in Bytes 1-3 is used.

3.2.24 Read Actuator Latency (71h)

This command is used to read the Actuator Latency. This command is required for Actuator calibration. The reset value is the latency value in the sequence header.

3.2.24.1 Read Parameters

This command has no read parameters.

Return Parameters

Table 3-81 describes the return parameters.

Table 3-81. Return Parameters

Parameter Bytes	Description
Byte 1	Latency (LSByte)
Byte 2	Latency
Byte 3	Latency (MSByte)
Byte 4	See below

Bit	Type	Description
7-2	R	Reserved
1	W	Latency Auto Scaling Enable 0h = Auto scaling disabled -- No clock dropping scaling is applied 1h = Auto scaling enabled
0	W	Manual Latency Enable 0h = Manual Latency disabled – Latency value stored in the sequence header will be used. 1h = Latency value used provided in Byte 1-3.

3.2.25 Write Actuator Gain (72h)

This command is used to specify the Actuator Gain parameter.

Write Parameters

Table 3-82 describes the write parameters.

Table 3-82. Write Parameters

Parameter Bytes	Description
Byte 1	Actuator Gain

Note

Value is presented in fixed point format.

1 = 0.007813

Valid range (0 to 1.9921875)

3.2.26 Read Actuator Gain (73h)

This command is used to read the Actuator Gain parameter.

3.2.26.1 Read Parameters

This command has no read parameters.

Return Parameters

[Table 3-83](#) describes the return parameters.

Table 3-83. Return Parameters

Parameter Bytes	Description
Byte 1	Actuator Gain

Note

Value is presented in fixed point format.

1 = 0.007813

Valid range (0 to 1.9921875)

3.2.27 Write Segment Length (74h)

This command is used to specify the Actuator Segment Length parameter.

Write Parameters

[Table 3-84](#) describes the write parameters.

Table 3-84. Write Parameters

Parameter Bytes	Description
Byte 1	Segment Length (LSByte)
Byte 2	Segment Length (MSByte)

Note

Valid segment length is 2 to 65535.

3.2.28 Read Segment Length (75h)

This command is used to read the Actuator Segment Length parameter.

3.2.28.1 Read Parameters

This command has no read parameters.

3.2.28.2 Return Parameters

[Table 3-85](#) describes the return parameters.

Table 3-85. Return Parameters

Parameter Bytes	Description
Byte 1	Segment Length (LSByte)

Table 3-85. Return Parameters (continued)

Parameter Bytes	Description
Byte 2	Segment Length (MSByte)

Note

Valid segment length is 2 to 65535.

3.2.29 Write Manual Actuator Sync Delay (76h)

This command is used to specify the Actuator Sync Delay parameter.

The reset value is pre-configured in the sequence header.

3.2.29.1 Write Parameters

Table 3-86 describes the write parameters.

Table 3-86. Write Parameters

Parameter Bytes	Description
Byte 1	Actuator Sync Delay (LSByte)
Byte 2	Actuator Sync Delay
Byte 3	Actuator Sync Delay (MSByte)
Byte 4	Manual / Auto Actuator Sync Delay enable

MSB	Byte 4						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Table 3-87. Write Manual Actuator Sync Delay Register Field Descriptions

Bit	Type	Description
7-2	R	Reserved
1	W	Auto-scaling enable. Applicable only when manual Actuator Sync Delay override mode is enabled, b(0)=1. 0h = No scaling is performed. Actuator Sync delay is applied as defined in Byte 1-3 1h = Auto scaling is performed with frame rate change.
0	W	Manual Actuator Sync Delay override enable 0h = Actuator Sync Delay defined in Byte 1 to 3 is not applied only when this bit is disabled. Instead, the Actuator Sync Delay defined in the flash as part of the sequence data is applied. 1h = Actuator Sync Delay defined in Byte 1 to 3 is applied only when this bit is enabled.

Note

This command is executed in conjunction with Write Actuator Latency command. Latency corrections are always made to the Actuator delay before writing to the hardware register. In case Latency correction is not required, then Latency should be set to 0.

3.2.30 Read Manual Actuator Sync Delay (77h)

This command is used to read Manual Actuator Sync Delay parameter.

The reset value is pre-configured in the sequence header.

3.2.30.1 Read Parameters

This command has no read parameters.

3.2.30.2 Return Parameters

Table 3-88 describes the return parameters.

Table 3-88. Return Parameters

Parameter Bytes		Description
Byte 1		Actuator Sync Delay (LSByte)
Byte 2		Actuator Sync Delay
Byte 3		Actuator Sync Delay (MSByte)
Byte 4		Manual / Auto Actuator Sync Delay enable

MSB	Byte 4						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7-2	R	Reserved
1	R	Auto-scaling enable. Applicable only when manual Actuator Sync Delay override mode is enabled, b(0)=1. 0h = No scaling is performed. Actuator Sync delay is applied as defined in Byte 1-3 1h = Auto scaling is performed with frame rate change.
0	R	Manual Actuator Sync Delay override enable 0h = Actuator Sync Delay defined in Byte 1 to 3 is not applied only when this bit is disabled. Instead, the Actuator Sync Delay defined in the flash as part of the sequence data is applied. 1h = Actuator Sync Delay defined in Byte 1 to 3 is applied only when this bit is enabled.

Note

This command is executed in conjunction with Write Actuator Latency command. Latency corrections are always made to the Actuator delay before writing to the hardware register. In case Latency correction is not required, then Latency should be set to 0.

3.2.31 Write Manual Actuator Offset (78h)

This command is used to specify the Manual Actuator Offset parameter.

3.2.31.1 Write Parameters

Table 3-89 describes the write parameters.

Table 3-89. Write Parameters

Parameter Bytes		Description
Byte 1		Manual Actuator Offset (LSByte)
Byte 2		Manual Actuator Offset (MSByte)

MSB	Byte 4						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7-4	R	Reserved
3:0	W	Manual Actuator Offset

Note

This Actuator Manual Offset is presented in fixed point format (1 = 00.0078130)
Valid values of Actuator Manual Offset is 0 – 31.9921875.

3.2.32 Read Manual Actuator Offset (79h)

This command is used to read the Manual Actuator Offset parameter.

3.2.32.1 Read Parameters

This command has no read parameters.

3.2.32.2 Return Parameters

[Table 3-90](#) describes the return parameters.

Table 3-90. Return Parameters

Parameter Bytes				Description			
Byte 1				Manual Actuator Offset (LSByte)			
Byte 2				Manual Actuator Offset (MSByte)			

MSB	Byte 4						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Bit	Type	Description
7:4	R	Reserved
3:0	W	Manual Actuator Offset

Note

This Actuator Manual Offset is presented in fixed point format (1 = 00.0078130)
Valid values of Actuator Manual Offset is 0 – 31.9921875.

3.3 Image Processing Control Commands

3.3.1 Write Local Area Brightness Boost Control (80h)

This command controls the LABB image processing functionality for the display module.

3.3.1.1 Write Parameters

Table 3-91 describes the write parameters.

Table 3-91. Write Parameters

Parameter Bytes	Description
Byte 1	See Table 3-92
Byte 2	LABB strength setting

Table 3-92. Byte 1 Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:4)	Sharpness strength						
b(3:2)	Reserved						
b(1:0)	LABB control: <ul style="list-style-type: none"> 0h: Disabled 1h: Enabled: Manual strength control 2h: Reserved 3h: Reserved 						

The key function of the LABB is to adaptively gain up darker parts of the image to achieve an overall brighter image.

For LABB strength, 0 indicates no boost applied and 255 indicates the maximum boost viable in a product. The strength is not a direct indication of the gain, since the gain varies depending on the image content.

Sharpness strength ranges from 0 to 15, with 0 indicating sharpness disabled and 15 indicating the maximum sharpness. The LABB function must be enabled to make use of sharpness.

LABB is supported in TPG, splash, and external input mode, but auto-disabled in curtain mode.

3.3.2 Read Local Area Brightness Boost Control (81h)

This command reads the state of the LABB image processing functionality for the display module.

3.3.2.1 Read Parameters

This command has no read parameters.

3.3.2.2 Return Parameters

Table 3-93 describes the return parameters.

Table 3-93. Return Parameters

Parameter Bytes	Description
Byte 1	See Table 3-94
Byte 2	LABB strength setting
Byte 3	LABB gain value

Table 3-94. Byte 1 Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:4)	Sharpness strength						

b(3:2)	Reserved
b(1:0)	LABB control: <ul style="list-style-type: none"> • 0h: Disabled • 1h: Enabled: Manual strength control • 2h: Reserved • 3h: Reserved

Table 3-95 shows the bit order and weighting for the LABB gain value, which ranges from 1 to 8 (the controller software limits the lower value to 1).

Table 3-95. Bit Weight Definition for LABB Gain Value

b7	b6	b5	b4	b3	b2	b1	b0
2 ²	2 ¹	2 ⁰	2 ⁻¹	2 ⁻²	2 ⁻³	2 ⁻⁴	2 ⁻⁵

The software equation to calculate LABB Gain as a fixed point value is shown below:

$$\text{LABB_gain} = \text{add_8lsb(APL)} / \text{pre_LABB_APL} \quad (//\text{add 8 LSBS (u8.0 / u8.0 = u8.8 / u8.0 = u8.8)})$$

3.3.3 Write CAIC Image Processing Control (84h)

This command controls the CAIC functionality for the display module.

3.3.3.1 Write Parameters

Table 3-96 describes the write parameters.

Table 3-96. Write Parameters

Parameter Bytes	Description
Byte 1	See Table 3-97
Byte 2	CAIC maximum lumens gain
Byte 3	CAIC clipping threshold

Table 3-97. Byte 1 Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7)	CAIC gain display enable: <ul style="list-style-type: none"> • 0h: Disabled • 1h: Enabled 						
b(6)	CAIC gain display scale: <ul style="list-style-type: none"> • 0h: 100% = 1024 pixels • 1h: 100% = 512 pixels 						
b(5:0)	Reserved						

The CAIC algorithm (Content Adaptive Illumination Control) provides adaptive control of the LED currents and the digital gain applied to the image.

The CAIC algorithm is enabled or disabled based on the method of LED current control selected by the user using the *Write LED Output Control Method* command. When enabled, the CAIC algorithm provides automatic control of the LED currents as specified by this command and the *Write LED Output Control Method* command.

The CAIC gain display provides a visual presentation of the instantaneous gain provided by the CAIC algorithm. The CAIC gain display is typically used as a debug tool and to show the performance of the algorithm. The CAIC gain display must never be used for normal operation. The display is composed of five bars, with the bottom three bars (green, red, and blue) showing the respective CAIC gain for each color. The top two bars are for TI debug use only.

Table 3-98 shows the bit order and weighting for the CAIC maximum lumens gain value, which has a valid range from 1.0 to 4.0. Values outside of this range are considered an error (invalid write parameter value – communication status) and the command does not execute.

Table 3-98. Bit Weight Definition for the CAIC Maximum Gain Value

b7	b6	b5	b4	b3	b2	b1	b0
2^2	2^1	2^0	2^{-1}	2^{-2}	2^{-3}	2^{-4}	2^{-5}

The CAIC maximum lumens gain parameter sets the maximum lumens gain for a pixel as a result of both digital gain and increasing LED currents. The CAIC maximum lumens gain parameter also serves to bias the CAIC algorithm towards either constant power (variable brightness) or constant lumens (variable power). Some examples are listed below:

- Maximum gain value = 1.0: This biases performance to constant lumens. In this case, LED power is reduced for those images where this is possible, but lumens do not increase or decrease.
- Maximum lumens gain value = 4.0: This biases performance to constant power. In this case, power is held constant for most images, while the lumens are gained up. For the small percent of images where the gain exceeds 4.0, lumens stop increasing and the power is reduced.

Table 3-99 shows the bit order and weighting for the CAIC clipping threshold value, which has a valid range from 0.0% to 4.0%. Values outside of this range are considered an error (invalid write parameter value – communication status) and the command does not execute.

Table 3-99. Bit Weight Definition for the CAIC Clipping Threshold Value

b7	b6	b5	b4	b3	b2	b1	b0
2^1	2^0	2^{-1}	2^{-2}	2^{-3}	2^{-4}	2^{-5}	2^{-6}

The CAIC clipping threshold parameter sets the percentage of pixels clipped by the CAIC algorithm over the full frame of active data, due to the digital gain applied by the CAIC algorithm.

Table 3-100 shows the bit order and weighting for the CAIC RGB intensity gain values, which have a valid range from 0.0 to almost 1.0. Values outside of this range are considered an error (invalid write parameter value – communication status) and the command does not execute.

Table 3-100. Bit Weight Definition for the CAIC RGB Intensity Gain Values

b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
Res	Res	Res	Res	Res	Res	2^{-1}	2^{-2}	2^{-3}	2^{-4}	2^{-5}	2^{-6}	2^{-7}	2^{-8}	2^{-9}	2^{-10}

CAIC can be enabled in TPG and external input mode, but auto-disabled in splash and curtain mode.

Table 3-101. LABB and CAIC Modes

Feature	TPG	Splash	Curtain	External Input
LABB	Supported	Supported	Auto-disabled	Supported
CAIC	Supported	Auto-disabled	Auto-disabled	Supported
Manual idle mode	Supported	Supported	Auto-disabled	Supported

3.3.4 Read CAIC Image Processing Control (85h)

This command reads the state of the CAIC functionality within the display module.

3.3.4.1 Read Parameters

This command has no read parameters.

3.3.4.2 Return Parameters

Table 3-102 describes the return parameters.

Table 3-102. Return Parameters

Parameter Bytes	Description
Byte 1	See Table 3-103
Byte 2	CAIC maximum lumens gain
Byte 3	CAIC clipping threshold

Table 3-103. Byte 1 Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7)	CAIC gain display enable: <ul style="list-style-type: none"> 0h: Disabled 1h: Enabled 						
b(6)	CAIC gain display scale: <ul style="list-style-type: none"> 0h: 100% = 1024 pixels 1h: 100% = 512 pixels 						
b(5:0)	Reserved						

Information on these parameters can be found in [Write CAIC Image Processing Control \(84h\)](#).

3.3.5 Write Color Coordinate Adjustment Control (86h)

This command controls the CCA image processing functionality for the display module.

3.3.5.1 Write Parameters

[Table 3-104](#) describes the write parameters.

Table 3-104. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:1)	Reserved						
b(0)	CCA enable: <ul style="list-style-type: none"> 0: Disabled 1: Enabled 						

This function must remain enabled during normal operation.

When CCA is disabled, an identity matrix is used.

3.3.6 Read Color Coordinate Adjustment Control (87h)

This command reads the state of the CCA image processing within the display module.

3.3.6.1 Read Parameters

This command has no read parameters.

3.3.6.2 Return Parameters

[Table 3-105](#) describes the return parameters.

Table 3-105. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:1)	Reserved						

b(0)	CCA enable: <ul style="list-style-type: none"> • 0: Disabled • 1: Enabled
------	---

3.3.7 Write Keystone Correction Control (88h)

This command controls the keystone correction image processing functionality for the display module.

3.3.7.1 Write Parameters

Table 3-106 describes the write parameters.

Table 3-106. Write Parameters

Parameter Bytes	Description
Byte 1	See Table 3-107
Byte 2	Optical throw ratio (LSByte)
Byte 3	Optical throw ratio (MSByte)
Byte 4	Optical DMD offset (LSByte)
Byte 5	Optical DMD offset (MSByte)

Table 3-107. Byte 1 Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0

b(7:1)	Reserved
b(0)	Keystone correction enable: <ul style="list-style-type: none"> • 0: Disabled • 1: Enabled

Keystone correction digitally compensates for distorted images when the projector is tilted up or down. Keystone correction is specified by the pitch angle (described in the *Write Keystone Projection Pitch Angle* command) and based on the throw ratio, vertical offset, and projector orientation. Each parameter is provided by this command. With this information, keystone correction corrects for both overall and local area aspect ratio distortion. For both full screen images and sub-images, the full active area of the DMD is keystone-corrected.

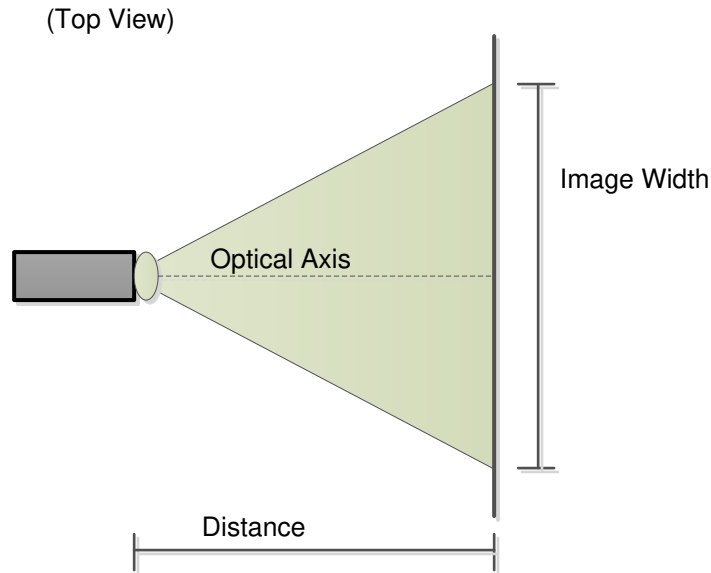
When keystone correction is enabled, the *Write Border Color* command sets the border color to black. Setting this parameter to any other color produces undesirable results.

Image rotation is allowed while keystone correction is enabled, but it may not be appropriate for all situations or configurations. The user is responsible for determining if the result is acceptable.

Table 3-108 shows the bit order and weighting for the optical throw ratio data. Figure 3-13 defines how this data is determined.

Table 3-108. Bit Weight Definition for the Optical Throw Ratio Data

b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
2 ⁷	2 ⁶	2 ⁵	2 ⁴	2 ³	2 ²	2 ¹	2 ⁰	2 ⁻¹	2 ⁻²	2 ⁻³	2 ⁻⁴	2 ⁻⁵	2 ⁻⁶	2 ⁻⁷	2 ⁻⁸



$$\text{Throw Ratio} = \text{Distance} / \text{Image Width}$$

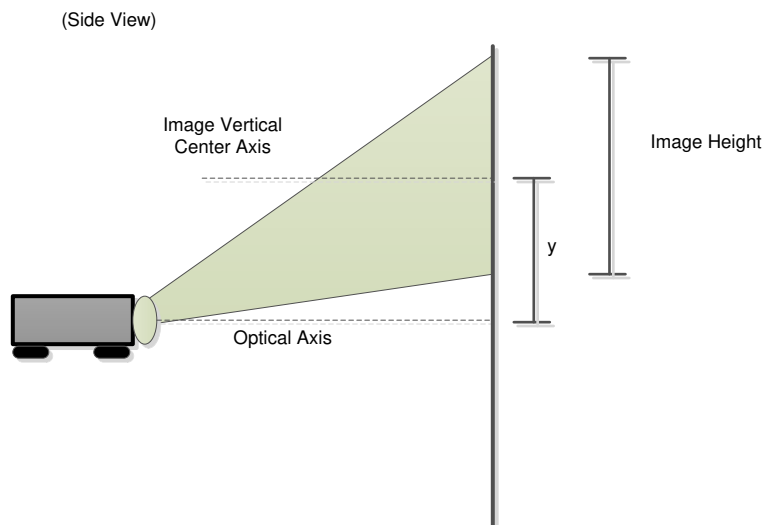
$$\text{Throw Ratio Register Value} = 256 \times \text{Throw Ratio}$$

Figure 3-13. Visual Definition and Calculation for Optical Throw Ratio Data

Table 3-109 shows the bit order and weighting for the two's complement optical DMD offset data. Figure 3-14 shows how this data is calculated, while Figure 3-15 shows how the sign of the offset data is determined. The user must insure that both the value and the sign of the offset data are correctly determined.

Table 3-109. Bit Weight Definition for the Optical DMD Offset Data

b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
2 ⁷	2 ⁶	2 ⁵	2 ⁴	2 ³	2 ²	2 ¹	2 ⁰	2 ⁻¹	2 ⁻²	2 ⁻³	2 ⁻⁴	2 ⁻⁵	2 ⁻⁶	2 ⁻⁷	2 ⁻⁸



$$\text{Vertical Offset} = 2 \times y / \text{Image Height}$$

$$\text{Vertical Offset Register Value} = 256 \times \text{Vertical Offset}$$

(Image Height is always a positive value, while 'y' can be positive or negative)

Figure 3-14. Method for Calculation for Optical DMD Offset Data

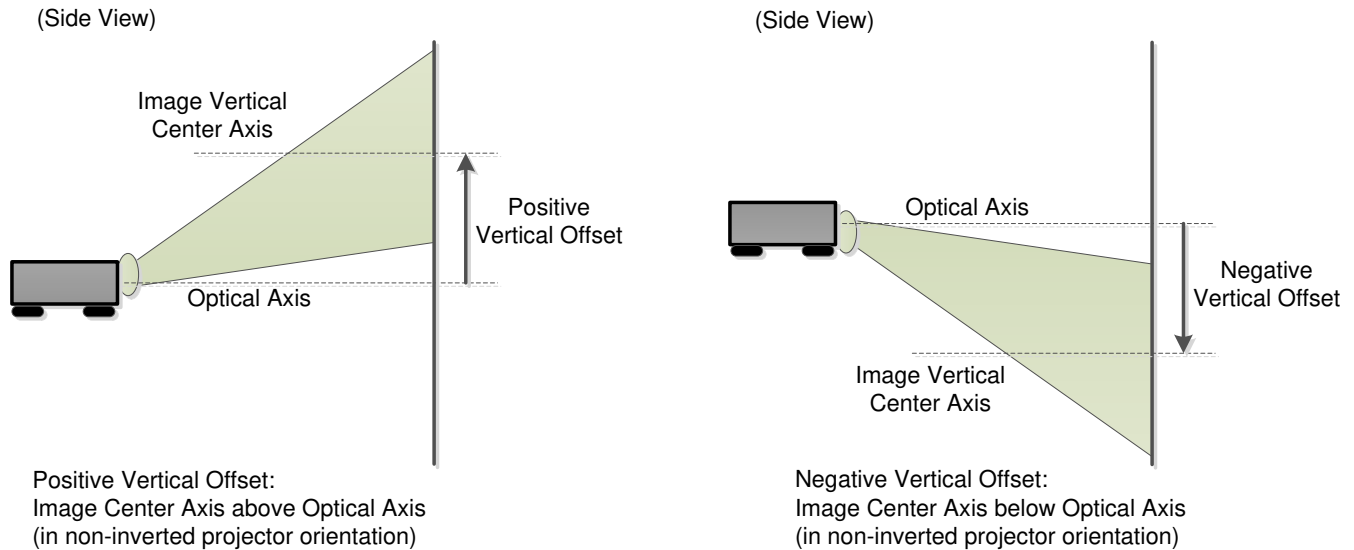


Figure 3-15. Sign Determination for Optical DMD Offset Data

Figure 3-16 shows examples of non-inverted and inverted projector orientation. This information is required for byte 1 of this command.

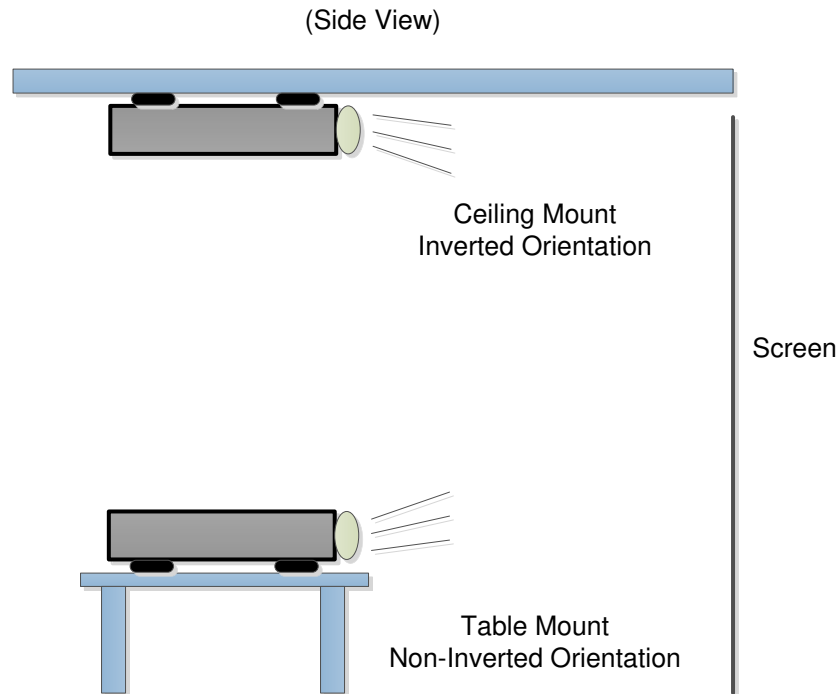


Figure 3-16. Examples of Non-Inverted and Inverted Projector Orientations

3.3.8 Read Keystone Correction Control (89h)

This command reads the state of the keystone correction image processing within the display module.

3.3.8.1 Read Parameters

This command has no read parameters.

3.3.8.2 Return Parameters

Table 3-110 describes the return parameters.

Table 3-110. Return Parameters

Parameter Bytes	Description
Byte 1	See Table 3-111
Byte 2	Optical throw ratio (LSByte)
Byte 3	Optical throw ratio (MSByte)
Byte 4	Optical DMD offset (LSByte)
Byte 5	Optical DMD offset (MSByte)

Table 3-111. Byte 1 Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:1)	Reserved						
b(0)	Keystone correction enable: <ul style="list-style-type: none"> 0: Disabled 1: Enabled 						

3.3.9 Write Keystone Projection Pitch Angle (BBh)

This command specifies the projection pitch angle for the display module.

3.3.9.1 Write Parameters

The following table describes the write parameters.

Table 3-112. Write Parameters

Parameter Bytes	Description
Byte 1	Projection pitch angle (LSByte)
Byte 2	Projection pitch angle (MSByte)

Default: 0000h

The following table shows the bit order and weighting for the 2's complement projection pitch angle data.

Table 3-113. Bit Weight Definition for the Projection Pitch Angle Data

b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
2 ⁷	2 ⁶	2 ⁵	2 ⁴	2 ³	2 ²	2 ¹	2 ⁰	2 ⁻¹	2 ⁻²	2 ⁻³	2 ⁻⁴	2 ⁻⁵	2 ⁻⁶	2 ⁻⁷	2 ⁻⁸

This command is used in conjunction with the [Section 3.3.7](#) command.

The projection pitch angle is limited to the range of –40 to 40 degrees. The following figure shows examples of the projection pitch angle.

(Side View)

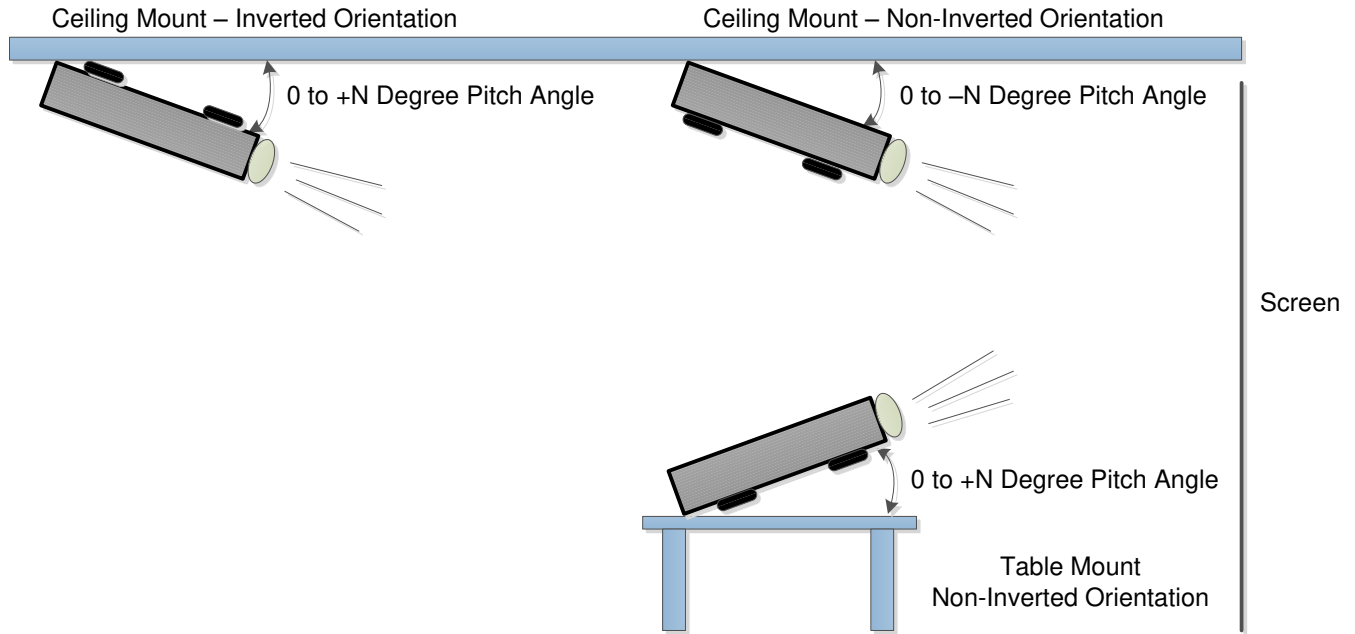


Figure 3-17. Examples of Projection Pitch Angle

3.3.10 Read Keystone Projection Pitch Angle (BCh)

This command reads the specified projection pitch angle for the display module.

3.3.10.1 Read Parameters

This command has no read parameters.

3.3.10.2 Return Parameters

The following table describes the return parameters.

Table 3-114. Return Parameters

Parameter Bytes	Description
Byte 1	Projection pitch angle (LSByte)
Byte 2	Projection pitch angle (MSByte)

3.4 General Setup Commands

3.4.1 Write Border Color (B2h)

This command specifies the onscreen border color for the display module.

3.4.1.1 Write Parameters

Table 3-115 describes the write parameters.

Table 3-115. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:3)	Reserved						
b(2:0)	Display border color: <ul style="list-style-type: none"> • 0h: Black • 1h: Red • 2h: Green • 3h: Blue • 4h: Cyan • 5h: Magenta • 6h: Yellow • 7h: White 						

Whenever the display image size is smaller than the active area of the DMD, the border color is used for all non-image pixels. Some examples using a border include a window box, pillar box, or letterbox image.

To display a pillar box image (see Figure 3-18), the user can use the border color defined by this command.

The border color specified by this command is separate from the curtain color defined in the *Display Image Curtain* command, though both display using the curtain capability.

Whenever the keystone capability is used, the user should set the border color to black. Setting this parameter to any other color when keystone is enabled produces undesirable results.

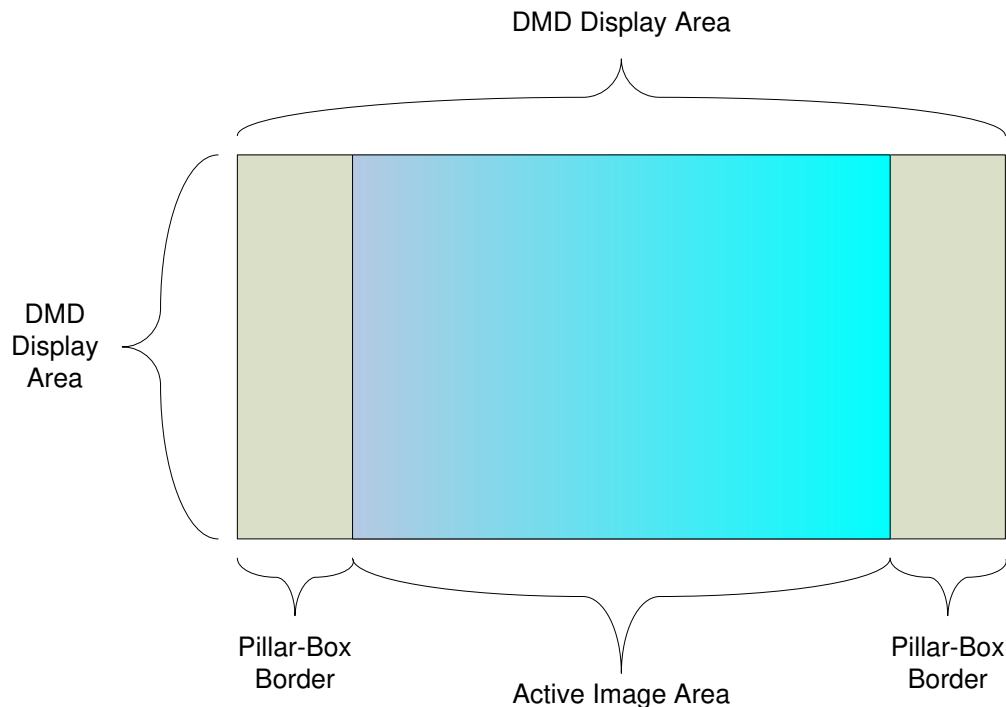


Figure 3-18. Pillar-Box Border Example

3.4.2 Read Border Color (B3h)

This command reads the state of the onscreen border color for the display module.

3.4.2.1 Read Parameters

This command has no read parameters.

3.4.2.2 Return Parameters

Table 3-116 describes the return parameters.

Table 3-116. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7)	Pillar-box border color source: <ul style="list-style-type: none"> 0h: Defined by this command 1h: Flash defined 24-bit color 						
b(6:3)	Reserved						
b(2:0)	Display border color: <ul style="list-style-type: none"> 0h: Black 1h: Red 2h: Green 3h: Blue 4h: Cyan 5h: Magenta 6h: Yellow 7h: White 						

For the special case of a pillar box image (see Figure 3-18), the user can use the border color defined by the *Write Border Color* command.

3.4.3 Write Parallel Interface Sync Polarity (B6h)

This command specifies the VSYNC and HSYNC polarity for the DLPC343x parallel interface.

3.4.3.1 Write Parameters

Table 3-117 describes the write parameters.

Table 3-117. Write Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:3)	Reserved						
b(2)	Manual Mode – Parallel Interface HSYNC Polarity: <ul style="list-style-type: none"> 0: Falling Edge Active (Negative Pulse) 1: Rising Edge Active (Positive Pulse) 						
b(1)	Manual Mode – Parallel Interface VSYNC Polarity: <ul style="list-style-type: none"> 0: Falling Edge Active (Negative Pulse) 1: Rising Edge Active (Positive Pulse) 						
b(0)	Parallel Interface Sync Polarity Mode: <ul style="list-style-type: none"> 0: In Automatic Mode 1: In Manual Mode 						

This command may be needed when using the parallel interface port. This command is not applicable for BT656 sources. In Automatic mode, the system can typically determine the appropriate polarity of the syncs. In Manual mode, the user is allowed to specify these polarities.

3.4.4 Read Parallel Interface Sync Polarity (B7h)

This command reads the state of the VSYNC and HSYNC polarity of the DLPC343x parallel interface.

3.4.4.1 Read Parameters

This command has no read parameters.

3.4.4.2 Return Parameters

Table 3-118 describes the return parameters.

Table 3-118. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:2)	Reserved						
b(1)	Manual Mode – Parallel Interface HSYNC Polarity:						
	<ul style="list-style-type: none"> • 0: Falling Edge Active (Negative Pulse) • 1: Rising Edge Active (Positive Pulse) 						
b(0)	Manual Mode – Parallel Interface VSYNC Polarity:						
	<ul style="list-style-type: none"> • 0: Falling Edge Active (Negative Pulse) • 1: Rising Edge Active (Positive Pulse) 						

3.4.5 Read Auto Framing Information (BAh)

This command is used to read data from the flash for the display module.

3.4.5.1 Read Parameters

This command has no read parameters.

3.4.5.2 Return Parameters

Table 3-119 describes the return parameters.

Table 3-119. Return Parameters

Parameter Bytes	Description
Byte 1	External Input VSYNC Rate (LSByte)
Byte 2	External Input VSYNC Rate
Byte 3	External Input VSYNC Rate
Byte 4	External Input VSYNC Rate (MSByte)
Byte 5	External Input Total Pixels per Line (LSByte)
Byte 6	External Input Total Pixels per Line (MSByte)
Byte 7	External Input Total Lines per Frame (LSByte)
Byte 8	External Input Total Lines per Frame (MSByte)
Byte 9	External Input Active Pixels per Line (LSByte)
Byte 10	External Input Active Pixels per Line (MSByte)
Byte 11	External Input Active Lines per Frame (LSByte)
Byte 12	External Input Active Lines per Frame (MSByte)
Byte 13	Pixel/Line Reference Clock Rate (LSByte)
Byte 14	Pixel/Line Reference Clock Rate (MSByte)

- In most cases, the above data can be measured by the system (even when manual data framing is used).
This data is provided for debug purposes only.
- The external input frame rate is returned as a count that is specified in units of 66.67ns (based on the internal 15MHz clock used to time between input frame syncs).
- The pixels per line and lines per frame parameters are to be '1' based. (that is, a value of 1280 active pixels indicates that there are 1280 active pixels per line).
- The pixels per line and lines per frame parameters are based on an internal sample clock for the CPU Bus or a measurement of the actual input pixel clock for the Parallel Bus. This clock rate is returned as the Pixel/Line Reference Clock Rate. This parameter value is the clock rate multiplied by 100 in MHz. For example, the parameter value for 60.00MHz is $60 \times 100 = 1770h$ (6000).

3.5 Administrative Commands

3.5.1 Read Short Status (D0h)

This command provides a short system status for the display module.

3.5.1.1 Read Parameters

This command has no read parameters.

3.5.1.2 Return Parameters

Table 3-120 describes the return parameters.

Table 3-120. Return Parameters

Parameter Bytes	Description
Byte 1	Short System Status

Table 3-121. Byte 1 Return Parameters

MSB	Byte 1 – General Status						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7)	Boot/main application: <ul style="list-style-type: none"> • 0: Boot • 1: Main 						
b(6)	Sensing Sequence error: <ul style="list-style-type: none"> • 0: No error • 1: Error 						
b(5)	Flash error: <ul style="list-style-type: none"> • 0: No error • 1: Error 						
b(4)	Flash erase Complete: <ul style="list-style-type: none"> • 0: Not Complete • 1: Complete 						
b(3)	System error: <ul style="list-style-type: none"> • 0: No error • 1: Error 						
b(2)	Reserved						
b(1)	Communication error: <ul style="list-style-type: none"> • 0: No error • 1: Error 						
b(0)	System initialization: <ul style="list-style-type: none"> • 0: Not complete • 1: Complete 						

The communication error bit indicates any error on the I²C command interfaces. Specific details about communication errors are available using the *Read Communication Status* command. Any errors other than a communication error are indicated by the system error bit. Specific details about system errors are available using the *Read System Status* command.

The communication error, and system error bits are cleared when the *Read Short Status* is read. The *Read Short Status* command must only be checked periodically, not continuously. Continuous access may severely impact system performance.

3.5.2 Read System Status (D1h)

This command reads system status information for the display module.

3.5.2.1 Read Parameters

This command has no read parameters.

3.5.2.2 Return Parameters

Table 3-122 describes the return parameters.

Table 3-122. Return Parameters

Parameter Bytes	Description
Byte 1	DMD interface status
Byte 2	LED status
Byte 3	Internal interrupt status
Byte 4	Misc. status

All system status error bits are cleared when the read system status is read.

Table 3-123. Byte 1 Return Parameters

MSB	Byte 1 – DMD Interface Status						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:3)	Reserved						
b(2)	DMD training error: <ul style="list-style-type: none"> • 0h: No error • 1h: Error 						
b(1)	DMD interface error: <ul style="list-style-type: none"> • 0h: No error • 1h: Error 						
b(0)	DMD device error: <ul style="list-style-type: none"> • 0h: No error • 1h: Error 						

The system sets the DMD device error for the following conditions:

- The system cannot read the DMD device ID from the DMD.
- The system-specified DMD device ID does not match the actual DMD device ID.

The system sets the DMD interface error when there are power management setup conflicts on this interface.

The system sets the DMD training error when the training algorithm cannot find a data eye that meets the specified requirements. For controller pins selected for training, DMD training will fail if less than 20 out of 50 continuous response values pass.

Table 3-124. Byte 2 Return Parameters

MSB	Byte 2 – LED Status						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7)	Reserved						
b(6)	LED no connection error: <ul style="list-style-type: none"> • 0h: No error • 1h: Error 						
b(5:3)	Reserved						

b(2)	Blue LED state: <ul style="list-style-type: none"> • 0h: Off • 1h: On
b(1)	Green LED state: <ul style="list-style-type: none"> • 0h: Off • 1h: On
b(0)	Red LED state: <ul style="list-style-type: none"> • 0h: Off • 1h: On

Table 3-125. Byte 3 Return Parameters

MSB	Byte 3 – Internal Interrupt Status						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:3)	Reserved						
b(2)	DC Power Low Voltage: <ul style="list-style-type: none"> • 0h: Supply voltage is normal • 1h: Supply voltage is low 						
b(1)	Sequence error: <ul style="list-style-type: none"> • 0h: No error • 1h: Error 						
b(0)	Sequence abort error: <ul style="list-style-type: none"> • 0h: No error • 1h: Error 						

The system reports **Sequence abort error** if Pulse Width Modulation (PWM) sequencer aborts the operation due to an error.

The system reports **Sequence error** if PWM sequencer encounter an error.

Table 3-126. Byte 4 Return Parameters

MSB	Byte 4 – Misc. Status						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:6)	Reserved						
b(5)	Watchdog timer timeout: <ul style="list-style-type: none"> • 0h: No timeout • 1h: Timeout 						
b(4)	Product configuration error: <ul style="list-style-type: none"> • 0h: No error • 1h: Error 						
b(3)	Leader versus follower operation: <ul style="list-style-type: none"> • 0h: Leader • 1h: Follower 						
b(2)	Single versus dual controller configuration: <ul style="list-style-type: none"> • 0h: Single • 1h: Dual 						
b(1:0)	Reserved						

The DLPC3437 chip set is a dual controller solution. One controller is configured as leader and the other as follower.

The system should report "Dual" configuration. Bit (3) will be Leader or Follower depending on which controller is responding to the command.

The system sets the product configuration error bit if it determines that some piece of the product configuration is not correct. Some examples are:

- Invalid controller or DMD combination
- Invalid controller or Power Management IC (DLPA200X or DLPA300X) combination
- Invalid flash build for the current controller, DMD, or Power Management IC configuration

The system sets the watchdog timer timeout bit if the system has been reset due to a watchdog timer timeout.

3.5.3 Read System Software Version (D2h)

This command reads the main application software version information for the display module.

3.5.3.1 Read Parameters

This command has no read parameters.

3.5.3.2 Return Parameters

[Table 3-127](#) describes the return parameters.

Table 3-127. Return Parameters

Parameter Bytes	Description
Byte 1	Controller main application software version – patch LSByte
Byte 2	Controller main application software version – patch MSByte
Byte 3	Controller main application software version – Minor
Byte 4	Controller main application software version – Major
Bytes 5 - 8	Reserved

3.5.4 Read Communication Status (D3h)

This command reads system status information for the display module.

3.5.4.1 Read Parameters

The read parameters are described in [Table 3-128](#).

Table 3-128. Read Parameters

Parameter Bytes	Description
Byte 1	Command bus status selection

Table 3-129. Byte 1 Read Parameters

MSB	Byte 1 – Command Bus Status Selection						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:2)	Reserved						
b(1:0)	Command bus status selection:						
	<ul style="list-style-type: none"> • 00: Reserved • 01: Reserved • 10: I²C only • 11: Reserved 						

This command returns the communication status for the specified command bus. For I²C only: This selection returns status bytes 5 through 6.

3.5.4.2 Return Parameters

Table 3-130 describes the return parameters.

Table 3-130. Return Parameters

Parameter Bytes	Description
Byte 1 - 4	Reserved
Byte 5	Communication status
Byte 6	Aborted op-code

All communication status error bits are cleared when the *Read Communication Status* is read.

Table 3-131. Byte 5 Return Parameters

MSB	Byte 5 – Communication Status						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7)	Reserved						
b(6)	Bus timeout by display error: <ul style="list-style-type: none"> • 0: No error • 1: Error 						
b(5)	Invalid number of command parameters: <ul style="list-style-type: none"> • 0: No error • 1: Error 						
b(4)	Read command error: <ul style="list-style-type: none"> • 0: No error • 1: Error 						
b(3)	Flash batch file error: <ul style="list-style-type: none"> • 0: No error • 1: Error 						
b(2)	Command processing error: <ul style="list-style-type: none"> • 0: No error • 1: Error 						
b(1)	Invalid command parameter value: <ul style="list-style-type: none"> • 0: No error • 1: Error 						
b(0)	Invalid command error: <ul style="list-style-type: none"> • 0: No error • 1: Error 						

The system sets the invalid command error bit when it does not recognize the command op-code. The invalid command op-code is reported in the I²C CMD error op-code byte of this status.

The system sets the invalid command parameter error bit when it detects that the value of a command parameter is not valid (for example, out of the allowed range).

The system sets the command processing error bit when a fault is detected when processing a command. In this case, the command aborts and the system moves on to the next command. The op-code for the aborted command is reported in the I²C CMD error op-code byte of this status.

The system sets the flash batch file error bit when an error occurs during the processing of a flash batch file. When this bit is set, typically another bit is set to indicate what kind of error was detected (for example, an invalid command error).

The system sets the read command error bit when the host terminates the read operation before all of the requested data has been provided, or if the host continues to request read data after all of the requested data has been provided.

The system sets the invalid number of command parameters error bit when too many or too few command parameters are received. In this case, the command aborts and the system moves on to the next command. The op-code for the aborted command is reported in the I²C CMD error op-code byte of this status.

The system sets the bus timeout by display error bit when the display releases control of the bus after the bus timeout value is exceeded.

Table 3-132. Byte 6 Return Parameters

MSB	Byte 6 – CMD Error Op-Code						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:0)		I ² C CMD error op-code					

The CMD error op-code is associated with various I²C communication status bits, and reports the op-code for an I²C command as noted.

3.5.5 Read Controller Device ID (D4h)

This command reads the Controller Device ID for the display module.

3.5.5.1 Read Parameters

This command has no read parameters.

3.5.5.2 Return Parameters

[Table 3-133](#) describes the return parameters.

Table 3-133. Return Parameters

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:4)		Reserved					
b(3:0)		Controller device ID					

The controller device ID are decoded using [Table 3-134](#).

Table 3-134. Controller Device ID Decode

Controller Device ID	Device Number	Application
09h	DLPC3437	Standalone (SD)

Unused controller device ID values are reserved.

3.5.6 Read DMD Device ID (D5h)

This command reads the DMD device ID for the display module.

3.5.6.1 Read Parameters

The read parameters are described in [Table 3-135](#).

Table 3-135. Read Parameters

MSB	Byte 1 – DMD Register Selection						LSB
b7	b6	b5	b4	b3	b2	b1	b0
b(7:3)		Reserved					

b(2:0)	DMD data selection: <ul style="list-style-type: none"> • 0h: DMD device ID • 1h – 7h: Reserved
--------	--

3.5.6.2 Return Parameters

Table 3-136 describes the return parameters.

Table 3-136. DMD Device ID Reference Table

DMD Device ID				Device Description
Byte 1 (Identifier)	Byte 2 (Byte Count)	Byte 3 (ID-msbyte)	Byte 4 (ID-lsbyte)	Resolution and Type
60h	0Bh	00h	7Fh/8Bh	0.33 1080P (1920x1080, Sub-LVDS)

3.5.7 Read System Temperature (D6h)

This command is used to read the system temperature using an external thermistor (if available).

3.5.7.1 Read Parameters

The command has no read parameters.

3.5.7.2 Return Parameters

Table 3-137 describes the return parameters.

Table 3-137. Return Parameters

Parameter Bytes	Description
Byte 1	LSByte
Byte 2	MSByte

Figure 3-19 shows the bit order and definition for the signed magnitude system temperature data, which will be returned in degrees C. The unspecified msbits (bits 15:12) will be set to '0'.

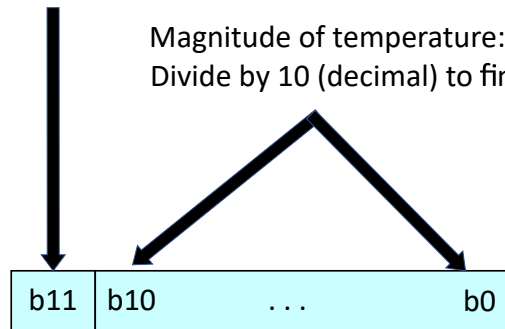
Sign of temperature:

0 = positive temperature:

1 = negative temperature:

Magnitude of temperature:

Divide by 10 (decimal) to find magnitude.



Example #1: b(11:0) = 000110101010
426d/10d = +42.6°C

Example #1: b(11:0) = 100110101010
426d/10d = -42.6°C

Figure 3-19. Bit Order and Definition for System Temperature

3.5.8 Read Flash Build Version (D9h)

This command reads the controller flash version for the display module.

3.5.8.1 Read Parameters

The command has no read parameters.

3.5.8.2 Return Parameters

Table 3-138 describes the return parameters.

Table 3-138. Return Parameters

Parameter Bytes	Description
Byte 1	Flash build version – patch LSBByte
Byte 2	Flash build version – patch MSByte
Byte 3	Flash build version – Minor
Byte 4	Flash build version – Major

The user specifies a version number for the controller flash build in the format specified by this command. This command allows the user to read back this version information.

3.5.9 Write Flash Batch File Delay (DBh)

This command is used to specify an execution time delay within a flash batch file for the display module.

3.5.9.1 Write Parameters (DBh)

Table 3-139 describes the write parameters.

Table 3-139. Write Parameters

Parameter Bytes	Description
Byte 1	Flash batch file delay (LSB)
Byte 2	Flash batch file delay (MSB)

This command is used to specify an execution delay time within a flash batch file. It can only be used within a flash batch file, and is not a valid command on the I²C interfaces.

The flash batch file delay is to be specified in units of 1 ms (for example, 500 ms = 1F4h).

Typical use of this command is in the auto-init flash batch file (batch file 0), but is valid for use in any batch file (See [Write Execute Flash Batch File \(2Dh\)](#)).

3.5.10 Read DMD I/F Training Data (DCh)

This command is used to read back the DMD interface training data for the display module.

3.5.10.1 Read Parameters

Table 3-140 describes the command read parameters.

Table 3-140. Read Parameters

Parameter Bytes	Description
Byte 1	DMD I/F training data selection (see below)

MSB	Byte 1 – DMD I/F Data Selection						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Table 3-141. Byte 1 Read DMD I/F Training Data (DCh) Register Field Descriptions

Bit	Type	Description
7-5	R	Reserved

Table 3-141. Byte 1 Read DMD I/F Training Data (DCh) Register Field Descriptions (continued)

Bit	Type	Description
4	R	Training data selection 0h = High/Low/Selected 1h = Full profile
3-0	R	Controller pin pair selection 0h = A 1h = B 2h = C 3h = D 4h = E 5h = F 6h = G 7h = H 8h - Fh = Reserved

This command returns the DMD I/F training data specified for the controller pin pair specified.

- High/Low/Selected: This selection returns bytes 1 through 4
- Full profile: This selection returns bytes 5 through 11

3.5.10.2 Return Parameters

Table 3-142 describes the return parameters.

Table 3-142. DMD I/F Training Data Return Parameters

Parameter Bytes	Description
Byte 1	High/Low/Selected (see below) (LSB)
Byte 2	High/Low/Selected (see below)
Byte 3	High/Low/Selected (see below)
Byte 4	High/Low/Selected (see below) (MSB)
Byte 5	Full profile (bits 7-0) (LSB)
Byte 6	Full profile (bits 15-8)
Byte 7	Full profile (bits 23-16)
Byte 8	Full profile (bits 31-24)
Byte 9	Full profile (bits 39-32)
Byte 10	Full profile (bits 47-40)
Byte 11	Full profile (bits 50-48) (MSB)

MSB	Byte 1 - 4						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Table 3-143. Byte 1 Read DMD I/F Training Data (DCh) Register Field Descriptions

Bit	Type	Description
7-6	R	Reserved
5	R	Training error 0h = No error 1h = Error
4	R	Pin pair selected for training 0h = No 1h = Yes

Table 3-143. Byte 1 Read DMD I/F Training Data (DCh) Register Field Descriptions (continued)

Bit	Type	Description
3-0	R	Controller pin pair selection 0h = A 1h = B 2h = C 3h = D 4h = E 5h = F 6h = G 7h = H 8h - Fh = Reserved

Table 3-144. Byte 2 Read DMD I/F Training Data (DCh) Register Field Descriptions

Bit	Type	Description
7-6	R	Reserved
5-0	R	Selected DLL (delay-locked loop) value

Table 3-145. Byte 3 Read DMD I/F Training Data (DCh) Register Field Descriptions

Bit	Type	Description
7-6	R	Reserved
5-0	R	Low pass DLL value

Table 3-146. Byte 4 Read DMD I/F Training Data (DCh) Register Field Descriptions

Bit	Type	Description
7-6	R	Reserved
5-0	R	High pass DLL value

This command is typically used for debug or characterization of the controller to DMD interface.

The return data is specified by the read parameter data.

DMD I/F training tests/calibrates the DLL that is associated with each controller pin pair, trying each of the DLL parameter values (0 to 50), looking for a pass ('0') or fail ('1') response for each value. Thus, the full training profile for each pin pair is made up of a 51 bit pass/fail result. This result is provided on full profile bits 50:0.

The full profile response has a region of passing DLL values. The highest DLL value for this region is returned as the high pass DLL value, the smallest DLL value is returned as the low pass DLL value, and the algorithm selected value as the selected DLL value.

This command does not run the DMD I/F training algorithm. This is done automatically by the system. This command returns the result from the most recent training event.

3.6 Flash Update Commands

Note that the flash commands described in this section cannot be used within batch files.

3.6.1 Read Flash Update PreCheck (DDh)

This command is used to verify that a pending flash update (write) is appropriate for the specified block of the display module flash.

3.6.1.1 Read Parameters

Table 3-147 describes the read parameters.

Table 3-147. Read Parameters

Parameter Bytes	Description
Byte 1	Flash build data size (LSB)
Byte 2	Flash build data size
Byte 3	Flash build data size
Byte 4	Flash build data size (MSB)

3.6.1.2 Return Parameters

Table 3-148 describes the return parameters.

Table 3-148. Return Parameters

MSB	Byte 1 - Flash PreCheck Results						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Table 3-149. Flash Update PreCheck Register Field Descriptions

Bit	Type	Description
7-3	R	Reserved
2	R	Package configuration (identifier) 0h = No error 1h = Error
1	R	Package configuration (collapsed) 0h = No error 1h = Error
0	R	Package size 0h = No error 1h = Error

This command is used in conjunction with the flash data type select command. This command would be sent after the flash data type has been selected, but before any other flash operation. The purpose is to verify that the desired flash update is compatible, and will fit within the existing flash space, for the current flash configuration.

The flash build data size specifies the size of the flash update package in bytes.

When the controller software receives the flash build data size, it will verify that the package is appropriate for the specified location. This includes size, identifier, sequence build type, and so forth.

A package size error indicates that the flash package is too large to fit into the specified location. A few examples are listed:

- If replacing the entire flash, the size of the flash build exceeds the size of the flash device in the system.
- If replacing the entire flash except for the user blocks, the size of the flash build will either overwrite some portion of the existing user blocks, or exceed the size of the flash device in the system.
- If replacing the look block, the size of the flash build exceeds the size of the existing look block in the flash.
- If replacing a single sequence (for example, a partial update), the size of the flash build exceeds the size of the existing splash screen.

A package configuration error indicates that the flash package is not appropriate for the flash update requested. An example is listed below.

- If replacing a single splash screen (for example, a partial update), and the specified splash screen index value (identifier) is not being used in the flash build. Partial updates can only replace an existing flash entity.

If an error is returned by this command, the user is responsible for correcting the error before updating the flash. If the user chooses to ignore the error and update the flash anyway, the system will allow this. In this case, the user is responsible for any problems or system behaviors that arise from this. It should also be noted that this pre-check does *not* cover all possible mismatches that might arise when replacing blocks or partial blocks in the flash.

3.6.2 Write Flash Data Type Select (DEh)

This command is used to specify the type of data that will be written to or read from the flash of the display module.

3.6.2.1 Write Parameters

[Table 3-150](#) describes the write parameters.

Table 3-150. Write Parameters

Parameter Bytes		Description	
Byte 1		Flash data type (See below)	
Byte 2		Optional: Partial data identifier (See Byte 1 Below)	
Byte 3		Optional: Partial data identifier (See Byte 1 Below)	
Byte 4		Optional: Partial data identifier (See Byte 1 Below)	

MSB	Byte 1						LSB
b7	b6	b5	b4	b3	b2	b1	b0

Table 3-151. Flash Data Type Select Register Field Descriptions

Bit	Type	Description - Flash data type
7-0	W	<p>Entire flash 00h = Entire flash 01h = Reserved 02h = Entire flash except user calibration data and user scratchpad data 03h - 0Fh = Reserved</p> <p>TI software 10h = Main software application 11h - 1Fh = Reserved</p> <p>TI application data 20h = TI application data set (AOM) 21h - 2Fh = Reserved</p> <p>User batch files 30h = User batch files 31h - 3Fh = Reserved</p> <p>Look data 40h = Look data set 41h - 4Fh = Reserved</p> <p>Sequence data 50h = Entire sequence data set 51h = Entire sequence data set (Reads only) 52h - 5Fh = Reserved</p> <p>Degamma/CMT data 60h = Entire degamma/CMT data set 61h = Partial degamma/CMT data set (reads only) 62h - 6Fh = Reserved</p> <p>CCA data 70h = CCA data set 71h - 7Fh = Reserved</p> <p>General LUT data 80h = CCA data set</p>

The flash data type command must be provided each time a new flash write or read operation is desired to ensure that the appropriate data type parameters are provided. The system expects four parameter bytes regardless of whether all four bytes are needed. Any unused bytes should be set to zero.

The flash data length must be provided to indicate the amount of flash data that will be provided for each write or read transaction.

The specified flash data will be written to or read from flash using the [Write Flash Start \(E1h\)](#), [Write Flash Continue \(E2h\)](#), [Read Flash Start \(E3h\)](#), and [Read Flash Continue \(E4h\)](#) commands.

While all of the flash data sets indicated can be written/replaced in their entirety, a few will also support partial writes/updates. Partial update command parameters will use an “odd” command number (for example, 91h, B1h) which will indicate that one to three additional command parameter bytes of information must be provided to specify which subset of data is to be updated. The additional command parameter data required is described below.

Table 3-152. Command Parameters for Partial Flash Data Set

Data Type (Writes Only)	2nd CMD Parameter (Byte 2)	3rd CMD Parameter (Byte 2)	4th CMD Parameter (Byte 2)	Comments
Partial user splash screen set	Splash number	N/A	N/A	A splash screen will be specified by its splash screen number
Partial user scratchpad data set	Sector number	N/A	N/A	If this data set is allocated more than one sector, each sector can be specified (0 = 1st sector, 1 = 2nd sector, and so forth)

Table 3-152. Command Parameters for Partial Flash Data Set (continued)

Data Type (Writes Only)	2nd CMD Parameter (Byte 2)	3rd CMD Parameter (Byte 2)	4th CMD Parameter (Byte 2)	Comments
Partial sequence data set	Look number	Sequence index number		A sequence data set will be specified by its sequence index number.
Partial CMT data set	Look number	Sequence index number		A CMT data set will be specified by its CMT index number.
Partial user splash screen set	Splash number	N/A	N/A	A Splash screen will be specified by its Splash screen number.
Partial user scratchpad data set	Splash number	Sub-sector address (LSB)	Sub-sector address (MSB)	If this data set is allocated more than one sector, each sector can be specified (0 = 1st sector, 1 = 2nd sector, and so forth) The host is also allowed to specify the start address within the sector specified in byte 2. This address needs to be a relative address within the specified sector (that is, the value can range from 0 to 4096), and must be a 32-bit aligned byte address.

While all of the flash data sets indicated can be read starting at the beginning of the data set, a few will also support read starts at the beginning of a data subset. The partial update command parameters which use an “odd” command number (for example, 41h, 43h, 75h) will indicate that one to three additional command parameter bytes must be provided to specify the start location for these reads. The additional command parameter data required is described in the previous table.

It is expected that all TI formatted factory calibration data, including the golden ratio, the power-up RGB currents, and the user thermister LUT trim data, will be stored in the user calibration block of the flash. It will be the responsibility of the user to manage updates to this block, which may require the user to read the entire block, modify, and then rewrite the entire block when making an update within the block.

While flash processing requires that flash commands be executed in the proper order (for example, flash must be erased prior to being written), due to the flexibility provided for flash updates, command order checking is not provided.

It is recommended that the user make use of the [Read Flash Update PreCheck \(DDh\)](#) command before updating an existing flash build.

The system allows the user to allocate up to four separable blocks of flash space for their own use (user scratchpad data). The user can also specify the size of each of these blocks, where each block can be one or more sectors in (one sector = 4 kB). This is all defined via the GUI. It is the responsibility of the user to manage these data sets, including updates, which may require the user to read an entire sector, modify, and then rewrite the entire sector when making an update within a sector. References to an unavailable data set will result in an invalid command parameter value error in the communication status.

3.6.3 Write Flash Data Length (DFh)

This command is used to specify the length of the data that will be written to or read from the flash of the display module.

3.6.3.1 Write Parameters

[Table 3-153](#) describes the write parameters.

Table 3-153. Write Parameters

Parameter Bytes	Description
Byte 1	Flash data length (LSB)
Byte 2	Flash data length (MSB)

Flash data length must be a multiple of four bytes.

The flash data length applies to each write or read transaction, not to the length of the data type selected.

The maximum data length allowed for each write transaction is 1024 bytes. The maximum data length allowed for each read transaction is 256 bytes.

While flash processing requires that flash commands be executed in the proper order (for example, flash must be erased prior to being written), due to the flexibility provided for flash updates, command order checking is not provided.

3.6.4 Write Erase Flash Data (E0h)

This command directs the display module to erase the specified flash data.

3.6.4.1 Write Parameters

Table 3-154 describes the write parameters.

Table 3-154. Write Parameters

Parameter Bytes	Description
Byte 1	Signature: Value = AAh
Byte 2	Signature: Value = BBh
Byte 3	Signature: Value = CCh
Byte 4	Signature: Value = DDh

When this command is executed, the system will erase all sectors associated with the data type specified by the flash data type select command. As such, this command does not make use of the flash data length parameter

Since the process of erasing flash sectors can take a significant amount of time, the flash erase complete status bit in the read short status command should be checked periodically (not continuously) to determine when this task has been completed. This bit will be set at the start of the erase process, and will be cleared when the erase process is complete. Flash writes should not be started before the erase process has been completed.

While flash processing requires that flash commands be executed in the proper order (for example, flash must be erased prior to being written), due to the flexibility provided for flash updates, command order checking is not provided.

The signature bytes are used to minimize unintended flash erases. The command opcode and four signature bytes must be received correctly before this command will be recognized and executed.

3.6.5 Write Flash Start (E1h)

This command is used to write data to the flash for the display module.

3.6.5.1 Write Parameters

Table 3-155 describes the write parameters.

Table 3-155. Write Parameters

Parameter Bytes	Description
Byte 1	Data byte 1
Byte 2	Data byte 2
Byte 3	Data byte 3
Byte 4	Data byte 4
Byte 5 ... n	Data byte 5 ... n

The flash data length command must be used to specify how much data will be sent by the write flash start command.

The write flash start command is used to write up to 1024 bytes of data starting at the first address of the data type selected. If more than 1024 bytes are to be written, the write flash continue command must be used. Up to 1024 bytes of data can be written with each write flash continue command, which starts at the end of the last data written.

The flash error bit of the write short status command will indicate if the flash update was successful. This bit will be set for an error at the end of each write transaction, however, once an error has been detected, this bit will remain in the error state until a new data type is selected (selecting a new data type will clear this bit). This will allow the user the option of checking the status between each write transaction, or at the end of the update of a specific data type. Once a write transaction has started, the flash status (and this error bit) will not be accessible until the write transaction has completed.

While flash processing requires that flash commands be executed in the proper order (for example, flash must be erased prior to being written), due to the flexibility provided for flash updates, command order checking is not provided.

3.6.6 Write Flash Continue (E2h)

This command is used if more than 1024 bytes of data has to be written to flash of the display module.

3.6.6.1 Write Parameters

Table 3-156 describes the write parameters.

Table 3-156. Write Parameters

Parameter Bytes	Description
Byte 1	Data byte 1
Byte 2	Data byte 2
Byte 3	Data byte 3
Byte 4	Data byte 4
Byte 5 ... n	Data byte 5 ... n

The flash data length command must be used to specify how much data will be sent by the write flash start command.

The [Write Flash Start \(E1h\)](#) command is used to write up to 1024 bytes of data starting at the first address of the data type selected. If more than 1024 bytes are to be written, the write flash continue command must be used. Up to 1024 bytes of data can be written with each write flash continue command, which starts at the end of the last data written.

The flash error bit of the write short status command will indicate if the flash update was successful. This bit will be set for an error at the end of each write transaction, however, once an error has been detected, this bit will remain in the error state until a new data type is selected (selecting a new data type will clear this bit). This will allow the user the option of checking the status between each write transaction, or at the end of the update of a specific data type. Once a write transaction has started, the flash status (and this error bit) will not be accessible until the write transaction has completed.

While flash processing requires that flash commands be executed in the proper order (for example, flash must be erased prior to being written), due to the flexibility provided for flash updates, command order checking is not provided.

3.6.7 Read Flash Start (E3h)

This command is used to read data from the flash for the display module.

3.6.7.1 Read Parameters

The command has no read parameters.

3.6.7.2 Return Parameters

Table 3-157 describes the return parameters.

Table 3-157. Return Parameters

Parameter Bytes	Description
Byte 1	Data byte 1
Byte 2	Data byte 2
Byte 3	Data byte 3

Table 3-157. Return Parameters (continued)

Parameter Bytes	Description
Byte 4	Data byte 4
Byte 5 ... n	Data byte 5 ... n

The flash data length command must be used to specify how much data is to be read by the read flash start command.

The read flash start command is used to read up to 256 bytes of data starting at the specified address, or at the first address of the data type selected. If more than 256 bytes are to be read, the read flash continue command must be used. Up to 256 bytes of data can be read with each read flash continue command, which starts at the end of the last data read.

While flash processing requires that flash commands be executed in the proper order (for example, flash must be erased prior to being written), due to the flexibility provided for flash updates, command order checking is not provided.

3.6.8 Read Flash Continue (E4h)

This command is used to continue to read data (if more than 256 bytes) from the flash for the display module.

3.6.8.1 Read Parameters

The command has no read parameters.

3.6.8.2 Return Parameters

[Table 3-158](#) describes the return parameters.

Table 3-158. Return Parameters

Parameter Bytes	Description
Byte 1	Data byte 1
Byte 2	Data byte 2
Byte 3	Data byte 3
Byte 4	Data byte 4
Byte 5 ... n	Data byte 5 ... n

The flash data length command must be used to specify how much data is to be read by the read flash continue command.

The read flash start command is used to read up to 256 bytes of data starting at the specified address, or at the first address of the data type selected. If more than 256 bytes are to be read, the read flash continue command must be used. Up to 256 bytes of data can be read with each read flash continue command, which starts at the end of the last data read.

While flash processing requires that flash commands be executed in the proper order (for example, flash must be erased prior to being written), due to the flexibility provided for flash updates, command order checking is not provided.

4 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision A (May 2019) to Revision B (January 2024)	Page
• Updates throughout document for consistency across related products.....	2

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