

A

B

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D

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B

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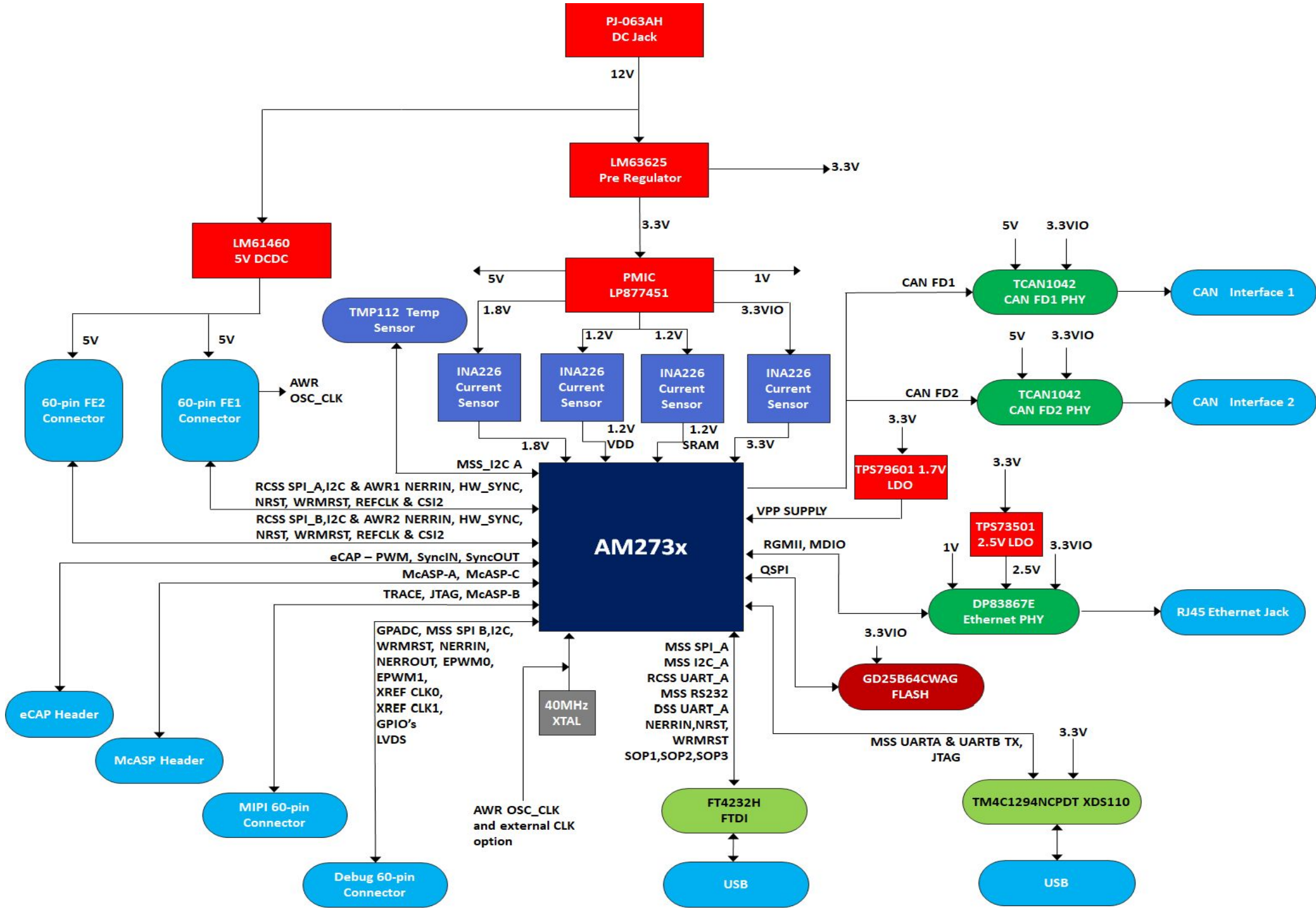
D

Revision History

Rev	ECN #	Approved Date	Approved by	Notes
B	1	11/16/2020	Adrian Ozer	1. QSPI Flash updated to GD25B64CWAG for bootloader compatibility
B	2	11/16/2020	Adrian Ozer	2. R416 made DNP by default
B	3	11/16/2020	Adrian Ozer	3. 10k pull up added to QSPI SO pin
C	4	04/07/2022	Mike Pridgen	4. McASP / IO Mux & eCAP Circuitry Added 5. Added 3V3 Pre_Regulator and Chariot PMIC Circuits 6. MUX_TRACE_DATA[6..0] Bus net names updated 7. RCSS_SPIB_HOSTIRQ0 is connected to U29.18 for MCPC_DAT4 8. U29.39 net name changed from RCSS_SPIB_MOSI 9. eCAP Header changed to 4 pins part

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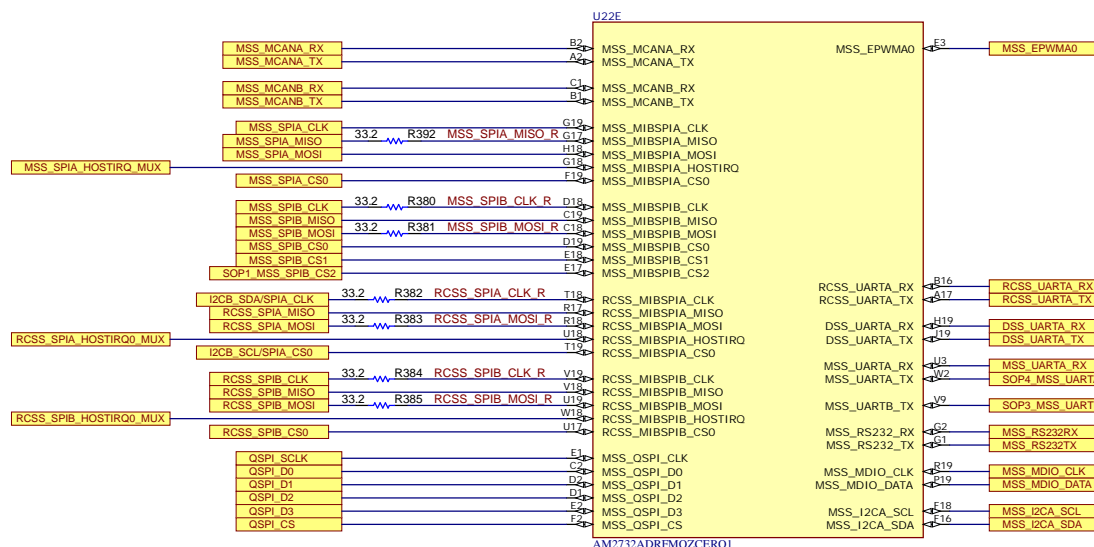
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TID #: <a href="#">N/A</a>	Project Title: <a href="#">TMDS273GPEVM</a>	
Number: <a href="#">PROC103</a>	Rev: <a href="#">C</a>	Sheet Title: <a href="#">CoverSheet</a>
SVN Rev: <a href="#">308 [Locally Modified]</a>	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">1</a> of <a href="#">28</a>
Drawn By: <a href="#">Adrian Ozer/Mike Pridgen</a>	File: <a href="#">PROC103C_CoverSheet.SchDoc</a>	Size: <a href="#">B</a>
Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>	Contact: <a href="#">http://www.ti.com/support</a>	

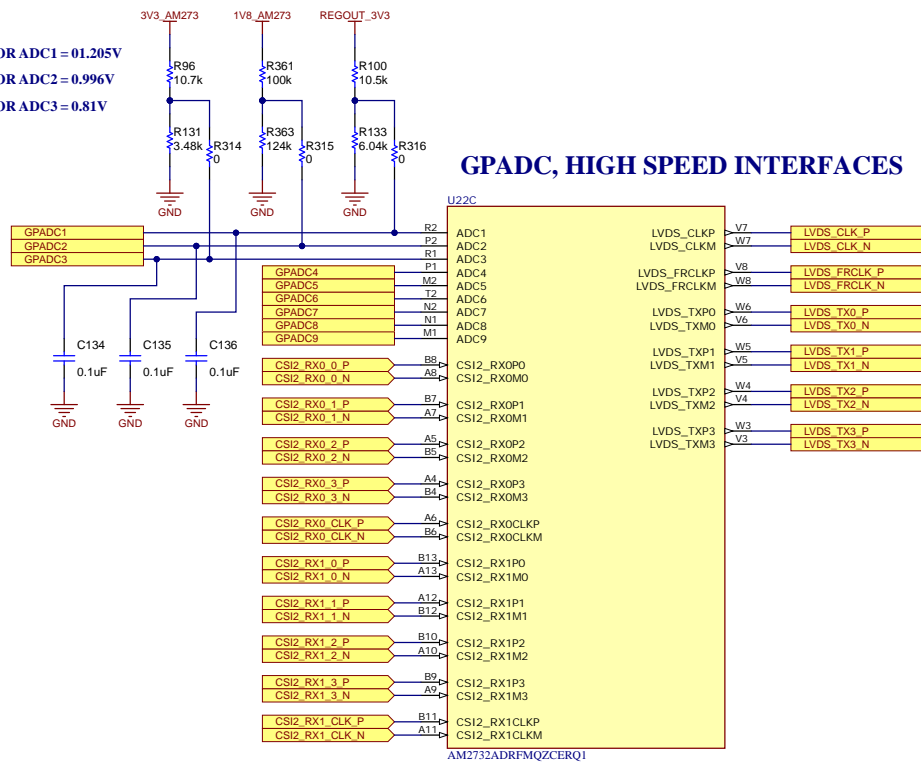
## AM273x IO REFERENCE



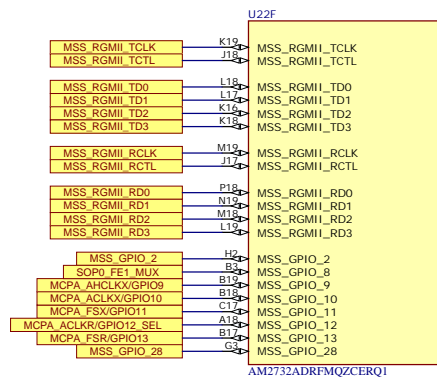
## SPI, CAN, QSPI, UART, I2C, MDI



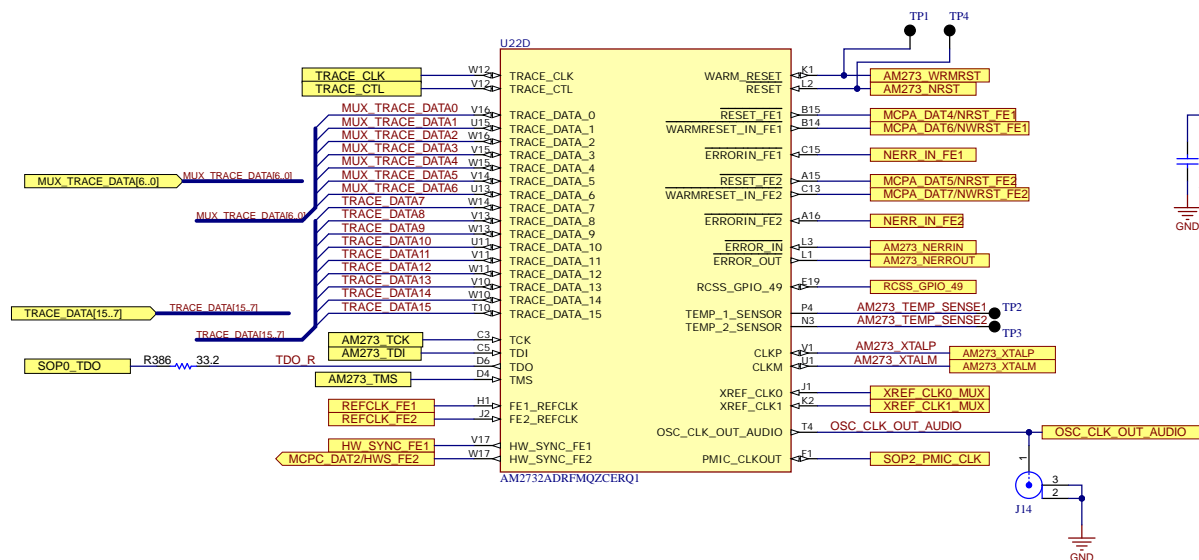
## GPADC, HIGH SPEED INTERFACES



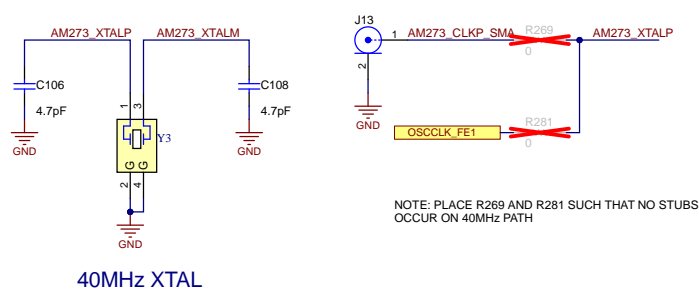
## RGMII, GPIO



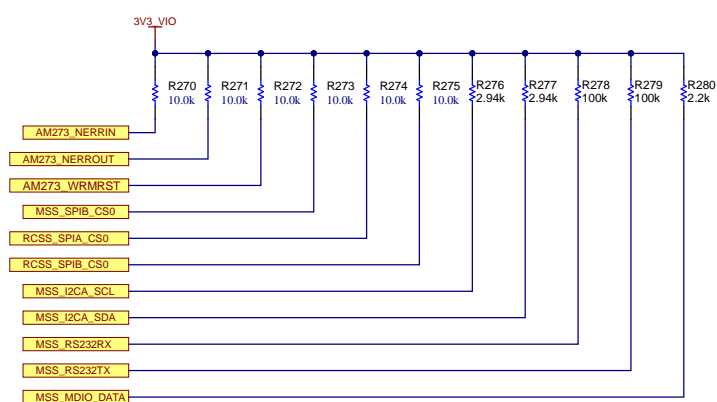
## TRACE, JTAG, RESET, ERROR, CLK



## 40MHZ CLOCK SOURCES

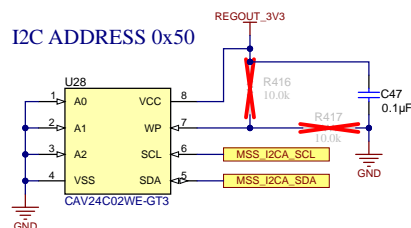


## PULLUPS/DOWNS

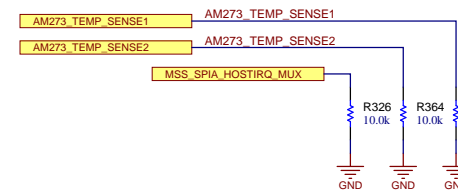
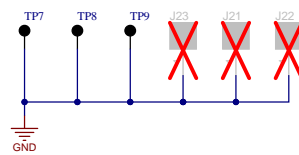


## BOARD ID EEPROM

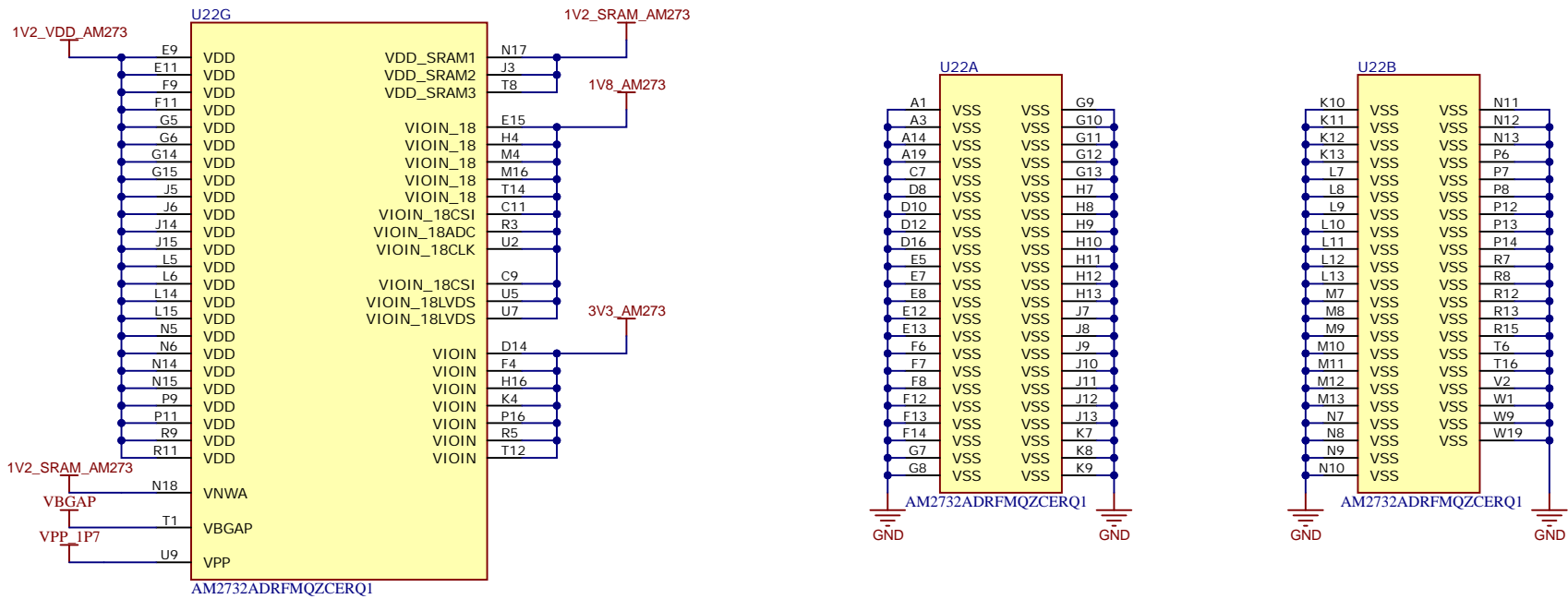
I2C ADDRESS 0x50



## GND TEST POINTS



AM273x POWER REFERENCE

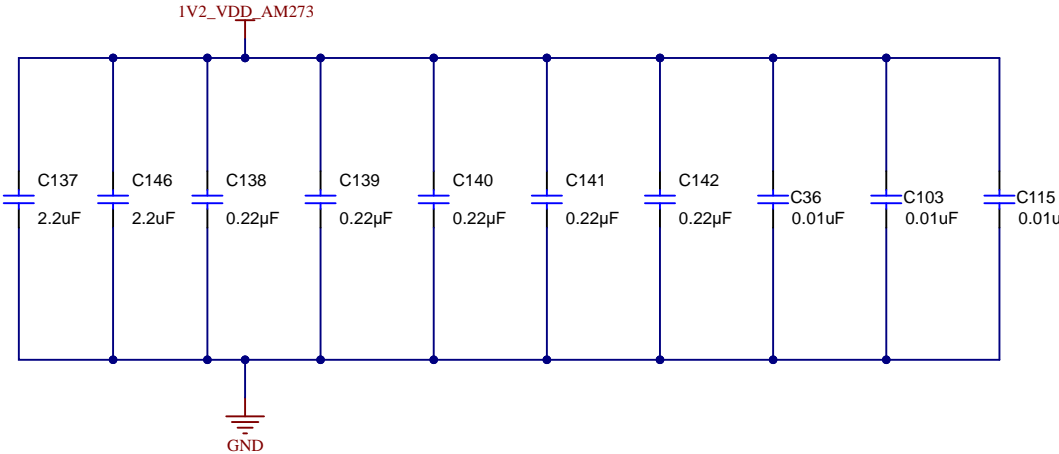


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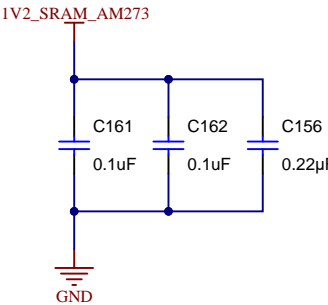
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TID #: <a href="#">N/A</a>	Project Title: <a href="#">TMDS273GPEVM</a>	
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SVN Rev: <a href="#">308 [Locally Modified]</a>	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">3</a> of <a href="#">28</a>
Drawn By:	File: <a href="#">PROC103C_AM273x_PWR_Reference.SchDoc</a>	Size: B
Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>	Contact: <a href="#">http://www.ti.com/support</a>	

AM273x DECOUPLING REFERENCE

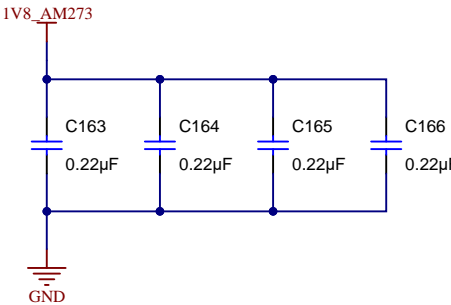
1.2V DIGITAL SUPPLY



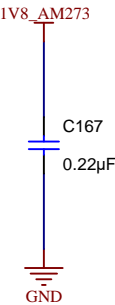
SRAM SUPPLY



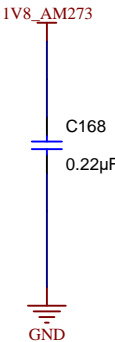
1.8V IO SUPPLY



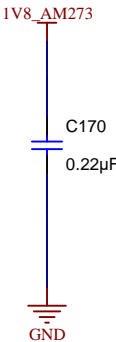
1.8V ADC SUPPLY



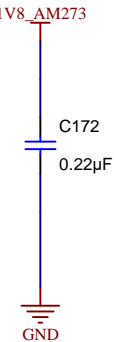
1.8V CLOCK SUPPLY



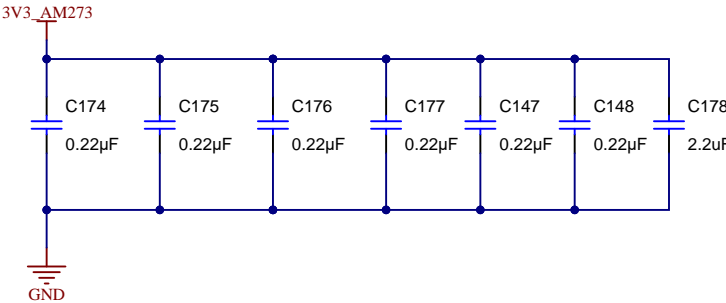
1.8V CSI SUPPLY



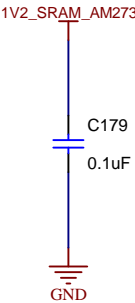
1.8V LVDS SUPPLY



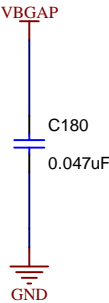
3.3V IO SUPPLY



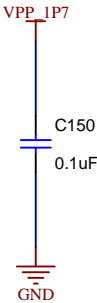
VNWA SUPPLY



BANDGAP SUPPLY



VPP SUPPLY



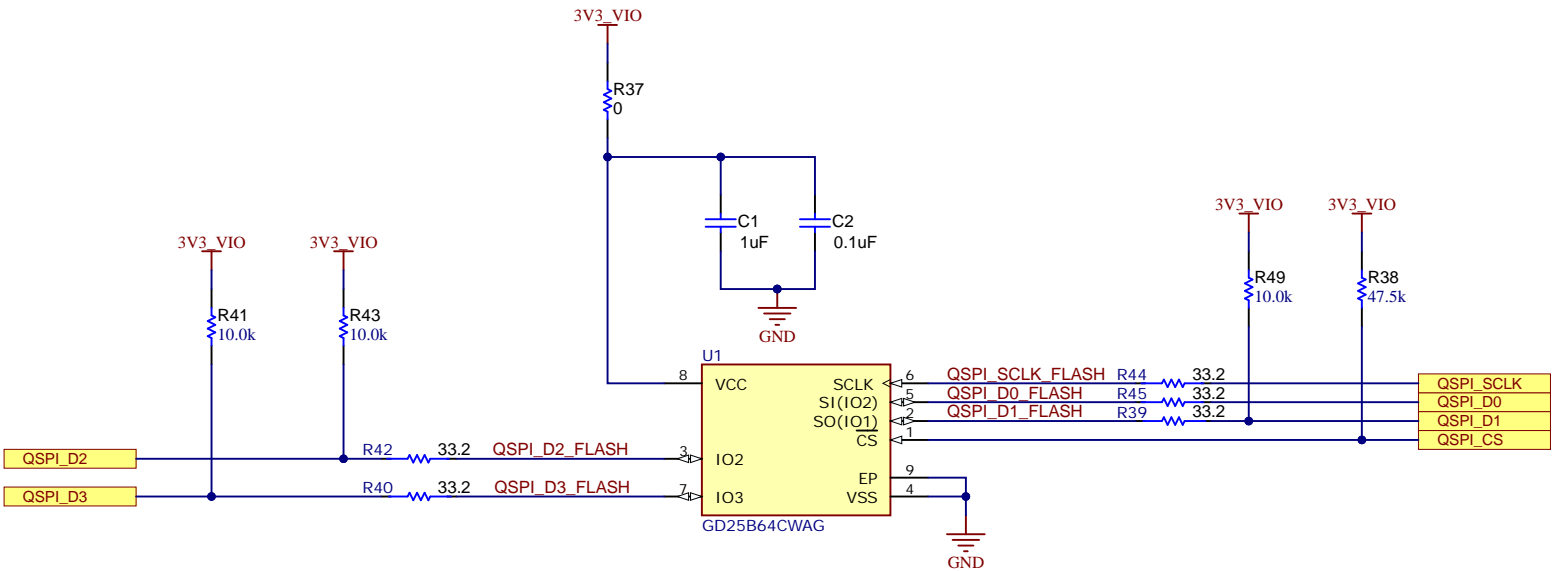
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QSPI FLASH REFERENCE

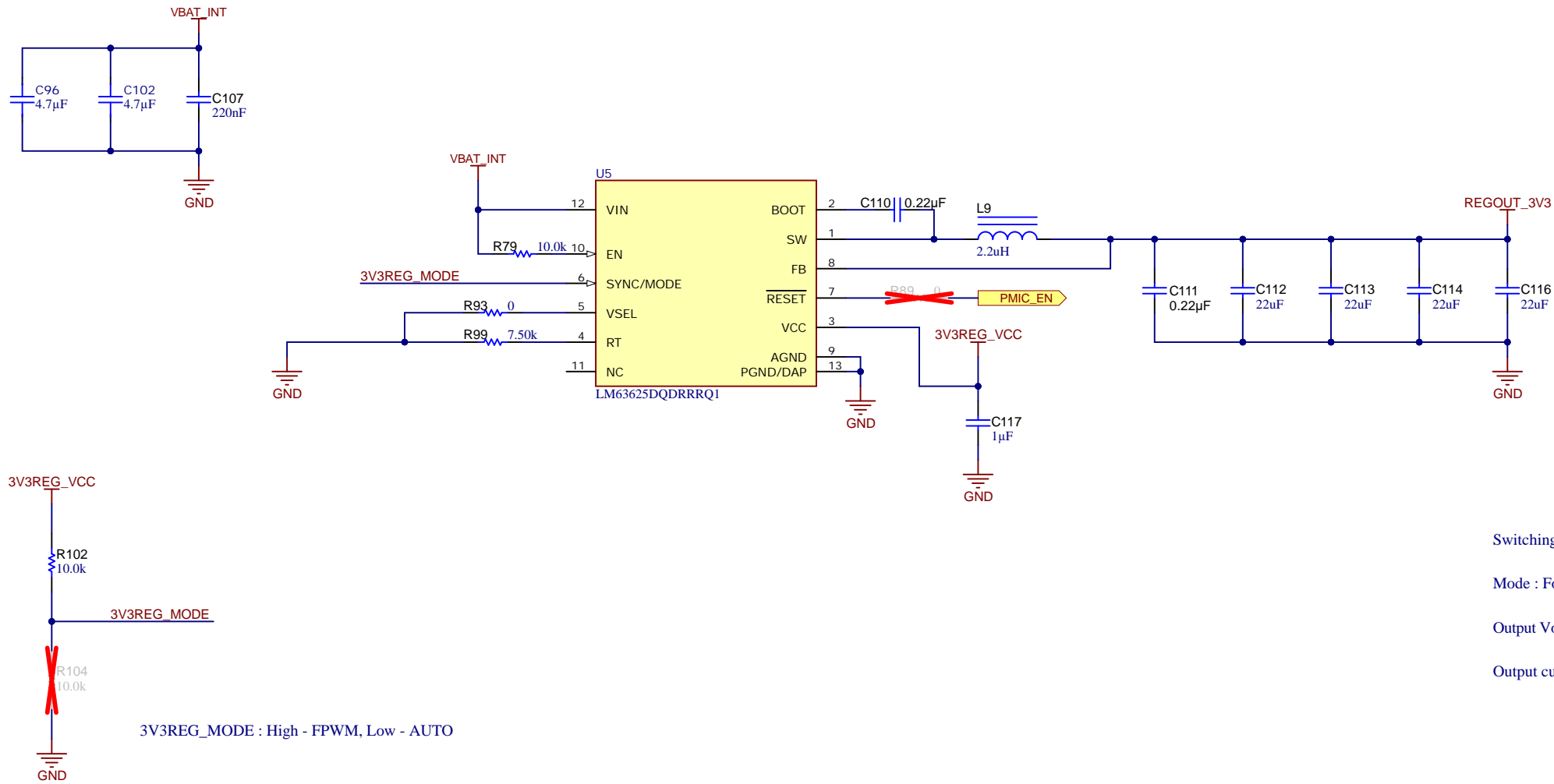
References  
GD25B64CWAG Datasheet



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Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>		Contact: <a href="#">http://www.ti.com/support</a>	

3V3 SUPPLY REFERENCE



Switching Frequency : 2.1 MHz

Mode : Forced PWM

Output Voltage : Fixed 3.3

Output current limit : 2.5A

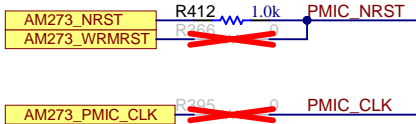
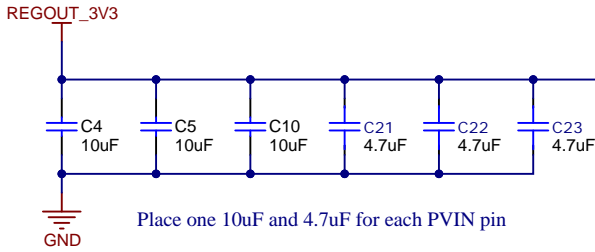
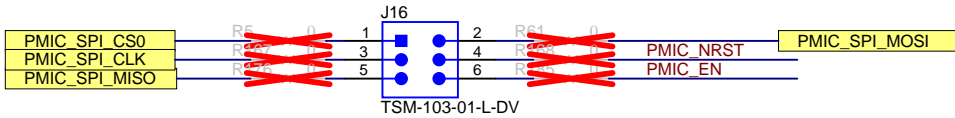
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SVN Rev: <a href="#">308 [Locally Modified]</a>	Assembly Variant: <a href="#">001</a>		Sheet: <a href="#">6</a> of <a href="#">28</a>
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Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>	Contact: <a href="#">http://www.ti.com/support</a>		

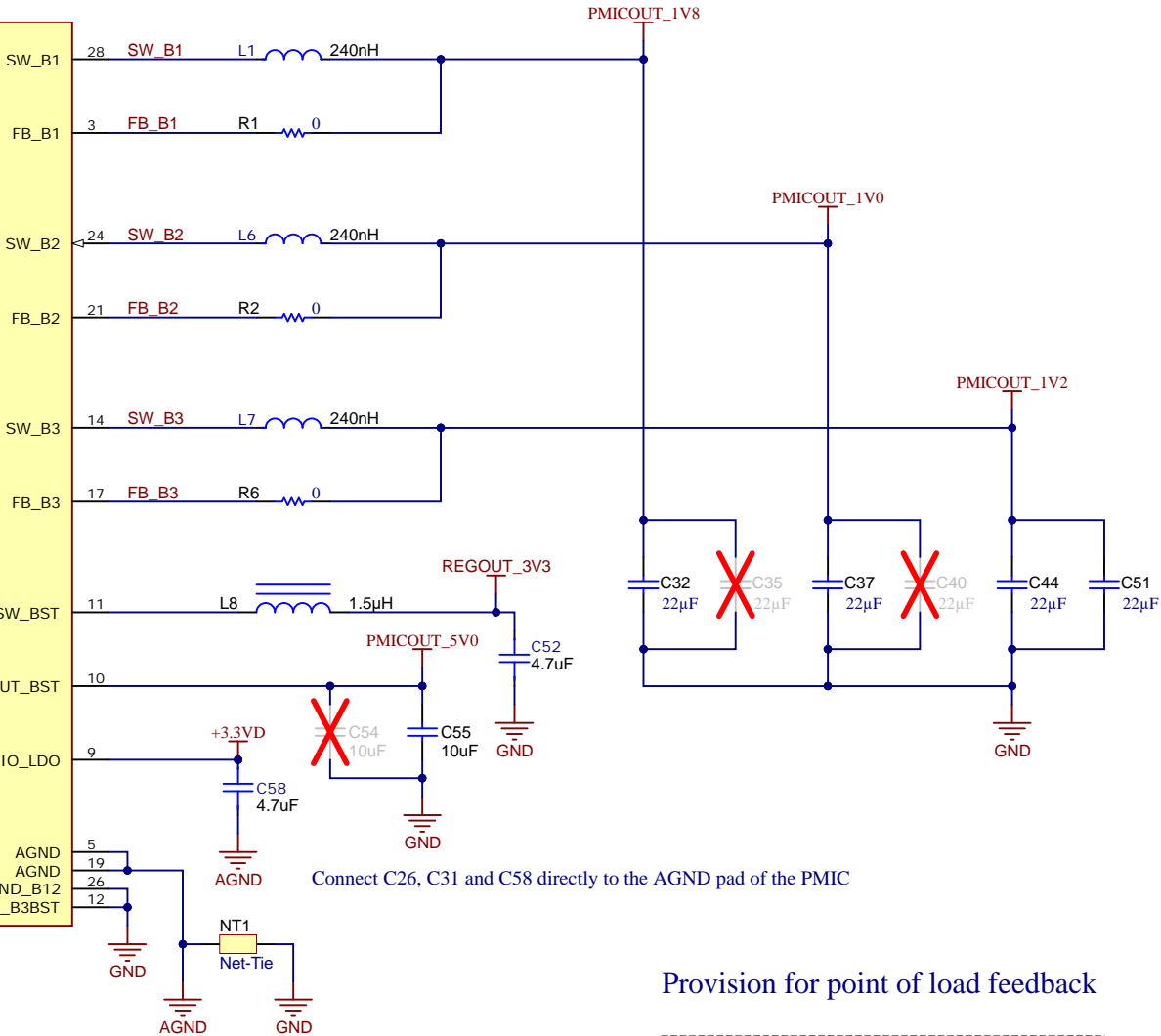
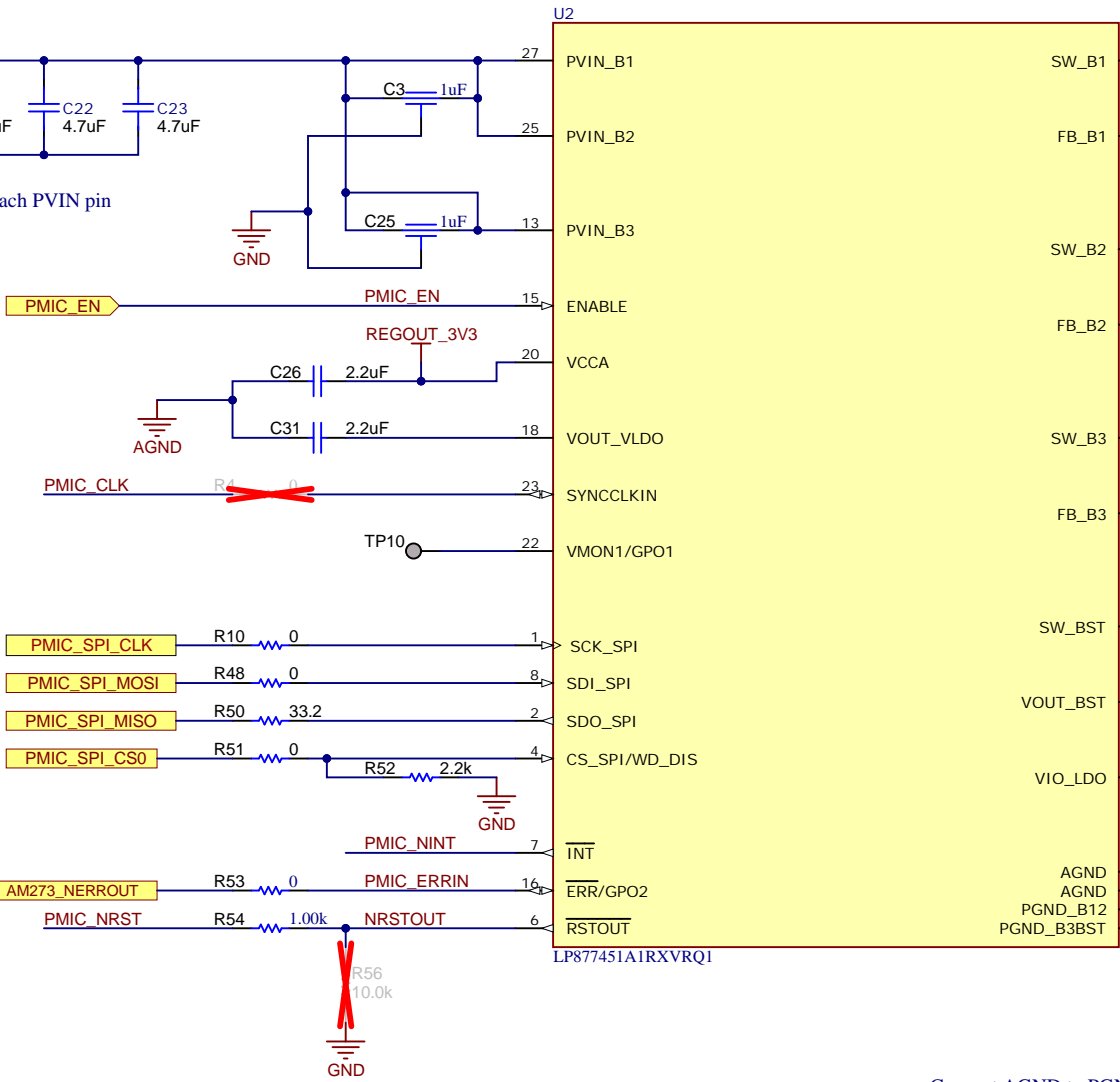
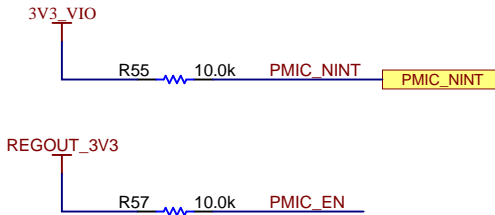


PMIC REFERENCE

DEBUG TEST PINS



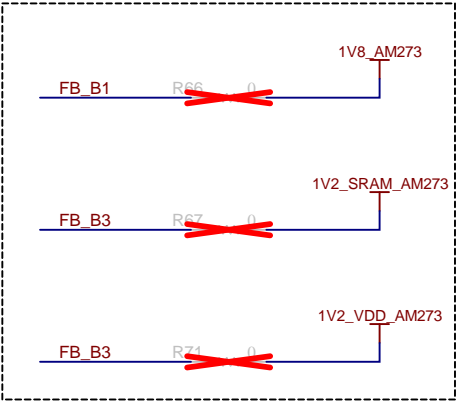
PULL UPS



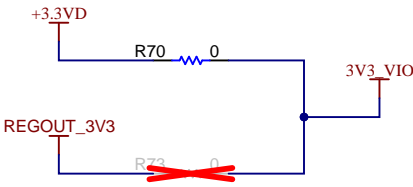
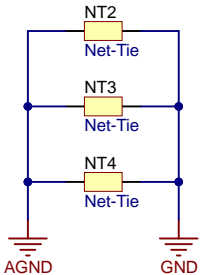
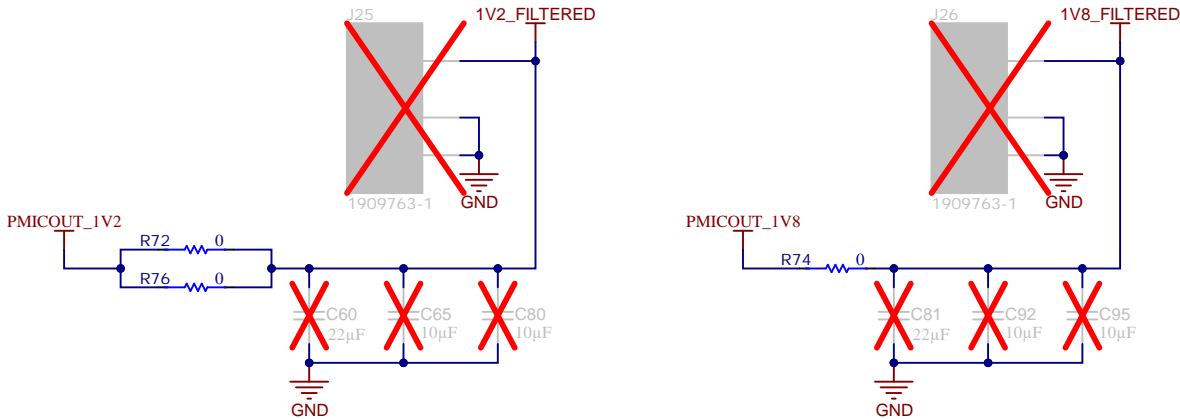
Connect C26, C31 and C58 directly to the AGND pad of the PMIC

Connect AGND to PGND in inner layer GND. In any case, PGND should not be connected to power pad on layer on which PMIC is placed

Provision for point of load feedback



PMIC LC FILTER



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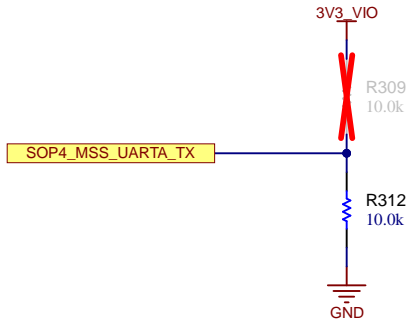
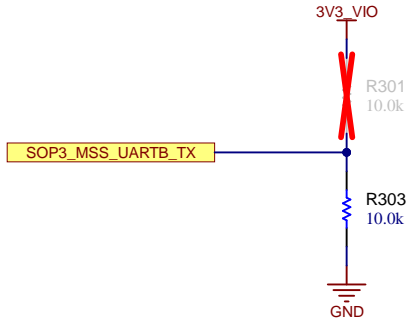
# AM273x SOP REFERENCE

A

## XTAL DETECT SOP CONFIG

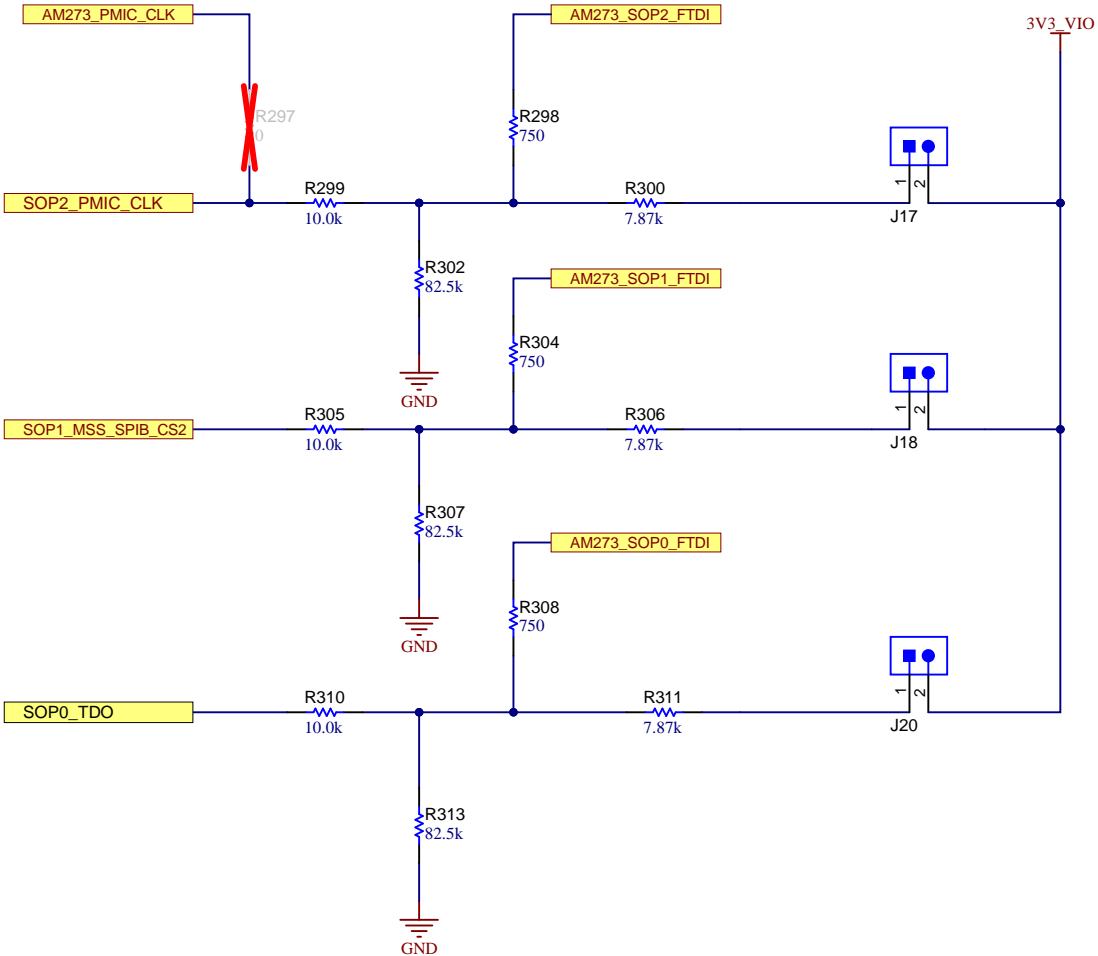
### SOP4, SOP3

40 MHz	00
45.1584 MHz	01
49.152 MHz	10
50 MHz	11



### SOP2, SOP1, SOP0

SOP_MODE1	SCAN/ATPG	010
SOP_MODE2	DEV/FLED/ORBIT	011
SOP_MODE3	THB	000
SOP_MODE4	FUNC	001
SOP_MODE5	DEV MANAGEMENT	101



B

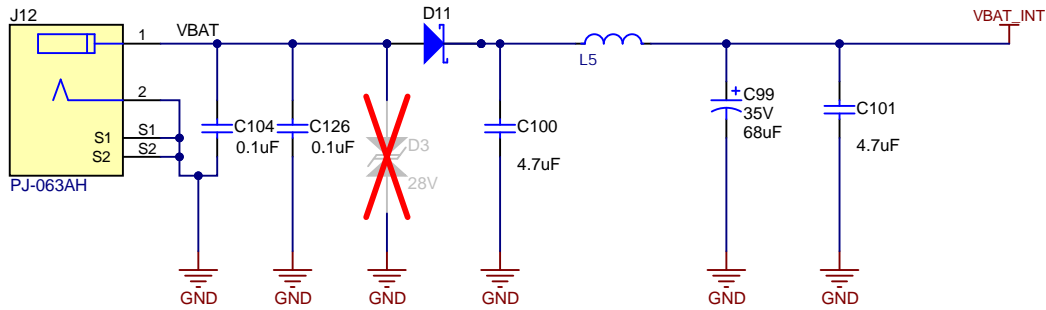
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D

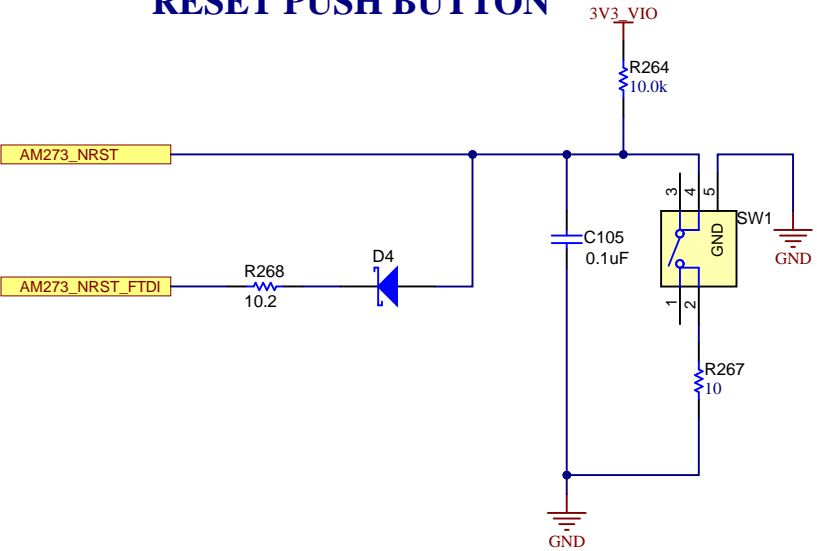


POWER IN, RESETS, AND LEDS

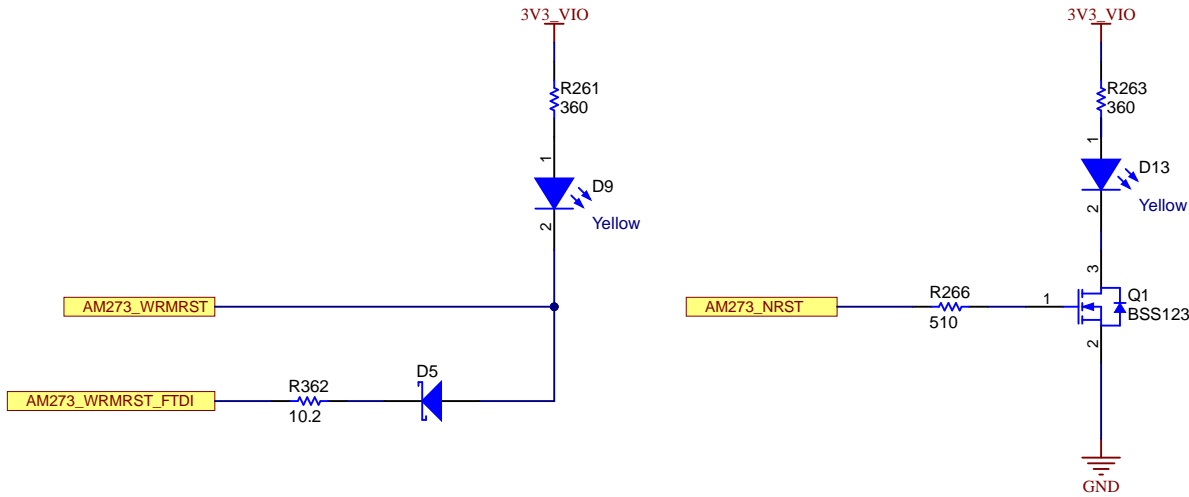
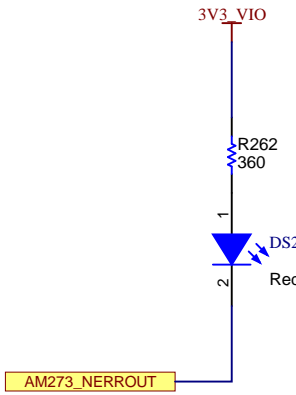
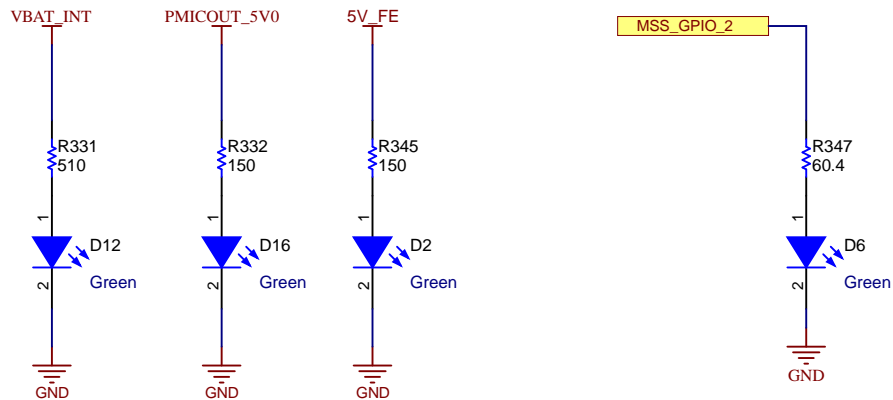
POWER JACK



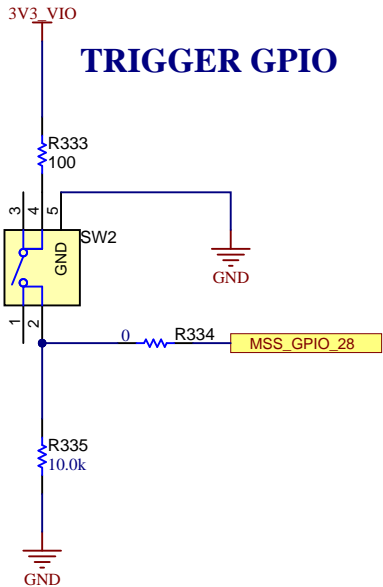
RESET PUSH BUTTON



INDICATION LEDS



TRIGGER GPIO



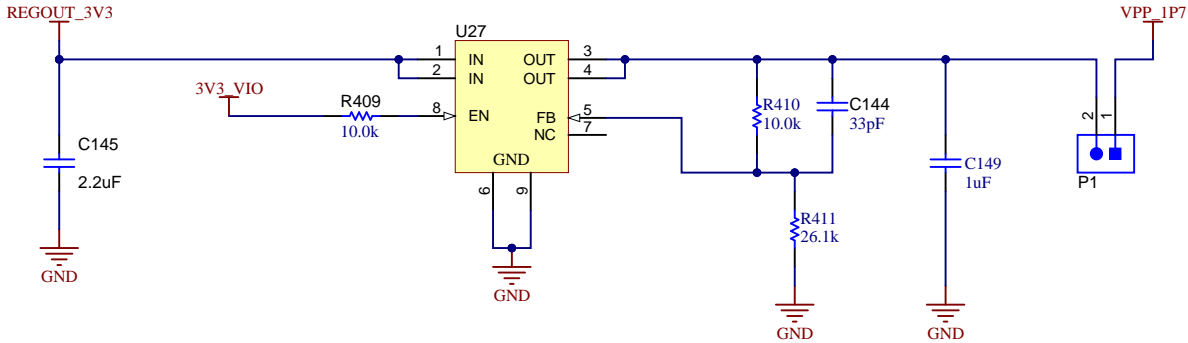
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Number: <a href="#">PROC103</a>	Rev: <a href="#">C</a>	Sheet Title:
SVN Rev: <a href="#">308 [Locally Modified]</a>	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">9</a> of <a href="#">28</a>
Drawn By:	File: <a href="#">PROC103C_PWR_RST_LED.SchDoc</a>	Size: <a href="#">B</a>
Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>	Contact: <a href="#">http://www.ti.com/support</a>	

References

[TPS79601 Datasheet](#)

VPP LDO



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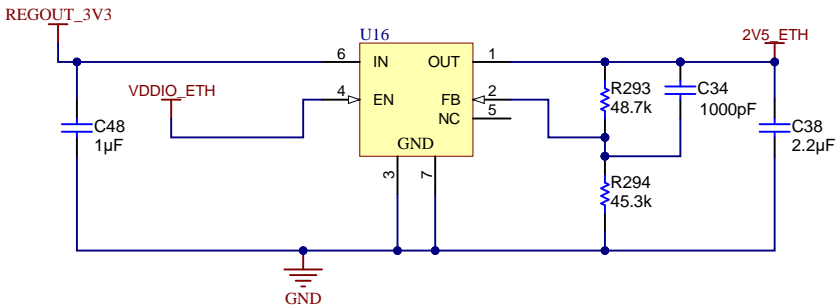
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Number: <a href="#">PROC103</a>	Rev: <a href="#">C</a>	Sheet Title:	
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Drawn By:	File: <a href="#">PROC103C_VPP_LDO.SchDoc</a>		Size: B
Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>	Contact: <a href="#">http://www.ti.com/support</a>		

References

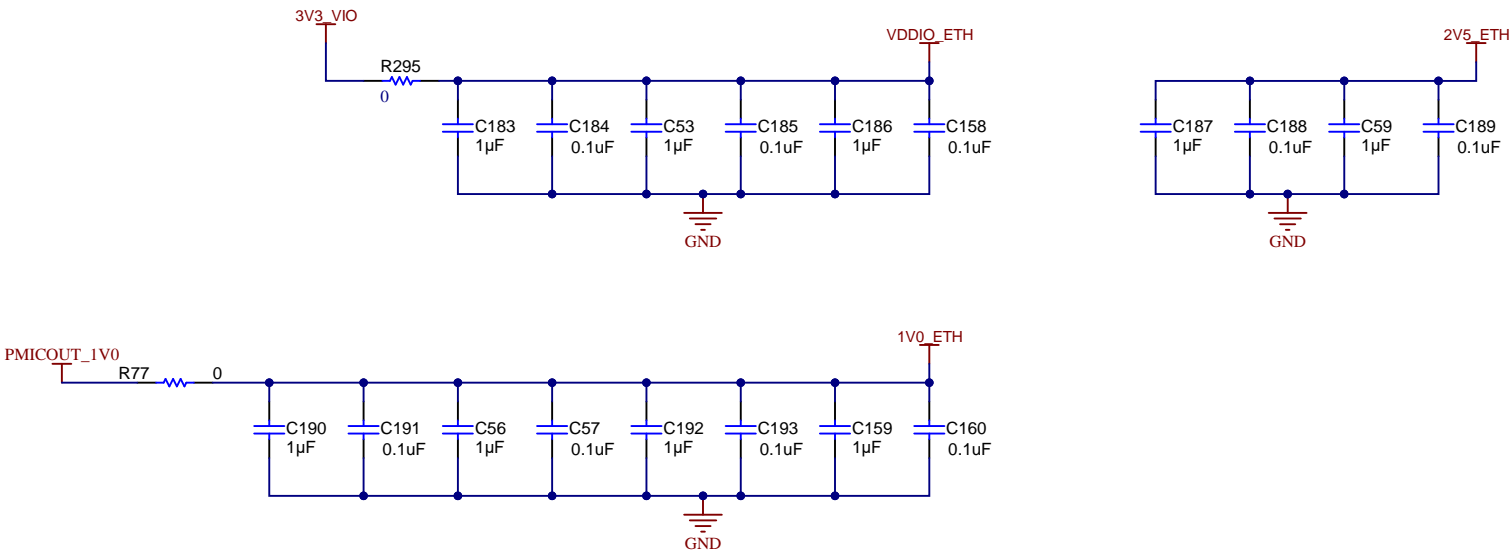
[TPS73501 Datasheet](#)

ETHERNET POWER

2.5V ANALOG SUPPLY



DECOUPLING CAPS



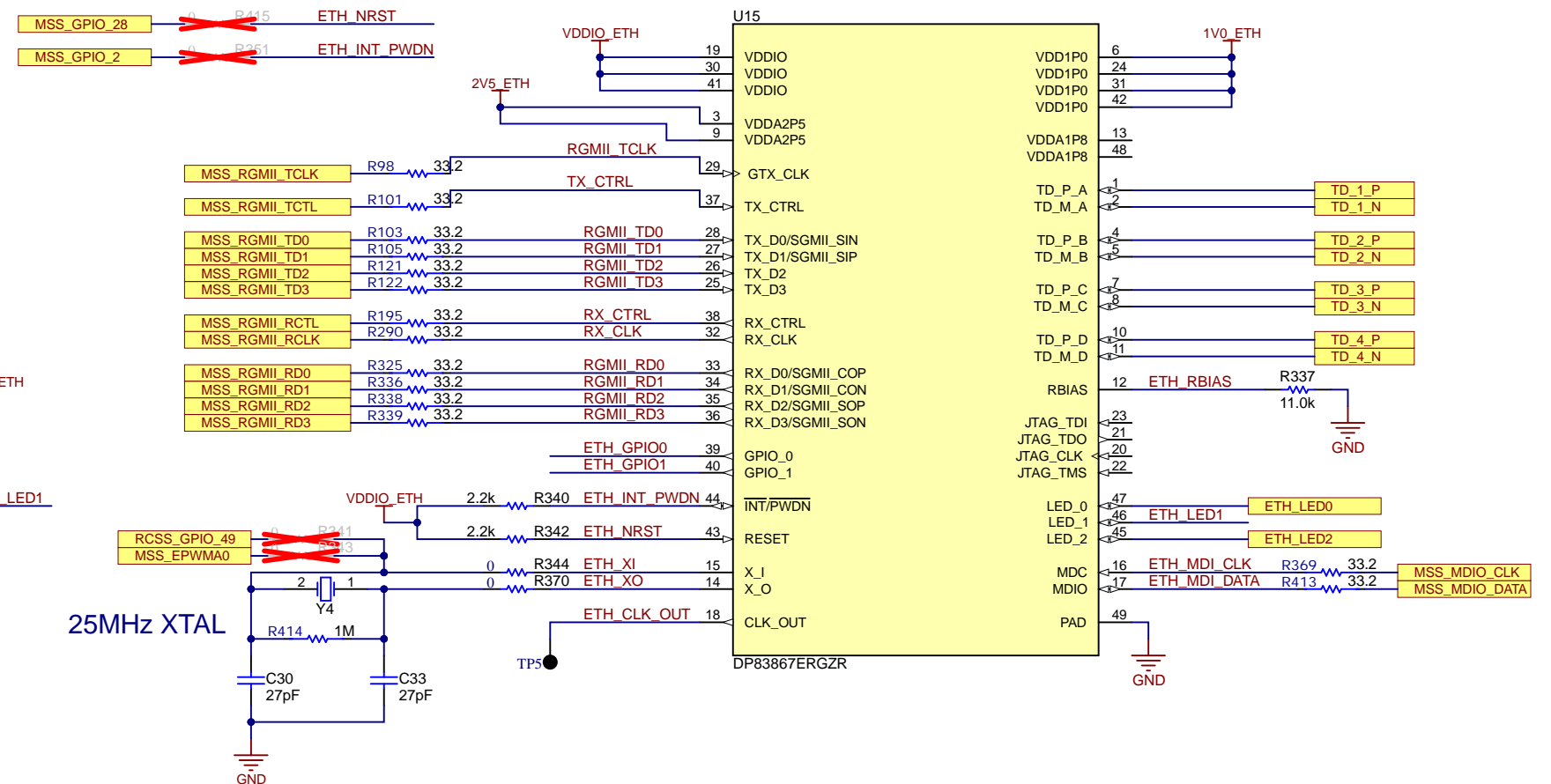
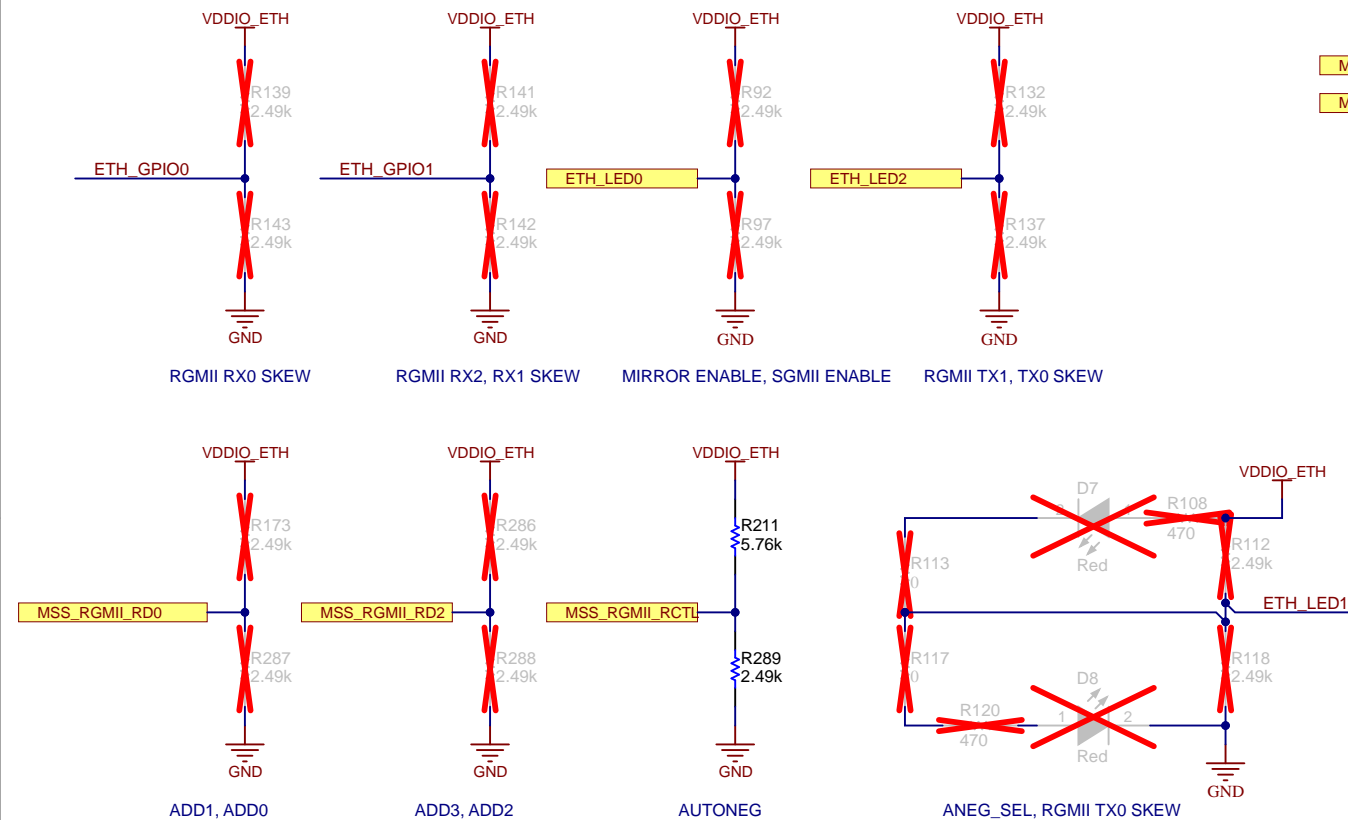
## References

[DP83867E Datasheet](#)

## ETHERNET PHY

## BOOTSTRAP CONFIGURATION PINS

Resistor Values must be changed to change Modes, refer to datasheet for proper values



DEFAULT CONFIGURATION:

ADD1, ADD0 = 0

ADD3, ADD2 = 0

AUTONEG = 1

RGMII RX0 SKEW = 0

RGMII RX2, RX1 SKEW = 0, 0

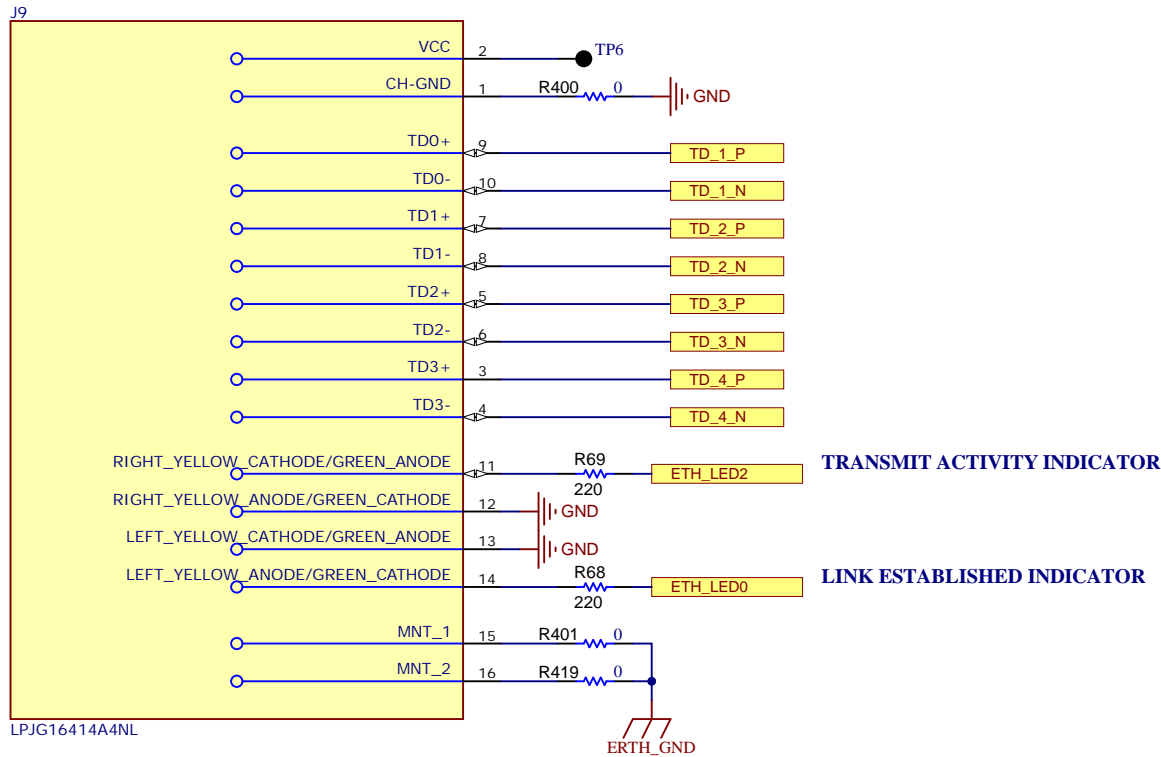
RGMII TX1, TX0 SKEW = 0, 0

ANEG\_SEL, RGMII TX0 SKEW = 0, 0

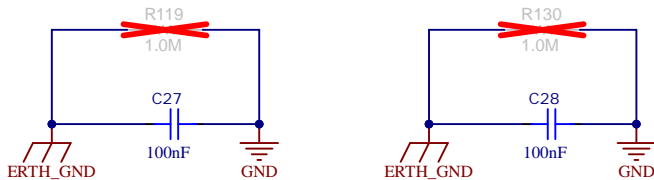
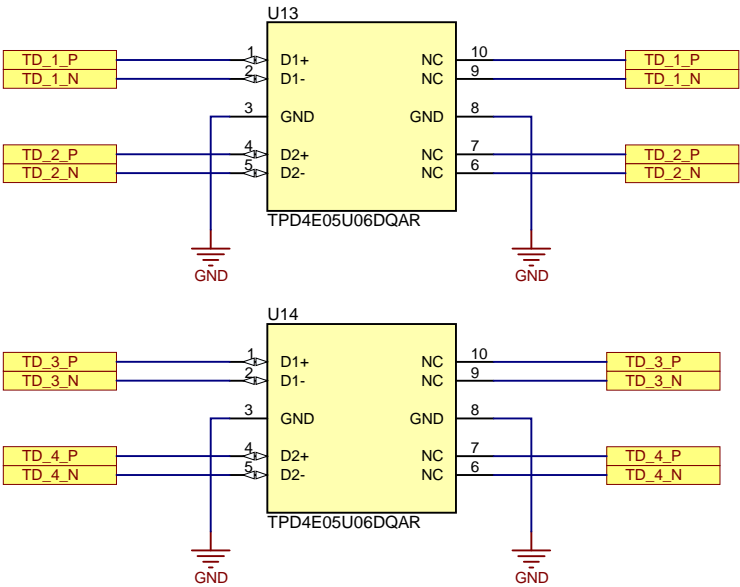
MIRROR ENABLE, SGMII ENABLE = 0, 0

ETHERNET MAGNETICS

RJ45 WITH MAGJACK



ETHERNET ESD PROTECTION



A

A

B

B

C

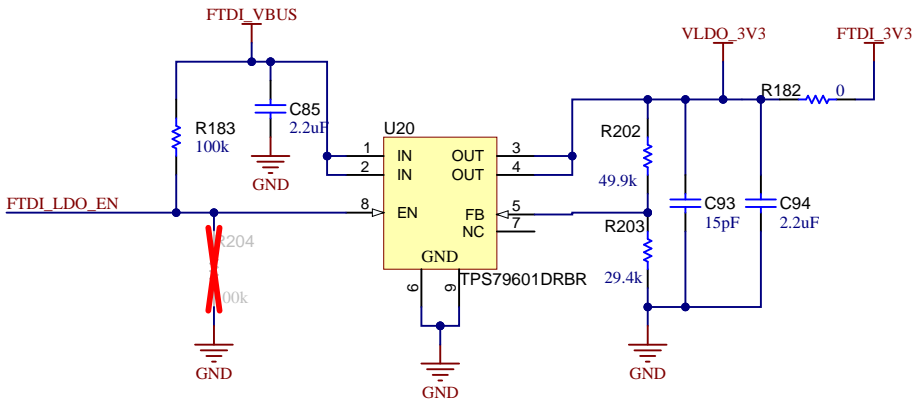
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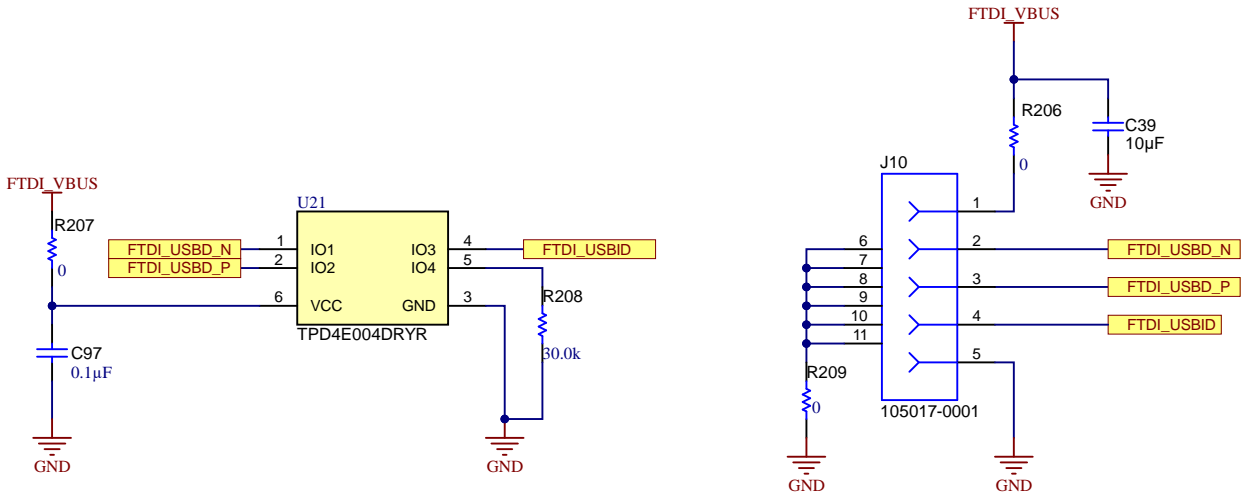
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FTDI (1/2)

3.3V LDO FOR FTDI



FTDI USB PORT



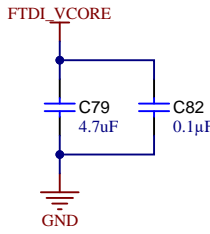
References

[FT4232H Datasheet](#)

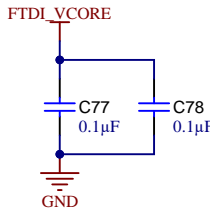
FTDI (2/2)

FTDI SUPPLY DECAPS

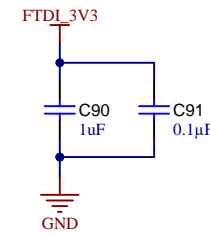
VCORE DECAPS



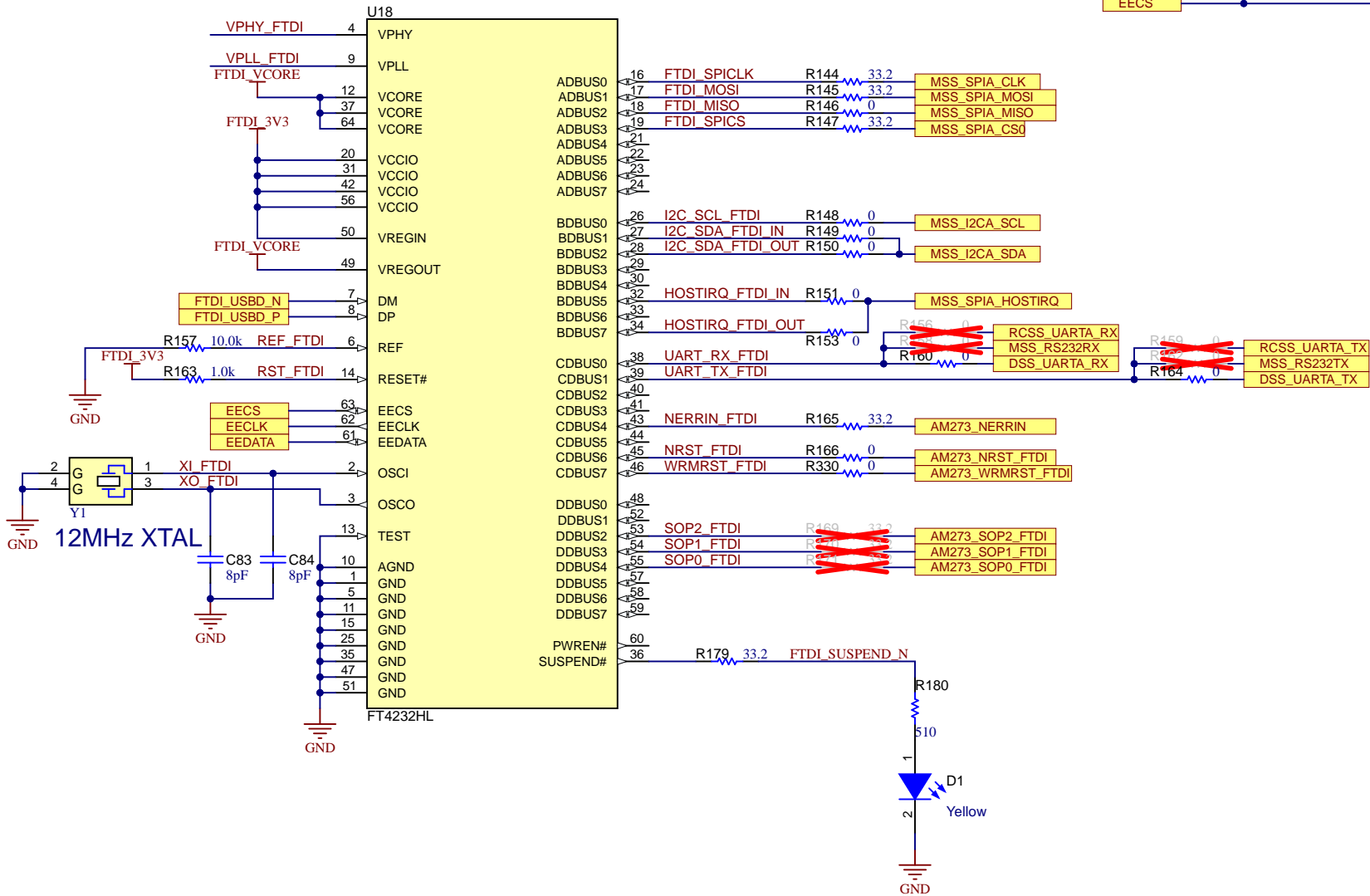
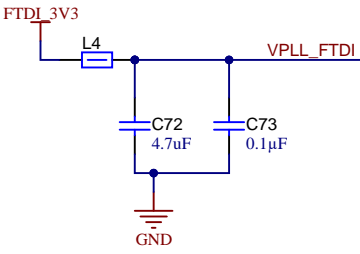
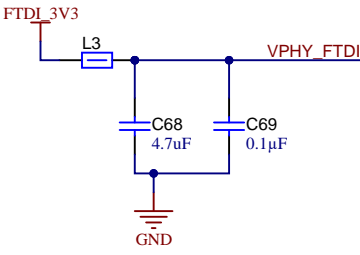
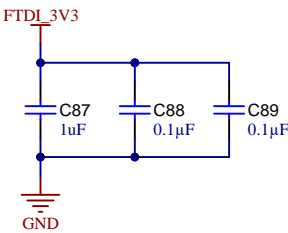
VREGOUT DECAPS



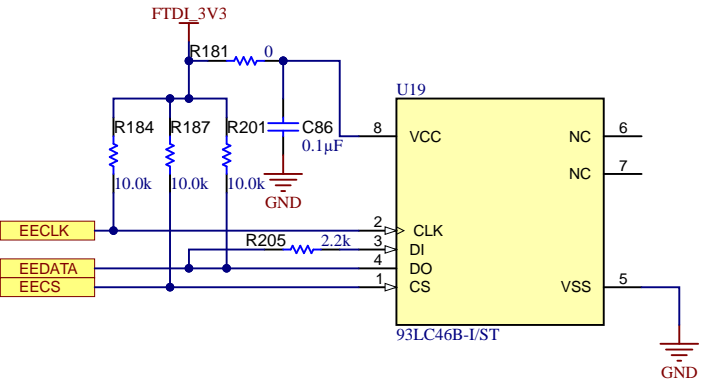
VREGIN DECAPS



VCCIO DECAPS



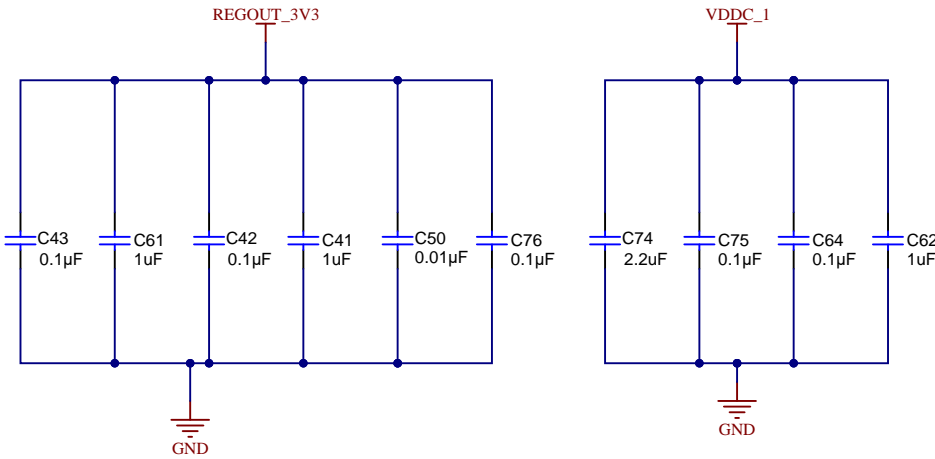
FTDI EEPROM



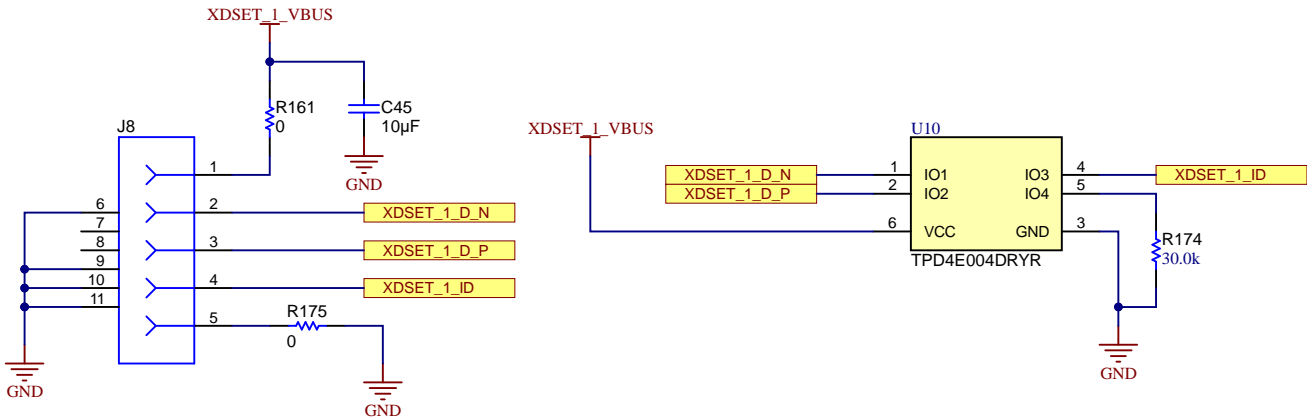


XDS110(1/2)

XDS110 DECOUPLING CAPS



XDS110 USB PORT



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Number: <a href="#">PROC103</a>	Rev: <a href="#">C</a>	Sheet Title: <a href="#">XDS110Interface_1A</a>	
SVN Rev: <a href="#">308 [Locally Modified]</a>		Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">16</a> of <a href="#">28</a>
Drawn By: <a href="#">Adrian Ozer</a>		File: <a href="#">PROC103C_XDS110Interface_1A.SchDoc</a>	Size: <a href="#">B</a>
Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>		Contact: <a href="#">http://www.ti.com/support</a>	



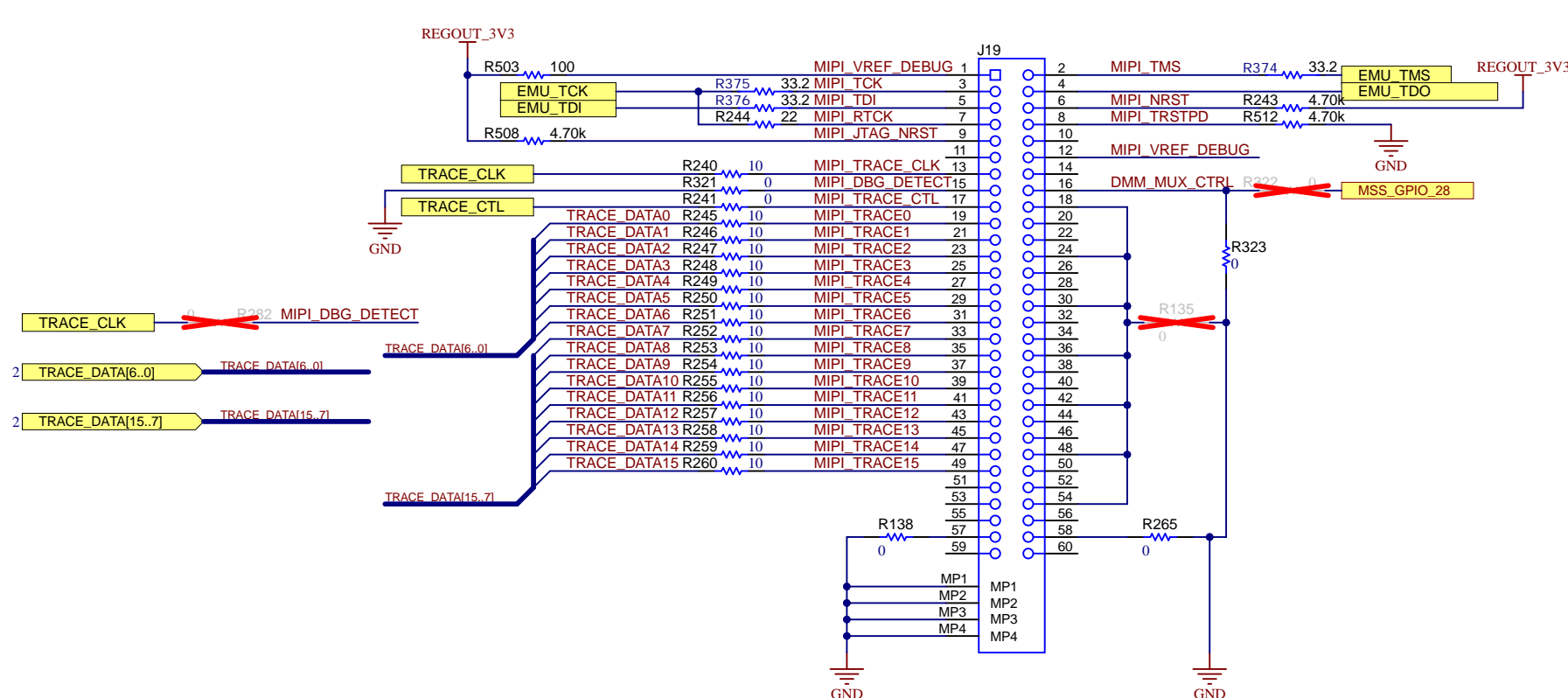
## References

## EMULATION AND TRACE HEADERS

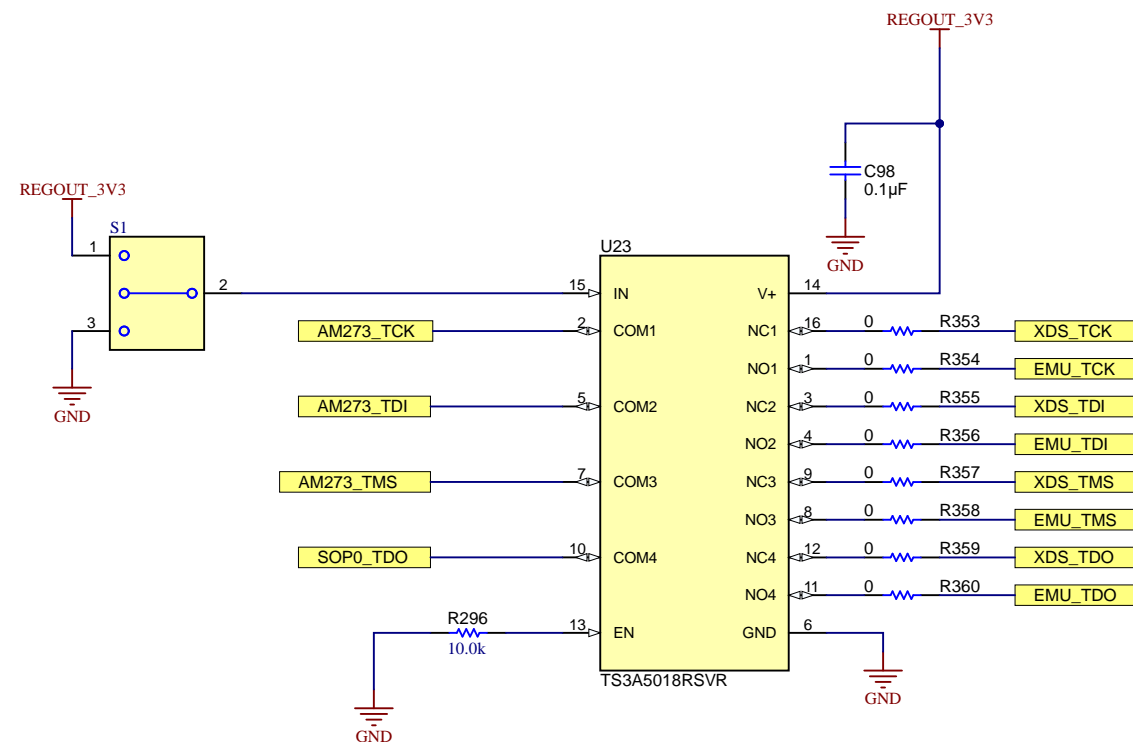
### XDS560v2 EMULATOR

## MIPI 60 PIN HEADER

**NOTE: DEFAULT CONFIGURATION IS FOR MIPI 60 PIN EMULATOR, SEE NOTES BELOW FOR DCA1000**  
**NOTE: REMOVE R503, R375, R374, R265, R138 BEFORE CONNECTING TO DCA1000**  
**NOTE: POPULATE R282, R322 FOR DMM ON DCA1000**



## JTAG MUX BETWEEN XDS110 AND MIPI 60 PIN

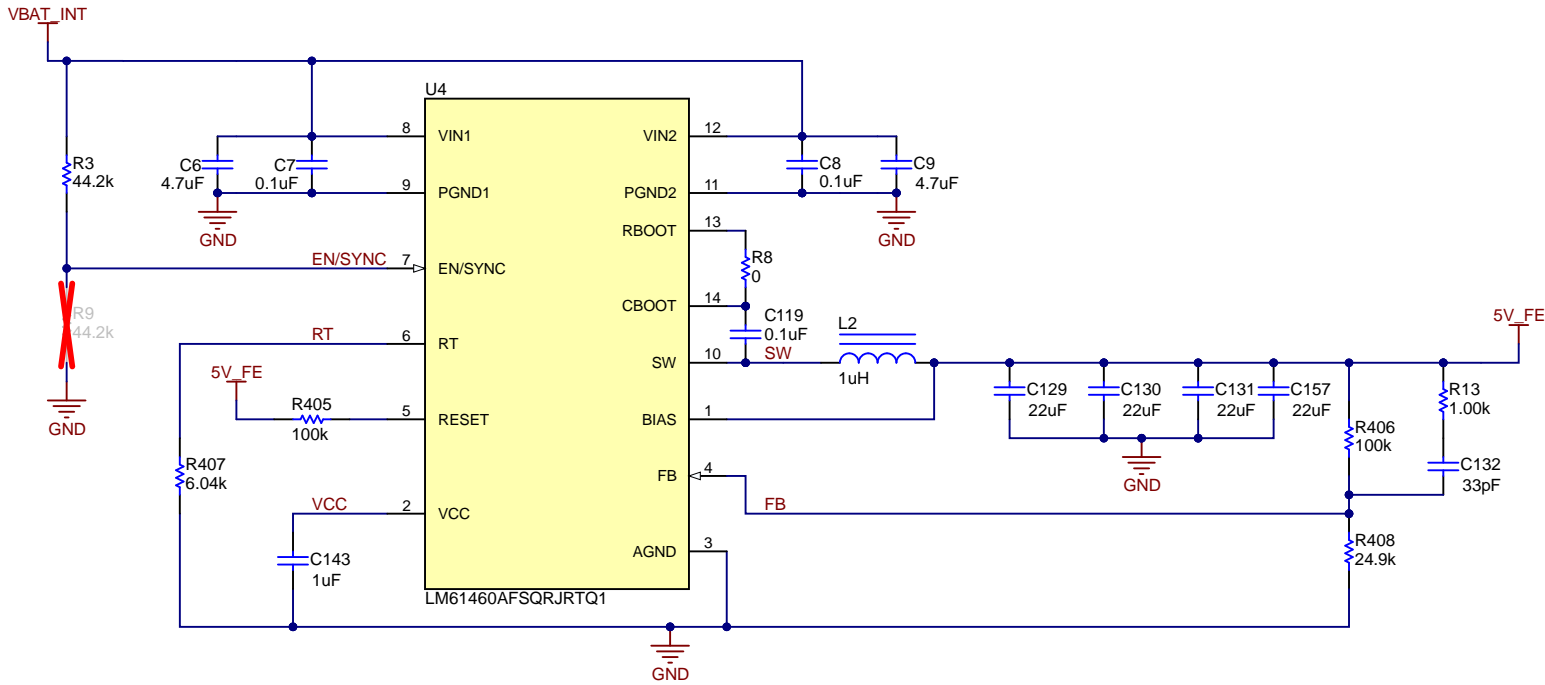


References

[LM61460 Datasheet](#)

5V SUPPLY FOR RADAR FRONT END

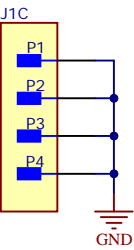
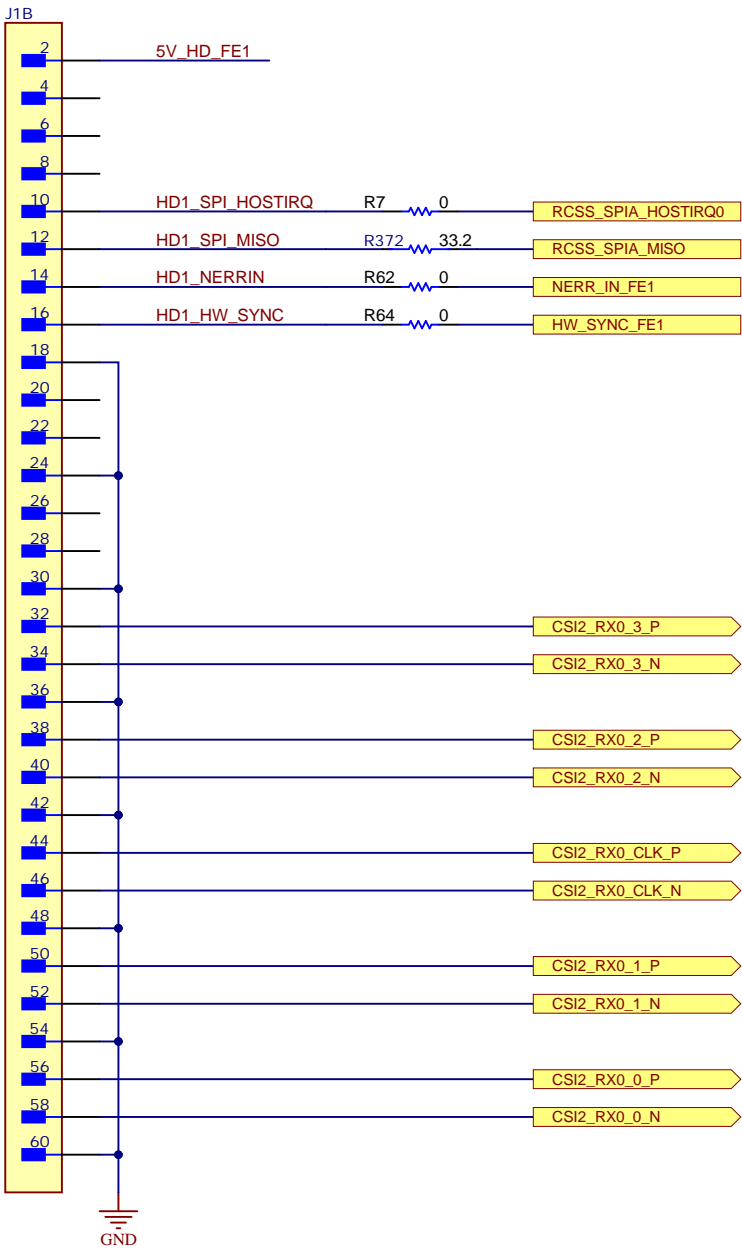
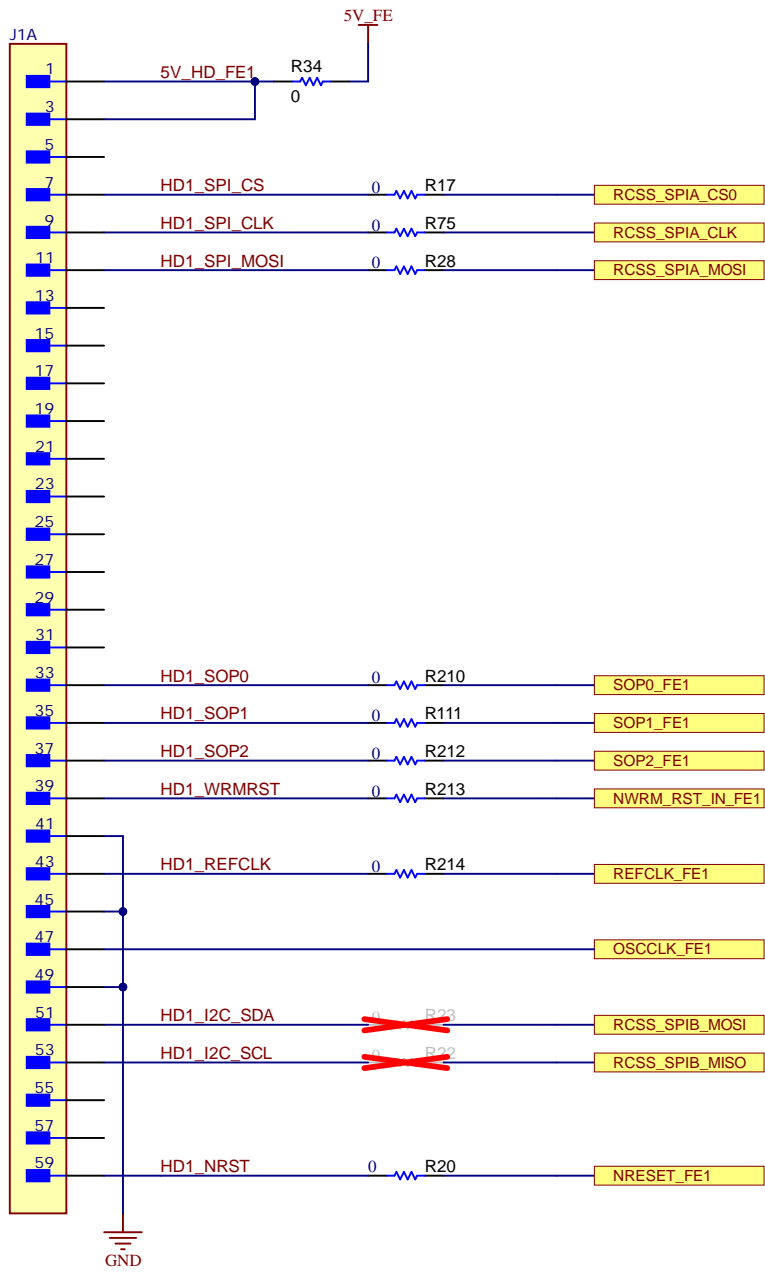
RADAR FRONT END 5V DCDC



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Number: <a href="#">PROC103</a>	Rev: <a href="#">C</a>	Sheet: 19 of 28
SVN Rev: 308 [Locally Modified]	Assembly Variant: 001	Size: B
Drawn By:	File: <a href="#">PROC103C_5V_FE_Supply.SchDoc</a>	
Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>	Contact: <a href="#">http://www.ti.com/support</a>	

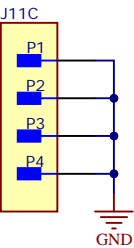
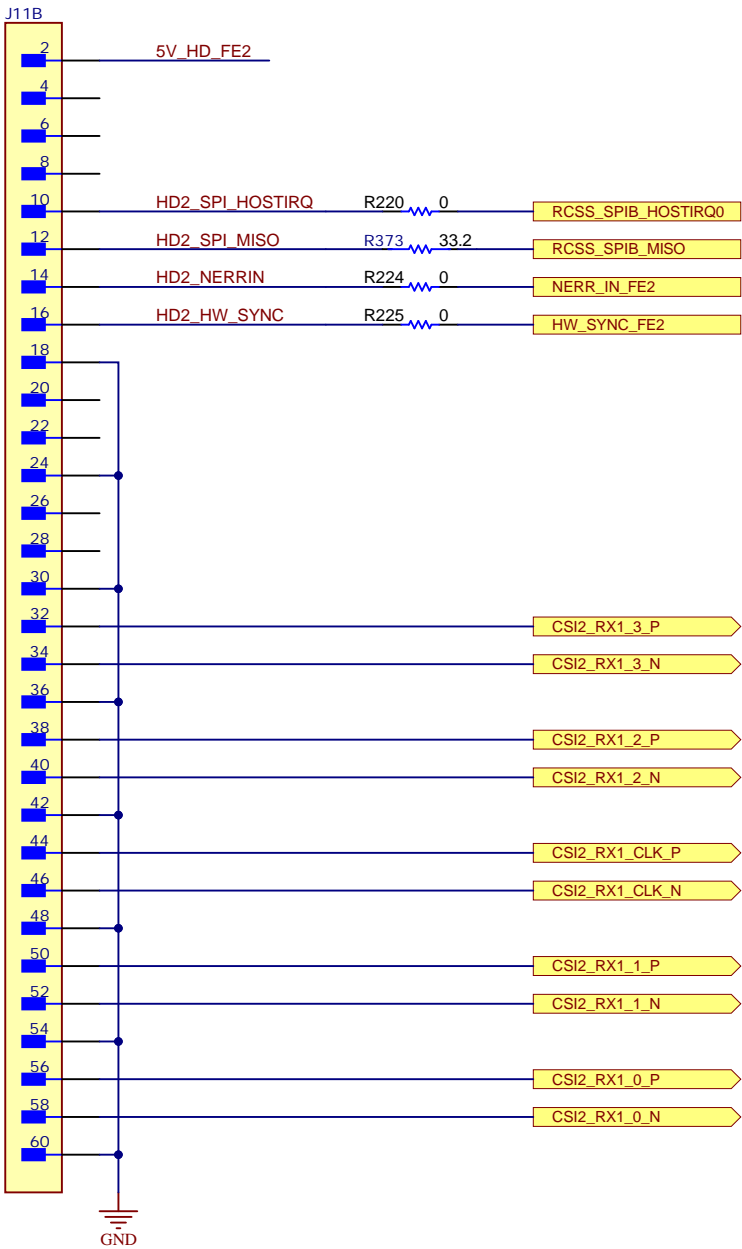
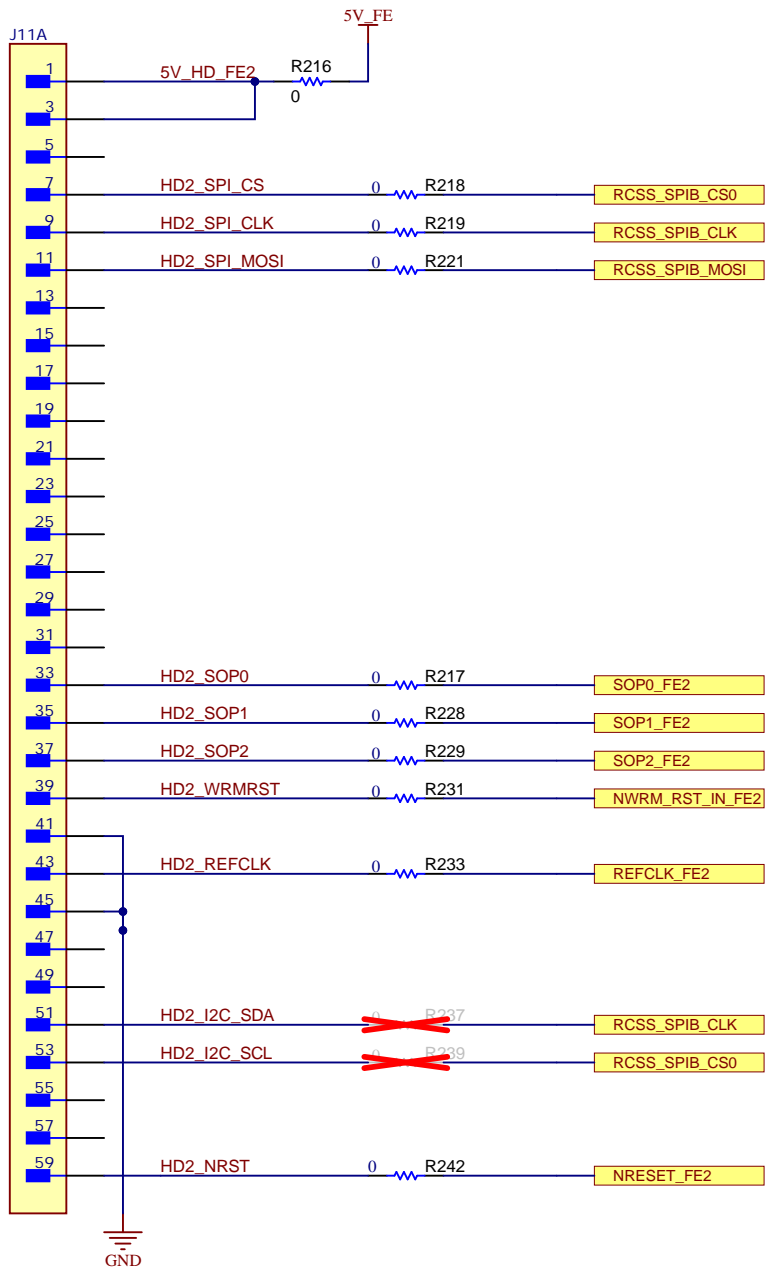
HD CONNECTOR FRONT END 1



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Number: <a href="#">PROC103</a>	Rev: <a href="#">C</a>	Sheet Title: <a href="#">HD Connector</a>	
SVN Rev: <a href="#">308 [Locally Modified]</a>		Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">20 of 28</a>
Drawn By: <a href="#">Adrian Ozer</a>		File: <a href="#">PROC103C_HD Connector1.SchDoc</a>	Size: <a href="#">B</a>
Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>		Contact: <a href="#">http://www.ti.com/support</a>	

HD CONNECTOR FRONT END 2

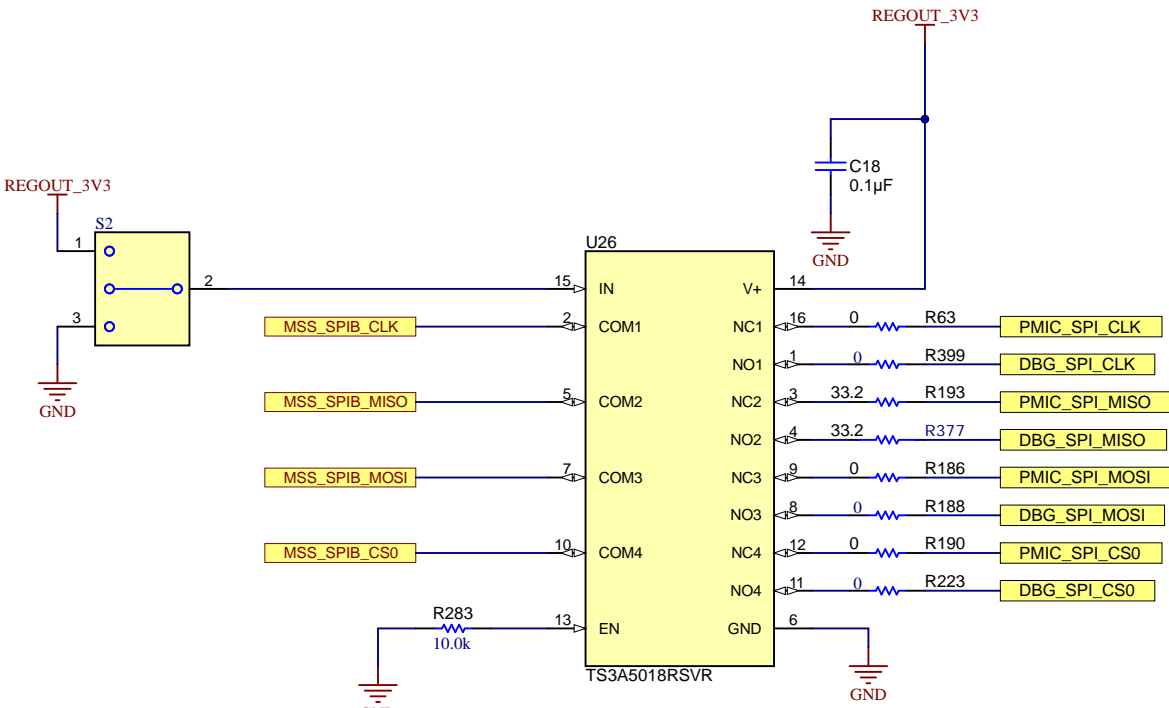
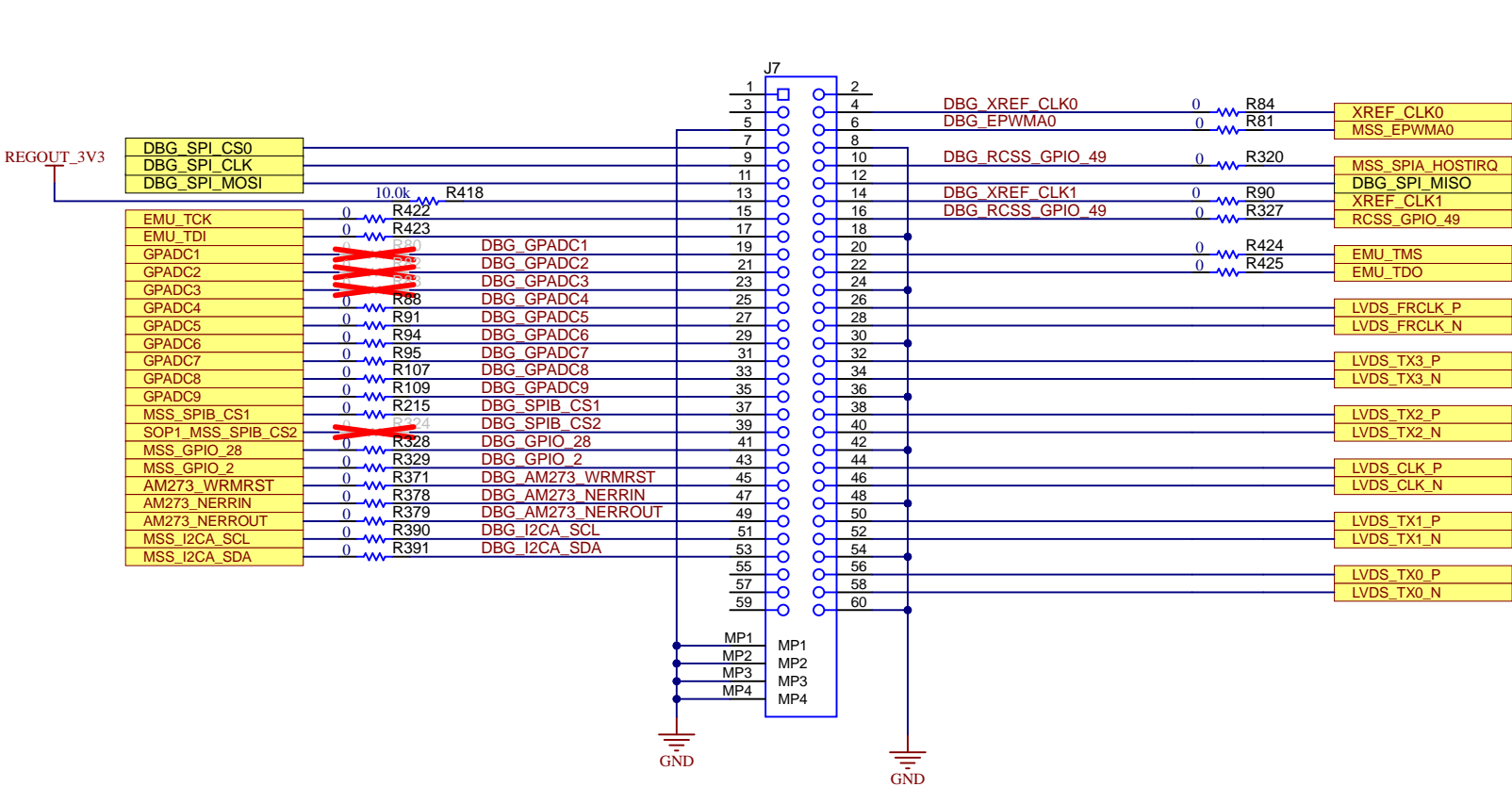


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Drawn By: <a href="#">Adrian Ozer</a>		File: <a href="#">PROC103C_HD Connector2.SchDoc</a>	Size: <a href="#">B</a>
Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>		Contact: <a href="#">http://www.ti.com/support</a>	

60 PIN DEBUG CONNECTOR

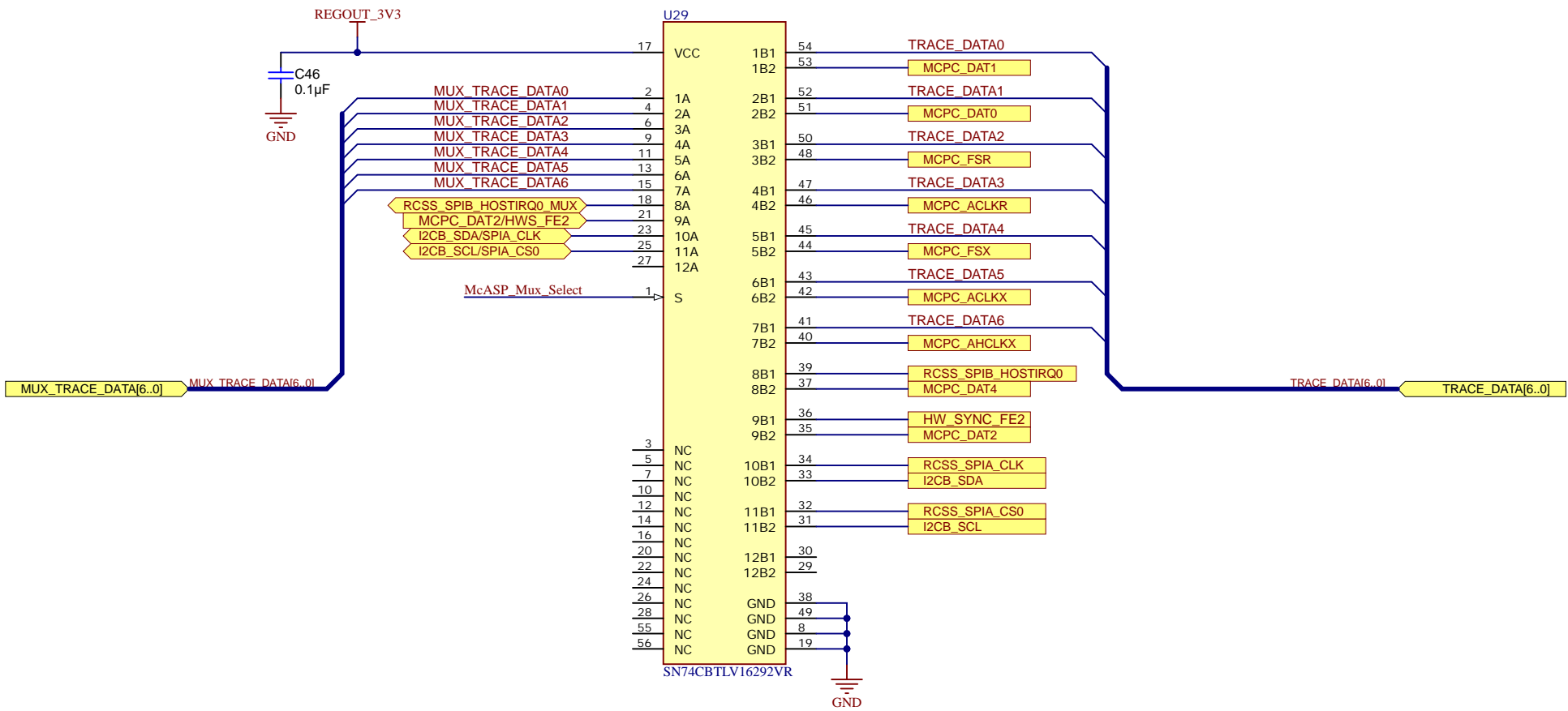
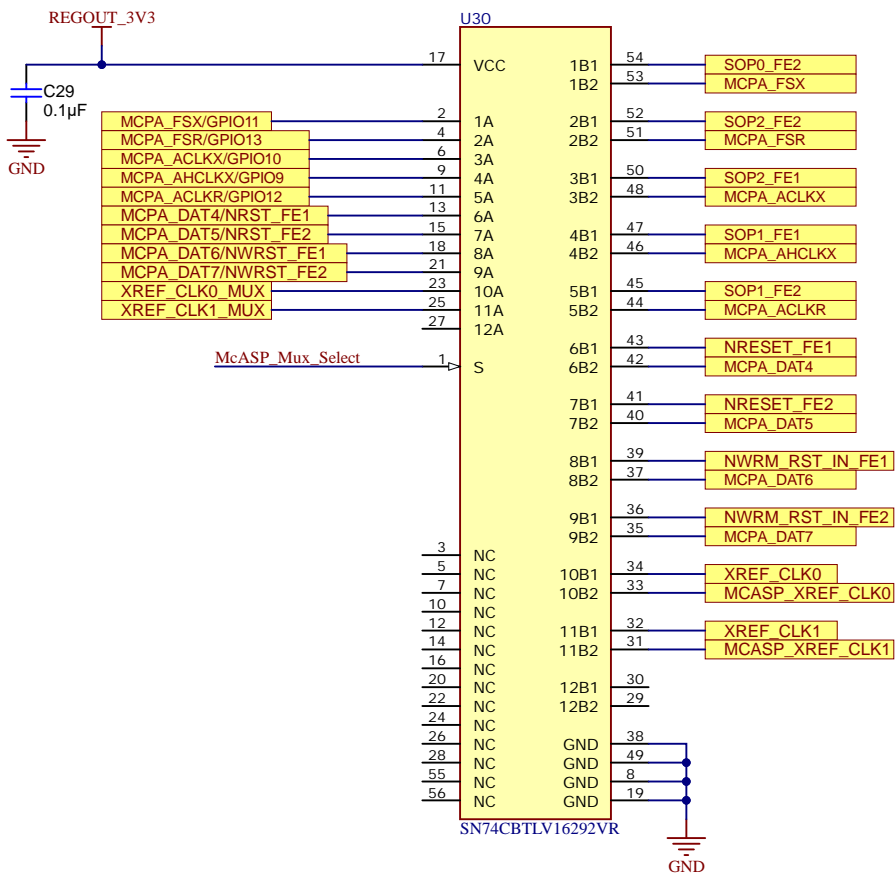
SPI MUX BETWEEN PMIC AND 60 PIN DEBUG CONNECTOR



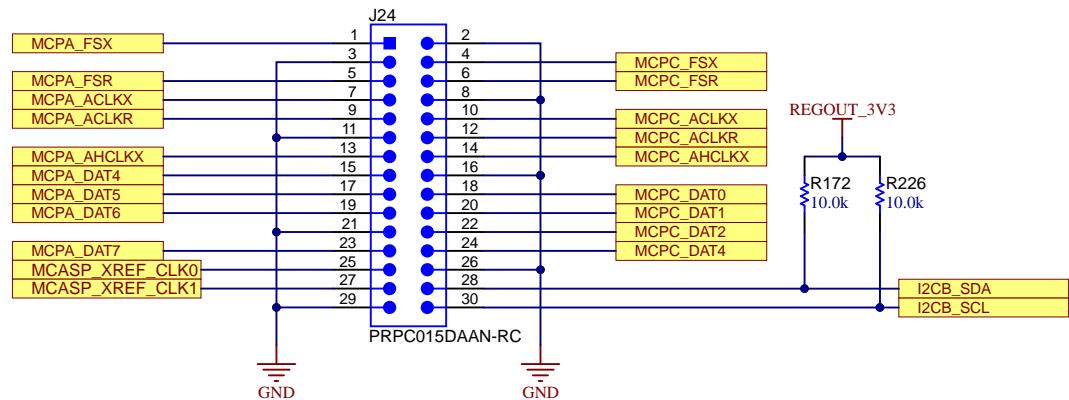
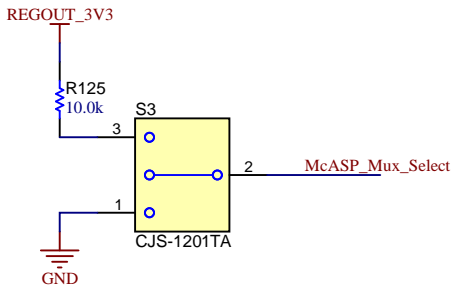
PLACE DBG SERIES RESISTORS NEAR 60 PIN CONNECTOR  
PLACE PMIC SERIES RESISTORS NEAR PMIC



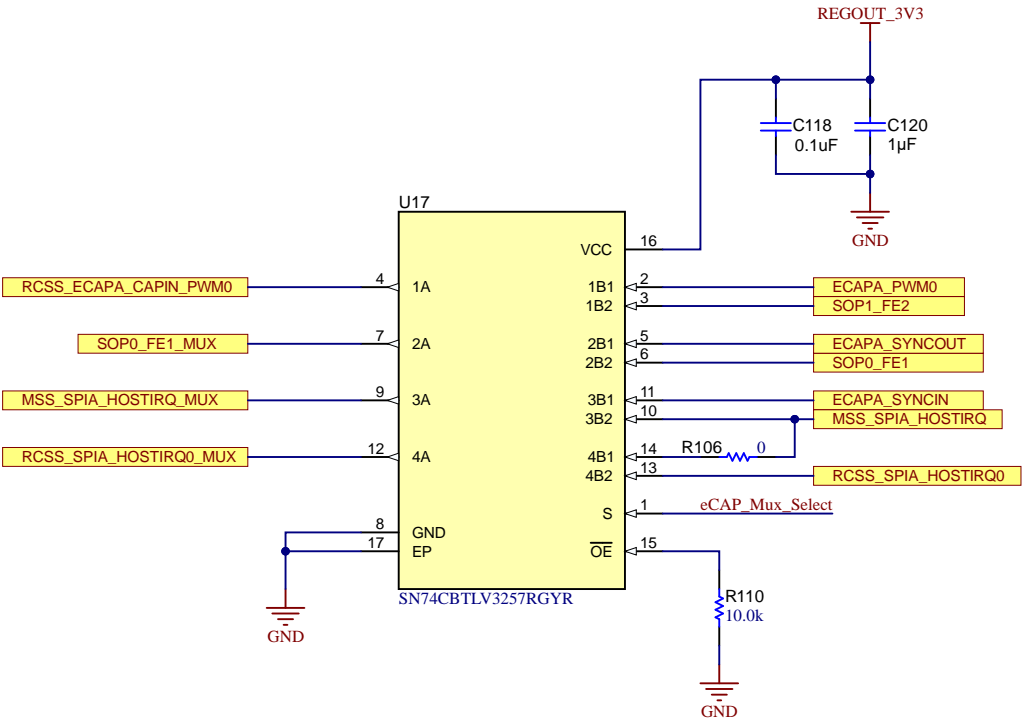
McASP / IO Mux



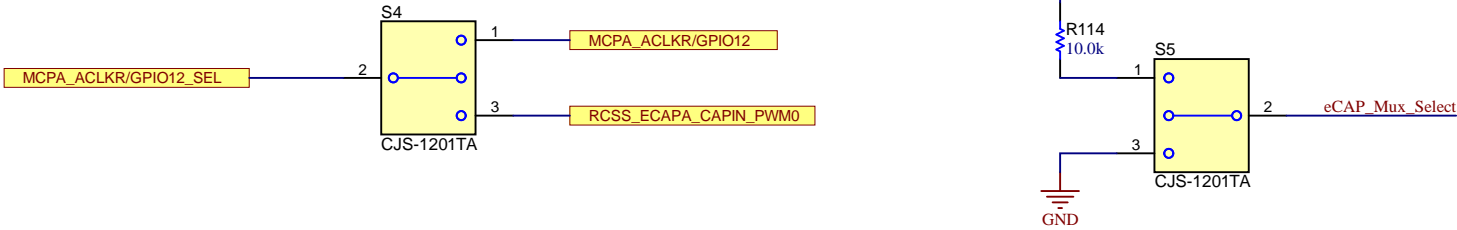
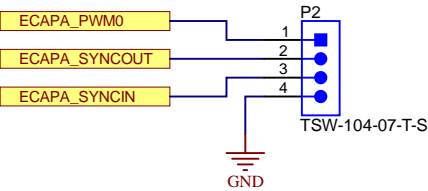
**McASP/IO Mux**  
This mux controls the signal routing between the default IO signals and the McASP Global Header J24. If McASP\_Mux\_Select is pulled low via the switch S3 the non-McASP signals will be routed to the non-McASP headers. If McASP\_Mux\_Select is pulled high, the signals will be routed to the McASP Global Header J24.



eCAP MUX



eCAP Connector



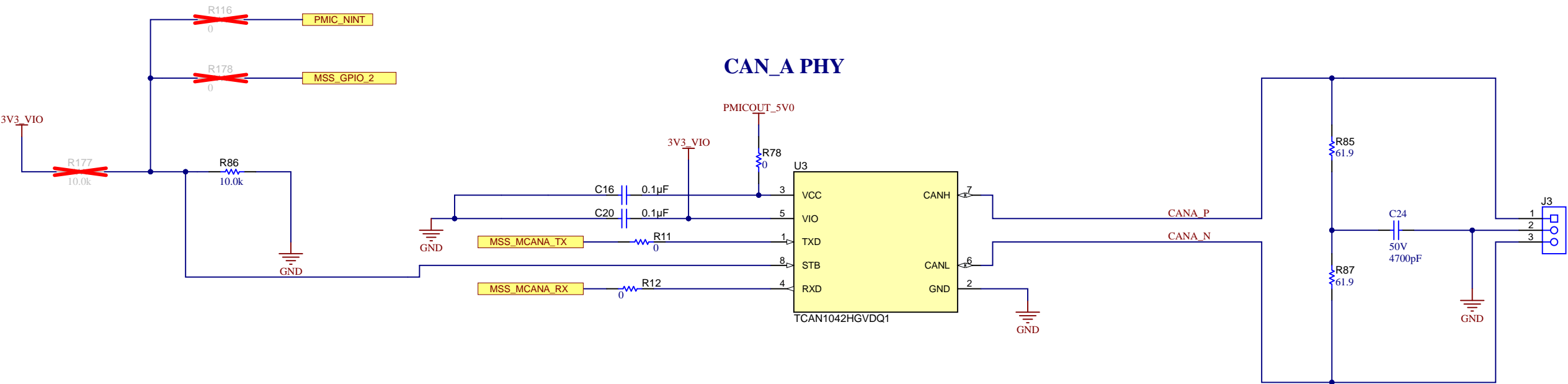
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Number: <a href="#">PROC103</a>	Rev: <a href="#">C</a>	Sheet Title:	
SVN Rev: <a href="#">308 [Locally Modified]</a>	Assembly Variant: <a href="#">001</a>		Sheet: <a href="#">24 of 28</a>
Drawn By:	File: <a href="#">PROC103C_eCAP_MUX.SchDoc</a>		Size: <a href="#">B</a>
Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>	Contact: <a href="#">http://www.ti.com/support</a>		

References

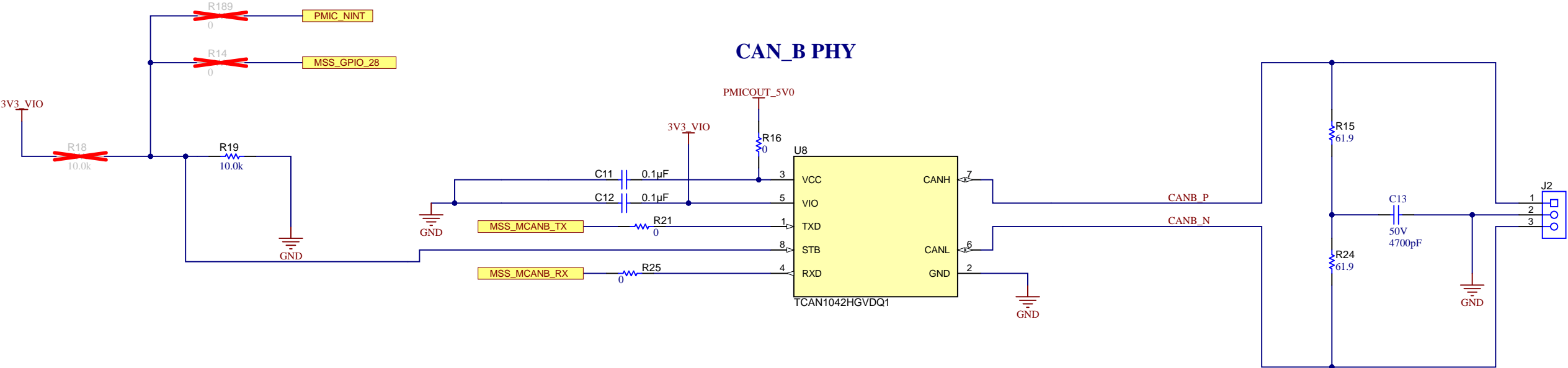
[TCAN1042 Datasheet](#)

CAN INTERFACE

CAN\_A PHY



CAN\_B PHY



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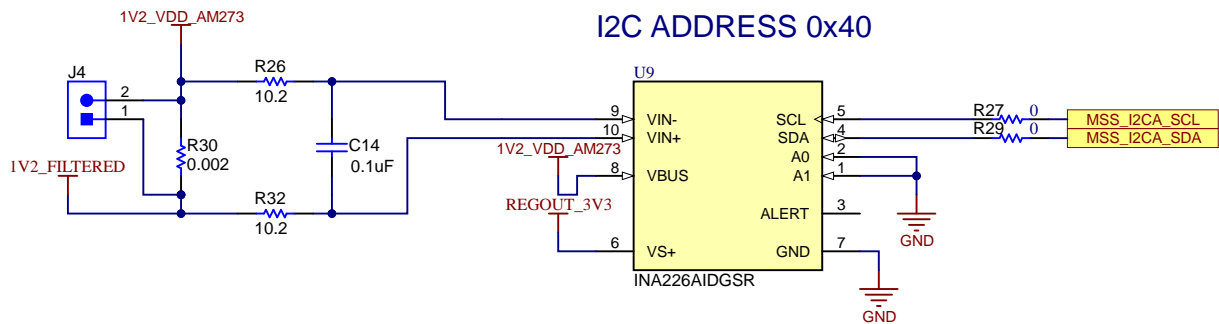
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Drawn By: <a href="#">Adrian Ozer/Mike Pridgen</a>	File: <a href="#">PROC103C_CAN_Interface.SchDoc</a>	Size: <a href="#">B</a>
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CURRENT SENSORS

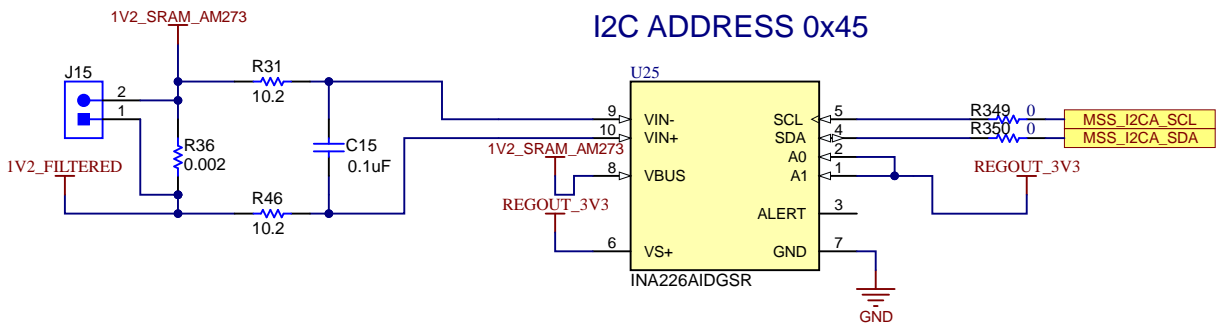
References

INA226 Datasheet

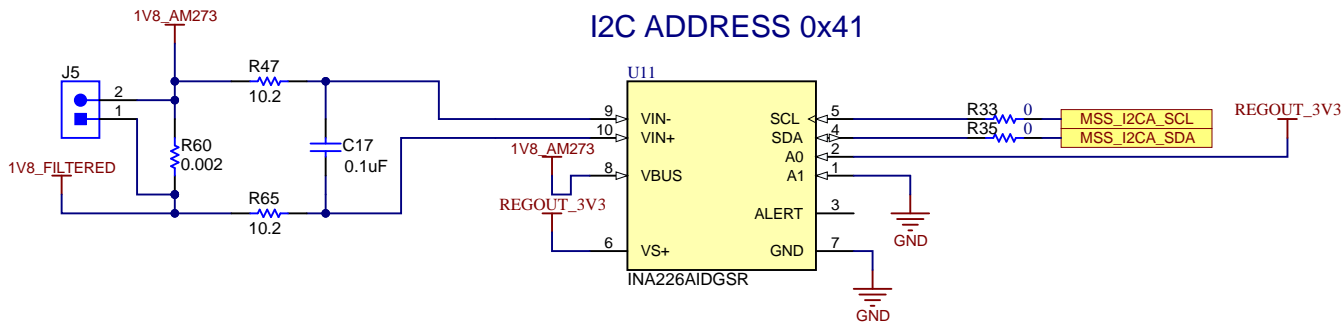
1.2V DIGITAL SUPPLY CURRENT SENSOR



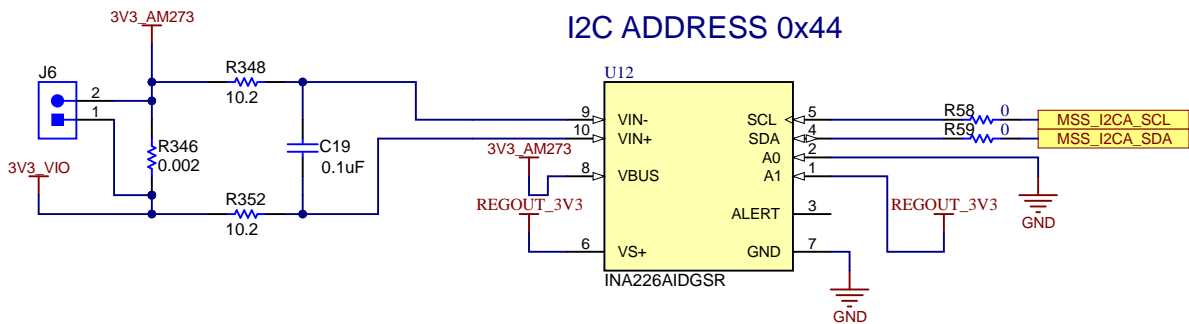
1.2V SRAM SUPPLY CURRENT SENSOR



1.8V SUPPLY CURRENT SENSOR



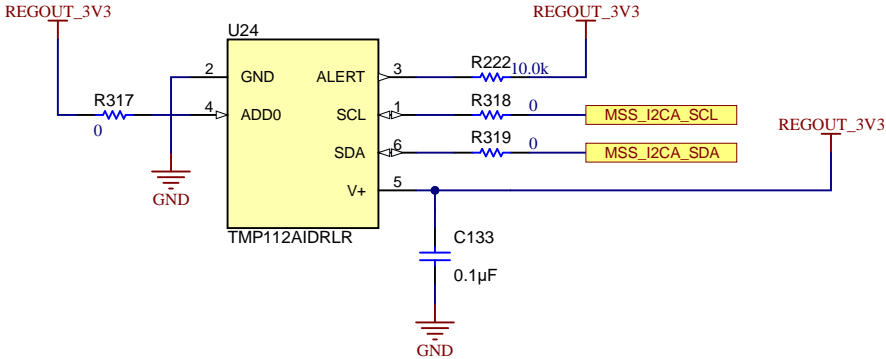
3.3V SUPPLY CURRENT SENSOR



TEMP SENSOR

References  
TMP112 Datasheet

I2C ADDRESS 0x49



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Number: <a href="#">PROC103</a>	Rev: <a href="#">C</a>	Sheet Title:	
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Drawn By:		File: <a href="#">PROC103C_Temp_Sensor.SchDoc</a>	Size: B
Engineer: <a href="#">Adrian Ozer/Mike Pridgen</a>		Contact: <a href="#">http://www.ti.com/support</a>	

