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* Model Usage Notes:
* A. Features have been modelled
* 1. Output Voltage Setting using VSEL pin
* 2. External Adjustable Soft-start through SSTIME parameter.
* 4. Frequency and Operation Mode Selection
* 5. Low-side FET Zero-Crossing
* 6. Low-side FET Negative Current Limit
* 7. Power Good
* 8. Over Voltage Lock Out (OVLO)
* 9. Under Voltage Protection (UVP)
* 10. UVLO/OVLO Protection
* 11. Remote Sense
* 12. Stacking Feature
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* B. Features have not been modelled
* 1. Operating Quiescent Current
* 2. Shutdown Current
* 3. Temperature dependent characteristics
* 4. Output Discharge
* 5. High side current limit
* 6. FSEL Pin Functionality
* 7. Features associated with I2C communications. Ex: Voltage range selection, Dynamic Voltage selection, etc.
* 8. Ground Pins have been tied to 0V internally and hence model does not support Inverting
   topologies.
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* C. Application Notes
* 1. All parameters are passed through F11 window from TOP level.
* 2. The parameter STEADY_STATE has been used to reach the steady state faster.
   Keep STEADY_STATE = 0 to observe start-up behaviour.
   Keep STEADY_STATE = 1 and appropriate IC on Inductor and capacitor to observe for faster Steady state.
* 3. You must keep STEADY_STATE =1 for AC analysis.
   Four default voltage for start-up can be selected by connecting appropriate resistors to VSEL pins. (Refer Datasheet)
   The parameter VOUT has been used to set output voltage other than these four default voltages.
   When VSEL is left floating, Output voltage is set by the VOUT parameter.
   Always specify VOUT parameter in F11 Window in steady state and loop response test benches.
* 4. The parameter SSTIME has been used to set the Device Soft Start time.
* 5. The parameter ILOAD has been used to set the Device Load Current.
* 6. The parameter FSW has been used to select switching frequency of the model.
   The parameter FSW_nom and Fsw_tol_percent define the default value and percentage variation across corner.
   FSW is calculated as per corner selection. Please refer to the F11 window in SIMPLIS schematic for more information.
* 7. The parameter LOUT has been used to select external filter inductor of the model.
   The parameter LOUT_nom and Lout_tol_percent define default value and the percentage variation across corner
   LOUT is calculated as per corner selection. Please refer to the F11 window in SIMPLIS schematic for more information.
* 8. The parameters Cc, Rz, Cc2 have been used to select external compensation.
* 9. The parameter compound_corner has been used to configure the model for typ/weak/strong process corner.
   Please refer to the F11 window in SIMPLIS schematic for more information.
* 10. The parameters Raux, Cac, Cac2, Caux, Rbias, ma_to_m2, Rrst_ma, Raux2 = Raux/ma_to_m2 are internal ramp
   and slope compensation generation parameters. These values are changed across process corners.
   Please refer to the F11 window in SIMPLIS schematic for default values.
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