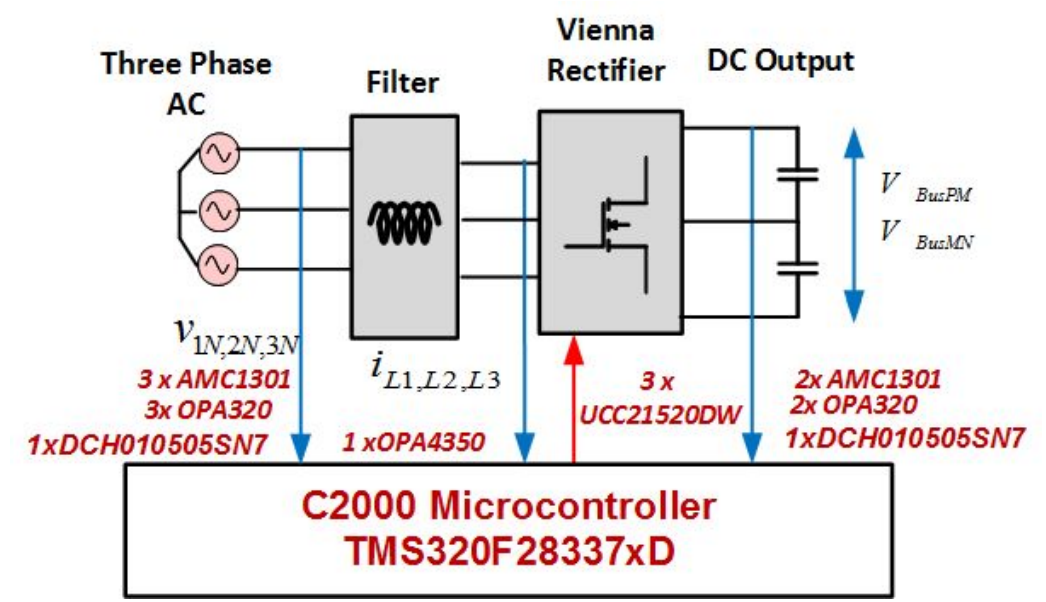
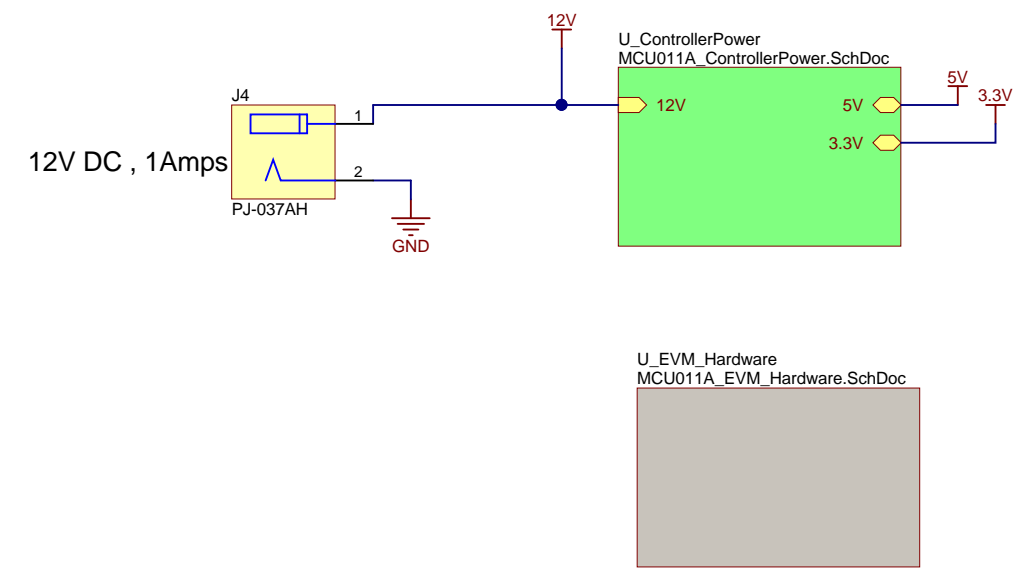
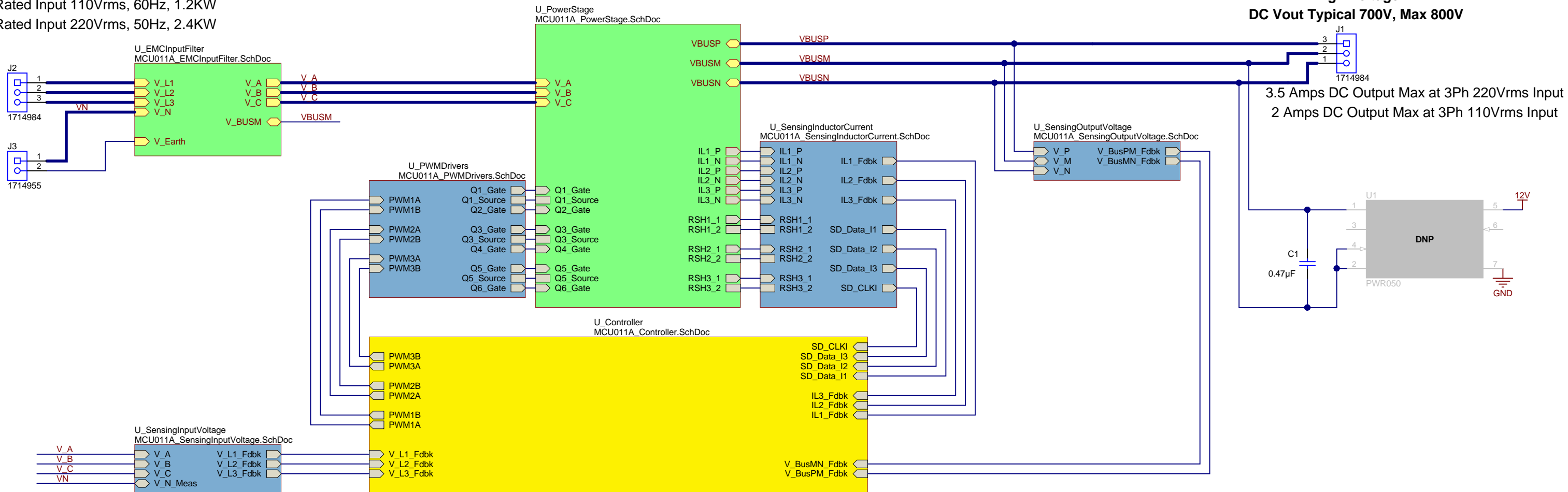


High Voltage
Three Phase 3Wire/ 4Wire/ 5Wire Input
 Rated Input 110Vrms, 60Hz, 1.2KW
 Rated Input 220Vrms, 50Hz, 2.4KW

High Voltage
DC Vout Typical 700V, Max 800V



**C2000 Microcontroller
 TMS320F28337xD**

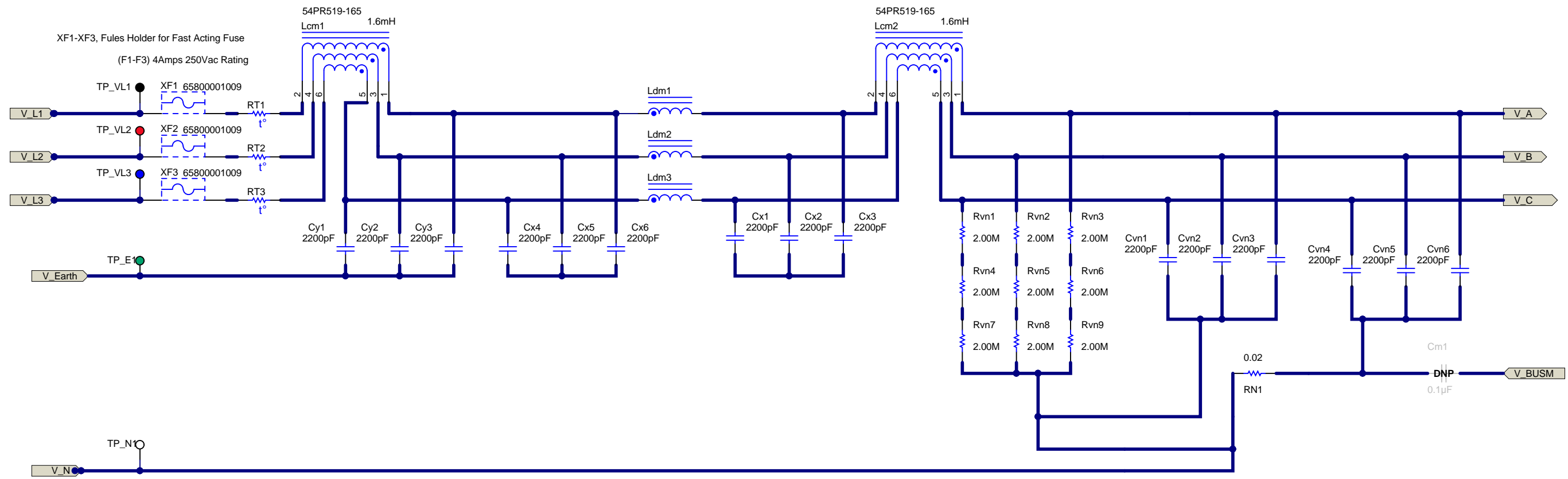
Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
E1	N/A	3/8/2016	N/A	First Revision of MCU011
E2	N/A	8/24/2016	N/A	Second Revision of MCU011
A	N/A	5/23/2016	N/A	Release Version MCU011

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Orderable: TIEVM-VIENNARECT	Designed for: Public Release	Mod. Date: 12/20/2017
TID #: TIDM-1000	Project Title: C2000 Three Phase PFC (Vienna Rectifier)	
Number: MCU011	Rev: A	Sheet Title: Cover Page
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 10
Drawn By: Manish Bhardwaj	File: MCU011A_CoverSheet.SchDoc	Size: B
Engineer: Manish Bhardwaj	Contact: http://www.ti.com/support	

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High Voltage



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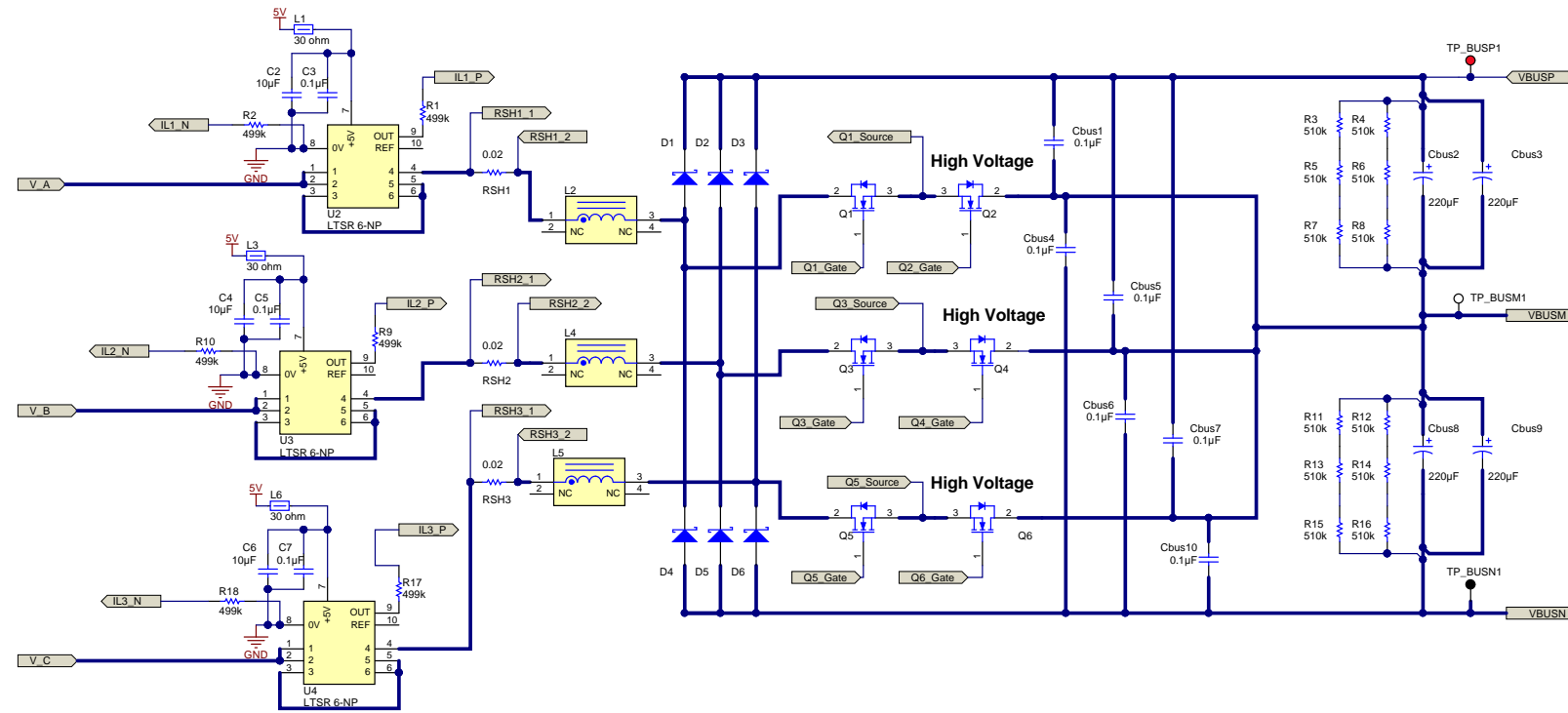
Orderable: TIEVM-VIENNARECT	Designed for: Public Release	Mod. Date: 5/26/2017
TID #: TIDM-1000	Project Title: C2000 Three Phase PFC (Vienna Rectifier)	
Number: MCU011	Rev: A	Sheet Title: EMC Input Filter
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 10
Drawn By:	File: MCU011A_EMCInputFilter.SchDoc	Size: B
Engineer: Manish Bhardwaj	Contact: http://www.ti.com/support	



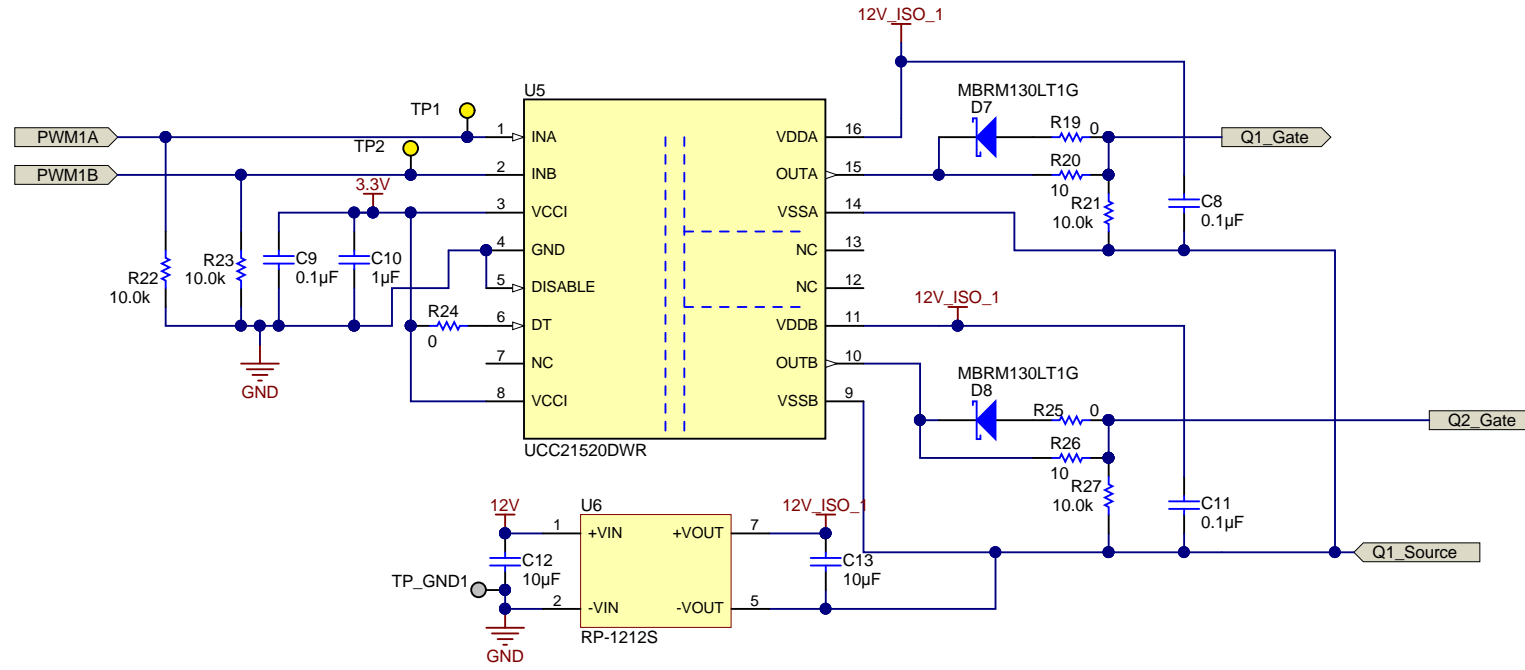
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All the nets highlighted are high current carrying and high voltage carrying, appropriate clearance and width is needed

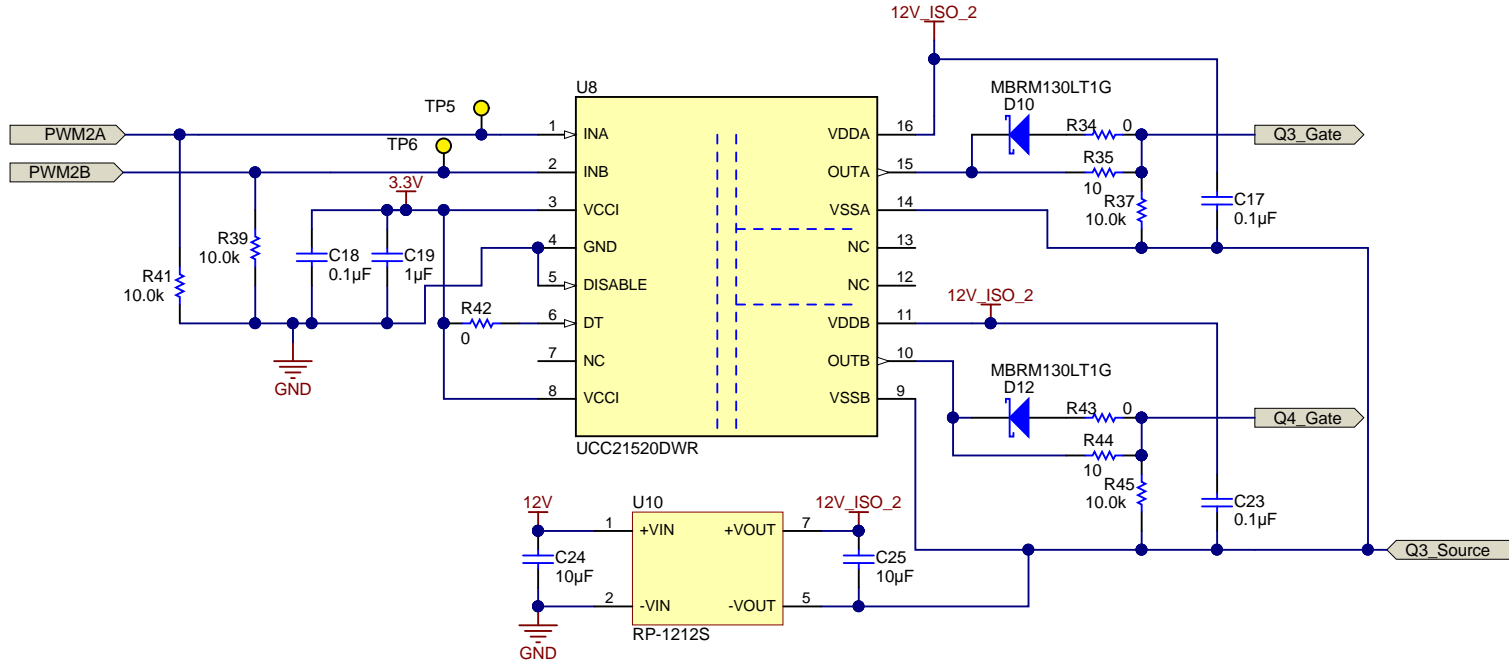
High Voltage



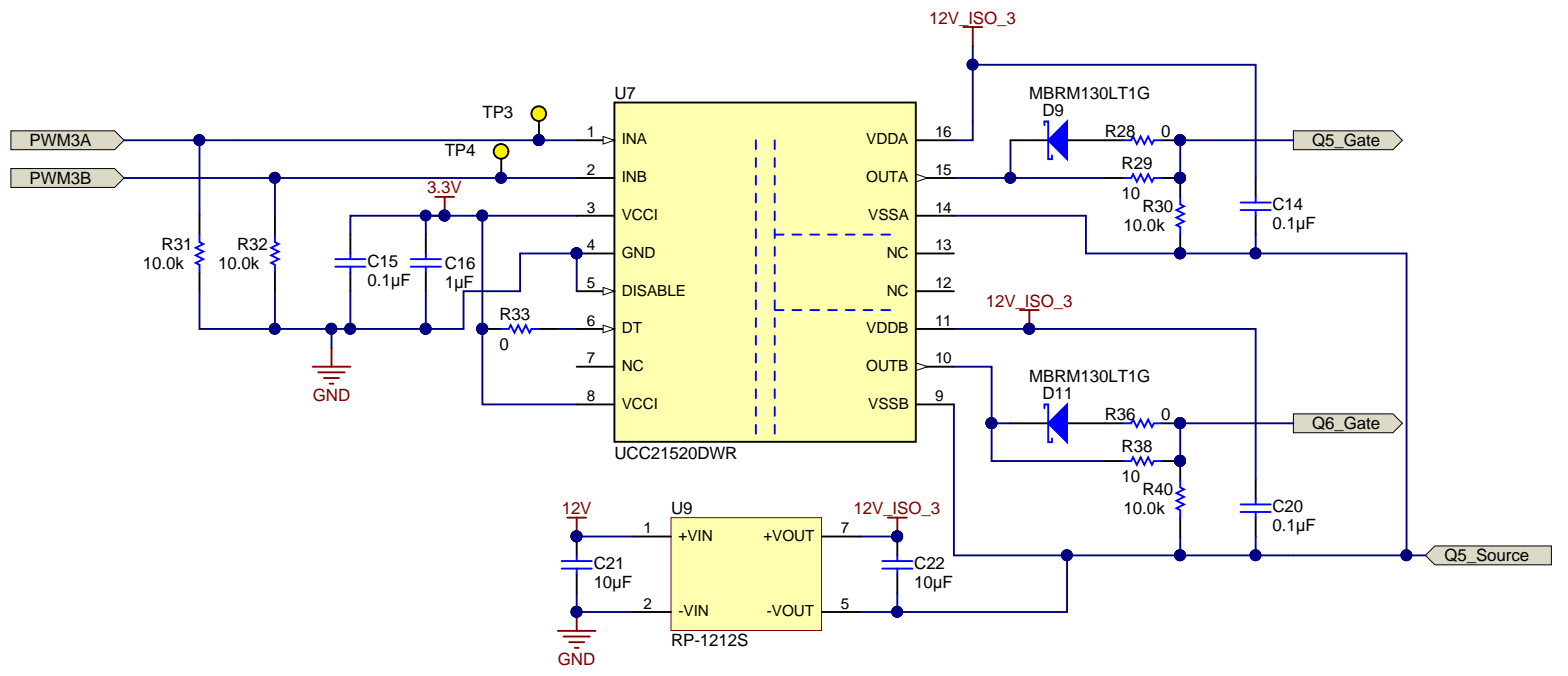
Place U1 Close to Q1 and Q2, and minimize distance between Q1_Gate and U1-OUTA, Q2-Gate and U1-OUTB, VSS1 &VSS2 and Q1-Source



High Voltage



Place U2 Close to Q3 and Q4, and minimize distance between Q3_Gate and U2-OUTA, Q4-Gate and U2-OUTB, VSS1 &VSS2 and Q3-Source

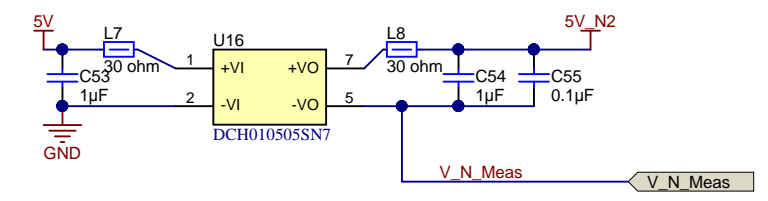
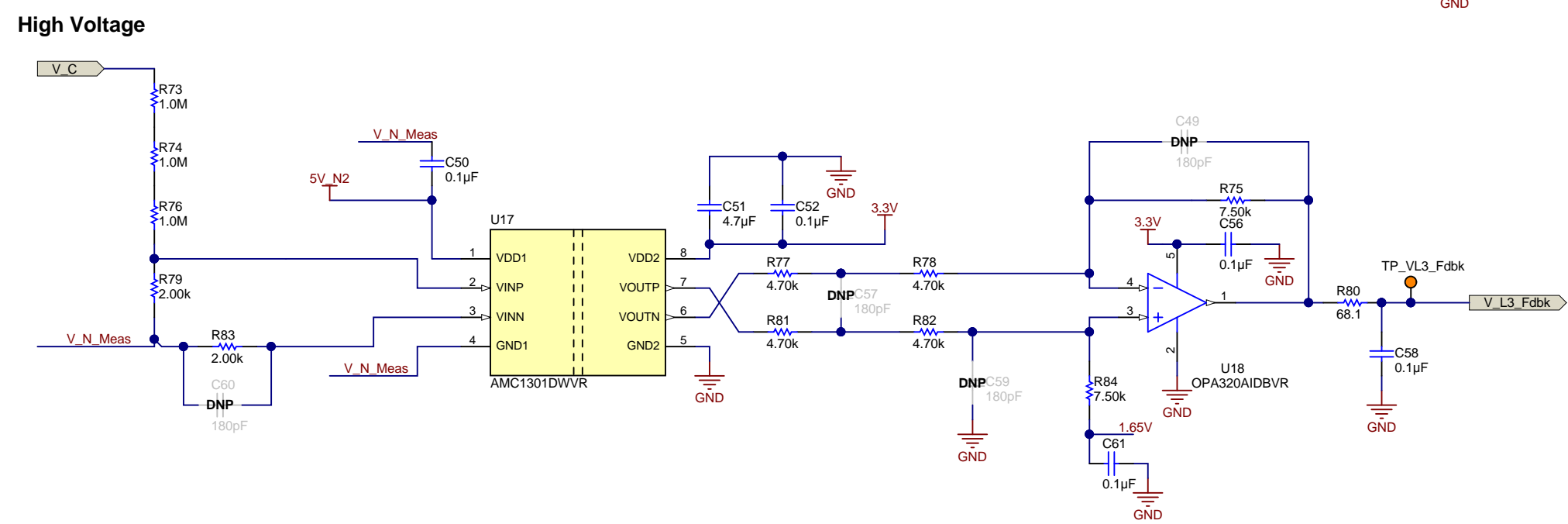
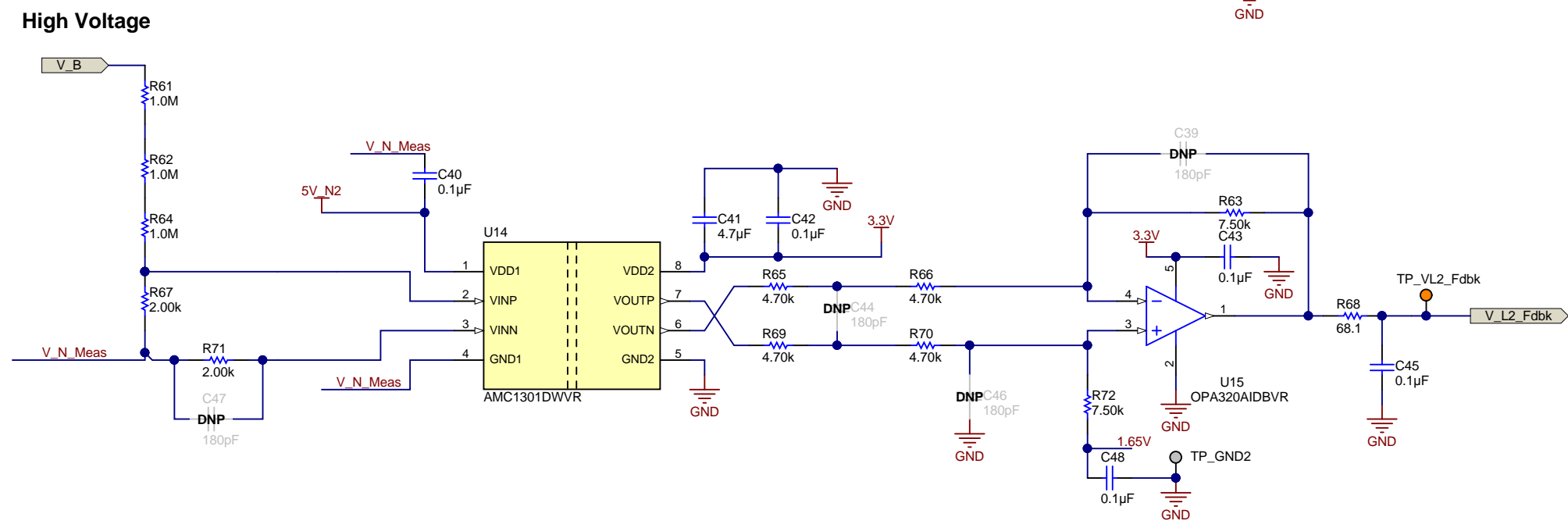
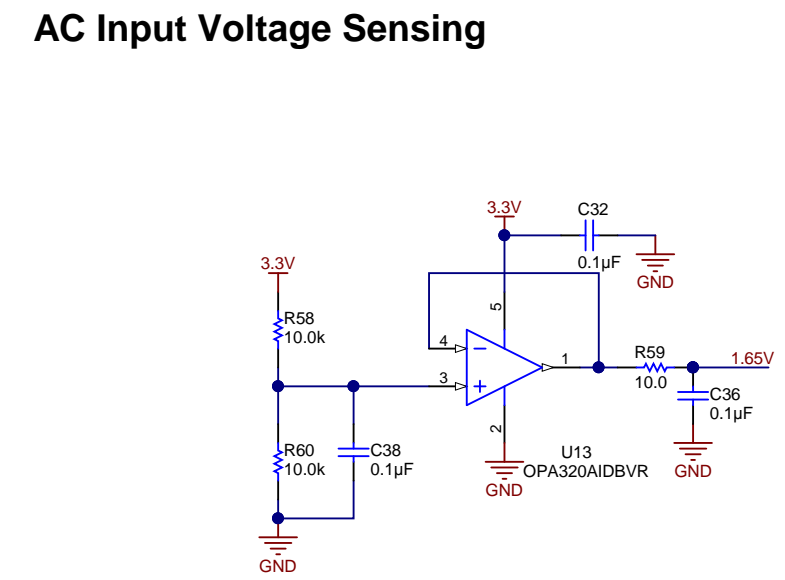
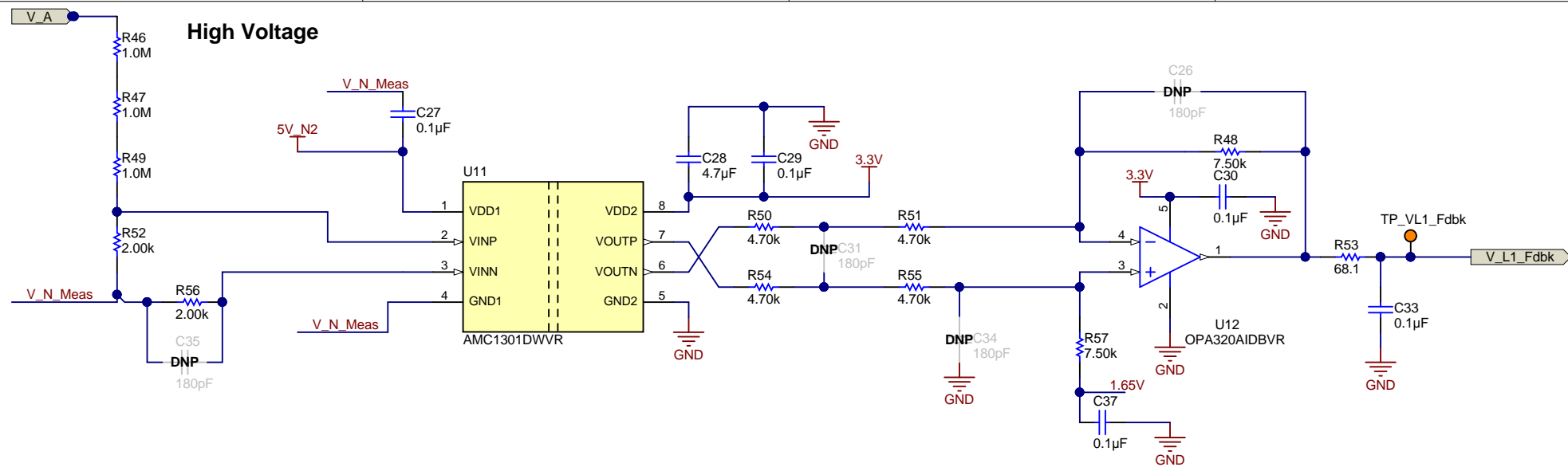


Place U3 Close to Q5 and Q6, and minimize distance between Q5_Gate and U3-OUTA, Q6-Gate and U3-OUTB, VSS1 &VSS2 and Q5-Source

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TID #: TIDM-1000	Project Title: C2000 Three Phase PFC (Vienna Rectifier)	
Number: MCU011	Rev: A	Sheet Title: PWM Gate Drivers
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 4 of 10
Drawn By: Manish Bhardwaj	File: MCU011A_PWMDrivers.SchDoc	Size: B
Engineer: Manish Bhardwaj	Contact: http://www.ti.com/support	





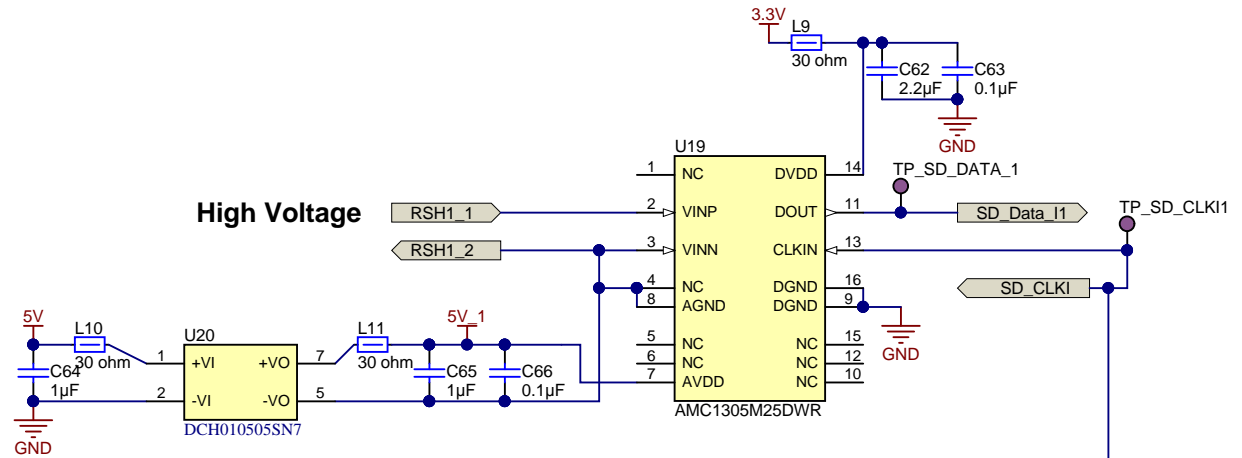
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Orderable: TIEVM-VIENNARECT	Designed for: Public Release	Mod. Date: 5/26/2017
TID #: TIDM-1000	Project Title: C2000 Three Phase PFC (Vienna Rectifier)	
Number: MCU011	Rev: A	Sheet Title: Input AC Voltage Sensing Interface Circuitry
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 5 of 10
Drawn By: Manish Bhardwaj	File: MCU011A_SensingInputVoltage.SchDoc	Size: B
Engineer: Manish Bhardwaj	Contact: http://www.ti.com/support	

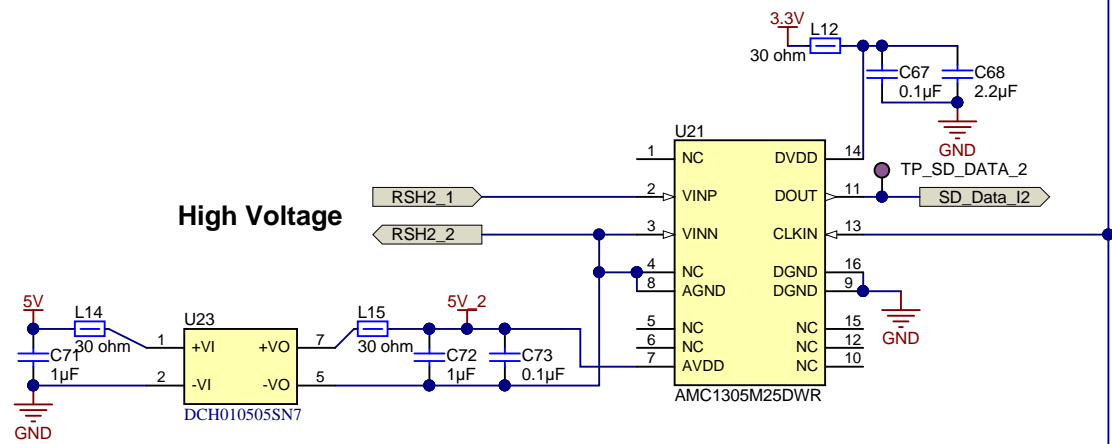


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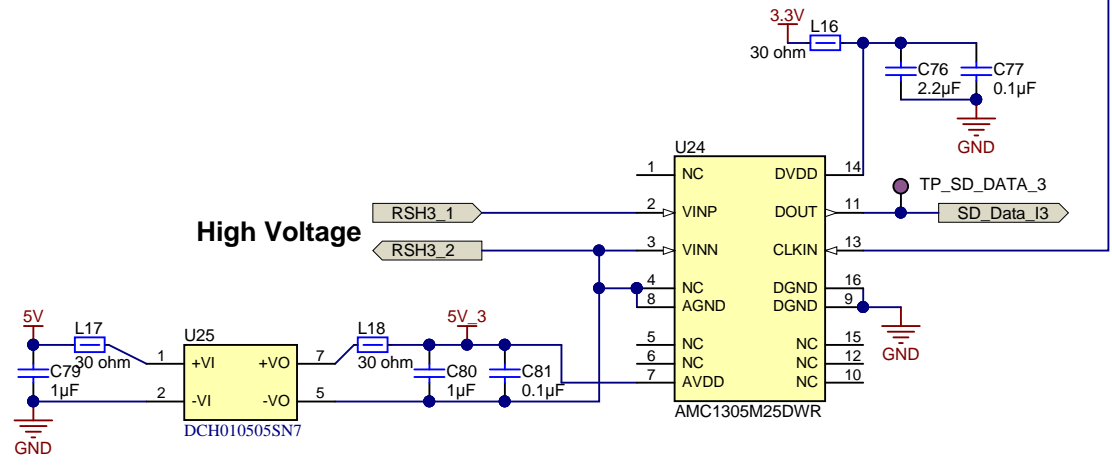
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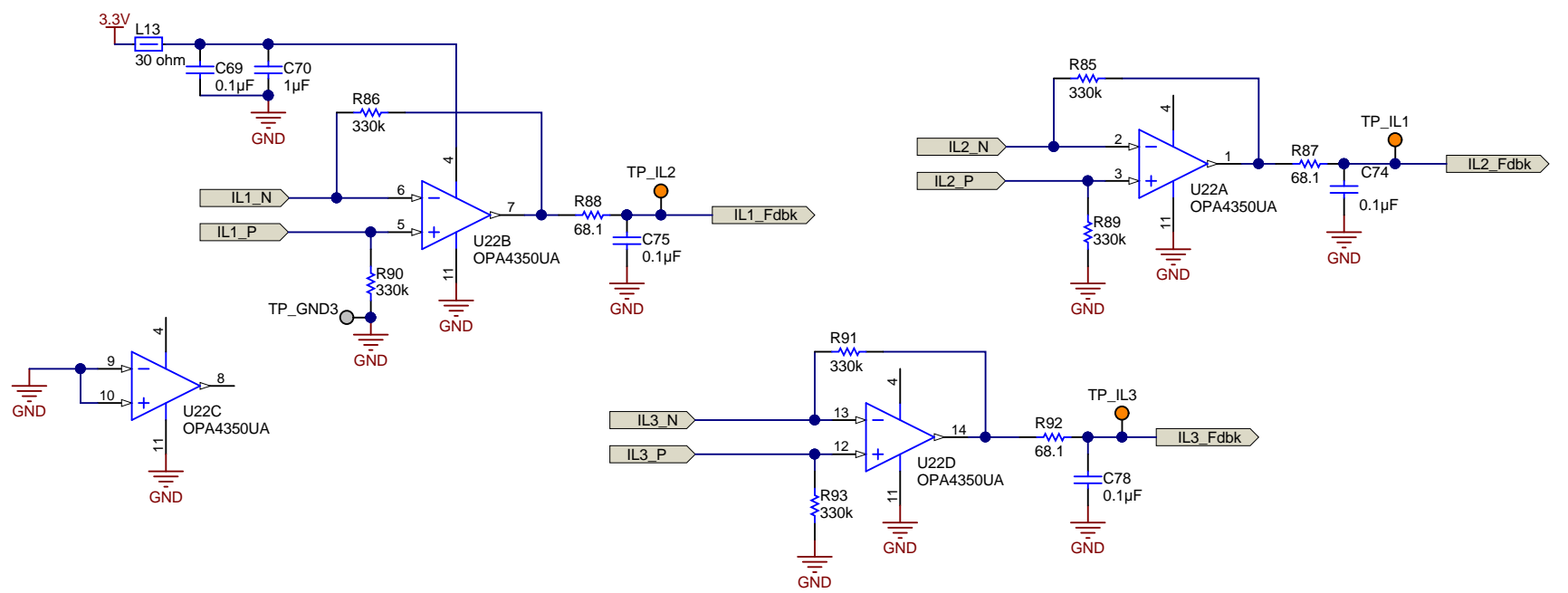
The connection from RSH1 should be close to U7, also a small GND island needs to be created w.r.t. RSH1_2 to help with the layout



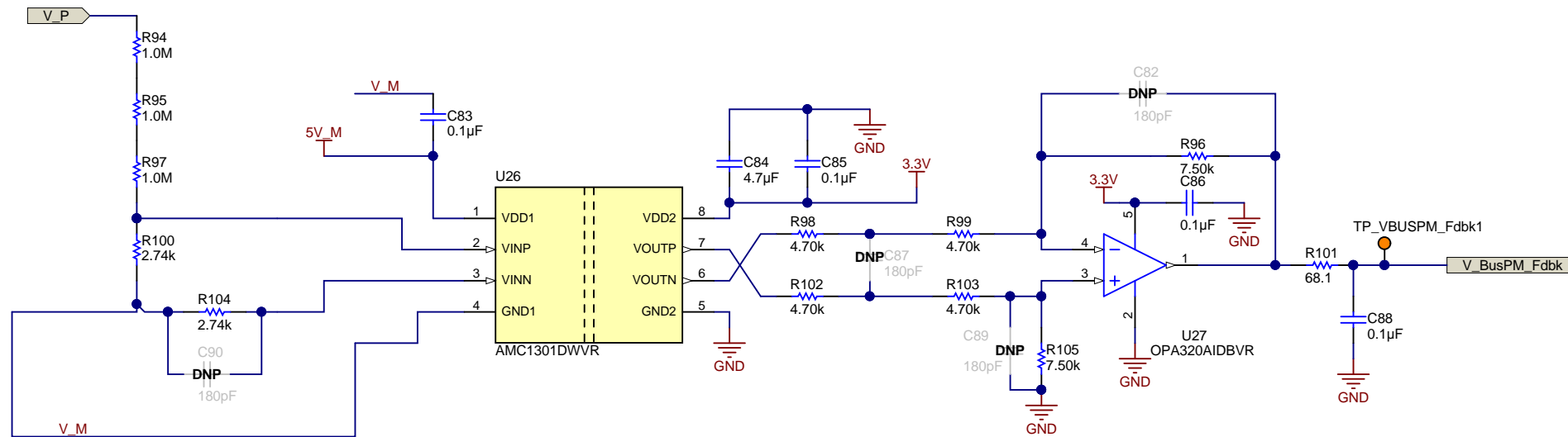
The connection from RSH2 should be close to U8, also a small GND island needs to be created w.r.t. RSH2_2 to help with the layout



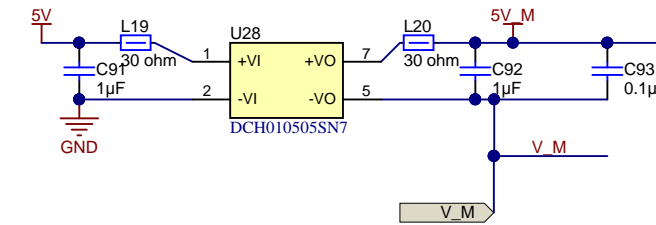
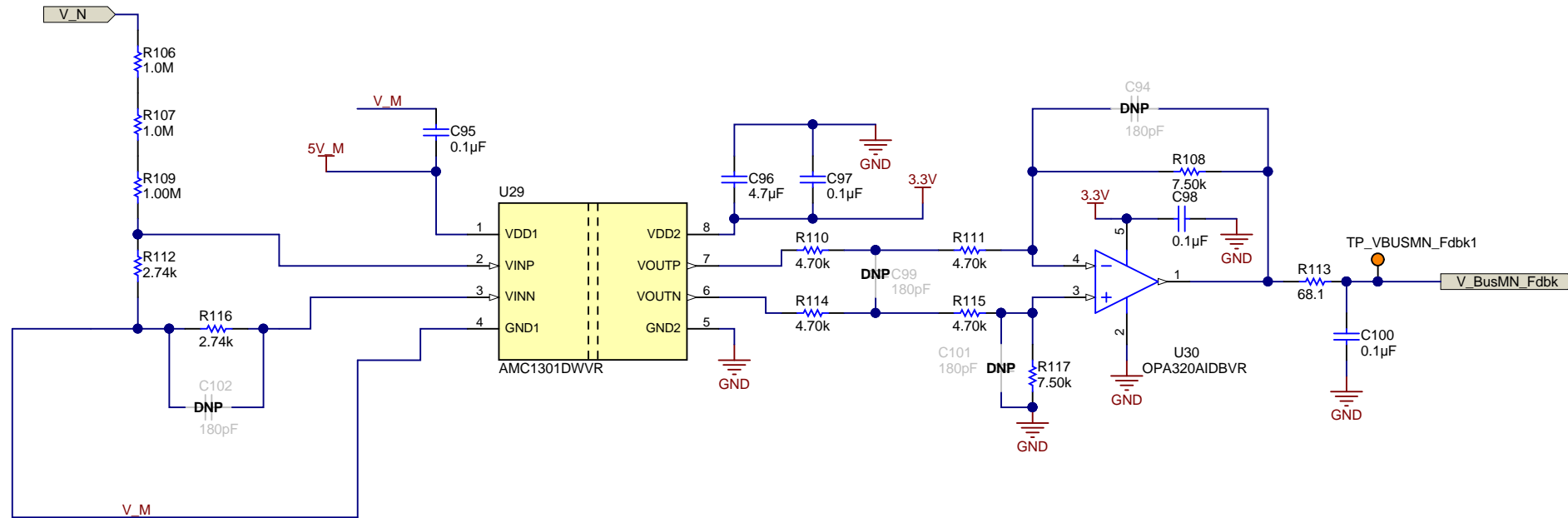
The connection from RSH3 should be close to U9, also a small GND island needs to be created w.r.t. RSH3_2 to help with the layout



High Voltage

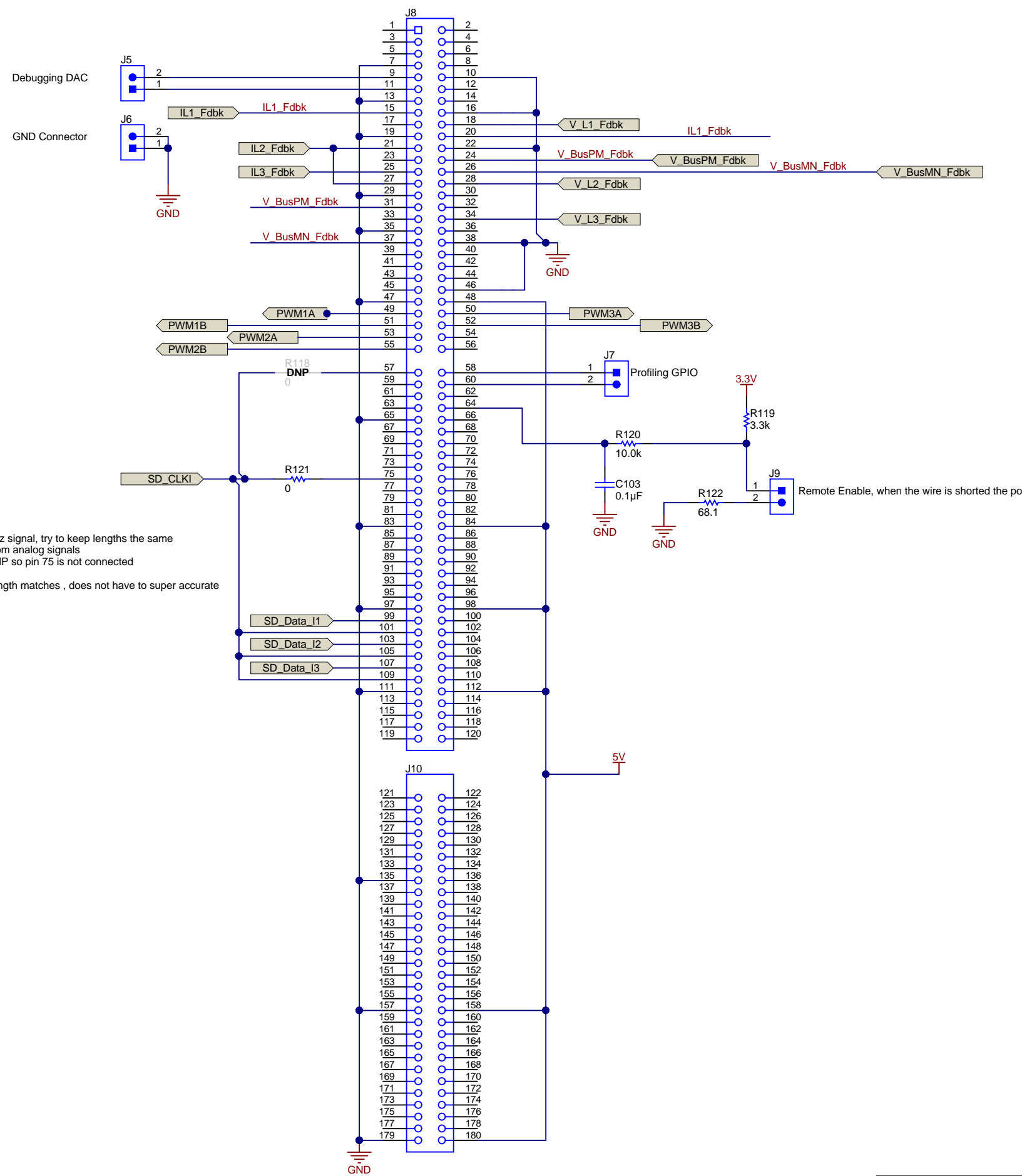


High Voltage



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Orderable: TIEVM-VIENNARECT	Designed for: Public Release	Mod. Date: 12/20/2017
TID #: TIDM-1000	Project Title: C2000 Three Phase PFC (Vienna Rectifier)	
Number: MCU011	Rev: A	Sheet Title: Output DC Bus Voltage Sensing
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 7 of 10
Drawn By:	File: MCU011A_SensingOutputVoltage.SchDoc	Size: B
Engineer: Manish Bhardwaj	Contact: http://www.ti.com/support	



SD CLK, SD_DATA1/2/3 are 20MHz signal, try to keep lengths the same
 These signals must be kept away from analog signals
 SD_CLK source is pin 57, R100 is DNP so pin 75 is not connected

Need to add some extra length from R99 to pin 101, 105 and 107 so that the length matches, does not have to be super accurate

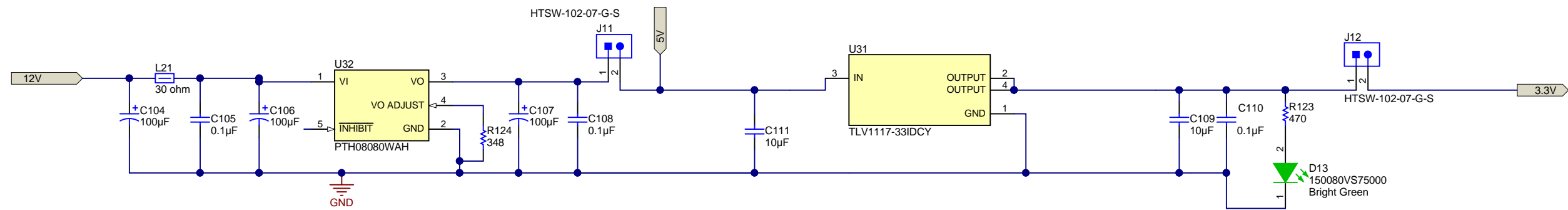
Remote Enable, when the wire is shorted the power stage is enabled

Please use HSEC180 PCB snippet.

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Orderable: TIEVM-VIENNARECT	Designed for: Public Release	Mod. Date: 2/10/2017
TID #: TIDM-1000	Project Title: C2000 Three Phase PFC (Vienna Rectifier)	
Number: MCU011	Rev: A	Sheet Title: Control Card Interface
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 8 of 10
Drawn By: Manish Bhardwaj	File: MCU011A_Controller.SchDoc	Size: B
Engineer: Manish Bhardwaj	Contact: http://www.ti.com/support	



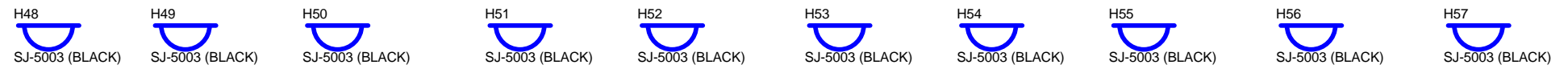
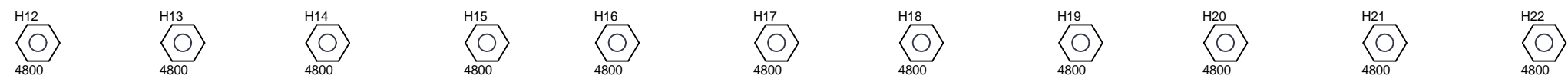
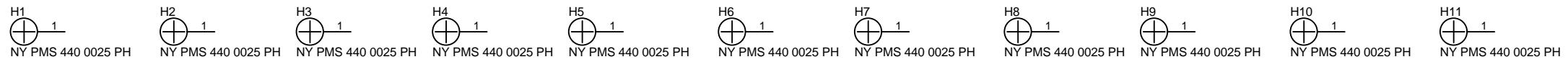


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Orderable: TIEVM-VIENNARECT	Designed for: Public Release	Mod. Date: 12/8/2016
TID #: TIDM-1000	Project Title: C2000 Three Phase PFC (Vienna Rectifier)	
Number: MCU011	Rev: A	Sheet Title: Board Bias Power Supply
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 9 of 10
Drawn By: Manish Bhardwaj	File: MCU011A_ControllerPower.SchDoc	Size: B
Engineer: Manish Bhardwaj	Contact: http://www.ti.com/support	



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PCB Number: MCU011
PCB Rev: A

PCB LOGO
Texas Instruments

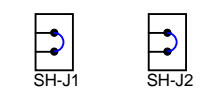
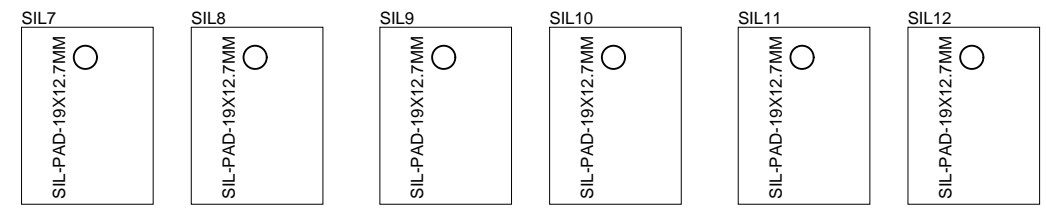
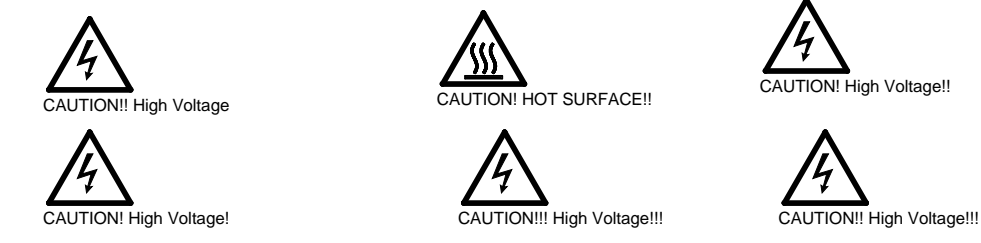
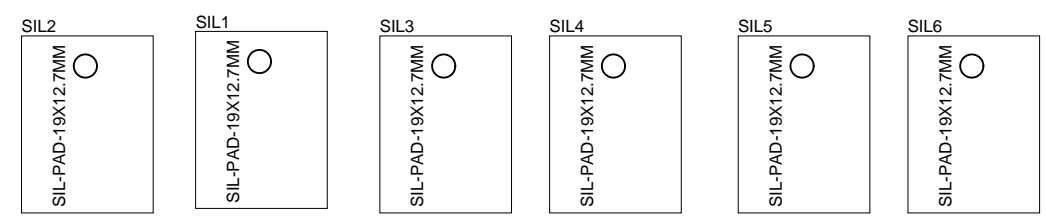
PCB LOGO
Pb-Free Symbol

PCB LOGO
FCC disclaimer

H23
MECH
MCH013A

CAUTION! High Voltage Input

PCB LOGO
CAUTION! Read User Guide Before Operating



Insulator pads for the switches and diodes to mount on the heat sink

ZZ1
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

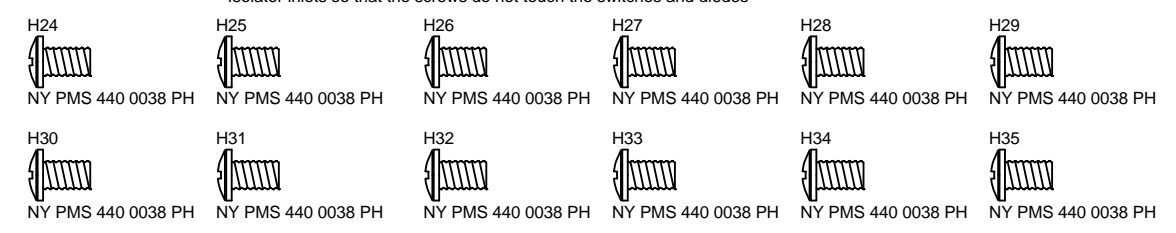
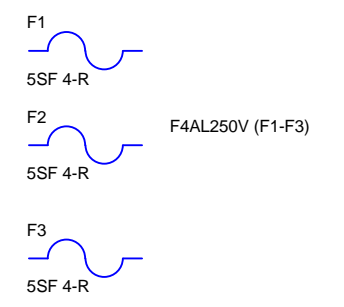
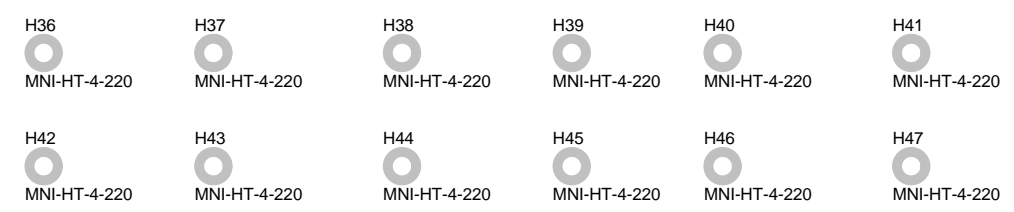
ZZ2
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ4
Assembly Note
Mount heatsink H16 as specified in the mechanical drawing and assembly instructions provided separately

ZZ5
Assembly Note
Paste SIL1-16 on the heat sink or on the back of D1-D6, Q1-Q6, place the insulators H17-H28 on D1-D6 and Q1-Q6

ZZ6
Assembly Note
Place Fuses F1-F3 in fuse holder XF1-XF3



Socket Head Screws to mount the switches and diodes on the heat sink

Orderable: TIEVM-VIENNARECT	Designed for: Public Release	Mod. Date: 1/4/2018
TID #: TIDM-1000	Project Title: C2000 Three Phase PFC (Vienna Rectifier)	
Number: MCU011	Rev: A	Sheet Title: EVM Hardware
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 10 of 10
Drawn By: Manish Bhardwaj	File: MCU011A_EVM_Hardware_SchDoc	Size: B
Engineer: Manish Bhardwaj	Contact: http://www.ti.com/support	

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