

LM7600xEVM User's Guide

The Texas Instruments LM76002EVM-500K and LM76003EVM-500K evaluation modules (EVM) help designers evaluate the operation and performance of the LM7600x wide-input voltage buck regulator. The output voltage for the device offers configurability from 1-V to 95% of V_{IN} , synchronous rectification, PFM, and forced PWM mode and a 300-kHz to 2.2-MHz adjustable frequency range with a 500-kHz default frequency. It also offers external frequency synchronization, power-good (PG) flag, and a precision enable to program undervoltage lockout (UVLO) and internal compensation. Built in protection includes current limit, overvoltage and undervoltage protection, and thermal shutdown. The LM7600x EVM is configured for an output voltage of 3.3 V and a switching frequency of 500 kHz. Refer to the [LM7600x data sheet](#) for additional features, detailed description, and available options.

The EVM contains one DC-DC converter (See [Table 1](#)).

Table 1. Device and Package Configurations

CONVERTER	IC	PACKAGE
U1	LM76003/2	(PWP) WQFN-30

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Trademarks

All trademarks are the property of their respective owners.

1 Setup

This section describes the test points and connectors on the EVM and how to properly connect, set up and use the LM7600x EVM. Please refer to [Figure 1](#) for a top view of the EVM and relative placement of the different test points and edge connector.

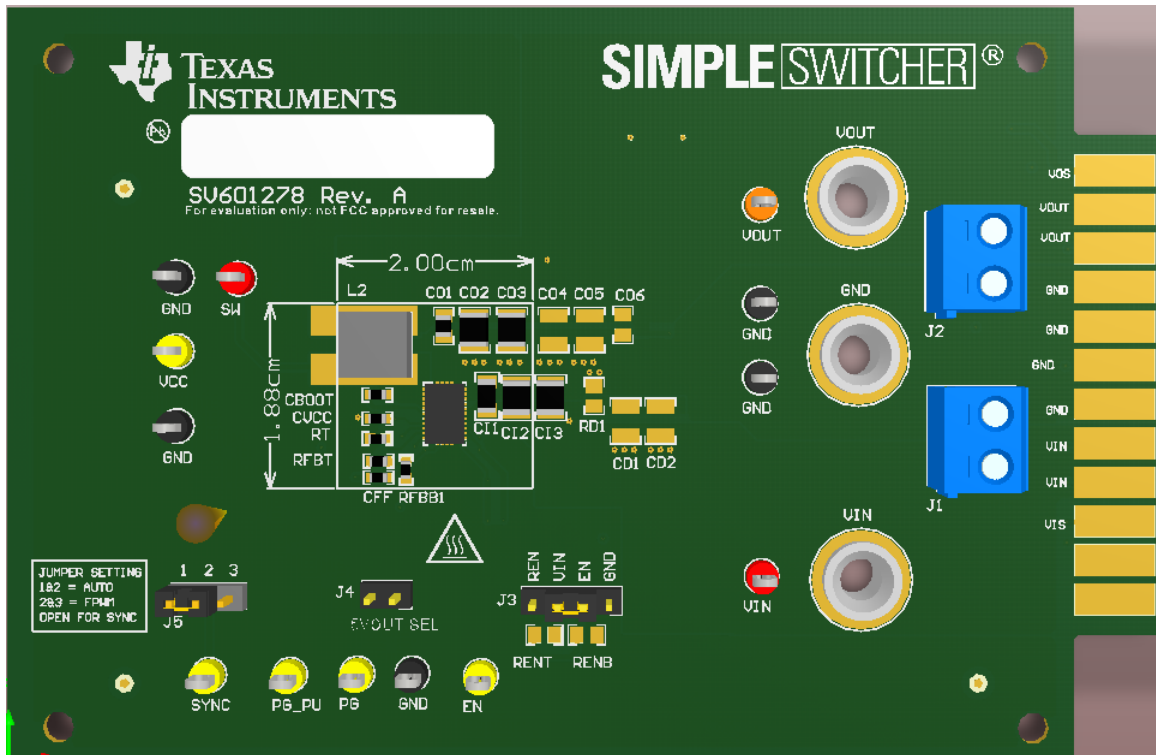


Figure 1. Top View of LM7600x EVM

1.1 Input/Output (I/O) Connector Description

VIN – Terminal on J1—is the power input terminal for the converter. The terminal edge connector also provides a power (VIN) and ground (GND) connection to allow the user to attach the EVM to a cable harness.

VOUT – Terminal on J2—is the regulated output voltage for the converter. The terminal edge connector also provides a power (VOUT) and ground (GND) connection to allow the user to attach the EVM to a cable harness.

GND – Terminal on J1 and J2—are the ground reference for the converter. The terminal edge connector also provides a GND connection for attaching the EVM to a cable harness.

EN – Testpoint—is used to enable the converter by supplying a voltage greater than 1.2 V (typ) or just to monitor the voltage on this pin whenever a resistor divider is in place (for precision enable applications). The regulator will be enabled when $V_{IN} > 3.5$ V. This threshold can be calculated by:

$$\text{Enable_Voltage} = V_{IH_EN} \cdot \left(1 + \frac{R_{ENT}}{R_{ENB}} \right)$$

where

- V_{IH_EN} is 1.2 V (typ)

(1)

PG – Testpoint—is used to monitor the power good flag. This flag indicates whether the output voltage has reached its regulation point. This pin is an open-drain output that requires a pullup resistor to the appropriate logic voltage (any voltage less than 20 V).

PG_PU – Testpoint—is the top connection of an optional 100-k Ω RPGOOD pullup resistor that ties directly to the open-drain PG pin. Supply an appropriate voltage to this test point, or tie it directly to the VOUT test point to observe the PG flag operation.

SYNC – Testpoint —is the input terminal for an optional external input clock to the converter as well a mode pin for converter. The external clock frequency must be between 350 kHz and 2.2 MHz, if used.

SW – Testpoint—is used to monitor the voltage on the switch pin and the switching frequency of the voltage regulator. Remove this test point before making any electromagnetic interference (EMI) measurements.

VIN_EMI – Edge Connector pin #11 and pin#12—is used to supply the input voltage through an on board LC filter (if one is needed for conducted EMI/EMC measurement). The L1 and CBULK component pads are located on the bottom side of the EVM. Please refer to the EVM schematic for initial suggestion of component values.

1.2 Voltage Setup

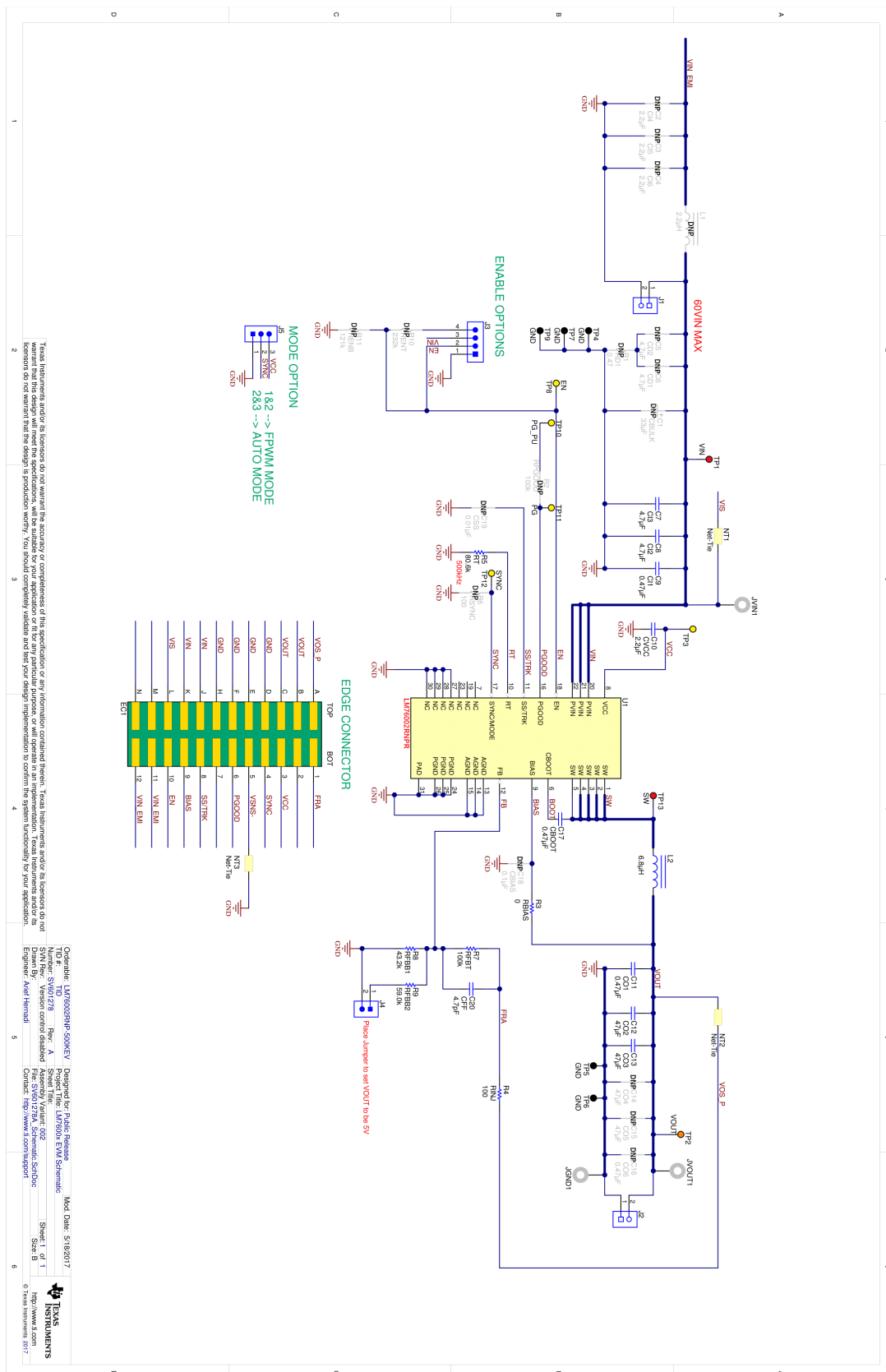
Set the input voltage (V_{IN}) range for the converter between the operating voltage range of 3.5 V to 60 V. If a load is driven, it should be applied to the VOUT terminal and should not exceed the maximum load current of 2.5 A for LM76002 and 3.5 A for LM76003.

1.3 Operation

For proper operation of the LM7600x, VIN, GND, and VOUT should be properly configured as stated above. In this configuration, the device starts up when power is applied, and the output voltage of the regulator (V_{OUT}) will come up to the proper value. The default setting for output voltage of the LM7600x is 3.3 V. Other output voltages can be set by replacing the feedback pin resistor dividers R_{FBT} and R_{FBB} ; please consult the datasheet for proper selection of these resistor values.

The default frequency for the LM7600xEVM is 500 kHz. If other frequencies are desired, within the frequency range of 300 kHz and 2.2 MHz, the R_T resistor value can be changed. See the [LM76002/03 device data sheet](#) for proper selection of the R_T resistor. Change inductor L2 and total output capacitance for proper control loop operation.

2 Schematic



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Doc Number: LM7602EVM-000001 Rev: A
 Number: SIV01278 Rev: A
 Date: 01/2017
 Version: 1.0
 File: SIV01278A_Schematic-SchDoc
 Contact: http://www.ti.com/support

Figure 2. LM7602 EVM Schematic

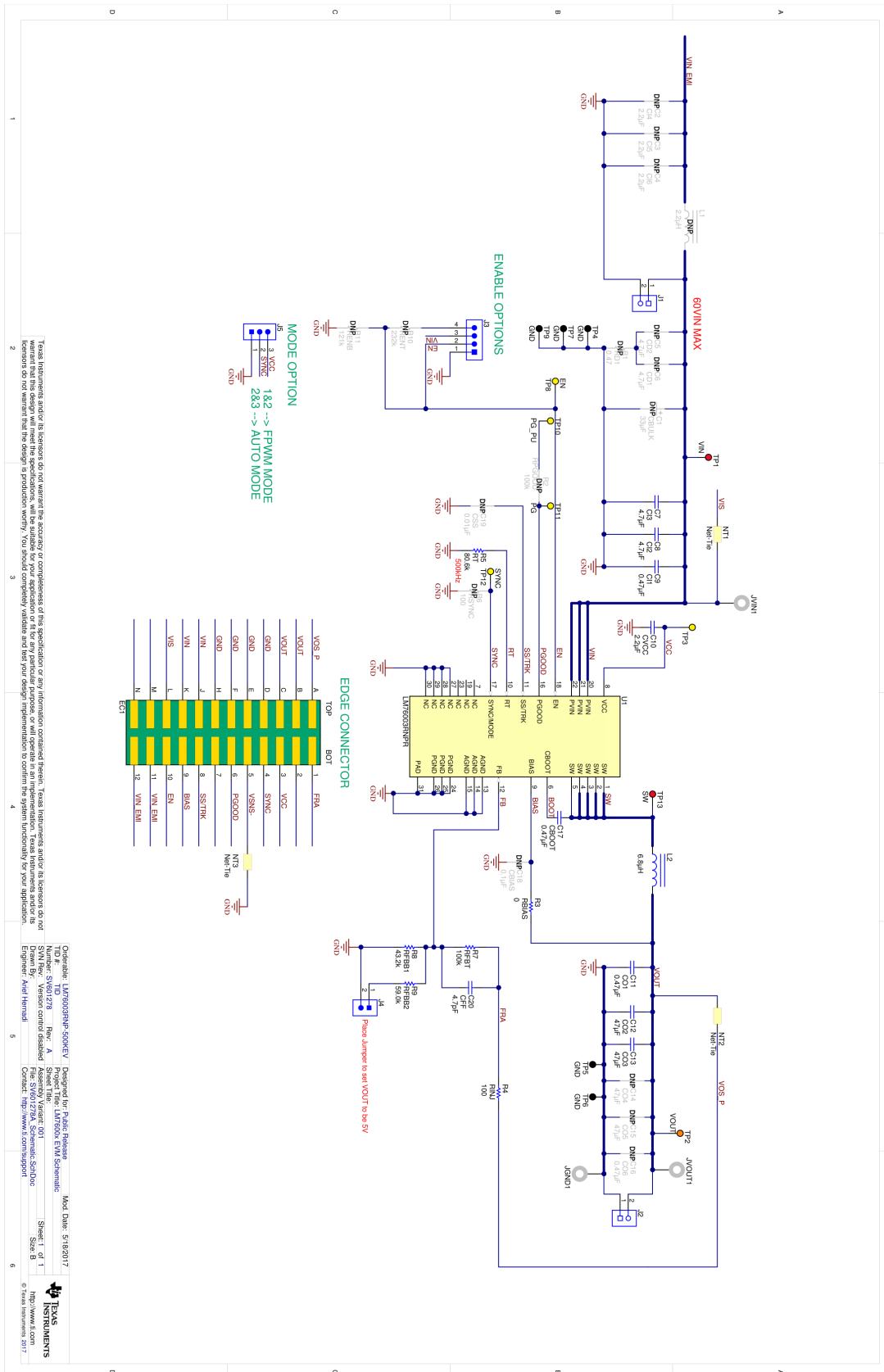


Figure 3. LM76003 EVM Schematic

3 Board Layout

Figure 4 through Figure 8 show the board layout for the LM7600xEVM. The EVM offers resistors, capacitors and test points to configure the output voltage, precision enable pin, set frequency and external clock synchronization.

The PWP WQFN-30 package offers an exposed thermal pad which must be soldered to the copper landing on the PCB for optimal thermal performance. The PCB consists of a 4-layer design. There are 2-oz copper planes on the top and bottom and 1-oz copper mid-layer planes to dissipate heat with an array of thermal vias under the thermal pad to connect to all four layers.

Test points have been provided for ease of use to connect the power supply, required load and to monitor critical signals. The 12-pin edge connector can also be used to facilitate the use of a cable harness if one is required (refer to the Table 2 section for mating connector part number).

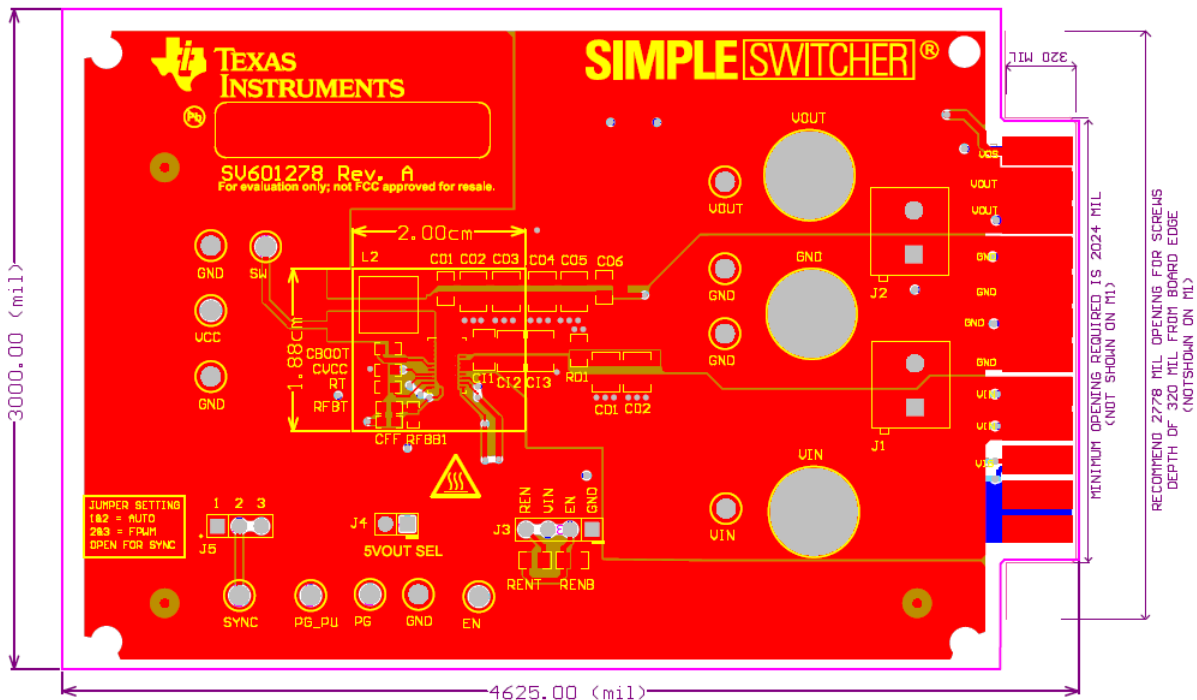


Figure 4. Top Silkscreen Layer

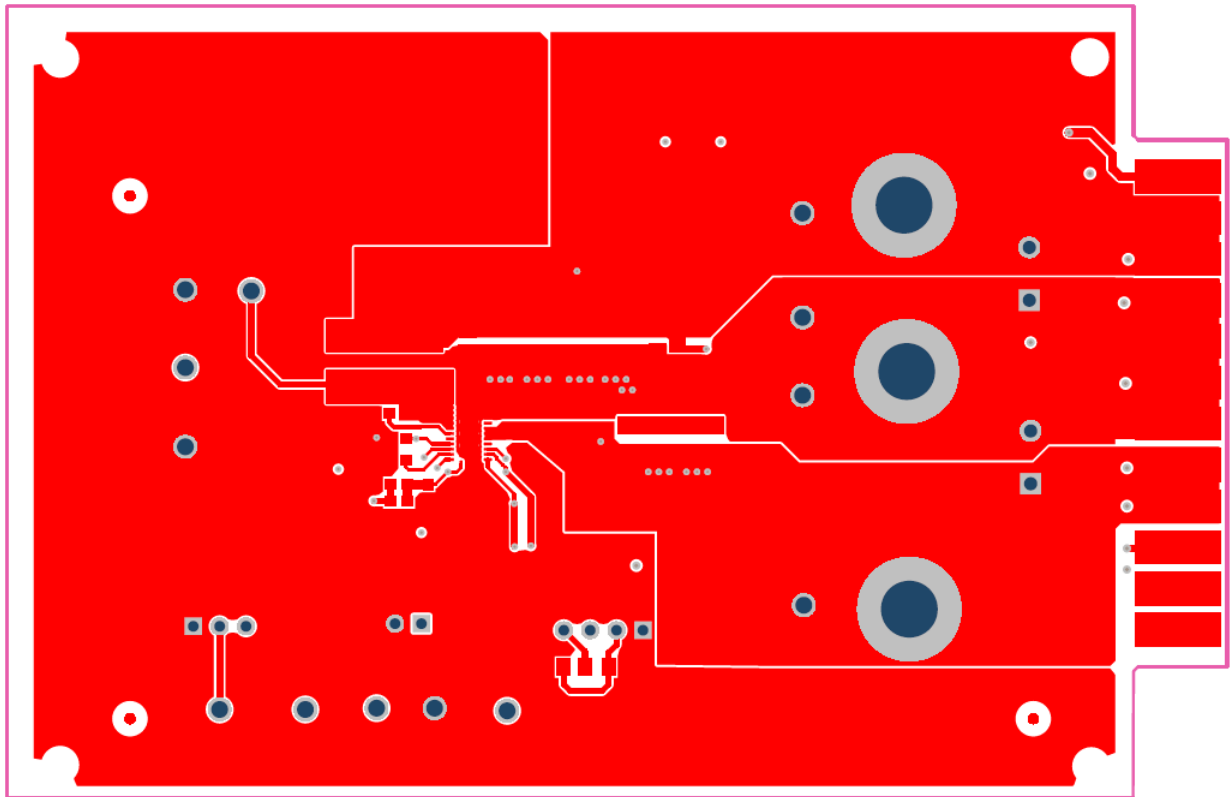


Figure 5. Top Layer Routing

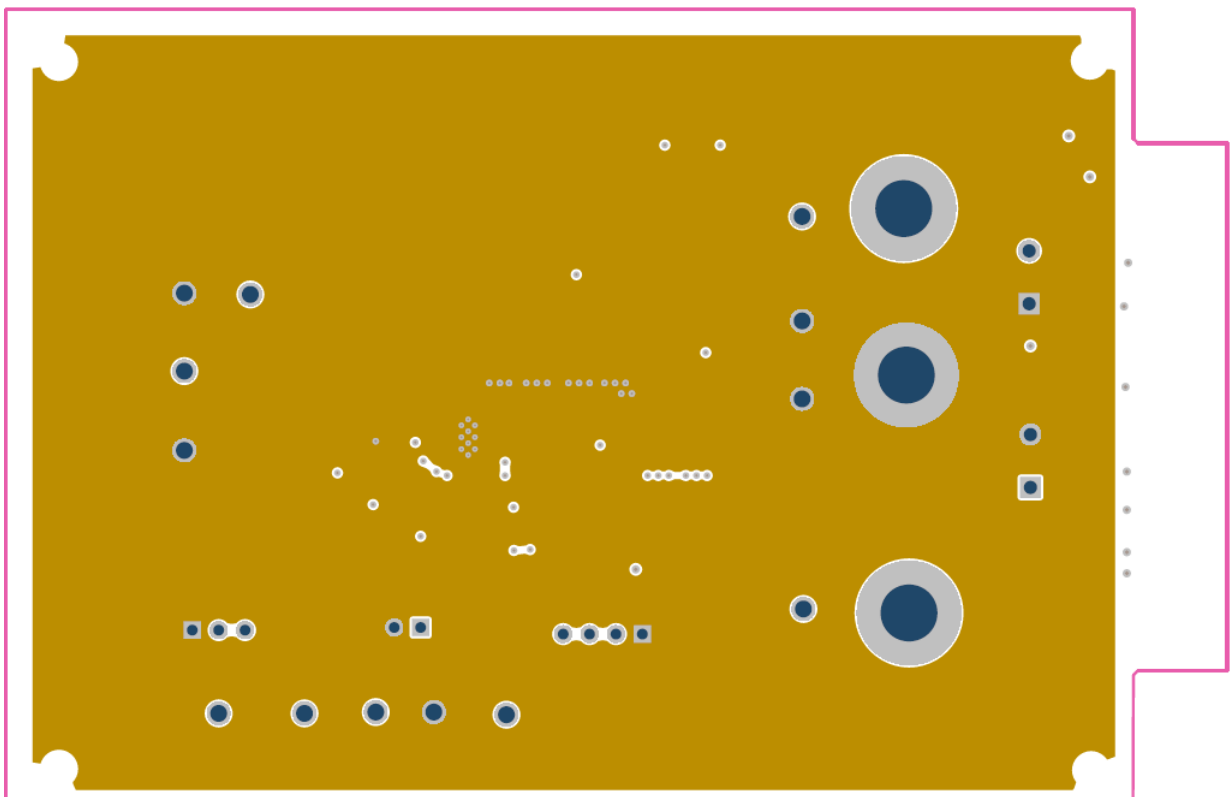


Figure 6. Mid Layer 1 Ground Plane

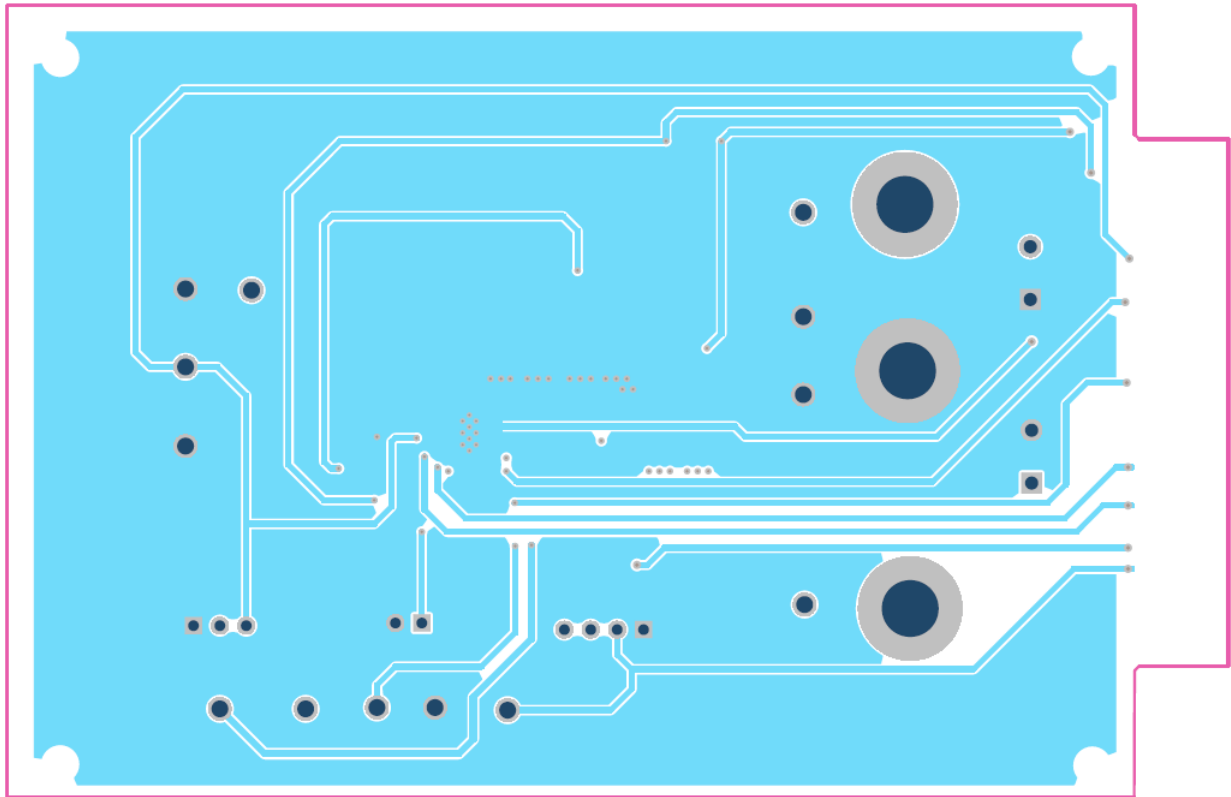


Figure 7. Mid Layer 2 Routing

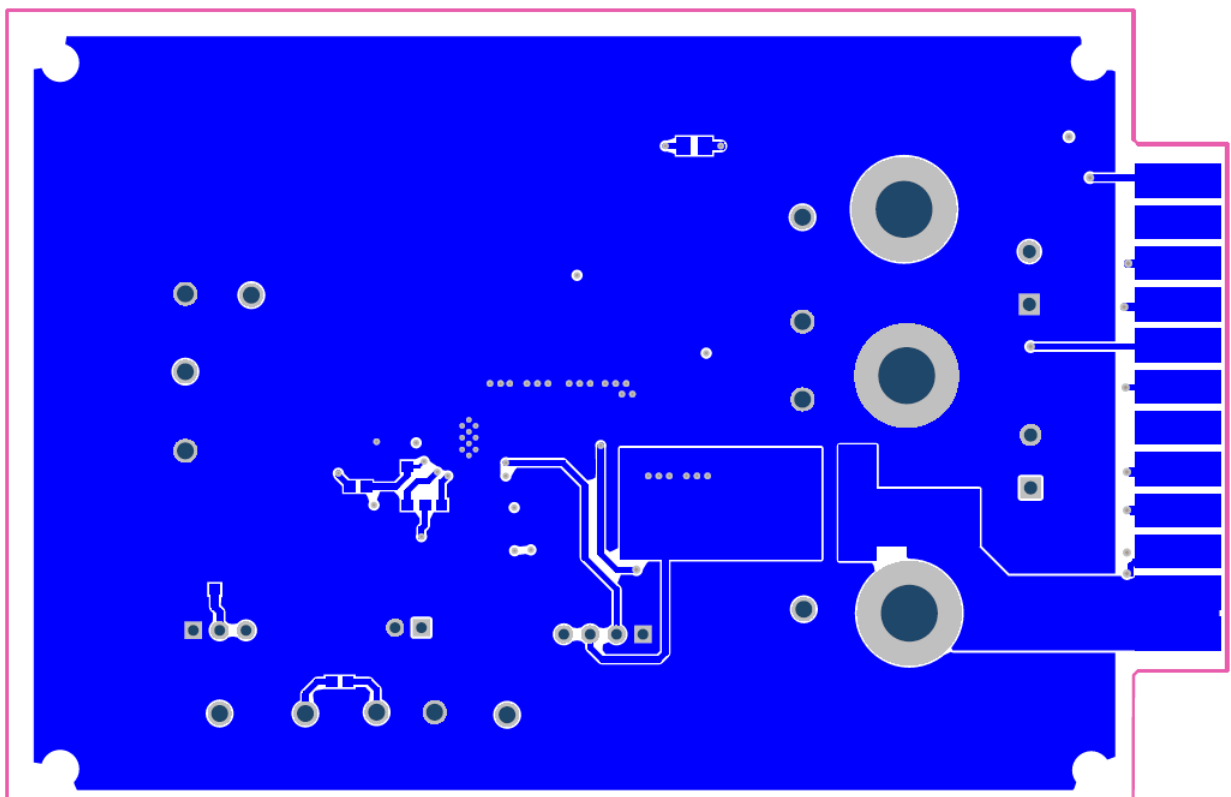


Figure 8. Bottom Layer Routing

4 Bill of Materials

Table 2. LM76002EVM-500K Bill of Materials (BOM) for 500 kHz Configuration

Designator	Comment	Description	Manufacturer	PartNumber	Quantity
!PCB1	Printed Circuit Board	Printed Circuit Board	Any	SV601278	1
C7, C8	CI3, CI2	CAP, CERM, 4.7 μ F, 100 V, +/- 10%, X7S, 1210	TDK	C3225X7S2A475K200AB	2
C9	CI1	CAP, CERM, 0.47 μ F, 100 V, +/- 10%, X7R, 1206	MuRata	GRM31MR72A474KA35L	1
C10	CVCC	CAP, CERM, 2.2 μ F, 10V, +/-10%, X6S, 0603	MuRata	GRM188C81A225KE34D	1
C11	CO1	CAP, CERM, 0.47 μ F, 50 V, +/- 10%, X7R, 0805	MuRata	GRM21BR71H474KA88L	1
C12, C13	CO2, CO3	CAP, CERM, 47 μ F, 10 V, +/- 10%, X7R, 1210	MuRata	GRM32ER71A476KE15L	2
C17	CBOOT	CAP, CERM, 0.47 μ F, 25V, +/-10%, X5R, 0603	MuRata	GRM188R61E474KA12D	1
J1, J2	J1, J2	Terminal Block, 5.08 mm, 2x1, Brass, TH	On-Shore Technology	ED120/2DS	2
J3	J3	Header, 100mil, 4x1, Gold, TH	Samtec	TSW-104-07-G-S	1
J4	J4	Header, 100mil, 2x1, Gold, TH	Samtec	TSW-102-07-G-S	1
J5	J5	Header, 100mil, 3x1, Gold, TH	Samtec	HTSW-103-07-G-S	1
JGND1, JVIN1, JVOUT1	JGND1, JVIN1, JVOUT1	Standard Banana Jack, Uninsulated, 8.9mm	Keystone	575-8	3
L2	L2	Inductor, Shielded, Composite, 6.8 μ H, 9.2 A, 0.02 ohm, SMD	Coilcraft	XAL6060-682MEB	1
LBL1	Size: 1.25" x 0.25"	Thermal Transfer Printable Labels, 1.250" W x 0.250" H - 10,000 per roll	Brady	THT-13-457-10	1
R3	RBIAS	RES, 0, 5%, 0.1 W, 0603	Vishay-Dale	CRCW06030000Z0EA	1
R4	RINJ	RES, 100, 1%, 0.125 W, 0805	Vishay-Dale	CRCW0805100RFKEA	1
R5	RT	RES, 80.6 k, 1%, 0.1 W, 0603	Vishay-Dale	CRCW060380K6FKEA	1
R7	RFBT	RES, 100 k, 1%, 0.1 W, 0603	Vishay-Dale	CRCW0603100KFKEA	1
R8	RFBB1	RES, 43.2 k, 1%, 0.1 W, 0603	Vishay-Dale	CRCW060343K2FKEA	1
R9	RFBB2	RES, 59.0 k, 1%, 0.1 W, 0603	Vishay-Dale	CRCW060359K0FKEA	1
SH-J1, SH-J2	SH-J1, SH-J2	Shunt, 100mil, Gold plated, Black	3M	969102-0000-DA	2
TP1, TP13	VIN, SW	Test Point, Multipurpose, Red, TH	Keystone	5010	2
TP2	VOUT	Test Point, Multipurpose, Orange, TH	Keystone	5013	1
TP3, TP8, TP10, TP11, TP12	VCC, EN, PG_PU, PG, SYNC	Test Point, TH Multipurpose, Yellow	Keystone	5014	5
TP4, TP5, TP6, TP7, TP9	GND	Test Point, Multipurpose, Black, TH	Keystone	5011	5
U1	LM76002RNPR	SIMPLE SWITCHER 3.5V to 60V 2.5A Synchronous Step-Down Voltage Regulator, RNP0030B (WQFN-30)	Texas Instruments	LM76002RNPR	1
C1	CBULK	CAP, AL, 33 μ F, 63 V, +/- 20%, SMD	Chemi-Con	EMVE630ADA330MHA0G	0
C2, C3, C4	CI4, CI5, CI6	CAP, CERM, 2.2 μ F, 100 V, +/- 10%, X7R, 1206_190	MuRata	GRM31CR72A225KA73L	0
C5, C6	CD2, CD1	CAP, CERM, 4.7 μ F, 100 V, +/- 10%, X7S, 1210	TDK	C3225X7S2A475K200AB	0

Table 2. LM76002EVM-500K Bill of Materials (BOM) for 500 kHz Configuration (continued)

C14, C15	CO4, CO5	CAP, CERM, 47 μ F, 10 V, +/- 10%, X7R, 1210	MuRata	GRM32ER71A476KE15L	0
C16	CO6	CAP, CERM, 0.47 μ F, 50 V, +/- 10%, X7R, 0805	MuRata	GRM21BR71H474KA88L	0
C19	CSS	CAP, CERM, 0.01 μ F, 100 V, +/- 20%, X7R, 0603	AVX	06031C103MAT2A	0
FID1, FID2, FID3	FID1, FID2, FID3	Fiducial mark. There is nothing to buy or mount.	N/A	N/A	0
L1	L1	Inductor, Shielded, Composite, 2.2 μ H, 12.9A, 0.0137 ohm, SMD	Coilcraft	XAL7030-222MEB	0
R1	RD1	RES, 0.47, 1%, 0.125 W, 0805	Panasonic	ERJ-6RQFR47V	0
R2	RPGOOD	RES, 100 k, 1%, 0.1 W, 0603	Vishay-Dale	CRCW0603100KFKEA	0
R6	RSYNC	RES, 100, 1%, 0.1 W, 0603	Vishay-Dale	CRCW0603100RFKEA	0
R10	RENT	RES, 232 k, 1%, 0.125 W, 0805	Vishay-Dale	CRCW0805232KFKEA	0
R11	RENB	RES, 121 k, 1%, 0.125 W, 0805	Vishay-Dale	CRCW0805121KFKEA	0

Table 3. LM76003EVM-500K Bill of Materials (BOM) for 500 kHz Configuration

Designator	Comment	Description	Manufacturer	PartNumber	Quantity
!PCB1	Printed Circuit Board	Printed Circuit Board	Any	SV601278	1
C7, C8	CI3, CI2	CAP, CERM, 4.7 μ F, 100 V, +/- 10%, X7S, 1210	TDK	C3225X7S2A475K200AB	2
C9	CI1	CAP, CERM, 0.47 μ F, 100 V, +/- 10%, X7R, 1206	MuRata	GRM31MR72A474KA35L	1
C10	CVCC	CAP, CERM, 2.2 μ F, 10V, +/-10%, X6S, 0603	MuRata	GRM188C81A225KE34D	1
C11	CO1	CAP, CERM, 0.47 μ F, 50 V, +/- 10%, X7R, 0805	MuRata	GRM21BR71H474KA88L	1
C12, C13	CO2, CO3	CAP, CERM, 47 μ F, 10 V, +/- 10%, X7R, 1210	MuRata	GRM32ER71A476KE15L	2
C17	CBOOT	CAP, CERM, 0.47 μ F, 25V, +/-10%, X5R, 0603	MuRata	GRM188R61E474KA12D	1
J1, J2	J1, J2	Terminal Block, 5.08 mm, 2x1, Brass, TH	On-Shore Technology	ED120/2DS	2
J3	J3	Header, 100mil, 4x1, Gold, TH	Samtec	TSW-104-07-G-S	1
J4	J4	Header, 100mil, 2x1, Gold, TH	Samtec	TSW-102-07-G-S	1
J5	J5	Header, 100mil, 3x1, Gold, TH	Samtec	HTSW-103-07-G-S	1
JGND1, JVIN1, JVOUT1	JGND1, JVIN1, JVOUT1	Standard Banana Jack, Uninsulated, 8.9mm	Keystone	575-8	3
L2	L2	Inductor, Shielded, Composite, 6.8 μ H, 9.2 A, 0.02 ohm, SMD	Coilcraft	XAL6060-682MEB	1
LBL1	Size: 1.25" x 0.25"	Thermal Transfer Printable Labels, 1.250" W x 0.250" H - 10,000 per roll	Brady	THT-13-457-10	1
R3	RBIAS	RES, 0, 5%, 0.1 W, 0603	Vishay-Dale	CRCW06030000Z0EA	1
R4	RINJ	RES, 100, 1%, 0.125 W, 0805	Vishay-Dale	CRCW0805100RFKEA	1
R5	RT	RES, 80.6 k, 1%, 0.1 W, 0603	Vishay-Dale	CRCW060380K6FKEA	1
R7	RFBT	RES, 100 k, 1%, 0.1 W, 0603	Vishay-Dale	CRCW0603100KFKEA	1
R8	RFBB1	RES, 43.2 k, 1%, 0.1 W, 0603	Vishay-Dale	CRCW060343K2FKEA	1
R9	RFBB2	RES, 59.0 k, 1%, 0.1 W, 0603	Vishay-Dale	CRCW060359K0FKEA	1
SH-J1, SH-J2	SH-J1, SH-J2	Shunt, 100mil, Gold plated, Black	3M	969102-0000-DA	2
TP1, TP13	VIN, SW	Test Point, Multipurpose, Red, TH	Keystone	5010	2

Table 3. LM76003EVM-500K Bill of Materials (BOM) for 500 kHz Configuration (continued)

TP2	VOUT	Test Point, Multipurpose, Orange, TH	Keystone	5013	1
TP3, TP8, TP10, TP11, TP12	VCC, EN, PG_PU, PG, SYNC	Test Point, TH Multipurpose, Yellow	Keystone	5014	5
TP4, TP5, TP6, TP7, TP9	GND	Test Point, Multipurpose, Black, TH	Keystone	5011	5
U1	LM76003RNPR	SIMPLE SWITCHER 3.5V to 60V 3.5A Synchronous Step-Down Voltage Regulator, RNP0030B (WQFN-30)	Texas Instruments	LM76003RNPR	1
C1	CBULK	CAP, AL, 33 μ F, 63 V, +/- 20%, SMD	Chemi-Con	EMVE630ADA330MHA0G	0
C2, C3, C4	CI4, CI5, CI6	CAP, CERM, 2.2 μ F, 100 V, +/- 10%, X7R, 1206_190	MuRata	GRM31CR72A225KA73L	0
C5, C6	CD2, CD1	CAP, CERM, 4.7 μ F, 100 V, +/- 10%, X7S, 1210	TDK	C3225X7S2A475K200AB	0
C14, C15	CO4, CO5	CAP, CERM, 47 μ F, 10 V, +/- 10%, X7R, 1210	MuRata	GRM32ER71A476KE15L	0
C16	CO6	CAP, CERM, 0.47 μ F, 50 V, +/- 10%, X7R, 0805	MuRata	GRM21BR71H474KA88L	0
C19	CSS	CAP, CERM, 0.01 μ F, 100 V, +/- 20%, X7R, 0603	AVX	06031C103MAT2A	0
FID1, FID2, FID3	FID1, FID2, FID3	Fiducial mark. There is nothing to buy or mount.	N/A	N/A	0
L1	L1	Inductor, Shielded, Composite, 2.2uH, 12.9A, 0.0137 ohm, SMD	Coilcraft	XAL7030-222MEB	0
R1	RD1	RES, 0.47, 1%, 0.125 W, 0805	Panasonic	ERJ-6RQFR47V	0
R2	RPGOOD	RES, 100 k, 1%, 0.1 W, 0603	Vishay-Dale	CRCW0603100KFKEA	0
R6	RSYNC	RES, 100, 1%, 0.1 W, 0603	Vishay-Dale	CRCW0603100RFKEA	0
R10	RENT	RES, 232 k, 1%, 0.125 W, 0805	Vishay-Dale	CRCW0805232KFKEA	0
R11	RENB	RES, 121 k, 1%, 0.125 W, 0805	Vishay-Dale	CRCW0805121KFKEA	0

5 Application Curves

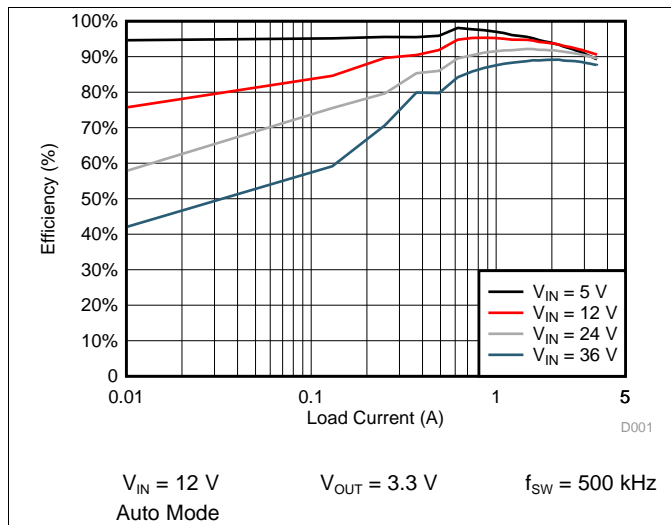


Figure 9. LM76003 Efficiency

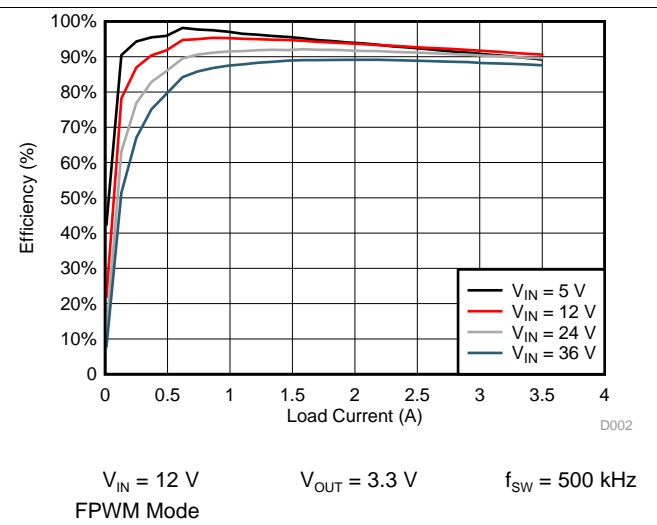


Figure 10. LM76003 Efficiency

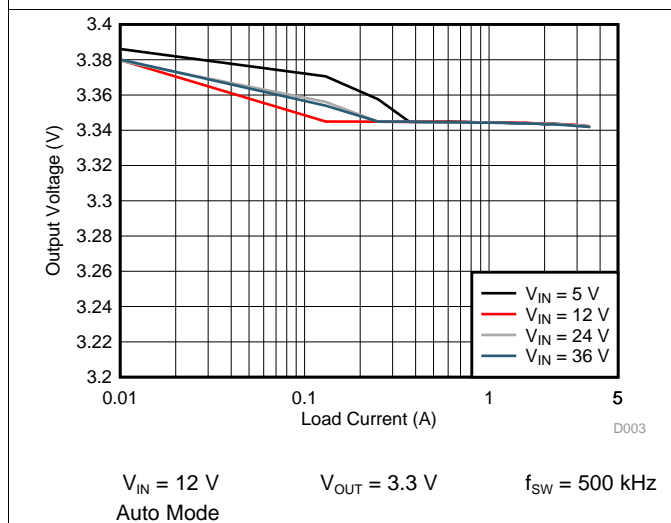


Figure 11. LM76003 Load and Line Regulation

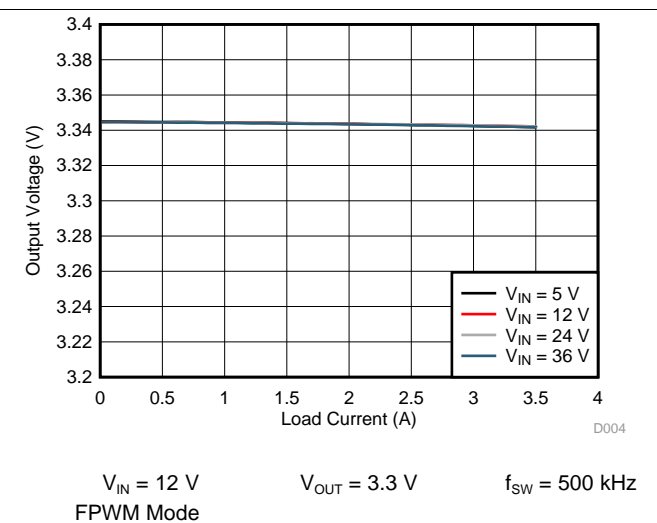
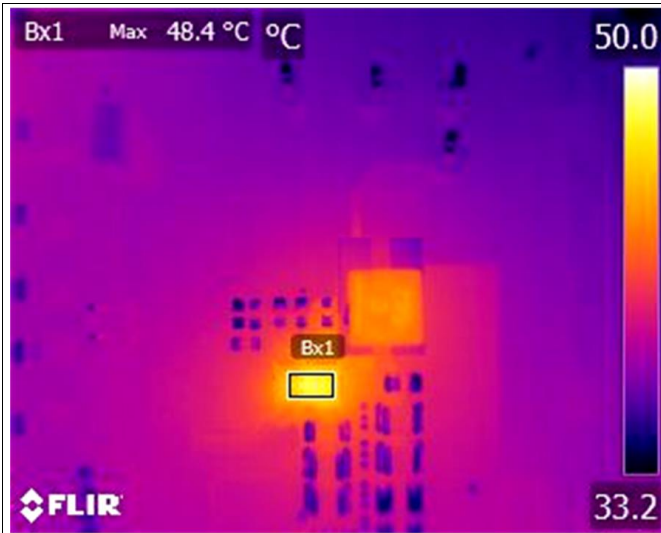
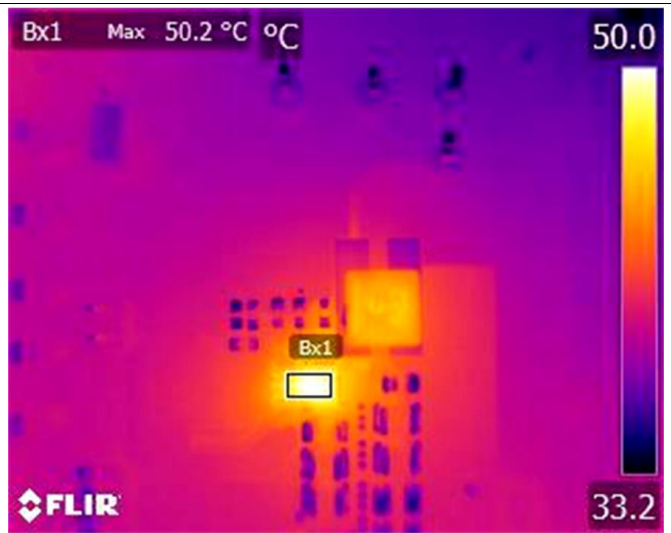


Figure 12. LM76003 Load and Line Regulation



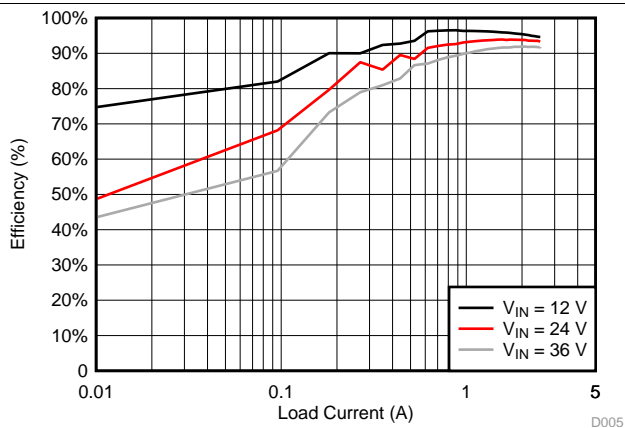
$V_{IN} = 12\text{ V}$ $V_{OUT} = 3.3\text{ V}$ $f_{SW} = 500\text{ kHz}$
3.5-A Load

Figure 13. LM76003 Thermal Picture



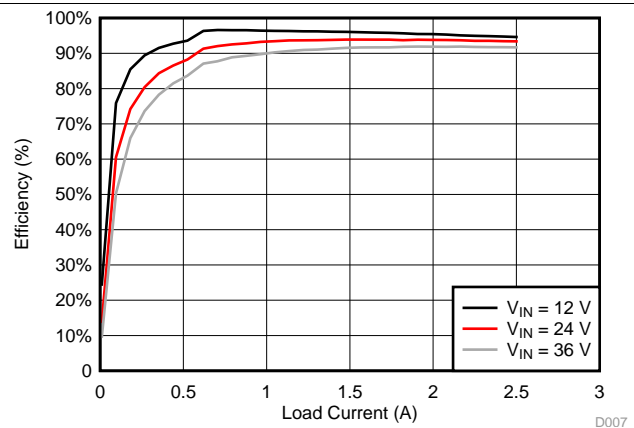
$V_{IN} = 24\text{ V}$ $V_{OUT} = 3.3\text{ V}$ $f_{SW} = 500\text{ kHz}$
3.5-A Load

Figure 14. LM76003 Thermal Picture



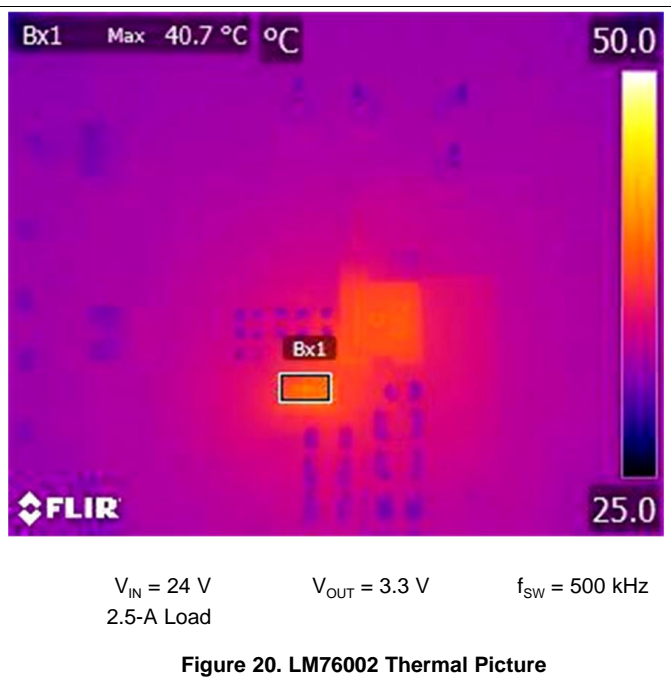
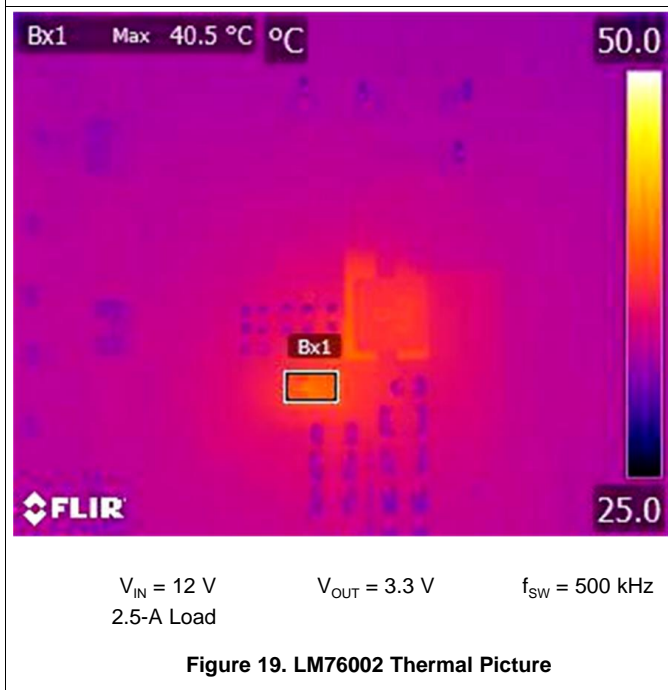
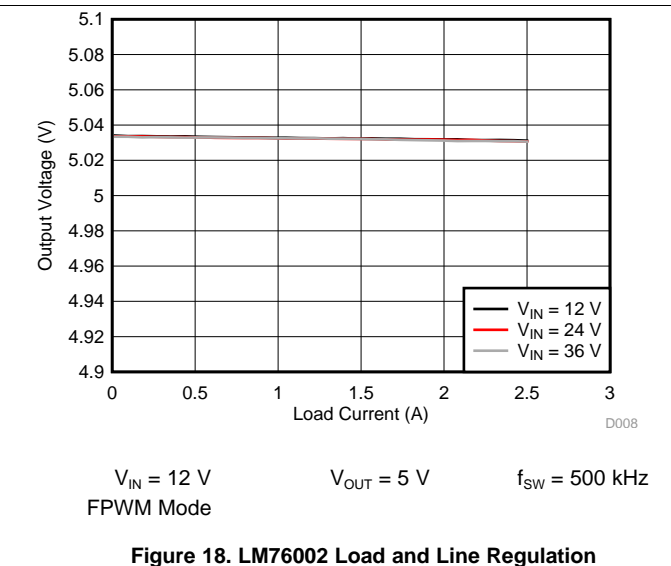
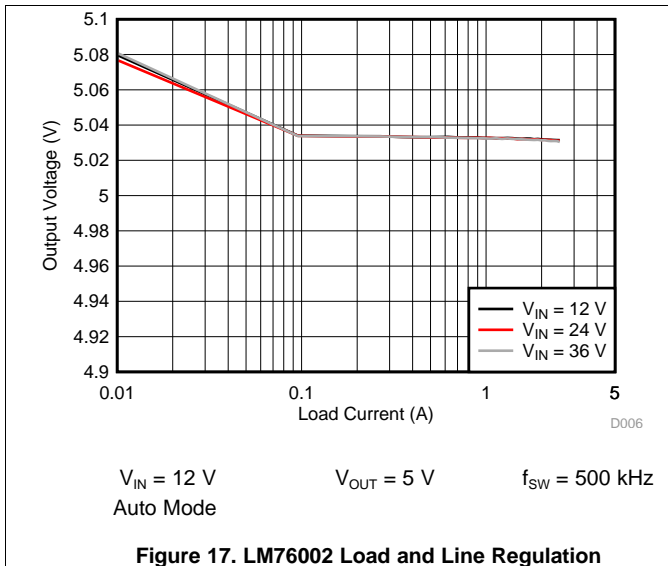
$V_{IN} = 12\text{ V}$ $V_{OUT} = 5\text{ V}$ $f_{SW} = 500\text{ kHz}$
Auto Mode

Figure 15. LM76002 Efficiency



$V_{IN} = 12\text{ V}$ $V_{OUT} = 5\text{ V}$ $f_{SW} = 500\text{ kHz}$
FPWM Mode

Figure 16. LM76002 Efficiency



STANDARD TERMS FOR EVALUATION MODULES

1. *Delivery:* TI delivers TI evaluation boards, kits, or modules, including any accompanying demonstration software, components, and/or documentation which may be provided together or separately (collectively, an "EVM" or "EVMs") to the User ("User") in accordance with the terms set forth herein. User's acceptance of the EVM is expressly subject to the following terms.
 - 1.1 EVMs are intended solely for product or software developers for use in a research and development setting to facilitate feasibility evaluation, experimentation, or scientific analysis of TI semiconductor products. EVMs have no direct function and are not finished products. EVMs shall not be directly or indirectly assembled as a part or subassembly in any finished product. For clarification, any software or software tools provided with the EVM ("Software") shall not be subject to the terms and conditions set forth herein but rather shall be subject to the applicable terms that accompany such Software
 - 1.2 EVMs are not intended for consumer or household use. EVMs may not be sold, sublicensed, leased, rented, loaned, assigned, or otherwise distributed for commercial purposes by Users, in whole or in part, or used in any finished product or production system.
2. *Limited Warranty and Related Remedies/Disclaimers:*
 - 2.1 These terms do not apply to Software. The warranty, if any, for Software is covered in the applicable Software License Agreement.
 - 2.2 TI warrants that the TI EVM will conform to TI's published specifications for ninety (90) days after the date TI delivers such EVM to User. Notwithstanding the foregoing, TI shall not be liable for a nonconforming EVM if (a) the nonconformity was caused by neglect, misuse or mistreatment by an entity other than TI, including improper installation or testing, or for any EVMs that have been altered or modified in any way by an entity other than TI, (b) the nonconformity resulted from User's design, specifications or instructions for such EVMs or improper system design, or (c) User has not paid on time. Testing and other quality control techniques are used to the extent TI deems necessary. TI does not test all parameters of each EVM. User's claims against TI under this Section 2 are void if User fails to notify TI of any apparent defects in the EVMs within ten (10) business days after delivery, or of any hidden defects with ten (10) business days after the defect has been detected.
 - 2.3 TI's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. TI's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by TI and that are determined by TI not to conform to such warranty. If TI elects to repair or replace such EVM, TI shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.
3. *Regulatory Notices:*
 - 3.1 *United States*
 - 3.1.1 *Notice applicable to EVMs not FCC-Approved:*

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.
 - 3.1.2 *For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:*

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

3.3 Japan

3.3.1 *Notice for EVMs delivered in Japan:* Please see http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
http://www.tij.co.jp/lstds/ti_ja/general/eStore/notice_01.page

3.3.2 *Notice for Users of EVMs Considered "Radio Frequency Products" in Japan:* EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above, User will be subject to penalties of Radio Law of Japan.

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2. 実験局の免許を取得後ご使用いただく。
3. 技術基準適合証明を取得後ご使用いただく。

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3.4 *European Union*

3.4.1 *For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):*

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

4 *EVM Use Restrictions and Warnings:*

4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.

4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.

4.3 *Safety-Related Warnings and Restrictions:*

4.3.1 User shall operate the EVM within TI's recommended specifications and environmental considerations stated in the user guide, other available documentation provided by TI, and any other applicable requirements and employ reasonable and customary safeguards. Exceeding the specified performance ratings and specifications (including but not limited to input and output voltage, current, power, and environmental ranges) for the EVM may cause personal injury or death, or property damage. If there are questions concerning performance ratings and specifications, User should contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may also result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM user guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, even with the inputs and outputs kept within the specified allowable ranges, some circuit components may have elevated case temperatures. These components include but are not limited to linear regulators, switching transistors, pass transistors, current sense resistors, and heat sinks, which can be identified using the information in the associated documentation. When working with the EVM, please be aware that the EVM may become very warm.

4.3.2 EVMs are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems. User assumes all responsibility and liability for proper and safe handling and use of the EVM by User or its employees, affiliates, contractors or designees. User assumes all responsibility and liability to ensure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard. User assumes all responsibility and liability for any improper or unsafe handling or use of the EVM by User or its employees, affiliates, contractors or designees.

4.4 User assumes all responsibility and liability to determine whether the EVM is subject to any applicable international, federal, state, or local laws and regulations related to User's handling and use of the EVM and, if applicable, User assumes all responsibility and liability for compliance in all respects with such laws and regulations. User assumes all responsibility and liability for proper disposal and recycling of the EVM consistent with all applicable international, federal, state, and local requirements.

5. *Accuracy of Information:* To the extent TI provides information on the availability and function of EVMs, TI attempts to be as accurate as possible. However, TI does not warrant the accuracy of EVM descriptions, EVM availability or other information on its websites as accurate, complete, reliable, current, or error-free.

6. *Disclaimers:*

6.1 EXCEPT AS SET FORTH ABOVE, EVMS AND ANY MATERIALS PROVIDED WITH THE EVM (INCLUDING, BUT NOT LIMITED TO, REFERENCE DESIGNS AND THE DESIGN OF THE EVM ITSELF) ARE PROVIDED "AS IS" AND "WITH ALL FAULTS." TI DISCLAIMS ALL OTHER WARRANTIES, EXPRESS OR IMPLIED, REGARDING SUCH ITEMS, INCLUDING BUT NOT LIMITED TO ANY EPIDEMIC FAILURE WARRANTY OR IMPLIED WARRANTIES OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF ANY THIRD PARTY PATENTS, COPYRIGHTS, TRADE SECRETS OR OTHER INTELLECTUAL PROPERTY RIGHTS.

6.2 EXCEPT FOR THE LIMITED RIGHT TO USE THE EVM SET FORTH HEREIN, NOTHING IN THESE TERMS SHALL BE CONSTRUED AS GRANTING OR CONFERRING ANY RIGHTS BY LICENSE, PATENT, OR ANY OTHER INDUSTRIAL OR INTELLECTUAL PROPERTY RIGHT OF TI, ITS SUPPLIERS/LICENSORS OR ANY OTHER THIRD PARTY, TO USE THE EVM IN ANY FINISHED END-USER OR READY-TO-USE FINAL PRODUCT, OR FOR ANY INVENTION, DISCOVERY OR IMPROVEMENT, REGARDLESS OF WHEN MADE, CONCEIVED OR ACQUIRED.

7. *USER'S INDEMNITY OBLIGATIONS AND REPRESENTATIONS.* USER WILL DEFEND, INDEMNIFY AND HOLD TI, ITS LICENSORS AND THEIR REPRESENTATIVES HARMLESS FROM AND AGAINST ANY AND ALL CLAIMS, DAMAGES, LOSSES, EXPENSES, COSTS AND LIABILITIES (COLLECTIVELY, "CLAIMS") ARISING OUT OF OR IN CONNECTION WITH ANY HANDLING OR USE OF THE EVM THAT IS NOT IN ACCORDANCE WITH THESE TERMS. THIS OBLIGATION SHALL APPLY WHETHER CLAIMS ARISE UNDER STATUTE, REGULATION, OR THE LAW OF TORT, CONTRACT OR ANY OTHER LEGAL THEORY, AND EVEN IF THE EVM FAILS TO PERFORM AS DESCRIBED OR EXPECTED.

8. *Limitations on Damages and Liability:*

8.1 *General Limitations.* IN NO EVENT SHALL TI BE LIABLE FOR ANY SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL, OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF THESE TERMS OR THE USE OF THE EVMS , REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. EXCLUDED DAMAGES INCLUDE, BUT ARE NOT LIMITED TO, COST OF REMOVAL OR REINSTALLATION, ANCILLARY COSTS TO THE PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES, RETESTING, OUTSIDE COMPUTER TIME, LABOR COSTS, LOSS OF GOODWILL, LOSS OF PROFITS, LOSS OF SAVINGS, LOSS OF USE, LOSS OF DATA, OR BUSINESS INTERRUPTION. NO CLAIM, SUIT OR ACTION SHALL BE BROUGHT AGAINST TI MORE THAN TWELVE (12) MONTHS AFTER THE EVENT THAT GAVE RISE TO THE CAUSE OF ACTION HAS OCCURRED.

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9. *Return Policy.* Except as otherwise provided, TI does not offer any refunds, returns, or exchanges. Furthermore, no return of EVM(s) will be accepted if the package has been opened and no return of the EVM(s) will be accepted if they are damaged or otherwise not in a resalable condition. If User feels it has been incorrectly charged for the EVM(s) it ordered or that delivery violates the applicable order, User should contact TI. All refunds will be made in full within thirty (30) working days from the return of the components(s), excluding any postage or packaging costs.

10. *Governing Law:* These terms and conditions shall be governed by and interpreted in accordance with the laws of the State of Texas, without reference to conflict-of-laws principles. User agrees that non-exclusive jurisdiction for any dispute arising out of or relating to these terms and conditions lies within courts located in the State of Texas and consents to venue in Dallas County, Texas. Notwithstanding the foregoing, any judgment may be enforced in any United States or foreign court, and TI may seek injunctive relief in any United States or foreign court.

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