

TPS23881

PoE 2 (IEEE 802.3bt)

Conformance Test Report



Document Revision: 1.0

May 14th, 2020

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Revision History

Revision	Description	Date
Rev 1.0	Initial document release including the Sifos 4-Pair PSA v5.2.00 test results	5 / 14 / 2020

Introduction

IEEE 802.3bt Compliance and PoE Interoperability:

IEEE802.3bt compliance is the foundation of interoperability and safety for any PoE enabled system. The use of non-compliant Power Sourcing Equipment (PSE) increases the risk that equipment connected to the system may not operate correctly or may even be damaged. This is further intensified with the release of the new 802.3bt standard expanding the array of equipment using PoE and the increased available power levels of up to 90W (sourced).

Sifos Technologies and UNH-IOL have established themselves as first and third-party houses for PoE suite testing.

Sifos Technologies <https://sifos.com>

Sifos Technologies provides a one-box solution to facilitate complete first-party testing and analysis of Power Sourcing Equipment (PSE) behaviors and overall compliance based on the IEEE 802.3bt specification. Sifos test coverage exceeds 95% of 802.3bt PSE PICS.

Ethernet Alliance (EA) <https://ethernetalliance.org/>

The Ethernet Alliance is a consortium of leading industry experts, university, and government professionals, and component vendors that has created a PoE Certification Program. This program is aimed specifically at simplifying the certification and identification of PoE products that are compliant to the IEEE802.3bt PoE standard through the use of logos that can be affixed to PoE equipment and a Certified Product Registry.

The EA has partnered with the University of New Hampshire InterOperability Laboratory (UNH-IOL <https://www.iol.unh.edu/>) to provide independent and vendor-neutral certification testing for both Gen 1 & Gen2 PoE logos (see table below). For additional information about the PoE logo certification program, feel free to read our [blog](#) on this topic.

Summary Table of PoE Compliance Terminology

Brand/ Acronym	IEEE Standard	Clause	Clause Title	Types	Classes	EA Certified Logo
PoE 1	802.3af	33	Power over Ethernet over 2-Pair	1	0-3	Gen 1
	802.3at			2	0-4	
PoE 2	802.3bt	145	Power over Ethernet	3	1-6 or 1-4 DS ¹	Gen 2
				4	7-8 or 5 DS ¹	

(1) "DS" is used to designate "Dual Signature" PDs

Sifos Test Results:

TPS23881 EVM: 4-Pair Type-4

Test Conditions:

Sifos HW: PSA-3000 Chassis with PSA-3202 Test Blades

Sifos SW: PSA v5.2.00


PSE HW: [TPS23881EVM-008](#) + [BOOST-PSEMTHR-007](#) evaluation module with $V_{PWR} = 55V$

PSE SRAM: v05

PSE Config: Auto mode with 4P Ports set to 90W (Type 4)

PSE Conformance Test Suite

May 11 2020 10:57 AM	
Port Count.....	2
Loop Count.....	1
PSE Tested: TI23881 Type-4	


	802.3bt 4Pr Conformance Report	
	version 5.2.00	
	PSE Type: 4 MDI-X+MDI	report version 5.1.17
	Safety Index*: 100%	Interop Index*: 100%
Error Log: None		

Chassis ID: 158.218.10.73	PSA-3000 Ports			Min	Max	Average	Low Limit	P/F	High Limit	P/F
	8-1	8-2	UNITS							
Test: det_v										
Open Circuit Voc A=	24.6	24.6	volts	24.6	24.6	24.6	0	Pass	30	Pass
Open Circuit Voc B=	24.6	24.5	volts	24.5	24.6	24.6	0	Pass	30	Pass
Backoff Voltage A=	0.1	0.1	volts	0.1	0.1	0.1	0	Pass	2.8	Pass
Backoff Voltage B=	0.1	0.1	volts	0.1	0.1	0.1	0	Pass	2.8	Pass
Backoff Voltage Ss=	5.8	5.8	volts	5.8	5.8	5.8	0	Pass	2.8	Info
Max Det Step V A=	7.96	7.97	volts	7.96	7.97	7.97	3.8	Pass	10	Pass
Max Det Step V B=	8.06	8.02	volts	8.02	8.06	8.04	3.8	Pass	10	Pass
Min Det Step V A=	3.99	4	volts	3.99	4	4	2.8	Pass	9	Pass
Min Det Step V B=	4.05	4.03	volts	4.03	4.05	4.04	2.8	Pass	9	Pass
Det Step Changes A=	3	3	****	3	3	3	1	Pass	9	Pass
Det Step Changes B=	3	3	****	3	3	3	1	Pass	9	Pass
Min Step DV A=	2.13	2.14	volts	2.13	2.14	2.14	1	Pass	7.2	Pass
Min Step DV B=	2.15	2.14	volts	2.14	2.15	2.15	1	Pass	7.2	Pass
Pre-Det CC Step V A=	3.77	3.78	volts	3.77	3.78	3.78	0	Pass	10	Pass
Pre-Det CC Step V B=	0	0	volts	0	0	0	0	Pass	10	Pass
Test: det_cc										
Presumed CC DET SEQ=	1	1	****	1	1	1	0	Pass	3	Pass
Conn Chk SS V A=	7.92	7.87	volts	7.87	7.92	7.9	2.8	Pass	10	Pass
Conn Chk SS V B=	7.94	7.92	volts	7.92	7.94	7.93	2.8	Pass	10	Pass
Conn Chk DS V A=	4.51	4.34	volts	4.34	4.51	4.43	2.8	Pass	10	Pass
Conn Chk DS V B=	5.1	5.07	volts	5.07	5.1	5.09	2.8	Pass	10	Pass
High Signature CC A=	1	1	****	1	1	1	1	Pass	1	Pass
High Signature CC B=	1	1	****	1	1	1	1	Pass	1	Pass
4Pair Start Fail=	0	0	****	0	0	0	0	Pass	0	Pass
Test: det_i										
Isc Init A=	0.32	0.32	mA	0.32	0.32	0.32	0	Pass	5	Pass
Isc Init B=	0.31	0.32	mA	0.31	0.32	0.32	0	Pass	5	Pass
Isc Det A=	0.32	0.3	mA	0.3	0.32	0.31	0	Pass	5	Pass
Isc Det B=	0.3	0.3	mA	0.3	0.3	0.3	0	Pass	5	Pass
Det Slew A=	0.0064	0.006	V/usec	0.006	0.0064	0.0062	0	Pass	0.1	Pass
Det Slew B=	0.006	0.006	V/usec	0.006	0.006	0.006	0	Pass	0.1	Pass
Test: det_range										
Rgood Max Single=	29	29	Kohm	29	29	29	27	Pass	32	Pass
Rgood Min Single=	17	17	Kohm	17	17	17	16	Pass	19	Pass
Cgood Max Single=	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rgood Max Dual A=	29	29	Kohm	29	29	29	27	Pass	32	Pass
Rgood Max Dual B=	29	29	Kohm	29	29	29	27	Pass	32	Pass
Rgood Min Dual A=	17	17	Kohm	17	17	17	16	Pass	19	Pass
Rgood Min Dual B=	17	17	Kohm	17	17	17	16	Pass	19	Pass
Cgood Max Dual A=	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Cgood Max Dual B=	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass

PSE Conformance Test Suite

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 Port Count..... 2
 Loop Count..... 1
 PSE Tested: TI23881 Type-4

 Sifos Technologies	802.3bt 4Pr Conformance Report version 5.2.00 report version 5.1.17	
	PSE Type: 4 MDI-X+MDI Safety Index*: 100% Error Log: None	Interop Index*: 100%

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports			Min	Max	Average	Low Limit	P/F	High Limit	P/F
	8-1	8-2	UNITS							
Test: det_time										
Detect Time Tdet A=	302.7	304.7	msec	302.7	304.7	303.7	0	Pass	500	Pass
Detect Time Tdet B=	300.8	298.8	msec	298.8	300.8	299.8	0	Pass	500	Pass
Backoff Time SS=	7.8	7.8	msec	7.8	7.8	7.8	0	Pass	9999	Pass
Det2Det Time=	164.1	164.1	msec	164.1	164.1	164.1	0	Pass	400	Pass
Test: det_rsource										
PSE Detect Source=	1	1	****	1	1	1	0	Pass	1	Pass
PSE Source Zout A=	300	300	Kohm	300	300	300	45	Pass	300	Pass
PSE Source Zout B=	300	300	Kohm	300	300	300	45	Pass	300	Pass
Test: cc_response										
Single Sig Response=	1	1	****	1	1	1	1	Pass	1	Pass
Dual Sig Response=	1	1	****	1	1	1	1	Pass	1	Pass
2Pair_PD A=	1	1	****	1	1	1	0	Pass	2	Pass
2Pair_PD B=	0	0	****	0	0	0	0	Pass	2	Pass
Test: class_v										
Vclass max SS=	18.6	18.5	volts	18.5	18.6	18.6	15.5	Pass	20.5	Pass
Vclass min SS=	18.4	18.3	volts	18.3	18.4	18.4	15.5	Pass	20.5	Pass
Vmark SS=	8.7	8.7	volts	8.7	8.7	8.7	7	Pass	10	Pass
Vreset SS=	-1	-1	****	-1	-1	-1	0	Pass	2.8	Pass
Vclass max DSA=	18.6	18.6	volts	18.6	18.6	18.6	15.5	Pass	20.5	Pass
Vclass max DSB=	18.6	18.5	volts	18.5	18.6	18.6	15.5	Pass	20.5	Pass
Vclass min DSA=	18.4	18.4	volts	18.4	18.4	18.4	15.5	Pass	20.5	Pass
Vclass min DSB=	18.4	18.3	volts	18.3	18.4	18.4	15.5	Pass	20.5	Pass
Vmark DSA=	8.7	8.7	volts	8.7	8.7	8.7	7	Pass	10	Pass
Vmark DSB=	8.7	8.7	volts	8.7	8.7	8.7	7	Pass	10	Pass
Vreset DSA=	-1	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Vreset DSB=	-1	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Test: class_time										
Class Probe SS=	0	0	****	0	0	0	0	Pass	1	Pass
EV Count 7 SS=	5	5	Events	5	5	5	1	Pass	5	Pass
Long EV1 Time SS=	97.7	95.7	msec	95.7	97.7	96.7	88	Pass	105	Pass
Min Class EV Time SS=	7.8	7.8	msec	7.8	7.8	7.8	6	Pass	20	Pass
Max Class EV Time SS=	9.8	9.8	msec	9.8	9.8	9.8	6	Pass	20	Pass
Min Mark EV Time SS=	7.8	7.8	msec	7.8	7.8	7.8	6	Pass	12	Pass
Max Mark EV Time SS=	11.7	9.7	msec	9.7	11.7	10.7	6	Pass	12	Pass
Final Mark EV Time SS=	7.8	7.8	msec	7.8	7.8	7.8	6	Pass	256	Pass
Cl Prb Reset Time SS=	-1	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class Probe DA=	0	0	****	0	0	0	0	Pass	1	Pass
EV Count 5D DA=	4	4	Events	4	4	4	1	Pass	4	Pass
Long EV1 Time DA=	97.7	97.7	msec	97.7	97.7	97.7	88	Pass	105	Pass
Min Class EV Time DA=	7.8	9.8	msec	7.8	9.8	8.8	6	Pass	20	Pass
Max Class EV Time DA=	9.8	9.8	msec	9.8	9.8	9.8	6	Pass	20	Pass
Min Mark EV Time DA=	7.8	6	msec	6	7.8	6.9	6	Pass	12	Pass
Max Mark EV Time DA=	7.8	7.8	msec	7.8	7.8	7.8	6	Pass	12	Pass
Final Mark EV Time DA=	9.7	9.7	msec	9.7	9.7	9.7	6	Pass	256	Pass
Cl Prb Reset Time DA=	-1	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class Probe DB=	0	0	****	0	0	0	0	Pass	1	Pass
EV Count 5D DB=	4	4	Events	4	4	4	1	Pass	4	Pass
Long EV1 Time DB=	97.7	97.7	msec	97.7	97.7	97.7	88	Pass	105	Pass
Min Class EV Time DB=	7.8	9.8	msec	7.8	9.8	8.8	6	Pass	20	Pass
Max Class EV Time DB=	9.8	9.8	msec	9.8	9.8	9.8	6	Pass	20	Pass
Min Mark EV Time DB=	7.8	6	msec	6	7.8	6.9	6	Pass	12	Pass
Max Mark EV Time DB=	7.8	7.8	msec	7.8	7.8	7.8	6	Pass	12	Pass
Final Mark EV Time DB=	9.7	9.7	msec	9.7	9.7	9.7	6	Pass	256	Pass
Cl Prb Reset Time DB=	-1	-1	****	-1	-1	-1	15	Pass	10000	Pass

PSE Conformance Test Suite


May 11 2020 10:57 AM	
Port Count.....	2
Loop Count.....	1
PSE Tested: TI23881 Type-4	

 Sifos Technologies Safety Index*: Error Log: None	802.3bt 4Pr Conformance Report version 5.2.00 report version 5.1.17	
	PSE Type: 4 MDI-X+MDI	Interop Index*: 100%
	Safety Index*: 100%	Interop Index*: 100%
	Error Log: None	

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports			Min	Max	Average	Low Limit	P/F	High Limit	P/F
	8-1	8-2	UNITS							
Test: class_response										
Class 3 Count=	1	1	****	1	1	1	1	Pass	1	Pass
Class 4 Count=	3	3	****	3	3	3	1	Pass	3	Pass
Class 5 Count=	4	4	****	4	4	4	1	Pass	4	Pass
Class 6 Count=	4	4	****	4	4	4	1	Pass	4	Pass
Class 7 Count=	5	5	****	5	5	5	1	Pass	5	Pass
Class 8 Count=	5	5	****	5	5	5	1	Pass	5	Pass
Class 2D Count A=	1	1	****	1	1	1	1	Pass	3	Pass
Class 2D Count B=	1	1	****	1	1	1	1	Pass	3	Pass
Class 3D Count A=	1	1	****	1	1	1	1	Pass	3	Pass
Class 3D Count B=	1	1	****	1	1	1	1	Pass	3	Pass
Class 4D Count A=	3	3	****	3	3	3	1	Pass	3	Pass
Class 4D Count B=	3	3	****	3	3	3	1	Pass	3	Pass
Class 5D Count A=	4	4	****	4	4	4	1	Pass	4	Pass
Class 5D Count B=	4	4	****	4	4	4	1	Pass	4	Pass
Max SS Class=	8	8	****	8	8	8	3	Pass	8	Pass
Max DS Class=	5	5	****	5	5	5	1	Pass	5	Pass
Init Grant Match=	1	1	****	1	1	1	1	Pass	1	Pass
2-Pair Pairset=	0	0	****	0	0	0	0	Pass	2	Pass
PRI 4pr Pairset=	12	12	****	12	12	12	1	Pass	12	Pass
Test: class_err										
Class Ilim A=	76.9	77.2	mA	76.9	77.2	77.1	51	Pass	100	Pass
Class Ilim B=	78.3	76.5	mA	76.5	78.3	77.4	51	Pass	100	Pass
Pwr Cl 52 SS=	0	0	****	0	0	0	0	Pass	0	Pass
Pwr Cl 52 DSA=	0	0	****	0	0	0	0	Pass	0	Pass
Pwr Cl 52 DSB=	0	0	****	0	0	0	0	Pass	0	Pass
Mark Ilim A=	77	77	mA	77	77	77	0	Pass	105	Pass
Mark Ilim B=	78	76	mA	76	78	77	0	Pass	105	Pass
Inval Sig EV2 SS=	0	0	****	0	0	0	0	Pass	1	Pass
Inval Sig EV4 SS=	0	0	****	0	0	0	0	Pass	1	Pass
Inval Sig EV5 SS=	0	0	****	0	0	0	0	Pass	1	Pass
Inval Sig EV2 DSA=	0	0	****	0	0	0	0	Pass	1	Pass
Inval Sig EV2 DSB=	0	0	****	0	0	0	0	Pass	1	Pass
Inval Sig EV4 DSA=	0	0	****	0	0	0	0	Pass	1	Pass
Inval Sig EV4 DSB=	0	0	****	0	0	0	0	Pass	1	Pass
Test: pwrup_time										
Pwr On Time Tpon SS=	229.7	225.8	msec	225.8	229.7	227.8	0	Pass	400	Pass
Pwr On Time Tpon DSA=	210.1	214.1	msec	210.1	214.1	212.1	0	Pass	400	Pass
Pwr On Time Tpon DSB=	214.1	214.1	msec	214.1	214.1	214.1	0	Pass	400	Pass
Pwrup Rise Time A=	22	22	usec	22	22	22	15	Pass	50000	Pass
Pwrup Rise Time B=	24	22	usec	22	24	23	15	Pass	50000	Pass
Pwr Stagger Time SS4=	60.3	0	msec	0	60.3	30.2	-1	Pass	75	Pass
Pwr Stagger Time SS5=	60.4	60.4	msec	60.4	60.4	60.4	0	Pass	75	Pass
Pwr Stagger Time DS=	0	0	msec	0	0	0	0	Pass	1000	Pass

PSE Conformance Test Suite

May 11 2020 10:57 AM	
Port Count.....	2
Loop Count.....	1
PSE Tested: TI23881 Type-4	


 Sifos Technologies Safety Index*: Error Log: None	802.3bt 4Pr Conformance Report version 5.2.00	
	PSE Type: 4 MDI-X+MDI	report version 5.1.17
	100%	Interop Index*: 100%

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports			Min	Max	Average	Low Limit	P/F	High Limit	P/F
	8-1	8-2	UNITS							
Test: pwrup_inrush										
Iinrush_min Class 3=	422.5	423	mA	422.5	423	422.8	400	Pass	9999	Pass
Iinrush_min Class 5=	422.8	423.6	mA	422.8	423.6	423.2	400	Pass	9999	Pass
Iinrush_min Class 7=	848.6	849.3	mA	848.6	849.3	849	800	Pass	9999	Pass
Iinrush_min Class 1D A=	422.8	423.5	mA	422.8	423.5	423.2	400	Pass	9999	Pass
Iinrush_min Class 1D B=	422.3	420.7	mA	420.7	422.3	421.5	400	Pass	9999	Pass
Iinrush_4P_max Class 3=	423.8	424.1	mA	423.8	424.1	424	0	Pass	450	Pass
Iinrush_4P_max2 Class 5=	423.6	424.3	mA	423.6	424.3	424	0	Pass	900	Pass
Iinrush_4P_max2 Class 7=	849.9	850.5	mA	849.9	850.5	850.2	0	Pass	900	Pass
Iinrush_2P_max Class 3=	423.4	423	mA	423	423.4	423.2	0	Pass	450	Pass
Iinrush_2P_max2 Class 7=	426.5	425.4	mA	425.4	426.5	426	0	Pass	600	Pass
Iinrush_2p_max Cl 1D A=	423.9	424.4	mA	423.9	424.4	424.2	0	Pass	450	Pass
Iinrush_2p_max Cl 1D B=	423.1	421.5	mA	421.5	423.1	422.3	0	Pass	450	Pass
Tinrush_minPr Class 3=	59.58	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Tinrush_maxPr Class 3=	59.58	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Tinrush_minPr Class 7=	59.58	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Tinrush_maxPr Class 7=	59.58	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Tinrush Class 1D A=	59.58	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Tinrush Class 1D B=	59.58	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Delay_Inrush_Class_7=	59.58	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Delay_Inrush_Class_2D A=	59.58	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
Delay_Inrush_Class_2D B=	59.58	59.58	msec	59.58	59.58	59.6	50	Pass	75	Pass
45ms Pwr Stat Class 7=	1	1	****	1	1	1	1	Pass	1	Pass
45ms Pwr Stat Class 2D A=	1	1	****	1	1	1	1	Pass	1	Pass
45ms Pwr Stat Class 2D B=	1	1	****	1	1	1	1	Pass	1	Pass
Vinrush Class 2D A=	30.8	30.8	volts	30.8	30.8	30.8	30	Pass	60	Pass
Vinrush Class 2D B=	30.8	30.7	volts	30.7	30.8	30.8	30	Pass	60	Pass
Test: pwr_on_v										
Vpse_Max_Alt_A=	55.13	55.13	V	55.13	55.13	55.13	52	Pass	57	Pass
Vpse_Max_Alt_B=	55.05	55.05	V	55.05	55.05	55.05	52	Pass	57	Pass
Vpse_Min_Alt_A=	54.23	54.15	V	54.15	54.23	54.19	52	Pass	57	Pass
Vpse_Min_Alt_B=	54.02	54.02	V	54.02	54.02	54.02	52	Pass	57	Pass
Vport_PSE_diff=	50	70	mV	50	70	60	0	Pass	150	Pass
V_ripple_A=	9	12	mVp-p	9	12	10.5	0	Pass	500	Pass
V_ripple_B=	9	8	mVp-p	8	9	8.5	0	Pass	500	Pass
V_noise_A=	19	17	mVp-p	17	19	18	0	Pass	200	Pass
V_noise_B=	18	16	mVp-p	16	18	17	0	Pass	200	Pass
V_trans_A=	54.144	54.08	V	54.08	54.144	54.112	52	Pass	57	Pass
V_trans_B=	53.952	53.968	V	53.952	53.968	53.96	52	Pass	57	Pass

PSE Conformance Test Suite

May 11 2020 10:57 AM

 Port Count..... 2
 Loop Count..... 1
 PSE Tested: **TI23881 Type-4**

 Sifos Technologies Safety Index*: Error Log: None	802.3bt 4Pr Conformance Report version 5.2.00 report version 5.1.17	
	PSE Type: 4 MDI-X+MDI Interop Index*: 100%	
	100%	
	100%	

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports			Min	Max	Average	Low Limit	P/F	High Limit	P/F
	8-1	8-2	UNITS							
Test: pwrn_pwracap										
Max Asgn Class SS=	8	8	****	8	8	8	1	Pass	8	Pass
Pcon c1=	4	4	W	4	4	4	3.9	Pass	99	Pass
Icon % c1=	102.4	103.1	%	102.4	103.1	102.8	100	Pass	125	Pass
Pcon c2=	7	7	W	7	7	7	3.9	Pass	99	Pass
Icon % c2=	106.8	106.8	%	106.8	106.8	106.8	100	Pass	125	Pass
Pcon c3=	15	15	W	15	15	15	3.9	Pass	99	Pass
Icon % c3=	112.5	112.5	%	112.5	112.5	112.5	100	Pass	125	Pass
Pcon c4=	30.3	30.3	W	30.3	30.3	30.3	3.9	Pass	99	Pass
Icon % c4=	112.3	112.2	%	112.2	112.3	112.3	100	Pass	125	Pass
Pcon c5=	45.9	45.9	W	45.9	45.9	45.9	3.9	Pass	99	Pass
Icon % c5=	104.2	104.1	%	104.1	104.2	104.2	100	Pass	125	Pass
Pcon c6=	61.6	61.6	W	61.6	61.6	61.6	3.9	Pass	99	Pass
Icon % c6=	106	106	%	106	106	106	100	Pass	125	Pass
Pcon c7=	76.3	76.2	W	76.2	76.3	76.3	3.9	Pass	99	Pass
Icon % c7=	103.8	103.7	%	103.7	103.8	103.8	100	Pass	125	Pass
Pcon c8=	90.8	92.5	W	90.8	92.5	91.7	3.9	Pass	99	Pass
Icon % c8=	103.5	105.4	%	103.5	105.4	104.5	100	Pass	125	Pass
Type N Enable=	1	1	****	1	1	1	1	Pass	1	Pass
Pclass LLDP 95%=	1	1	****	1	1	1	1	Pass	1	Pass
Pclass LLDP 75%=	1	1	****	1	1	1	1	Pass	1	Pass
Max Asgn Class DS=	5	5	****	5	5	5	1	Pass	5	Pass
Pcon c1DA=	4	4	W	4	4	4	3.9	Pass	99	Pass
Icon % c1DA=	102.3	102.3	%	102.3	102.3	102.3	100	Pass	125	Pass
Pcon c2DB=	7	7	W	7	7	7	3.9	Pass	99	Pass
Icon % c2DB=	104.4	104.4	%	104.4	104.4	104.4	100	Pass	125	Pass
Pcon c3DA=	15.5	15.5	W	15.5	15.5	16	3.9	Pass	99	Pass
Icon % c3DA=	112.4	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon c4DB=	30.3	30.3	W	30.3	30.3	30	3.9	Pass	99	Pass
Icon % c4DB=	104.2	104.2	%	104.2	104.2	104.2	100	Pass	125	Pass
Pcon c5DA=	45.4	46.3	W	45.4	46.3	46	3.9	Pass	99	Pass
Icon % c5DA=	103.9	105.7	%	103.9	105.7	104.8	100	Pass	125	Pass
Test: pwrn_unbal										
pseP2pUnbal c4A=	1	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c4B=	1	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c5A=	1	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c5B=	1	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c6A=	1	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c6B=	1	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c7A=	1	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c7B=	1	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c8A=	1	1	****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c8B=	1	1	****	1	1	1	1	Pass	1	Pass

PSE Conformance Test Suite

May 11 2020 10:57 AM

 Port Count..... 2
 Loop Count..... 1
 PSE Tested: **TI23881 Type-4**

802.3bt 4Pr Conformance Report

version 5.2.00

report version 5.1.17

 PSE Type: **4 MDI-X+MDI**


 Interop Index*: **100%**

 Safety Index*: **100%**

Error Log: None

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports			Min	Max	Average	Low Limit	P/F	High Limit	P/F
	8-1	8-2	UNITS							
Test: pwrn_maxi										
Ilim 2p max SSA=	1251.8	1251.5	mA	1251.5	1251.8	1251.7	0	Pass	1750	Pass
Ilim 2p max SSB=	1251.8	1251.3	mA	1251.3	1251.8	1251.6	0	Pass	1750	Pass
Tlim SS=	59.38	59.38	msec	59.38	59.38	59.4	6	Pass	75	Pass
Ilim 2p max DSA=	1251.5	1251.5	mA	1251.5	1251.5	1251.5	0	Pass	1750	Pass
Ilim 2p max DSB=	1085.3	1078.6	mA	1078.6	1085.3	1082	0	Pass	1750	Pass
Tlim DSA=	59.38	59.38	msec	59.38	59.38	59.4	6	Pass	75	Pass
Tlim DSB=	59.38	59.38	msec	59.38	59.38	59.4	6	Pass	75	Pass
Ilim min cAB3=	400	400	mA	400	400	400	400	Pass	1750	Pass
Max trans c3=	1	1	****	1	1	1	1	Pass	1	Pass
Ilim min cAB4=	684	684	mA	684	684	684	684	Pass	1750	Pass
Max trans c4=	1	1	****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB5=	580	580	mA	580	580	580	580	Pass	1750	Pass
Max trans c5=	1	1	****	1	1	1	1	Pass	1	Pass
Ilim min cAB6=	720	720	mA	720	720	720	720	Pass	1750	Pass
Max trans c6=	1	1	****	1	1	1	1	Pass	1	Pass
Ilim min cAB7=	850	850	mA	850	850	850	850	Pass	1750	Pass
Max trans c7=	1	1	****	1	1	1	1	Pass	1	Pass
Ilim min cAB8=	1005	1005	mA	1005	1005	1005	1005	Pass	1750	Pass
Max trans c8=	1	1	****	1	1	1	1	Pass	1	Pass
Ilim min cAB3D=	400	400	mA	400	400	400	400	Pass	1750	Pass
Max trans c3D=	1	1	****	1	1	1	1	Pass	1	Pass
Ilim min cAB4D=	684	684	mA	684	684	684	684	Pass	1750	Pass
Max_trans_c4D=	1	1	****	1	1	1	1	Pass	1	Pass
Ilim min cAB5D=	990	990	mA	990	990	990	990	Pass	1750	Pass
Max_trans_c5D=	1	1	****	1	1	1	1	Pass	1	Pass
Vtrans 2p A=	54.5	54.48	V	54.48	54.5	54.5	48.4	Pass	57	Pass
Vtrans 2p B=	54.35	54.38	V	54.35	54.38	54.4	48.4	Pass	57	Pass
Iport_max_type4=	0	0	****	0	0	0	0	Pass	0	Pass
Ilips_type4=	0	0	****	0	0	0	0	Pass	0	Pass
Test: pwrn_overld										
Ipeak c1=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c2=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c3=	1	1	****	1	1	1	1	Pass	1	Pass
Vport Ipeak c3=	54.88	54.88	V	54.88	54.88	54.9	52	Pass	57	Pass
Ipeak 5%DC c3=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c4=	1	1	****	1	1	1	1	Pass	1	Pass
Vport Ipeak c4=	54.7	54.7	V	54.7	54.7	54.7	52	Pass	57	Pass
Ipeak 5%DC c4=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c5=	1	1	****	1	1	1	1	Pass	1	Pass
Vport Ipeak c5=	54.48	54.48	V	54.48	54.48	54.5	52	Pass	57	Pass
Ipeak 5%DC c5=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c6=	1	1	****	1	1	1	1	Pass	1	Pass
Vport Ipeak c6=	54.3	54.3	V	54.3	54.3	54.3	52	Pass	57	Pass
Ipeak 5%DC c6=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c7=	1	1	****	1	1	1	1	Pass	1	Pass
Vport Ipeak c7=	54.08	54.08	V	54.08	54.08	54.1	52	Pass	57	Pass
Ipeak 5%DC c7=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c8=	1	1	****	1	1	1	1	Pass	1	Pass
Vport Ipeak c8=	53.88	53.9	V	53.88	53.9	53.9	52	Pass	57	Pass
Ipeak 5%DC c8=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c1D=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c2D=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c3D=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c4D=	1	1	****	1	1	1	1	Pass	1	Pass
Ipeak c5D=	1	1	****	1	1	1	1	Pass	1	Pass

PSE Conformance Test Suite	
May 11 2020	10:57 AM
Port Count.....	2
Loop Count.....	1
PSE Tested: TI23881 Type-4	

 Sifos® Technologies Safety Index*: Error Log: None	802.3bt 4Pr Conformance Report	
	PSE Type: 4 MDI-X+MDI	version 5.2.00
	Interop Index*: 100%	report version 5.1.17
	100%	

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports			Min	Max	Average	Low Limit	P/F	High Limit	P/F
	8-1	8-2	UNITS							
Test: mps_dc_valid										
Ihold_c3=	6	5	mA	5	6	5.5	4	Pass	9	Pass
Ihold_2p_c3A=	-1	-1	mA	-1	-1	-1	2	Pass	5	Pass
Ihold_2p_c3B=	-1	-1	mA	-1	-1	-1	2	Pass	5	Pass
Ihold_c5=	7	7	mA	7	7	7	4	Pass	14	Pass
Ihold_2p_c5A=	4	3	mA	3	4	3.5	2	Pass	7	Pass
Ihold_2p_c5B=	3	3	mA	3	3	3	2	Pass	7	Pass
Ihold_c7=	7	7	mA	7	7	7	4	Pass	14	Pass
Ihold_2p_c7A=	4	3	mA	3	4	3.5	2	Pass	7	Pass
Ihold_2p_c7B=	4	3	mA	3	4	3.5	2	Pass	7	Pass
Ihold_2p_c2DA=	4	3	mA	3	4	3.5	2	Pass	7	Pass
Ihold_2p_c2DB=	4	3	mA	3	4	3.5	2	Pass	7	Pass
LP MPS Tol_c3=	1	1	****	1	1	1	1	Pass	1	Pass
LP MPS Tol_c5=	1	1	****	1	1	1	1	Pass	1	Pass
LP MPS Tol_c7=	1	1	****	1	1	1	1	Pass	1	Pass
LP MPS Tol_c2D=	1	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrn										
Tmpdo_c3A=	361.3	-1	msec	-1	361.3	361.3	320	Pass	400	Pass
Tmpdo_c3B=	-1	363.3	msec	-1	363.3	363.3	320	Pass	400	Pass
Tmpdo_c5A=	363.3	363.3	msec	363.3	363.3	363.3	320	Pass	400	Pass
Tmpdo_c5B=	363.3	363.3	msec	363.3	363.3	363.3	320	Pass	400	Pass
Tmpdo_c7A=	363.3	363.3	msec	363.3	363.3	363.3	320	Pass	400	Pass
Tmpdo_c7B=	363.3	361.3	msec	361.3	363.3	362.3	320	Pass	400	Pass
Tmpdo_c2DA=	363.3	361.3	msec	361.3	363.3	362.3	320	Pass	400	Pass
4pr Stat_c2DA=	1	1	****	1	1	1	0	Pass	1	Pass
Tmpdo_c2DB=	361.3	363.3	msec	361.3	363.3	362.3	320	Pass	400	Pass
4pr Stat_c2DB=	1	1	****	1	1	1	0	Pass	1	Pass
Test: pwrn_time										
Turnoff time Toff A=	23.9	22.9	msec	22.9	23.9	23.4	0	Pass	500	Pass
Turnoff time Toff B=	24.1	25.3	msec	24.1	25.3	24.7	0	Pass	500	Pass
Cout A=	88.7	75.7	nF	75.7	88.7	82.2	0	Pass	520	Pass
Cout B=	77.3	91.6	nF	77.3	91.6	84.5	0	Pass	520	Pass
Output_Rp A=	126	149	Kohm	126	149	138	45	Pass	9999	Pass
Output_Rp B=	156	131	Kohm	131	156	144	45	Pass	9999	Pass
Test: pwrn_v										
Error_Delay_SS A=	1016	1016	msec	1016	1016	1016	750	Pass	9999	Pass
Error_Delay_SS B=	1406	1406	msec	1406	1406	1406	750	Pass	9999	Pass
Error_Delay_DS A=	1035	1016	msec	1016	1035	1026	750	Pass	9999	Pass
Error_Delay_DS B=	1406	1406	msec	1406	1406	1406	750	Pass	9999	Pass
Idle_Voff_SS A=	0.1	0.1	V	0.1	0.1	0.1	0	Pass	2.8	Pass
Idle_Voff_SS B=	0.1	0.1	V	0.1	0.1	0.1	0	Pass	2.8	Pass
Idle_Voff_DS A=	0.1	0.1	V	0.1	0.1	0.1	0	Pass	2.8	Pass
Idle_Voff_DS B=	0.1	0.1	V	0.1	0.1	0.1	0	Pass	2.8	Pass
Test Port Model Number:	3202	3202								
Test Port Hardware Version:	9	9								
Test Port Firmware Version:	4.14	4.14								

FirmPSE EVM: 4-Pair Type-4

Test Conditions:

Sifos HW: PSA-3000 Chassis with PSA-3202 Test Blades
Sifos SW: PSA v5.2.00
PSE HW: [TPS23881EVM-083](#) + [PSEMTHR24EVM-081](#) evaluation module with $V_{PWR} = 55V$
PSE SW: FirmPSE v01.33.00 with PSE SRAM v03
PSE Config: 24x 4P Ports with Class Based Power Limiting & Dynamic Power Policing

PSE Conformance Test Suite		
May 14 2020	6:43 PM	
Port Count.....	4	
Loop Count.....	1	
PSE Tested: TI_FirmPse_4p Type-4		

Sifos® Technologies		802.3bt 4Pr Conformance Report	
Safety Index*: 100%		Interop Index*: 100%	
Error Log: None		version 5.2.00	
		report version 5.1.17	

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports					Min	Max	Average	Low Limit	P/F	High Limit	P/F
	5-1	5-2	6-1	6-2	UNITS							
Test: det_v												
Open_Circuit_Voc_A=	24.6	24.5	24.5	24.6	volts	24.5	24.6	24.6	0	Pass	30	Pass
Open_Circuit_Voc_B=	24.8	24.7	24.5	24.6	volts	24.5	24.8	24.7	0	Pass	30	Pass
Backoff_Voltage_A=	0.1	0.1	0.1	0.1	volts	0.1	0.1	0.1	0	Pass	2.8	Pass
Backoff_Voltage_B=	0.1	0.1	0.1	0.1	volts	0.1	0.1	0.1	0	Pass	2.8	Pass
Backoff_Voltage_Ss=	6.1	5.9	5.7	6	volts	5.7	6.1	5.9	0	Pass	2.8	Info
Max_Det_Step_V_A=	8.29	8.31	8.15	8.14	volts	8.14	8.31	8.22	3.8	Pass	10	Pass
Max_Det_Step_V_B=	8.28	8.38	8.21	8.22	volts	8.21	8.38	8.27	3.8	Pass	10	Pass
Min_Det_Step_V_A=	4.5	4.51	4.4	4.41	volts	4.4	4.51	4.46	2.8	Pass	9	Pass
Min_Det_Step_V_B=	4.5	4.54	4.45	4.47	volts	4.45	4.54	4.49	2.8	Pass	9	Pass
Det_Step_Changes_A=	3	3	3	3	****	3	3	3	1	Pass	9	Pass
Det_Step_Changes_B=	3	3	3	3	****	3	3	3	1	Pass	9	Pass
Min_Step_DV_A=	2.2	2.21	2.2	2.17	volts	2.17	2.21	2.2	1	Pass	7.2	Pass
Min_Step_DV_B=	2.2	2.23	2.19	2.2	volts	2.19	2.23	2.21	1	Pass	7.2	Pass
Pre-Det_CC_Step_V_A=	4.15	4.17	4.08	4.06	volts	4.06	4.17	4.12	0	Pass	10	Pass
Pre-Det_CC_Step_V_B=	0	0	0	0	volts	0	0	0	0	Pass	10	Pass
Test: det_cc												
Presumed_CC_DET_SEQ=	1	1	1	1	****	1	1	1	0	Pass	3	Pass
Conn_Chk_SS_V_A=	8.16	8.26	8.24	8.02	volts	8.02	8.26	8.17	2.8	Pass	10	Pass
Conn_Chk_SS_V_B=	8.18	8.26	8.27	8.08	volts	8.08	8.27	8.2	2.8	Pass	10	Pass
Conn_Chk_DS_V_A=	8.06	8.02	7.95	7.94	volts	7.94	8.06	7.99	2.8	Pass	10	Pass
Conn_Chk_DS_V_B=	8.03	8.06	7.95	7.97	volts	7.95	8.06	8	2.8	Pass	10	Pass
High_Signature_CC_A=	1	1	1	1	****	1	1	1	1	Pass	1	Pass
High_Signature_CC_B=	1	1	1	1	****	1	1	1	1	Pass	1	Pass
4Pair_Start_Fail=	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Test: det_i												
Isc_Init_A=	0.29	0.32	0.32	0.29	mA	0.29	0.32	0.31	0	Pass	5	Pass
Isc_Init_B=	0.29	0.3	0.32	0.32	mA	0.29	0.32	0.31	0	Pass	5	Pass
Isc_Det_A=	0.27	0.32	0.32	0.27	mA	0.27	0.32	0.3	0	Pass	5	Pass
Isc_Det_B=	0.27	0.27	0.32	0.32	mA	0.27	0.32	0.3	0	Pass	5	Pass
Det_Slew_A=	0.0054	0.0064	0.0064	0.0054	V/usec	0.0054	0.0064	0.0059	0	Pass	0.1	Pass
Det_Slew_B=	0.0054	0.0054	0.0064	0.0064	V/usec	0.0054	0.0064	0.0059	0	Pass	0.1	Pass
Test: det_range												
Rgood_Max_Single=	30	30	30	30	Kohm	30	30	30	27	Pass	32	Pass
Rgood_Min_Single=	17	17	17	17	Kohm	17	17	17	16	Pass	19	Pass
Cgood_Max_Single=	0.1	0.1	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rgood_Max_Dual_A=	29	29	29	29	Kohm	29	29	29	27	Pass	32	Pass
Rgood_Max_Dual_B=	29	29	29	29	Kohm	29	29	29	27	Pass	32	Pass
Rgood_Min_Dual_A=	17	17	17	17	Kohm	17	17	17	16	Pass	19	Pass
Rgood_Min_Dual_B=	17	17	17	17	Kohm	17	17	17	16	Pass	19	Pass
Cgood_Max_Dual_A=	0.1	0.1	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Cgood_Max_Dual_B=	0.1	0.1	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass


PSE Conformance Test Suite	
Port Count.....	4
Loop Count.....	1
PSE Tested: TI_FirmPse_4p Type-4	

Sifos® Technologies		802.3bt 4Pr Conformance Report	
Safety Index*: 100%		PSE Type: 4 MDI-X+MDI	
Error Log: None		Interop Index*: 100%	
		version 5.2.00 report version 5.1.17	

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports					Min	Max	Average	Low Limit	P/F	High Limit	P/F
	5-1	5-2	6-1	6-2	UNITS							
Test: det time												
Detect Time Tdet A=	304.7	304.7	304.7	304.7	msec	304.7	304.7	304.7	0	Pass	500	Pass
Detect Time Tdet B=	298.8	298.8	298.8	298.8	msec	298.8	298.8	298.8	0	Pass	500	Pass
Backoff Time SS=	7.8	7.8	7.8	7.8	msec	7.8	7.8	7.8	0	Pass	9999	Pass
Det2Det Time=	162.1	164.1	164.1	164.1	msec	162.1	164.1	163.6	0	Pass	400	Pass
Test: det_rsource												
PSE Detect Source=	1	1			****	1	1	1	0	Pass	1	Pass
PSE Source Zout A=	300	300	300	300	Kohm	300	300	300	45	Pass	300	Pass
PSE Source Zout B=	300	300	300	300	Kohm	300	300	300	45	Pass	300	Pass
Test: cc_response												
Single Sig Response=	1	1			****	1	1	1	1	Pass	1	Pass
Dual Sig Response=	1	1			****	1	1	1	1	Pass	1	Pass
2Pair PD A=	0	0			****	0	1	0	0	Pass	2	Pass
2Pair PD B=	1	1			****	0	1	1	0	Pass	2	Pass
Test: class_v												
Vclass_max SS=	18.9	18.8	18.7	18.5	volts	18.5	18.9	18.7	15.5	Pass	20.5	Pass
Vclass_min SS=	18.7	18.6	18.4	18.3	volts	18.3	18.7	18.5	15.5	Pass	20.5	Pass
Vmark SS=	9	9	8.7	8.7	volts	8.7	9	8.9	7	Pass	10	Pass
Vreset SS=	-1	-1	-1	-1	****	-1	-1	-1	0	Pass	2.8	Pass
Vclass_max DSA=	18.7	18.6	18.7	18.5	volts	18.5	18.7	18.6	15.5	Pass	20.5	Pass
Vclass_max DSB=	18.9	18.8	18.5	18.8	volts	18.5	18.9	18.8	15.5	Pass	20.5	Pass
Vclass_min DSA=	18.5	18.3	18.5	18.3	volts	18.3	18.5	18.4	15.5	Pass	20.5	Pass
Vclass_min DSB=	18.7	18.6	18.3	18.5	volts	18.3	18.7	18.5	15.5	Pass	20.5	Pass
Vmark DSA=	8.8	8.7	8.7	8.7	volts	8.7	8.8	8.7	7	Pass	10	Pass
Vmark DSB=	9	8.9	8.7	8.9	volts	8.7	9	8.9	7	Pass	10	Pass
Vreset DSA=	-1	-1	-1	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Vreset DSB=	-1	-1	-1	-1	****	-1	-1	-1	-1	Pass	2.8	Pass
Test: class_time												
Class Probe SS=	0	0			****	0	0	0	0	Pass	1	Pass
EV Count 7 SS=	5	5			Events	5	5	5	1	Pass	5	Pass
Long EV1 Time SS=	97.7	99.6	97.7	97.7	msec	97.7	99.6	98.2	88	Pass	105	Pass
Min Class EV Time SS=	7.8	9.7	9.8	7.8	msec	7.8	9.8	8.8	6	Pass	20	Pass
Max Class EV Time SS=	9.8	9.8	9.8	9.8	msec	9.8	9.8	9.8	6	Pass	20	Pass
Min Mark EV Time SS=	7.8	6			msec	6	7.8	6.5	6	Pass	12	Pass
Max Mark EV Time SS=	11.7	9.7	9.7	11.7	msec	9.7	11.7	10.7	6	Pass	12	Pass
Final Mark EV Time SS=	6	7.8	7.8	7.8	msec	6	7.8	7.4	6	Pass	256	Pass
Cl Prb Reset Time SS=	-1	-1	-1	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class Probe DA=	0	0			****	0	0	0	0	Pass	1	Pass
EV Count 5D DA=	4	4			Events	4	4	4	1	Pass	4	Pass
Long EV1 Time DA=	97.7	97.7	97.7	97.6	msec	97.6	97.7	97.7	88	Pass	105	Pass
Min Class EV Time DA=	9.8	9.8	7.8	9.7	msec	7.8	9.8	9.3	6	Pass	20	Pass
Max Class EV Time DA=	9.8	9.8	9.8	9.8	msec	9.8	9.8	9.8	6	Pass	20	Pass
Min Mark EV Time DA=	6	6	7.8	6	msec	6	7.8	6.5	6	Pass	12	Pass
Max Mark EV Time DA=	7.8	7.8	7.8	7.8	msec	7.8	7.8	7.8	6	Pass	12	Pass
Final Mark EV Time DA=	9.7	9.7	9.7	11.7	msec	9.7	11.7	10.2	6	Pass	256	Pass
Cl Prb Reset Time DA=	-1	-1	-1	-1	****	-1	-1	-1	15	Pass	10000	Pass
Class Probe DB=	0	0			****	0	0	0	0	Pass	1	Pass
EV Count 5D DB=	4	4			Events	4	4	4	1	Pass	4	Pass
Long EV1 Time DB=	97.7	97.6	99.6	97.7	msec	97.6	99.6	98.2	88	Pass	105	Pass
Min Class EV Time DB=	9.8	7.8	9.8	7.8	msec	7.8	9.8	8.8	6	Pass	20	Pass
Max Class EV Time DB=	9.8	9.8	9.8	9.8	msec	9.8	9.8	9.8	6	Pass	20	Pass
Min Mark EV Time DB=	6	7.8	6	7.8	msec	6	7.8	6.9	6	Pass	12	Pass
Max Mark EV Time DB=	7.8	7.8	7.8	7.8	msec	7.8	7.8	7.8	6	Pass	12	Pass
Final Mark EV Time DB=	9.7	11.7	9.7	9.7	msec	9.7	11.7	10.2	6	Pass	256	Pass
Cl Prb Reset Time DB=	-1	-1	-1	-1	****	-1	-1	-1	15	Pass	10000	Pass

PSE Conformance Test Suite


May 14 2020 6:43 PM	
Port Count.....	4
Loop Count.....	1
PSE Tested: TI_FirmPse_4p Type-4	

	802.3bt 4Pr Conformance Report version 5.2.00	
	PSE Type: 4 MDI-X+MDI	report version 5.1.17
	Safety Index*: 100%	Interop Index*: 100%
	Error Log: None	

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports				UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F
	5-1	5-2	6-1	6-2								
Test: class response												
Class 3 Count=	1	1			****	1	1	1	1	Pass	1	Pass
Class 4 Count=	3	3			****	3	3	3	1	Pass	3	Pass
Class 5 Count=	4	4			****	4	4	4	1	Pass	4	Pass
Class 6 Count=	4	4			****	4	4	4	1	Pass	4	Pass
Class 7 Count=	5	5			****	5	5	5	1	Pass	5	Pass
Class 8 Count=	5	5			****	5	5	5	1	Pass	5	Pass
Class 2D Count A=	1	1			****	1	1	1	1	Pass	3	Pass
Class 2D Count B=	1	1			****	1	1	1	1	Pass	3	Pass
Class 3D Count A=	1	1			****	1	1	1	1	Pass	3	Pass
Class 3D Count B=	1	1			****	1	1	1	1	Pass	3	Pass
Class 4D Count A=	3	3			****	3	3	3	1	Pass	3	Pass
Class 4D Count B=	3	3			****	3	3	3	1	Pass	3	Pass
Class 5D Count A=	4	4			****	4	4	4	1	Pass	4	Pass
Class 5D Count B=	4	4			****	4	4	4	1	Pass	4	Pass
Max SS Class=	8	8			****	8	8	8	3	Pass	8	Pass
Max DS Class=	5	5			****	5	5	5	1	Pass	5	Pass
Init Grant Match=	1	1			****	1	1	1	1	Pass	1	Pass
2-Pair Pairset=	0	0			****	0	0	0	0	Pass	2	Pass
PRI 4pr Pairset=	12	12	12	12	****	12	12	12	1	Pass	12	Pass
Test: class err												
Class Ilim A=	77.4	76.4	76.5	76.1	mA	76.1	77.4	76.6	51	Pass	100	Pass
Class Ilim B=	77.8	77.2	76.7	77.1	mA	76.7	77.8	77.2	51	Pass	100	Pass
Pwr Cl 52 SS=	0	0			****	0	0	0	0	Pass	0	Pass
Pwr Cl 52 DSA=	0	0			****	0	0	0	0	Pass	0	Pass
Pwr Cl 52 DSB=	0	0			****	0	0	0	0	Pass	0	Pass
Mark Ilim A=	77	77	77	76	mA	76	77	76.8	0	Pass	105	Pass
Mark Ilim B=	78	75	77	46	mA	46	78	69	0	Pass	105	Pass
Inval Sig EV2 SS=	0	0			****	0	0	0	0	Pass	1	Pass
Inval Sig EV4 SS=	0	0			****	0	0	0	0	Pass	1	Pass
Inval Sig EV5 SS=	0	0			****	0	0	0	0	Pass	1	Pass
Inval Sig EV2 DSA=	0	0			****	0	0	0	0	Pass	1	Pass
Inval Sig EV2 DSB=	0	0			****	0	0	0	0	Pass	1	Pass
Inval Sig EV4 DSA=	0	0			****	0	0	0	0	Pass	1	Pass
Inval Sig EV4 DSB=	0	0			****	0	0	0	0	Pass	1	Pass
Test: pwrup time												
Pwr On Time Tpon SS=	229.7	233.6	229.7	229.7	msec	229.7	233.6	230.7	0	Pass	400	Pass
Pwr On Time Tpon DSA=	214.1	214.1	214.1	214.1	msec	214.1	214.1	214.1	0	Pass	400	Pass
Pwr On Time Tpon DSB=	214.1	210.2	214.1	214.1	msec	210.2	214.1	213.1	0	Pass	400	Pass
Pwrup Rise Time A=	24	24	24	24	usec	24	24	24	15	Pass	50000	Pass
Pwrup Rise Time B=	24	24	24	24	usec	24	24	24	15	Pass	50000	Pass
Pwr Stagger Time SS4=	60.8	61	61	60.9	msec	60.8	61	60.9	-1	Pass	75	Pass
Pwr Stagger Time SS5=	60.7	61	60.9	60.9	msec	60.7	61	60.9	0	Pass	75	Pass
Pwr Stagger Time DS=	474.7	476.1	475.5	476.3	msec	474.7	476.3	475.7	0	Pass	1000	Pass

PSE Conformance Test Suite

May 14 2020 6:43 PM	
Port Count.....	4
Loop Count.....	1
PSE Tested: TI_FirmPse_4p Type-4	

	802.3bt 4Pr Conformance Report	
	version 5.2.00	
	PSE Type: 4 MDI-X+MDI	report version 5.1.17
	Safety Index*: 100%	Interop Index*: 100%
Error Log: None		

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports					Min	Max	Average	Low Limit	P/F	High Limit	P/F
	5-1	5-2	6-1	6-2	UNITS							
Test: pwrup_inrush												
Iinrush_min_Class 3=	420	416.3	414.3	416.6	mA	414.3	420	416.8	400	Pass	9999	Pass
Iinrush_min_Class 5=	415.9	415.3	423.5	417	mA	415.3	423.5	417.9	400	Pass	9999	Pass
Iinrush_min_Class 7=	837.9	835.6	841.6	840.3	mA	835.6	841.6	838.9	800	Pass	9999	Pass
Iinrush_min_Class 1D A=	415.4	415.2	414.7	417	mA	414.7	417	415.6	400	Pass	9999	Pass
Iinrush_min_Class 1D B=	420.2	416.5	423	417.7	mA	416.5	423	419.4	400	Pass	9999	Pass
Iinrush 4P max Class 3=	421.1	417.2	415.2	417.2	mA	415.2	421.1	417.7	0	Pass	450	Pass
Iinrush 4P max2 Class 5=	416.5	415.8	424.2	417.5	mA	415.8	424.2	418.5	0	Pass	900	Pass
Iinrush 4P max2 Class 7=	839.4	836.5	842.7	841	mA	836.5	842.7	839.9	0	Pass	900	Pass
Iinrush 2P max Class 3=	421.2	415.3	423.7	416.8	mA	415.3	423.7	419.3	0	Pass	450	Pass
Iinrush 2P max2 Class 7=	421.8	418.4	424.5	420.3	mA	418.4	424.5	421.3	0	Pass	600	Pass
Iinrush 2p max C1 1D A=	416.1	416.1	415.2	417.8	mA	415.2	417.8	416.3	0	Pass	450	Pass
Iinrush 2p max C1 1D B=	421.2	417.3	424	418.5	mA	417.3	424	420.3	0	Pass	450	Pass
Tinrush_minPr Class 3=	59.18	59.58	59.58	59.58	msec	59.18	59.58	59.5	50	Pass	75	Pass
Tinrush_maxPr Class 3=	59.18	59.58	59.58	59.58	msec	59.18	59.58	59.5	50	Pass	75	Pass
Tinrush_minPr Class 7=	59.18	59.58	59.18	59.18	msec	59.18	59.58	59.3	50	Pass	75	Pass
Tinrush_maxPr Class 7=	59.18	59.58	59.18	59.18	msec	59.18	59.58	59.3	50	Pass	75	Pass
Tinrush Class 1D A=	59.18	59.58	59.18	59.18	msec	59.18	59.58	59.3	50	Pass	75	Pass
Tinrush Class 1D B=	59.18	59.58	59.18	59.58	msec	59.18	59.58	59.4	50	Pass	75	Pass
Delay Inrush Class 7=	59.18	59.58	59.58	59.18	msec	59.18	59.58	59.4	50	Pass	75	Pass
Delay Inrush Class 2D A=	59.18	59.58	59.18	59.58	msec	59.18	59.58	59.4	50	Pass	75	Pass
Delay Inrush Class 2D B=	59.18	59.58	59.58	59.58	msec	59.18	59.58	59.5	50	Pass	75	Pass
45ms Pwr Stat Class 7=	1	1			****	1	1	1	1	Pass	1	Pass
45ms Pwr Stat Class 2D A=	1	1			****	1	1	1	1	Pass	1	Pass
45ms Pwr Stat Class 2D B=	1	1			****	1	1	1	1	Pass	1	Pass
Vinrush Class 2D A=	30.9	30.8	30.7	30.8	volts	30.7	30.9	30.8	30	Pass	60	Pass
Vinrush Class 2D B=	30.9	30.8	30.9	30.7	volts	30.7	30.9	30.8	30	Pass	60	Pass
Test: pwrcon_v												
Vpse Max Alt A=	54.98	54.98	54.95	54.95	V	54.95	54.98	54.97	52	Pass	57	Pass
Vpse Max Alt B=	54.98	54.98	54.98	55	V	54.98	55	54.99	52	Pass	57	Pass
Vpse Min Alt A=	53.85	53.83	53.85	53.8	V	53.8	53.85	53.83	52	Pass	57	Pass
Vpse Min Alt B=	53.8	53.8	53.8	53.85	V	53.8	53.85	53.81	52	Pass	57	Pass
Vport_PSE_diff=	0	0	20	20	mV	0	20	10	0	Pass	150	Pass
V_ripple A=	9	12	9	8	mVp-p	8	12	9.5	0	Pass	500	Pass
V_ripple B=	8	8			mVp-p	8	8	8	0	Pass	500	Pass
V_noise A=	6	7			mVp-p	6	8	7	0	Pass	200	Pass
V_noise B=	15	14	14	14	mVp-p	14	15	14.3	0	Pass	200	Pass
V_trans A=	53.792	53.76	53.76	53.728	V	53.728	53.792	53.76	52	Pass	57	Pass
V_trans B=	53.728	53.728	53.744	53.776	V	53.728	53.776	53.744	52	Pass	57	Pass

PoE 2 Conformance Report

 May 14th, 2020: Revision 1.0


PSE Conformance Test Suite		
May 14 2020	6:43 PM	
Port Count.....	4	
Loop Count.....	1	
PSE Tested: TL_FirmPse_4p Type-4		

Sifos® Technologies		802.3bt 4Pr Conformance Report	
Safety Index*: 100%		PSE Type: 4 MDI-X+MDI	
Error Log: None		Interop Index*: 100%	
		version 5.2.00	
		report version 5.1.17	

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports					Min	Max	Average	Low Limit	P/F	High Limit	P/F
	5-1	5-2	6-1	6-2	UNITS							
Test: pwrcon pwrcap												
Max Asgn Class SS=	8	8			****	8	8	8	1	Pass	8	Pass
Pcon c1=	4	4			W	4	4	4	3.9	Pass	99	Pass
Icon % c1=	102.3	102.3	102.3	102.3	%	102.3	102.3	102.3	100	Pass	125	Pass
Pcon c2=	6.9	6.9	6.9	6.9	W	6.9	6.9	6.9	3.9	Pass	99	Pass
Icon % c2=	105.1	105.1	105.1	105.1	%	105.1	105.1	105.1	100	Pass	125	Pass
Pcon c3=	15	15	15	15	W	15	15	15	3.9	Pass	99	Pass
Icon % c3=	112.4	112.4	112.4	112.4	%	112.4	112.4	112.4	100	Pass	125	Pass
Pcon c4=	30.4	30.3	30.3	30.3	W	30.3	30.4	30.3	3.9	Pass	99	Pass
Icon % c4=	112.3	112.2	112.2	112.2	%	112.2	112.3	112.2	100	Pass	125	Pass
Pcon c5=	45.1	45.1	45.1	45.1	W	45.1	45.1	45.1	3.9	Pass	99	Pass
Icon % c5=	102.1	102.1	102.1	102.1	%	102.1	102.1	102.1	100	Pass	125	Pass
Pcon c6=	60.5	59.4	60.5	60.5	W	59.4	60.5	60.2	3.9	Pass	99	Pass
Icon % c6=	104	102.1	104	103.9	%	102.1	104	103.5	100	Pass	125	Pass
Pcon c7=	74.9	74.9	74.9	74.9	W	74.9	74.9	74.9	3.9	Pass	99	Pass
Icon % c7=	101.8	101.7	101.7	101.8	%	101.7	101.8	101.8	100	Pass	125	Pass
Pcon c8=	89.4	89.3	89.4	89.4	W	89.3	89.4	89.4	3.9	Pass	99	Pass
Icon % c8=	101.5	101.4	101.4	101.4	%	101.4	101.5	101.4	100	Pass	125	Pass
Type N Enable=	1	1			****	1	1	1	1	Pass	1	Pass
Pclass LLDP 95%=	1	1			****	1	1	1	1	Pass	1	Pass
Pclass LLDP 75%=	1	1			****	1	1	1	1	Pass	1	Pass
Max Asgn Class DS=	5	5			****	5	5	5	1	Pass	5	Pass
Pcon c1DA=	3.9	4	3.9	3.9	W	3.9	4	4	3.9	Pass	99	Pass
Icon % c1DA=	100.6	102	100.6	100.6	%	100.6	102	101	100	Pass	125	Pass
Pcon c2DB=	7	6.9	7	7	W	6.9	7	7	3.9	Pass	99	Pass
Icon % c2DB=	105.1	103	105.2	105.2	%	103	105.2	104.6	100	Pass	125	Pass
Pcon c3DA=	15.5	15.5	15.5	15.5	W	15.5	15.5	16	3.9	Pass	99	Pass
Icon % c3DA=	112.3	112.4	112.4	112.3	%	112.3	112.4	112.4	100	Pass	125	Pass
Pcon c4DB=	30.3	29.7	30.3	30.3	W	29.7	30.3	30	3.9	Pass	99	Pass
Icon % c4DB=	104.1	102.1	104.2	104.1	%	102.1	104.2	103.6	100	Pass	125	Pass
Pcon c5DA=	44.6	44.6	44.6	44.6	W	44.6	44.6	45	3.9	Pass	99	Pass
Icon % c5DA=	101.5	101.5	101.5	101.5	%	101.5	101.5	101.5	100	Pass	125	Pass
Test: pwrcon unbal												
pseP2pUnbal c4A=	1	1			****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c4B=	1	1			****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c5A=	1	1			****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c5B=	1	1			****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c6A=	1	1			****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c6B=	1	1			****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c7A=	1	1			****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c7B=	1	1			****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c8A=	1	1			****	1	1	1	1	Pass	1	Pass
pseP2pUnbal c8B=	1	1			****	1	1	1	1	Pass	1	Pass

PSE Conformance Test Suite

May 14 2020 6:43 PM	
Port Count.....	4
Loop Count.....	1
PSE Tested: TI_FirmPse_4p Type-4	


	802.3bt 4Pr Conformance Report version 5.2.00	
	PSE Type: 4 MDI-X+MDI	Interop Index*: 100%
Safety Index*: 100%	report version 5.1.17	
Error Log: None		

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports					Min	Max	Average	Low Limit	P/F	High Limit	P/F
	5-1	5-2	6-1	6-2	UNITS							
Test: pwrn_maxi												
Ilim_2p_max_SSA=	538.3	516.4	520.3	1099.4	mA	516.4	1099.4	668.6	0	Pass	1750	Pass
Ilim_2p_max_SSB=	1077.8	1029.5	1049.4	1055.4	mA	1029.5	1077.8	1053	0	Pass	1750	Pass
Tlim_SS=	59.38	59.38	59.38	59.38	msec	59.38	59.38	59.4	6	Pass	75	Pass
Ilim_2p_max_DSA=	478	486.3	526.3	1051.4	mA	478	1051.4	635.5	0	Pass	1750	Pass
Ilim_2p_max_DSB=	1108.3	570.1	1055.9	1089.9	mA	570.1	1108.3	956.1	0	Pass	1750	Pass
Tlim_DSA=	59.38	59.38	59.38	59.38	msec	59.38	59.38	59.4	6	Pass	75	Pass
Tlim_DSB=	59.38	59.38	59.38	59.38	msec	59.38	59.38	59.4	6	Pass	75	Pass
Ilim_min_cAB3=	400	400	400	400	mA	400	400	400	400	Pass	1750	Pass
Max_trans_c3=	1	1			****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB4=	684	684	684	684	mA	684	684	684	684	Pass	1750	Pass
Max_trans_c4=	1	1			****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB5=	580	580	580	580	mA	580	580	580	580	Pass	1750	Pass
Max_trans_c5=	1	1			****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB6=	720	720	720	720	mA	720	720	720	720	Pass	1750	Pass
Max_trans_c6=	1	1			****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB7=	850	850	850	850	mA	850	850	850	850	Pass	1750	Pass
Max_trans_c7=	1	1			****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB8=	1005	1005	1005	1005	mA	1005	1005	1005	1005	Pass	1750	Pass
Max_trans_c8=	1	1			****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB3D=	400	400	400	400	mA	400	400	400	400	Pass	1750	Pass
Max_trans_c3D=	1	1			****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB4D=	684	684	684	684	mA	684	684	684	684	Pass	1750	Pass
Max_trans_c4D=	1	1			****	1	1	1	1	Pass	1	Pass
Ilim_min_cAB5D=	990	990	990	990	mA	990	990	990	990	Pass	1750	Pass
Max_trans_c5D=	1	1			****	1	1	1	1	Pass	1	Pass
Vtrans_2p_A=	54.2	54.17	54.2	54.17	V	54.17	54.2	54.2	48.4	Pass	57	Pass
Vtrans_2p_B=	54.2	54.17	54.2	54.2	V	54.17	54.2	54.2	48.4	Pass	57	Pass
Iport_max_type4=	0	0			****	0	0	0	0	Pass	0	Pass
Itps_type4=	0	0			****	0	0	0	0	Pass	0	Pass
Test: pwrn_overld												
Ipeak_c1=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c2=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c3=	1	1			****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c3=	54.8	54.77	54.77	54.77	V	54.77	54.8	54.8	52	Pass	57	Pass
Ipeak_5%DC_c3=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c4=	1	1			****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c4=	54.55	54.58	54.55	54.55	V	54.55	54.58	54.6	52	Pass	57	Pass
Ipeak_5%DC_c4=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c5=	1	1			****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c5=	54.33	54.33	54.33	54.33	V	54.33	54.33	54.3	52	Pass	57	Pass
Ipeak_5%DC_c5=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c6=	1	1			****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c6=	54.1	54.1	54.13	54.1	V	54.1	54.13	54.1	52	Pass	57	Pass
Ipeak_5%DC_c6=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c7=	1	1			****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c7=	53.88	53.85	53.88	53.88	V	53.85	53.88	53.9	52	Pass	57	Pass
Ipeak_5%DC_c7=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c8=	1	1			****	1	1	1	1	Pass	1	Pass
Vport_Ipeak_c8=	53.63	53.63	53.65	53.63	V	53.63	53.65	53.6	52	Pass	57	Pass
Ipeak_5%DC_c8=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c1D=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c2D=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c3D=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c4D=	1	1			****	1	1	1	1	Pass	1	Pass
Ipeak_c5D=	1	1			****	1	1	1	1	Pass	1	Pass

PSE Conformance Test Suite

May 14 2020 6:43 PM

 Port Count..... 4
 Loop Count..... 1
 PSE Tested: **TI_FirmPse_4p Type-4**

	802.3bt 4Pr Conformance Report version 5.2.00 report version 5.1.17	
	PSE Type: 4 MDI-X+MDI	Interop Index*: 100%
	Safety Index*: 100%	Error Log: None

Chassis ID: 158.218.10.73 TestLoop: 1	PSA-3000 Ports					Min	Max	Average	Low Limit	P/F	High Limit	P/F
	5-1	5-2	6-1	6-2	UNITS							
Test: mps_dc valid												
Ihold c3=	5	5			mA	5	5	5	4	Pass	9	Pass
Ihold 2p c3A=	-1	-1	-1	-1	mA	-1	-1	-1	2	Pass	5	Pass
Ihold 2p c3B=	-1	-1	-1	-1	mA	-1	-1	-1	2	Pass	5	Pass
Ihold c5=	7	7			mA	5	7	6.3	4	Pass	14	Pass
Ihold 2p c5A=	3	4			mA	3	4	3.3	2	Pass	7	Pass
Ihold 2p c5B=	4	3			mA	3	4	3.3	2	Pass	7	Pass
Ihold c7=	7	6			mA	6	7	6.3	4	Pass	14	Pass
Ihold 2p c7A=	4	4			mA	3	4	3.5	2	Pass	7	Pass
Ihold 2p c7B=	4	3			mA	3	4	3.3	2	Pass	7	Pass
Ihold 2p c2DA=	3	4			mA	3	4	3.3	2	Pass	7	Pass
Ihold 2p c2DB=	4	3			mA	3	4	3.3	2	Pass	7	Pass
LP MPS Tol c3=	1	1			****	1	1	1	1	Pass	1	Pass
LP MPS Tol c5=	1	1			****	1	1	1	1	Pass	1	Pass
LP MPS Tol c7=	1	1			****	1	1	1	1	Pass	1	Pass
LP MPS Tol c2D=	1	1			****	1	1	1	1	Pass	1	Pass
Test: mps_dc pwrn												
Tmpdo c3A=	-1	-1	361.3	-1	msec	-1	361.3	361.3	320	Pass	400	Pass
Tmpdo c3B=	361.3	361.3	-1	361.3	msec	-1	361.3	361.3	320	Pass	400	Pass
Tmpdo c5A=	361.3	363.3	361.3	361.3	msec	361.3	363.3	362.3	320	Pass	400	Pass
Tmpdo c5B=	361.3	363.3	361.3	361.3	msec	361.3	363.3	361.8	320	Pass	400	Pass
Tmpdo c7A=	361.3	363.3	361.3	361.3	msec	361.3	363.3	361.8	320	Pass	400	Pass
Tmpdo c7B=	361.3	363.3	361.3	361.3	msec	361.3	363.3	361.8	320	Pass	400	Pass
Tmpdo c2DA=	361.3	361.3	361.3	361.3	msec	361.3	361.3	361.3	320	Pass	400	Pass
4pr Stat c2DA=	1	1			****	1	1	1	0	Pass	1	Pass
Tmpdo c2DB=	361.3	363.3	361.3	361.3	msec	361.3	363.3	361.8	320	Pass	400	Pass
4pr Stat c2DB=	1	1			****	1	1	1	0	Pass	1	Pass
Test: pwrn time												
Turnoff time Toff A=	24.3	24.2	23.6	24.7	msec	23.6	24.7	24.2	0	Pass	500	Pass
Turnoff time Toff B=	25.7	24.6	25	24.9	msec	24.6	25.7	25.1	0	Pass	500	Pass
Cout A=	78.7	78.5	84.5	84.3	nF	78.5	84.5	81.5	0	Pass	520	Pass
Cout B=	97.4	82.7	87.1	86.5	nF	82.7	97.4	88.4	0	Pass	520	Pass
Output Rp A=	153	154	133	143	Kohm	133	154	146	45	Pass	9999	Pass
Output Rp B=	123	145	138	139	Kohm	123	145	136	45	Pass	9999	Pass
Test: pwrn v												
Error_Delay SS A=	1016	1016	1016	1016	msec	1016	1016	1016	750	Pass	9999	Pass
Error_Delay SS B=	1387	1406	1387	1387	msec	1387	1406	1392	750	Pass	9999	Pass
Error_Delay DS A=	1016	1016	1016	1426	msec	1016	1426	1119	750	Pass	9999	Pass
Error_Delay DS B=	1406	1426	1406	1016	msec	1016	1426	1314	750	Pass	9999	Pass
Idle_Voff SS A=	0.1	0.1	0.1	0.1	V	0.1	0.1	0.1	0	Pass	2.8	Pass
Idle_Voff SS B=	0.1	0.1	0.1	0.1	V	0.1	0.1	0.1	0	Pass	2.8	Pass
Idle_Voff DS A=	0.1	0.1	0.1	0.1	V	0.1	0.1	0.1	0	Pass	2.8	Pass
Idle_Voff DS B=	0.1	0.1	0.1	0.1	V	0.1	0.1	0.1	0	Pass	2.8	Pass
Test Port Model Number:	3202	3202	3202	3202								
Test Port Hardware Version:	8	8										
Test Port Firmware Version:	4.14	4.14	4.14	4.14								

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