



**ABSTRACT**

This report discusses the results of the total ionizing dose (TID) testing for TPS7H5004-SP, Texas Instruments 4-V to 14-V radiation-hardness-assured, current mode, dual output PWM controller optimized for DC-DC converters in space applications. The study was done to determine TID effects under high dose rate (HDR) up to 100 krad(Si). The results show that all samples passed within the specified limits up to 100 krad(Si).

**Table of Contents**

<b>1 Device Information</b> .....	2
1.1 Product Description.....	2
1.2 Device Details.....	2
<b>2 Total Dose Test Setup</b> .....	4
2.1 Test Overview.....	4
2.2 Test Description and Facilities.....	4
2.3 Test Setup Details.....	4
2.4 Test Configuration and Condition.....	6
<b>3 TI Characterization Test Results</b> .....	7
3.1 TID Characterization Summary Results.....	7
3.2 Specification Compliance Matrix.....	8
<b>4 Applicable and Reference Documents</b> .....	11
4.1 Applicable Documents.....	11
4.2 Reference Documents.....	11
<b>5 Revision History</b> .....	11
<b>A Appendix: Specifications Requiring Clarification</b> .....	12
<b>B Appendix: HDR TID Report Data</b> .....	14
<b>C Appendix: LDR TID Report Data</b> .....	15

**List of Figures**

Figure 1-1. TPS7H5004-SP Device Picture.....	3
Figure 2-1. Bias Diagram Used in TID Exposure.....	5

**List of Tables**

Table 1-1. Device and Exposure Details for HDR.....	2
Table 1-2. Device and Exposure Details for LDR.....	3
Table 2-1. HDR: 3-krad(Si) to 100-krad(Si)/s biased device information.....	6
Table 2-2. HDR: 3-krad(Si) to 100-krad(Si)/s unbiased device information.....	6
Table 2-3. LDR: 3-krad(Si) to 100-krad(Si)/s biased device information.....	6
Table 2-4. LDR: 3-krad(Si) to 100-krad(Si)/s unbiased device information.....	6
Table 3-1. TPS7H5004-SP Electrical Parameters Table.....	8
Table A-1. Specifications Requiring Clarification - Set #1.....	12
Table A-2. Specifications Requiring Clarification - Set #2.....	12
Table A-3. Specifications Requiring Clarification - Set #3.....	12
Table A-4. Specifications Requiring Clarification - Set #4.....	13

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## 1 Device Information

### 1.1 Product Description

The TPS7H5004-SP is a dual output radiation-tolerant PWM controller suited for usage in non-synchronous push-pull and full-bridge topologies. The controller has configurable leading edge blank time. The maximum duty cycle for this device is 50% and is attained by connecting the DCL pin to AVSS. The device has high switching frequency capability, small footprint, and 0.613 V +0.7%/-1% reference.

The TPS7H5004-SP can be driven using an external clock through the SYNC pin or run using its internal oscillator at a frequency programmed by the user. Other programmable features include the UVLO threshold, soft start, and slope compensation. The TPS7H5004-SP is packaged in a very small 22-pin ceramic dual flat package.

### 1.2 Device Details

[Table 1-1](#) lists the device information used in the TID HDR characterization.

[Table 1-2](#) lists the device information used in the TID LDR characterization.

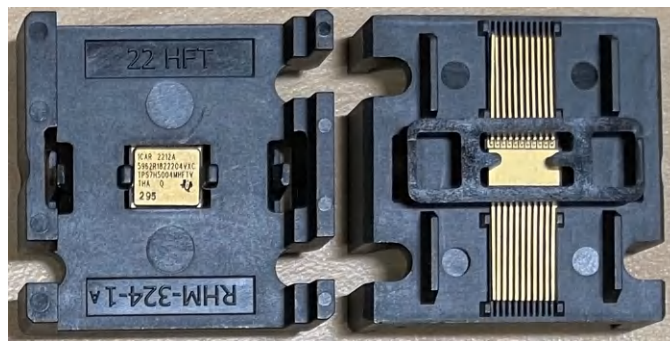
TPS7H5004-SP is part of TPS7H500x-family PWM controllers. Although each device in the SP family of PWM controllers has distinct functionality, all of the controllers share an identical device architecture, layout, and process. The differences are the features enabled in each device. As such, the radiation performance due to dose effects is expected to be similar for all of the devices in the TPS7H500x-SP family. For the full-featured device, TPS7H5001-SP, both LDR and HDR testing were performed. All electrical specifications were tested subsequent to the LDR and HDR exposure and were within datasheet specifications, as detailed in TPS7H5001-SP TID report. Examination of the TPS7H5001-SP post irradiation data indicates that the larger parametric shifts occurred after HDR exposure. As such, each device in the TPS7H500x-SP family was individually TID tested under HDR. For LDR testing information and post-LDR electrical characterization for the TPS7H5004-SP, please refer to TPS7H5001-SP TID LDR test data in Appendix C.

**Table 1-1. Device and Exposure Details for HDR**

TID Exposure Details	
TI Device	TPS7H5004-SP
TI Part Name	5962R1822204VXC
Package	22-pin CFP (HFT)
Technology	LBC7
Quantity Tested	HDR - 70
Lot Accept/Reject	All levels tested and passed up to 100 krad(Si) for HDR
HDR Radiation Facility	DCLAB, Texas Instruments, Dallas, TX
HDR Dose Level	3 krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), 100 krad(Si)
HDR Dose Rate	233.58 rads(Si)/s
HDR Radiation Source	Gammacell 220 Excel (GC-220E) Co-60
Irradiation Temperature	Ambient room temperature

**Table 1-2. Device and Exposure Details for LDR**

TID Exposure Details	
TI Device	TPS7H5001-SP
TI Part Name	5962R1822201VXC
Package	22-pin CFP (HFT)
Technology	LBC7
Quantity Tested	LDR - 77
Lot Accept/Reject	All levels tested and passed up to 100 krad(Si) for LDR
HDR Radiation Facility	Radiation Test Solutions (RTS) - Colorado Springs, CO
HDR Dose Level	3 krad(Si), 10 krad(Si), 30 krad(Si), 50 krad(Si), 100 krad(Si)
HDR Dose Rate	0.01 rads(Si)/s
HDR Radiation Source	Gammacell (JLS-81-22) Co-60
Irradiation Temperature	Ambient room temperature



**Figure 1-1. TPS7H5004-SP Device Picture**

## 2 Total Dose Test Setup

### 2.1 Test Overview

The TPS7H5004-SP was tested according to MIL-STD-883, Test Method 1019.9. For this testing, Conditions A and D were used. The product was irradiated up to the target radiation level, and then put through full electrical parametric testing on the production automated test equipment (ATE). All devices remained functional passing all parametric test limits.

### 2.2 Test Description and Facilities

The TPS7H5004-SP HDR exposure was performed on biased and unbiased devices in a Co-60 gamma cell at TI facility in Santa Clara, California. The un-attenuated dose rate of this cell is 233.58 rads(Si)/s. After exposure, the devices were packed in dry ice (per MIL-STD-883 Method 1019.9 section 3.10) and bring to Junkins lab in TI Dallas for full post-radiation electrical evaluation using Texas Instruments ATE. ATE test limits are set per SMD electrical limits based on qualification and characterization data. Post-radiation measurements were taken within 30 minutes of removing the devices from the dry ice container. The devices were allowed to reach room temperature prior to electrical post-radiation measurements.

The TPS7H5001-SP LDR exposure was performed on biased and unbiased devices in a Co-60 gammacell under a 10-mrad(Si)/s exposure rate. The dose rate of the irradiator used in the exposure ranges from < 10 mrad(Si)/s to a maximum of approximately 84 rad(Si)/s, determined by the distance from the source. For the LDR (10 mrad(Si)/s) exposure, the test box was positioned approximately 2 m from the source. The exposure boards are housed in a lead-aluminum box (as specified in MIL-STD-883 TM 1019.9) to harden the gamma spectrum and minimize dose enhancement effects. The irradiator calibration is maintained by Logmire Laboratories using Thermoluminescence Dosimeters (TLDs) traceable to the National Institute of Standards and Technology (NIST) and the dosimetry was verified using TLDs prior to the radiation exposures. After exposure, devices were electrically tested at Texas Instruments. ATE test limits are set per SMD electrical limits based on qualification and characterization data.

### 2.3 Test Setup Details

The devices under HDR and LDR exposure were tested in two conditions, biased and unbiased, as described in the following.

#### 2.3.1 Unbiased

For the unbiased HDR and LDR conditions, the exposure was performed with all pins grounded. LDR data taken from TPS7H5001-SP.

#### 2.3.2 Biased

[Figure 2-1](#) shows the diagram for HDR and LDR exposure with biased condition. Bias diagram is same as TPS7H5001-SP as below, except pin# 2, 3, 12 and 13 are NC (no connect) for TPS7H5003-SP.

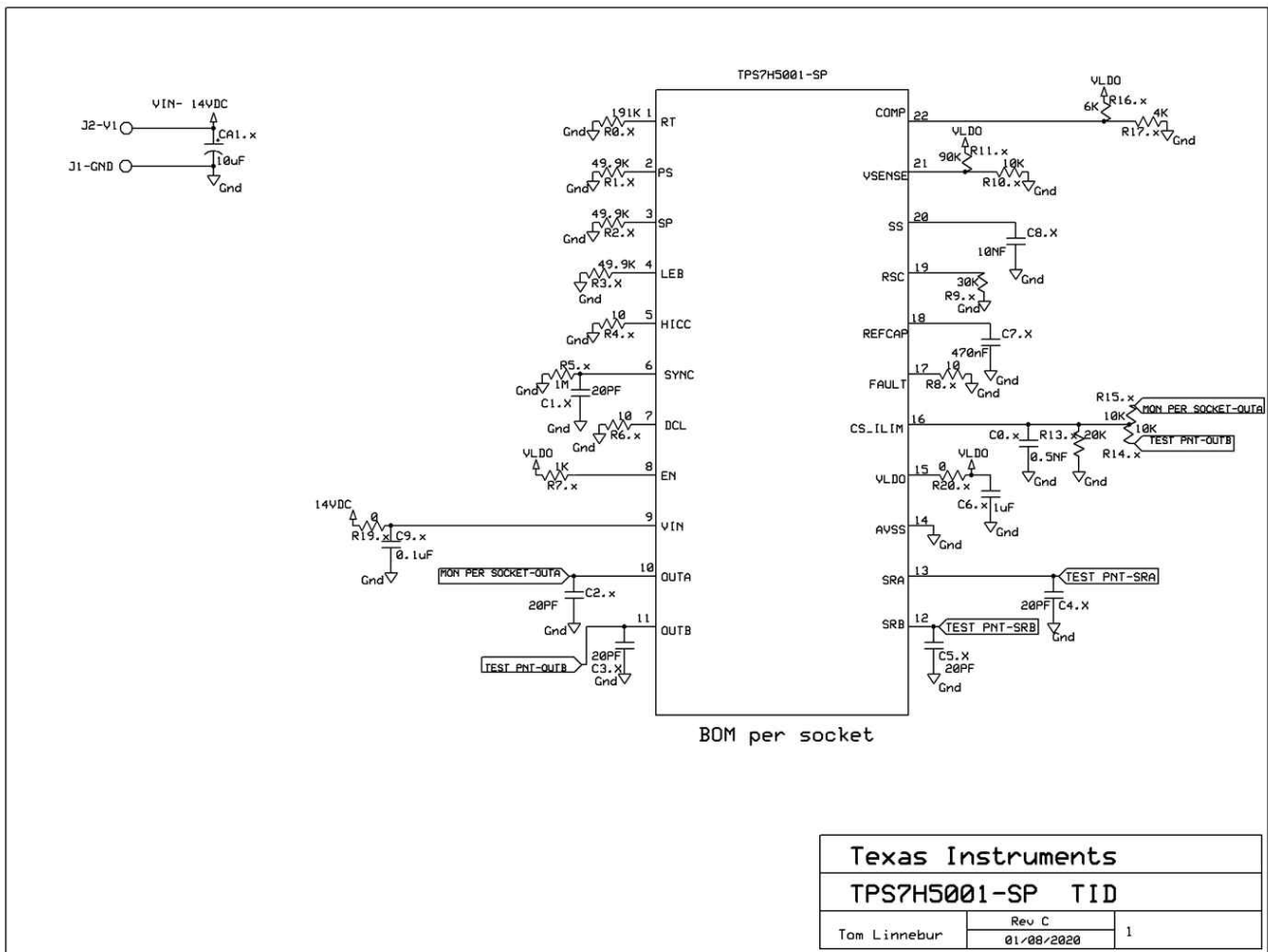


Figure 2-1. Bias Diagram Used in TID Exposure

## 2.4 Test Configuration and Condition

HDR devices were stressed at 50 krad(Si) and 100 krad(Si) for biased and unbiased conditions.

**Table 2-1. HDR: 3-krad(Si) to 100-krad(Si)/s biased device information**

Total Samples: 44				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-krad(Si)
192, 193, 194, 196, 197	207, 209, 210, 211, 212	224, 225, 226, 227, 229	272, 275, 276, 277, 278	317, 318, 319, 320, 321, 322, 323, 324, 326, 327, 328, 329, 332, 333, 334, 335, 336, 337, 338, 339, 340, 341, 342, 343

**Table 2-2. HDR: 3-krad(Si) to 100-krad(Si)/s unbiased device information**

Total Samples: 26				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-krad(Si)
200, 202, 203, 204, 206	214, 217, 218, 219, 222	230, 231, 232, 234, 237	279, 280, 281, 282, 283	344, 346, 349, 350, 351, 352

**Table 2-3. LDR: 3-krad(Si) to 100-krad(Si)/s biased device information**

Total Samples: 48				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-krad(Si)
61, 62, 63, 64, 65, 66	73, 74, 75, 76, 77, 78	86, 89, 91, 92, 94, 95	106, 107, 108, 109, 110, 111	129, 131, 132, 133, 134, 135, 138, 139, 140, 141, 142, 143, 144, 220, 221, 224, 225, 226, 228, 232, 233, 236, 238, 243

**Table 2-4. LDR: 3-krad(Si) to 100-krad(Si)/s unbiased device information**

Total Samples: 29				
Exposure Levels				
3-krad(Si)	10-krad(Si)	30-krad(Si)	50-krad(Si)	100-krad(Si)
67, 68, 69, 70, 71, 72	79, 80, 81, 82, 83, 84	100, 101, 102, 103, 104, 105	113, 114, 115, 116, 126, 128	244, 246, 248, 374, 376

## 3 TI Characterization Test Results

### 3.1 TID Characterization Summary Results

The parametric data for the TPS7H5004-SP passes up to 100-krad(Si) HDR. The drifts of SMD electrical parameters through HDR were within the SMD limits.

The parametric data for the TPS7H5001-SP passes up to 100-krad(Si) LDR TID irradiation. The drifts of SMD electrical parameters through LDR were within the SMD limits. TPS7H5004-SP is spin of TPS7H5001-SP, sharing an identical device architecture, layout, and process. The differences are the features enabled in each device. As such, the radiation performance due to dose effects is expected to be similar for the full-featured device, TPS7H5001-SP, and TPS7H5004-SP under LDR TID irradiation..

Overall, the TPS7H5004-SP showed a strong degree of hardness to HDR TID irradiation up to 100 krad(Si) for both biased and unbiased exposure conditions. The measurements taken post-irradiation for each sample set showed a marginal shift for most parameters at each dose level for both biased and unbiased. The parameters that did show a greater degree of change between pre- and post-irradiation were still within the Electrical Performance Characteristics specified in Standard Microcircuit Drawing (Data Sheet Electrical Parameters). See [Table 3-1](#) for the Data Sheet Electrical Parameters and Associated Tests.

See [Appendix A](#) for Specifications Requiring Clarification.

See [Appendix B](#) for HDR report up to 100 krad(Si) of TPS7H5004-SP.

See [Appendix C](#) for LDR report up to 100 krad(Si) of TPS7H5001-SP.

### 3.2 Specification Compliance Matrix

**Table 3-1. TPS7H5004-SP Electrical Parameters Table**

PARAMETER	TEST CONDITION	TPS7H5004-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
<b>SUPPLY VOLTAGES AND CURRENTS</b>						
IDD Operating supply current	$f_{SW} = 500$ kHz, No load for OUTA and OUTB		6.25	8	mA	5.24 __IDD_ACT_500K_NOLOAD_4V, 5.25 __IDD_ACT_500K_NOLOAD_5V, 5.26 __IDD_ACT_500K_NOLOAD_12V, 5.27 __IDD_ACT_500K_NOLOAD_14V
	$f_{SW} = 1$ MHz, No load for OUTA and OUTB		6.75	9.5	mA	5.28 __IDD_ACT_1M_NOLOAD_4V, 5.29 __IDD_ACT_1M_NOLOAD_5V, 5.30 __IDD_ACT_1M_NOLOAD_12V, 5.31 __IDD_ACT_1M_NOLOAD_14V
	$f_{SW} = 2$ MHz, No load for OUTA and OUTB		8.5	13.5	mA	5.36 __IDD_ACT_2M_NOLOAD_4V, 5.37 __IDD_ACT_2M_NOLOAD_5V, 5.38 __IDD_ACT_2M_NOLOAD_12V, 5.39 __IDD_ACT_2M_NOLOAD_14V
	$f_{SW} = 500$ kHz, $C_{LOAD} = 100$ pF for OUTA and OUTB		7.5	9.5	mA	5.48 __IDD_ACT_500K_4V, 5.49 __IDD_ACT_500K_5V, 5.50 __IDD_ACT_500K_12V, 5.51 __IDD_ACT_500K_14V
	$f_{SW} = 1$ MHz, $C_{LOAD} = 100$ pF for OUTA and OUTB		9	12	mA	5.52 __IDD_ACT_1M_4V, 5.53 __IDD_ACT_1M_5V, 5.54 __IDD_ACT_1M_12V, 5.55 __IDD_ACT_1M_14V
	$f_{SW} = 2$ MHz, $C_{LOAD} = 100$ pF for OUTA and OUTB		14	19.5	mA	5.60 __IDD_ACT_2M_4V, 5.61 __IDD_ACT_2M_5V, 5.62 __IDD_ACT_2M_12V, 5.63 __IDD_ACT_2M_14V
I <sub>DD(dis)</sub> Standby current	EN = 0 V			3	mA	5.1 __IDD_DIS_4V, 5.2 __IDD_DIS_5V, 5.3 __IDD_DIS_12V, 5.4 __IDD_DIS_14V
VLDO Internal linear regulator output voltage	5 V ≤ VIN ≤ 14 V, f <sub>sw</sub> ≤ 1 MHz	4.75	5	5.2	V	5.65 __V_LDO_100K_5V, 5.66 __V_LDO_100K_12V, 5.67 __V_LDO_100K_14V, 5.69 __V_LDO_200K_5V, 5.70 __V_LDO_200K_12V, 5.71 __V_LDO_200K_14V, 5.73 __V_LDO_500K_5V, 5.74 __V_LDO_500K_12V, 5.75 __V_LDO_500K_14V, 5.77 __V_LDO_1M_5V, 5.78 __V_LDO_1M_12V, 5.79 __V_LDO_1M_14V
	5 V ≤ VIN ≤ 14 V, f <sub>sw</sub> = 2 MHz	4.65	5	5.2	V	5.85 __V_LDO_2M_5V, 5.86 __V_LDO_2M_12V, 5.87 __V_LDO_2M_14V
<b>ENABLE AND UNDERVOLTAGE LOCKOUT</b>						
V <sub>ENR</sub> EN threshold rising		0.57	0.6	0.65	V	6.5 __V_EN_RISE_4V, 6.8 __V_EN_RISE_5V, 6.11 __V_EN_RISE_12V, 6.14 __V_EN_RISE_14V
V <sub>ENF</sub> EN threshold falling		0.47	0.5	0.55	V	6.6 __V_EN_FALL_4V, 6.9 __V_EN_FALL_5V, 6.12 __V_EN_FALL_12V, 6.15 __V_EN_FALL_14V
V <sub>ENH</sub> EN hysteresis voltage		85	95	105	mV	6.7 __V_EN_HYS_4V, 6.10 __V_EN_HYS_5V, 6.13 __V_EN_HYS_12V, 6.16 __V_EN_HYS_14V
I <sub>EN</sub> EN pin input leakage current	VIN = 14 V, EN = 5 V		5	50	nA	6.1 __I_EN_LEAK_4V, 6.2 __I_EN_LEAK_5V, 6.3 __I_EN_LEAK_12V, 6.4 __I_EN_LEAK_14V
VLDO <sub>UVLOR</sub> VLDO UVLO rising		3.44	3.55	3.66	V	6.34 __UVLO_VLDO_RISE_1MHz, 6.37 __UVLO_VLDO_RISE_100kHz, 6.40 __UVLO_VLDO_RISE_200kHz, 6.43 __UVLO_VLDO_RISE_500kHz, 6.46 __UVLO_VLDO_RISE_2MHz
VLDO <sub>UVLOF</sub> VLDO UVLO falling		3.29	3.4	3.51	V	6.35 __UVLO_VLDO_FALL_1MHz, 6.38 __UVLO_VLDO_FALL_100kHz, 6.41 __UVLO_VLDO_FALL_200kHz, 6.44 __UVLO_VLDO_FALL_500kHz, 6.47 __UVLO_VLDO_FALL_2MHz
VLDO <sub>UVLOH</sub> VLDO UVLO hysteresis		115	135	160	mV	6.36 __UVLO_VLDO_HYS_1MHz, 6.39 __UVLO_VLDO_HYS_100kHz, 6.42 __UVLO_VLDO_HYS_200kHz, 6.45 __UVLO_VLDO_HYS_500kHz, 6.48 __UVLO_VLDO_HYS_2MHz
<b>SOFT START</b>						
I <sub>SS</sub> Soft-start current	SS = 0.3 V	1.98	2.7	3.32	μA	7.1 __I_SS_4V, 7.3 __I_SS_5V, 7.5 __I_SS_12V, 7.7 __I_SS_14V
<b>ERROR AMPLIFIER</b>						
E <sub>A<sub>gm</sub></sub> Transconductance	-2 μA < I <sub>COMP</sub> < 2 μA, V <sub>(COMP)</sub> = 1 V	1150	1800	2500	μA/V	8.9 __EA_GM_4V, 8.10 __EA_GM_5V, 8.11 __EA_GM_12V, 8.12 __EA_GM_14V



**Table 3-1. TPS7H5004-SP Electrical Parameters Table (continued)**

PARAMETER	TEST CONDITION	TPS7H5004-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
EA <sub>ISRC</sub> Error amplifier source current	V <sub>(COMP)</sub> = 1 V, 100-mV input overdrive	100		190	μA	8.13 __EA_I_SOURCE_4V, 8.14 __EA_I_SOURCE_5V, 8.15 __EA_I_SOURCE_12V, 8.16 __EA_I_SOURCE_14V
EA <sub>ISNK</sub> Error amplifier sink current	V <sub>(COMP)</sub> = 1 V, 100-mV input overdrive	100		190	μA	8.17 __EA_I_SINK_4V, 8.18 __EA_I_SINK_5V, 8.19 __EA_I_SINK_12V, 8.20 __EA_I_SINK_14V
EA <sub>OS</sub> Error amplifier offset voltage		-2		2	mV	8.5 __EA_OS_4V, 8.6 __EA_OS_5V, 8.7 __EA_OS_12V, 8.8 __EA_OS_14V
<b>OSCILLATOR</b>						
SYNC <sub>RT</sub> SYNC out low-to-high rise time (10%/90%)	C <sub>LOAD</sub> = 25 pF		6	15	ns	9.1 __T_SYNC_RISE_100kHz_4V, 9.5 __T_SYNC_RISE_200kHz_4V, 9.9 __T_SYNC_RISE_500kHz_4V, 9.13 __T_SYNC_RISE_1MHz_4V, 9.17 __T_SYNC_RISE_1p5MHz_4V, 9.21 __T_SYNC_RISE_2MHz_4V, 9.25 __T_SYNC_RISE_100kHz_5V, 9.29 __T_SYNC_RISE_200kHz_5V, 9.33 __T_SYNC_RISE_500kHz_5V, 9.37 __T_SYNC_RISE_1MHz_5V, 9.41 __T_SYNC_RISE_1p5MHz_5V, 9.45 __T_SYNC_RISE_2MHz_5V, 9.49 __T_SYNC_RISE_100kHz_12V, 9.53 __T_SYNC_RISE_200kHz_12V, 9.57 __T_SYNC_RISE_500kHz_12V, 9.61 __T_SYNC_RISE_1MHz_12V, 9.65 __T_SYNC_RISE_1p5MHz_12V, 9.69 __T_SYNC_RISE_2MHz_12V, 9.73 __T_SYNC_RISE_100kHz_14V, 9.77 __T_SYNC_RISE_200kHz_14V, 9.81 __T_SYNC_RISE_500kHz_14V, 9.85 __T_SYNC_RISE_1MHz_14V, 9.89 __T_SYNC_RISE_1p5MHz_14V, 9.93 __T_SYNC_RISE_2MHz_14V
SYNC <sub>FT</sub> SYNC out high-to-low fall time (10%/90%)	C <sub>LOAD</sub> = 25 pF		6	17	ns	9.2 __T_SYNC_FALL_100kHz_4V, 9.6 __T_SYNC_FALL_200kHz_4V, 9.10 __T_SYNC_FALL_500kHz_4V, 9.14 __T_SYNC_FALL_1MHz_4V, 9.18 __T_SYNC_FALL_1p5MHz_4V, 9.22 __T_SYNC_FALL_2MHz_4V, 9.26 __T_SYNC_FALL_100kHz_5V, 9.30 __T_SYNC_FALL_200kHz_5V, 9.34 __T_SYNC_FALL_500kHz_5V, 9.38 __T_SYNC_FALL_1MHz_5V, 9.42 __T_SYNC_FALL_1p5MHz_5V, 9.46 __T_SYNC_FALL_2MHz_5V, 9.50 __T_SYNC_FALL_100kHz_12V, 9.54 __T_SYNC_FALL_200kHz_12V, 9.58 __T_SYNC_FALL_500kHz_12V, 9.62 __T_SYNC_FALL_1MHz_12V, 9.66 __T_SYNC_FALL_1p5MHz_12V, 9.70 __T_SYNC_FALL_2MHz_12V, 9.74 __T_SYNC_FALL_100kHz_14V, 9.78 __T_SYNC_FALL_200kHz_14V, 9.82 __T_SYNC_FALL_500kHz_14V, 9.86 __T_SYNC_FALL_1MHz_14V, 9.90 __T_SYNC_FALL_1p5MHz_14V, 9.94 __T_SYNC_FALL_2MHz_14V
SYNC <sub>OL</sub> SYNC out low level	I <sub>OL</sub> = 10 mA			500	mV	9.186 __SYNC_VOL_4V, 9.187 __SYNC_VOL_5V, 9.188 __SYNC_VOL_12V, 9.189 __SYNC_VOL_14V
EXT <sub>DT</sub> Externally set frequency detection time	RT = Open, f = 200 kHz			20	μs	9.185 __T_SYNC_DETECT
FSW <sub>EXT</sub> Externally set frequency	RT = 1.07 MΩ	95	105	115	kHz	9.4 __FSW_EXT_RT_100kHz_4V, 9.28 __FSW_EXT_RT_100kHz_5V, 9.52 __FSW_EXT_RT_100kHz_12V, 9.76 __FSW_EXT_RT_100kHz_14V
	RT = 511 kΩ	190	210	230	kHz	9.8 __FSW_EXT_RT_200kHz_4V, 9.32 __FSW_EXT_RT_200kHz_5V, 9.56 __FSW_EXT_RT_200kHz_12V, 9.80 __FSW_EXT_RT_200kHz_14V
	RT = 90.9 kΩ	900	1000	1100	kHz	9.16 __FSW_EXT_RT_1MHz_4V, 9.40 __FSW_EXT_RT_1MHz_5V, 9.64 __FSW_EXT_RT_1MHz_12V, 9.88 __FSW_EXT_RT_1MHz_14V
	RT = 34.8 kΩ	1700	2000	2300	kHz	9.24 __FSW_EXT_RT_2MHz_4V, 9.48 __FSW_EXT_RT_2MHz_5V, 9.72 __FSW_EXT_RT_2MHz_12V, 9.96 __FSW_EXT_RT_2MHz_14V
<b>VOLTAGE REFERENCE</b>						
VREF Internal voltage reference initial tolerance	Measured at COMP, 25°C	0.609	0.613	0.615	V	8.1 __VREF_4V, 8.2 __VREF_5V, 8.3 __VREF_12V, 8.4 __VREF_14V

**Table 3-1. TPS7H5004-SP Electrical Parameters Table (continued)**

PARAMETER	TEST CONDITION	TPS7H5004-SP DATA SHEET				TEST #
		MIN	TYP	MAX	UNIT	
REFCAP REFCAP voltage	REFCAP = 470 nF	1.213	1.225	1.237	V	5.88 __V_REFCAP_100K_4V, 5.89 __V_REFCAP_100K_5V, 5.90 __V_REFCAP_100K_12V, 5.91 __V_REFCAP_100K_14V, 5.92 __V_REFCAP_200K_4V, 5.93 __V_REFCAP_200K_5V, 5.94 __V_REFCAP_200K_12V, 5.95 __V_REFCAP_200K_14V, 5.96 __V_REFCAP_500K_4V, 5.97 __V_REFCAP_500K_5V, 5.98 __V_REFCAP_500K_12V, 5.99 __V_REFCAP_500K_14V, 5.100 __V_REFCAP_1M_4V, 5.101 __V_REFCAP_1M_5V, 5.102 __V_REFCAP_1M_12V, 5.103 __V_REFCAP_1M_14V, 5.104 __V_REFCAP_1P5M_4V, 5.105 __V_REFCAP_1P5M_5V, 5.106 __V_REFCAP_1P5M_12V, 5.107 __V_REFCAP_1P5M_14V, 5.108 __V_REFCAP_2M_4V, 5.109 __V_REFCAP_2M_5V, 5.110 __V_REFCAP_2M_12V, 5.111 __V_REFCAP_2M_14V
<b>CURRENT SENSE, CURRENT LIMIT, AND HICCUP</b>						
CCSR	COMP to CS_ILIM ratio	2.00	2.06	2.12		10.49 __CCSR_Ratio
V <sub>CS_ILIM</sub> Current limit (over-current) threshold			1.05	1.09	V	10.1 __V_CS_ILIM_OC_Rise_4V, 10.2 __V_CS_ILIM_OC_Fall_4V, 10.3 __V_CS_ILIM_OC_Rise_5V, 10.4 __V_CS_ILIM_OC_Fall_5V, 10.5 __V_CS_ILIM_OC_Rise_12V, 10.6 __V_CS_ILIM_OC_Fall_12V, 10.7 __V_CS_ILIM_OC_Rise_14V, 10.8 __V_CS_ILIM_OC_Fall_14V
<b>FAULT</b>						
V <sub>FLTR</sub> FLT threshold rising		0.57	0.6	0.65	V	12.1 __V_FAULT_RISE_4V, 12.4 __V_FAULT_RISE_5V, 12.7 __V_FAULT_RISE_12V, 12.10 __V_FAULT_RISE_14V
V <sub>FLTF</sub> FLT threshold falling		0.47	0.5	0.55	V	12.2 __V_FAULT_FALL_4V, 12.5 __V_FAULT_FALL_5V, 12.8 __V_FAULT_FALL_12V, 12.11 __V_FAULT_FALL_14V
V <sub>FLTH</sub> FLT hysteresis voltage		90	100	110	mV	12.3 __V_FAULT_HYS_4V, 12.6 __V_FAULT_HYS_5V, 12.9 __V_FAULT_HYS_12V, 12.12 __V_FAULT_HYS_14V
T <sub>FLT</sub> FLT minimum pulse width	V <sub>FLT</sub> = 1 V	0.4		1.4	μs	12.14 __T_FAULT_MIN
t <sub>DFLT</sub> FLT delay duration	f <sub>sw</sub> = 100 kHz	140	152	169	μs	12.15 __T_FAULT_DELAY_100kHz
	f <sub>sw</sub> = 200 kHz	66	78	86	μs	12.17 __T_FAULT_DELAY_200kHz
	f <sub>sw</sub> = 1 MHz	14	17	21	μs	12.21 __T_FAULT_DELAY_1MHz
	f <sub>sw</sub> = 2 MHz	7	11	14	μs	12.23 __T_FAULT_DELAY_2MHz
<b>PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS</b>						
Rise/fall time	R <sub>LOAD</sub> = 50 kΩ, C <sub>LOAD</sub> = 100 pF, 10% to 90%		10	17	ns	13.1 __OUTA_RISE_1MHz_4V, 13.9 __OUTA_RISE_1MHz_5V, 13.17 __OUTA_RISE_1MHz_12V, 13.25 __OUTA_RISE_1MHz_14V, 13.3 __OUTA_FALL_1MHz_4V, 13.11 __OUTA_FALL_1MHz_5V, 13.19 __OUTA_FALL_1MHz_12V, 13.27 __OUTA_FALL_1MHz_14V, 13.2 __OUTB_RISE_1MHz_4V, 13.10 __OUTB_RISE_1MHz_5V, 13.18 __OUTB_RISE_1MHz_12V, 13.26 __OUTB_RISE_1MHz_14V, 13.2 __OUTB_RISE_1MHz_4V, 13.10 __OUTB_RISE_1MHz_5V, 13.20 __OUTB_FALL_1MHz_12V, 13.28 __OUTB_FALL_1MHz_14V
<b>MINIMUM ON-TIME</b>						
t <sub>MIN</sub> Minimum on-time	LEB = 10 kΩ, 5 V ≤ VIN ≤ 14 V			85	ns	13.145 __OUT_T_ON_MIN
<b>DUTY CYCLE</b>						
D <sub>MAX</sub> Maximum duty cycle	DCL = AVSS	45	48	50	%	13.160 __MAX_DC_DCL_AVSS

## 4 Applicable and Reference Documents

### 4.1 Applicable Documents

- Texas Instruments, [TPS7H500x-SP Radiation-Hardness-Assured 2-MHz Current Mode PWM Controllers datasheet \(Rev. B\)](#)
- Texas Instruments, [TPS7H500xEVM-CVAL User's Guide](#)
- Texas Instruments, [TPS7H5001-SP Total Ionizing Dose \(TID\) \(Rev. A\)](#)
- Texas Instruments, [TPS7H500x-SP Single-Event Effects \(SEE\) \(Rev. A\)](#)

### 4.2 Reference Documents

Texas Instruments total ionizing dose radiation (total dose) test procedure follows the standards put forth in MIL-STD-883 TM 1019. The document can be found at the DLA website.

## 5 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision * (March 2022) to Revision A (August 2022)	Page
• .....	1
• HDR TID Plots include 3/10/30/50/100 Krad(Si) irradiation in both biased and unbiased condition.....	14
• LDR TID plots of TPS7H5001-SP are added. This include 3/10/30/50/100 Krad(Si) irradiation in both biased and unbiased condition.....	15

## A Appendix: Specifications Requiring Clarification

**Table A-1. Specifications Requiring Clarification - Set #1**

Data sheet specifications for which there is no parametric data and, therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5004-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
V <sub>IN</sub> Operating voltage		4		14	V
F <sub>SYNC</sub> SYNC in frequency range		200		4000	kHz
D <sub>SYNC</sub> SYNC in duty cycle	Duty cycle of external clock	40		60	%

**Table A-2. Specifications Requiring Clarification - Set #2**

Data sheet specifications for which no data is available in TID report as specification was verified through bench testing. TID testing is by ATE.

PARAMETER	TEST CONDITION	TPS7H5004-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
E <sub>A<sub>DC</sub></sub> DC gain	V <sub>SENSE</sub> = 0.6 V		10000		V/V
E <sub>A<sub>r<sub>o</sub></sub></sub> Error amplifier output resistance			7		MΩ
E <sub>A<sub>I<sub>B</sub></sub></sub> Error amplifier bias current				35	nA
E <sub>A<sub>B<sub>W</sub></sub></sub> Bandwidth			10		MHz
S <sub>Y<sub>N</sub>C<sub>I<sub>L</sub></sub></sub> SYNC in low-level	V <sub>IN</sub> < 5 V			0.8	V
	V <sub>IN</sub> ≥ 5 V			0.8	V
S <sub>Y<sub>N</sub>C<sub>I<sub>H</sub></sub></sub> SYNC in high-level	V <sub>IN</sub> < 5 V		3.5		V
	V <sub>IN</sub> ≥ 5 V		3.5		V
(V <sub>LDO</sub> - S <sub>Y<sub>N</sub>C<sub>O<sub>H</sub></sub></sub> ) SYNC out high level	I <sub>O<sub>H</sub></sub> = 10 mA			0.5	V

**Table A-3. Specifications Requiring Clarification - Set #3**

Data sheet specifications for which there is no 'MIN'/'MAX' value, only 'TYP', therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5004-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
I <sub>H<sub>I<sub>C</sub>C</sub>_D<sub>E</sub>L</sub> Hiccup delay current	C <sub>S</sub> _L <sub>I<sub>M</sub></sub> = 1.3 V, C <sub>O<sub>M</sub>P</sub> = 3 V, V <sub>SENSE</sub> = REF <sub>C<sub>A</sub>P</sub> /2 V, C <sub>H<sub>I<sub>C</sub>C</sub></sub> = 3 nF, L <sub>E<sub>B</sub></sub> = 49.9 kΩ, f <sub>S<sub>W</sub></sub> = 100 kHz		80		μA
I <sub>H<sub>I<sub>C</sub>C</sub>_R<sub>S</sub>T</sub> Hiccup restart current			1		μA
V <sub>H<sub>I<sub>C</sub>C</sub>_P<sub>U</sub></sub> Hiccup pull-up threshold			1.0		V
V <sub>H<sub>I<sub>C</sub>C</sub>_S<sub>D</sub></sub> Hiccup shut-down threshold			0.6		V
V <sub>H<sub>I<sub>C</sub>C</sub>_R<sub>S</sub>T</sub> Hiccup restart threshold			0.3		V
Slope compensation	f <sub>S<sub>W</sub></sub> = 100 kHz, R <sub>S<sub>C</sub></sub> = 1.18 MΩ		0.033		V/μs
	f <sub>S<sub>W</sub></sub> = 200 kHz, R <sub>S<sub>C</sub></sub> = 562 kΩ		0.066		V/μs
	f <sub>S<sub>W</sub></sub> = 1000 kHz, R <sub>S<sub>C</sub></sub> = 100 kΩ		0.333		V/μs
	f <sub>S<sub>W</sub></sub> = 2000 kHz, R <sub>S<sub>C</sub></sub> = 49.9 kΩ		0.666		V/μs
PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS Low-level threshold	I <sub>S<sub>I<sub>N</sub>K</sub></sub> = 10 mA		0.5		V

**Table A-3. Specifications Requiring Clarification - Set #3 (continued)**

Data sheet specifications for which there is no 'MIN'/'MAX' value, only 'TYP', therefore, not in the TID report. Functionality is assured by ATE testing.

PARAMETER	TEST CONDITION	TPS7H5004-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
PRIMARY AND SYNCHRONOUS RECTIFIER OUTPUTS High-level threshold	$I_{SOURCE} = 10 \text{ mA}$		4.5		V
$R_{SRC\_P}$ Output source resistance	$I_{OUT} = 20 \text{ mA}$		15		$\Omega$
$R_{SINK\_P}$ Output sink resistance	$I_{OUT} = 20 \text{ mA}$		15		$\Omega$

**Table A-4. Specifications Requiring Clarification - Set #4**

Data sheet specifications that are not covered in TID report because specification condition is at temperature other than 25°C. TID testing is at 25°C only.

PARAMETER	TEST CONDITION	TPS7H5004-SP DATA SHEET			
		MIN	TYP	MAX	UNIT
VREF Internal voltage reference	Measured at COMP, -55°C	0.607	0.609	0.612	V
	Measured at COMP, +125°C	0.611	0.614	0.617	V

## B Appendix: HDR TID Report Data

### Description of identifiers for HDR TID report

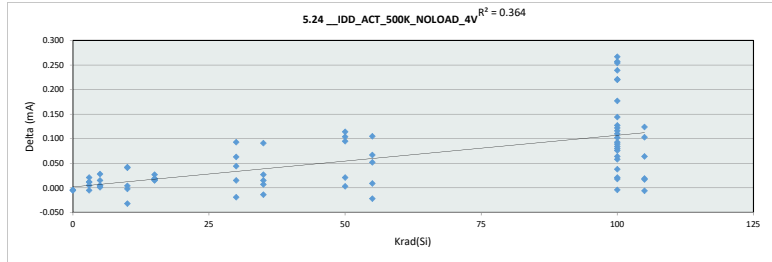
IDENTIFIER - Krad(Si)	DESCRIPTION
0	Control Unit
3	3 krad(Si) biased
5	3 krad(Si) unbiased
10	10 krad(Si) biased
15	10 krad(Si) unbiased
30	30 krad(Si) biased
35	30 krad(Si) unbiased
50	50 krad(Si) biased
55	50 krad(Si) unbiased
100	100 krad(Si) biased
105	100 krad(Si) unbiased

HDR TID Report  
TPS7H5004-SP

HDR TID Report  
TPS7H5004-SP

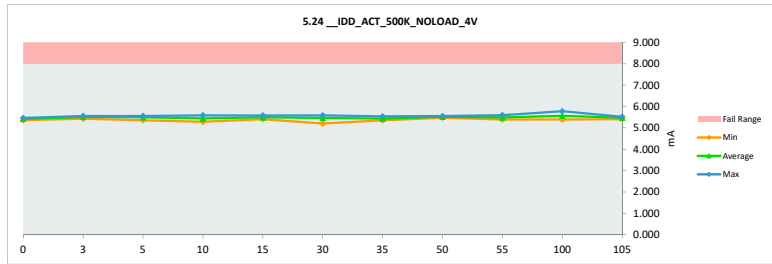
5.24 __IDD_ACT_500K_NOLOAD_4V	
Test Site	
Tester	
Test Number	
Unit	mA      mA
Max Limit	8            8
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.366	5.362	-0.004
0	297	5.449	5.444	-0.005
0	298	5.471	5.466	-0.005
3	192	5.529	5.550	0.021
3	193	5.437	5.432	-0.005
3	194	5.503	5.515	0.012
3	196	5.547	5.559	0.012
3	197	5.490	5.495	0.005
5	200	5.519	5.547	0.028
5	202	5.517	5.521	0.004
5	203	5.480	5.485	0.005
5	204	5.342	5.357	0.015
5	206	5.557	5.558	0.001
10	207	5.547	5.589	0.042
10	209	5.416	5.420	0.004
10	210	5.445	5.486	0.041
10	211	5.319	5.287	-0.032
10	212	5.429	5.427	-0.002
15	214	5.453	5.468	0.015
15	217	5.523	5.539	0.016
15	218	5.388	5.407	0.019
15	219	5.553	5.580	0.027
15	222	5.457	5.475	0.018
30	224	5.454	5.547	0.093
30	225	5.442	5.505	0.063
30	226	5.223	5.204	-0.019
30	227	5.574	5.589	0.015
30	229	5.432	5.476	0.044
35	230	5.533	5.540	0.007
35	231	5.512	5.539	0.027
35	232	5.383	5.369	-0.014
35	234	5.261	5.352	0.091
35	237	5.379	5.394	0.015
50	272	5.403	5.517	0.114
50	275	5.454	5.475	0.021
50	276	5.459	5.554	0.095
50	277	5.515	5.518	0.003
50	278	5.430	5.534	0.104
55	279	5.389	5.494	0.105
55	280	5.489	5.541	0.052
55	281	5.531	5.598	0.067
55	282	5.400	5.409	0.009
55	283	5.413	5.391	-0.022
100	317	5.604	5.726	0.122
100	318	5.431	5.449	0.018
100	319	5.387	5.497	0.110
100	320	5.470	5.550	0.080
100	321	5.309	5.453	0.144
100	322	5.486	5.562	0.076
100	323	5.295	5.422	0.127
100	324	5.406	5.499	0.093
100	326	5.542	5.781	0.239
100	327	5.384	5.561	0.177
100	328	5.478	5.594	0.116
100	329	5.520	5.777	0.257
100	332	5.468	5.735	0.267
100	333	5.302	5.391	0.089
100	334	5.528	5.586	0.058
100	335	5.568	5.670	0.102
100	336	5.504	5.725	0.221
100	337	5.489	5.553	0.064
100	338	5.434	5.455	0.021
100	339	5.517	5.771	0.254
100	340	5.410	5.406	-0.004
100	341	5.372	5.456	0.084
100	342	5.295	5.515	0.220
100	343	5.400	5.438	0.038
105	344	5.462	5.479	0.017
105	346	5.456	5.520	0.064
105	349	5.390	5.409	0.019
105	350	5.494	5.488	-0.006
105	351	5.326	5.450	0.124
105	352	5.388	5.491	0.103
	Max	5.604	5.781	0.267
	Average	5.446	5.506	0.060
	Min	5.223	5.204	-0.032
	Std Dev	0.080	0.107	0.072



5.24 __IDD_ACT_5	
Test Site	
Tester	
Test Number	
Max Limit	8      mA
Min Limit	

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	5.362	5.432	5.357	5.287	5.407	5.204	5.352	5.475	5.391	5.391	5.409
Average	5.424	5.510	5.494	5.442	5.494	5.464	5.439	5.520	5.487	5.566	5.473
Max	5.466	5.559	5.558	5.589	5.580	5.589	5.540	5.554	5.598	5.781	5.520
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

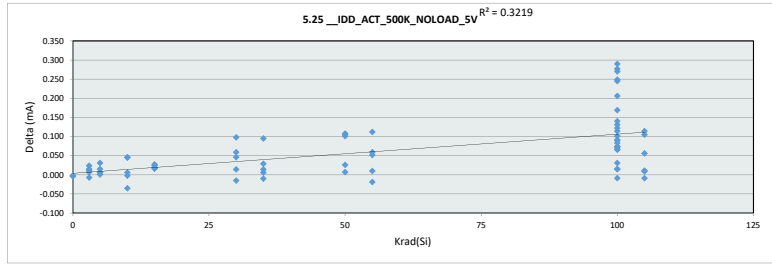




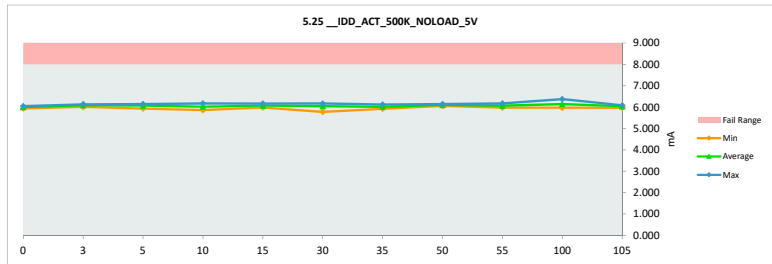
HDR TID Report  
TPS7H5004-SP

5.25 __IDD_ACT_500K_NOLOAD_5V	
Test Site	
Tester	
Test Number	
Unit	mA      mA
Max Limit	8            8
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.941	5.937	-0.004
0	297	6.021	6.019	-0.002
0	298	6.044	6.041	-0.003
3	192	6.107	6.131	0.024
3	193	6.014	6.007	-0.007
3	194	6.071	6.083	0.012
3	196	6.118	6.133	0.015
3	197	6.059	6.066	0.007
5	200	6.095	6.126	0.031
5	202	6.096	6.103	0.007
5	203	6.053	6.059	0.006
5	204	5.914	5.929	0.015
5	206	6.140	6.141	0.001
10	207	6.121	6.167	0.046
10	209	5.991	5.997	0.006
10	210	6.016	6.061	0.045
10	211	5.893	5.858	-0.035
10	212	6.005	6.003	-0.002
15	214	6.028	6.046	0.018
15	217	6.098	6.114	0.016
15	218	5.959	5.983	0.024
15	219	6.132	6.159	0.027
15	222	6.032	6.051	0.019
30	224	6.030	6.128	0.098
30	225	6.021	6.080	0.059
30	226	5.787	5.772	-0.015
30	227	6.152	6.166	0.014
30	229	6.010	6.056	0.046
35	230	6.107	6.113	0.006
35	231	6.085	6.114	0.029
35	232	5.953	5.943	-0.010
35	234	5.827	5.922	0.095
35	237	5.952	5.966	0.014
50	272	5.982	6.090	0.108
50	275	6.030	6.056	0.026
50	276	6.036	6.137	0.101
50	277	6.090	6.097	0.007
50	278	6.004	6.109	0.105
55	279	5.956	6.068	0.112
55	280	6.063	6.115	0.052
55	281	6.110	6.169	0.059
55	282	5.976	5.986	0.010
55	283	5.991	5.972	-0.019
100	317	6.180	6.302	0.122
100	318	5.999	6.014	0.015
100	319	5.961	6.062	0.101
100	320	6.046	6.119	0.073
100	321	5.884	6.024	0.140
100	322	6.056	6.130	0.074
100	323	5.864	5.978	0.114
100	324	5.974	6.064	0.090
100	326	6.124	6.373	0.249
100	327	5.959	6.128	0.169
100	328	6.052	6.183	0.131
100	329	6.090	6.360	0.270
100	332	6.038	6.328	0.290
100	333	5.878	5.969	0.091
100	334	6.100	6.166	0.066
100	335	6.149	6.232	0.083
100	336	6.082	6.327	0.245
100	337	6.064	6.130	0.066
100	338	6.010	6.025	0.015
100	339	6.089	6.366	0.277
100	340	5.986	5.977	-0.009
100	341	5.949	6.020	0.071
100	342	5.865	6.071	0.206
100	343	5.967	5.998	0.031
105	344	6.039	6.050	0.011
105	346	6.026	6.082	0.056
105	349	5.956	5.965	0.009
105	350	6.070	6.061	-0.009
105	351	5.895	6.000	0.105
105	352	5.957	6.071	0.114
	Max	6.180	6.373	0.290
	Average	6.019	6.080	0.061
	Min	5.787	5.772	-0.035
	Std Dev	0.082	0.112	0.074



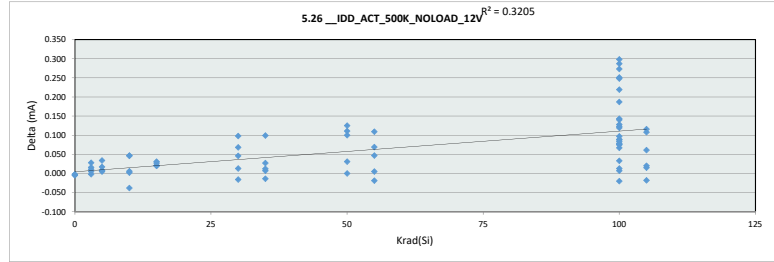
5.25 __IDD_ACT_5		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		5.937	6.007	5.929	5.858	5.983	5.772	5.922	6.056	5.972	5.969	5.965
Average		5.999	6.084	6.072	6.017	6.071	6.040	6.012	6.098	6.062	6.139	6.038
Max		6.041	6.133	6.141	6.167	6.159	6.166	6.114	6.137	6.169	6.373	6.082
UL		8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000



HDR TID Report  
TPS7H5004-SP

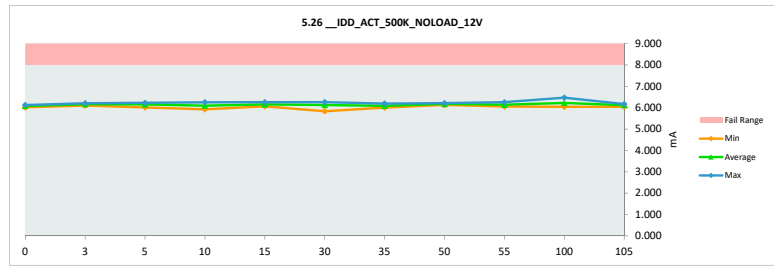
5.26_IDD_ACT_500K_NOLOAD_12V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	8
Min Limit	8

Krad(Si)	Serial #	PRE	POST	Delta
0	295	6.018	6.013	-0.005
0	297	6.091	6.089	-0.002
0	298	6.128	6.124	-0.004
3	192	6.178	6.206	0.028
3	193	6.099	6.097	-0.002
3	194	6.141	6.152	0.011
3	196	6.178	6.194	0.016
3	197	6.131	6.136	0.005
5	200	6.176	6.210	0.034
5	202	6.173	6.181	0.008
5	203	6.129	6.136	0.007
5	204	5.990	6.007	0.017
5	206	6.217	6.222	0.005
10	207	6.201	6.248	0.047
10	209	6.077	6.083	0.006
10	210	6.088	6.134	0.046
10	211	5.955	5.917	-0.038
10	212	6.078	6.080	0.002
15	214	6.095	6.116	0.021
15	217	6.187	6.207	0.020
15	218	6.036	6.063	0.027
15	219	6.221	6.252	0.031
15	222	6.114	6.134	0.020
30	224	6.127	6.225	0.098
30	225	6.098	6.166	0.068
30	226	5.844	5.828	-0.016
30	227	6.244	6.257	0.013
30	229	6.077	6.123	0.046
35	230	6.180	6.187	0.007
35	231	6.157	6.184	0.027
35	232	6.015	6.001	-0.014
35	234	5.903	6.002	0.099
35	237	6.029	6.041	0.012
50	272	6.050	6.175	0.125
50	275	6.095	6.126	0.031
50	276	6.107	6.207	0.100
50	277	6.162	6.162	0.000
50	278	6.072	6.183	0.111
55	279	6.042	6.151	0.109
55	280	6.112	6.159	0.047
55	281	6.183	6.252	0.069
55	282	6.045	6.050	0.005
55	283	6.074	6.055	-0.019
100	317	6.259	6.387	0.128
100	318	6.076	6.083	0.007
100	319	6.033	6.152	0.119
100	320	6.112	6.190	0.078
100	321	5.934	6.077	0.143
100	322	6.117	6.193	0.076
100	323	5.915	6.038	0.123
100	324	6.050	6.138	0.088
100	326	6.204	6.455	0.251
100	327	6.021	6.208	0.187
100	328	6.120	6.260	0.140
100	329	6.171	6.444	0.273
100	332	6.114	6.412	0.298
100	333	5.944	6.041	0.097
100	334	6.170	6.245	0.075
100	335	6.239	6.328	0.089
100	336	6.147	6.395	0.248
100	337	6.152	6.219	0.067
100	338	6.090	6.103	0.013
100	339	6.180	6.467	0.287
100	340	6.078	6.058	-0.020
100	341	6.019	6.102	0.083
100	342	5.938	6.157	0.219
100	343	6.039	6.072	0.033
105	344	6.102	6.122	0.020
105	346	6.100	6.161	0.061
105	349	6.024	6.039	0.015
105	350	6.144	6.126	-0.018
105	351	5.959	6.067	0.108
105	352	6.049	6.165	0.116
	Max	6.259	6.467	0.298
	Average	6.093	6.157	0.063
	Min	5.844	5.828	-0.038
	Std Dev	0.086	0.116	0.077



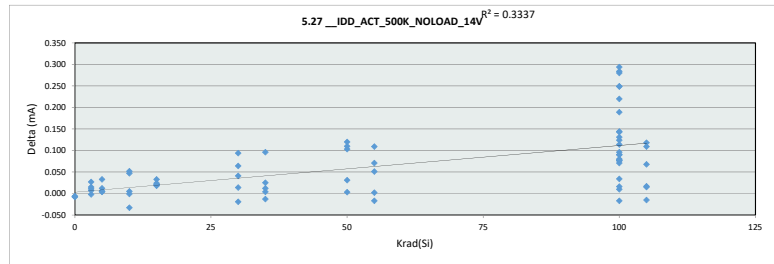
5.26_IDD_ACT_5	
Test Site	
Tester	
Test Number	
Max Limit	8 mA
Min Limit	8 mA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	6.013	6.097	6.007	5.917	6.063	5.828	6.001	6.126	6.050	6.038	6.039
Average	6.075	6.157	6.151	6.092	6.154	6.120	6.083	6.171	6.133	6.218	6.113
Max	6.124	6.206	6.222	6.248	6.252	6.257	6.187	6.207	6.252	6.467	6.165
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

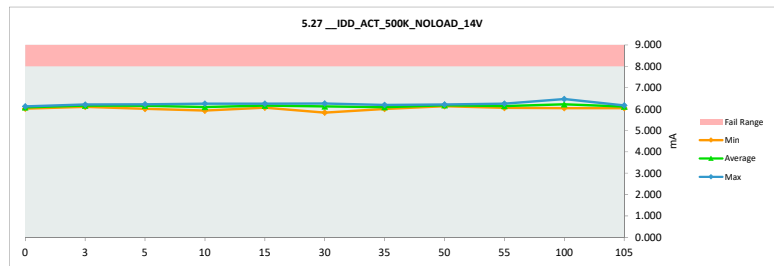


HDR TID Report  
TPS7H5004-SP

5.27 __IDD_ACT_500K_NOLOAD_14V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	8
			mA	8
Krad(Si)	Serial #	PRE	POST	Delta
0	295	6.025	6.017	-0.008
0	297	6.099	6.092	-0.007
0	298	6.131	6.125	-0.006
3	192	6.184	6.211	0.027
3	193	6.105	6.103	-0.002
3	194	6.144	6.155	0.011
3	196	6.183	6.198	0.015
3	197	6.132	6.139	0.007
5	200	6.180	6.213	0.033
5	202	6.179	6.184	0.005
5	203	6.135	6.140	0.005
5	204	5.997	6.009	0.012
5	206	6.221	6.225	0.004
10	207	6.202	6.254	0.052
10	209	6.081	6.086	0.005
10	210	6.091	6.138	0.047
10	211	5.959	5.926	-0.033
10	212	6.084	6.083	-0.001
15	214	6.102	6.120	0.018
15	217	6.191	6.211	0.020
15	218	6.043	6.067	0.024
15	219	6.222	6.255	0.033
15	222	6.116	6.137	0.021
30	224	6.134	6.228	0.094
30	225	6.106	6.170	0.064
30	226	5.852	5.833	-0.019
30	227	6.246	6.260	0.014
30	229	6.084	6.125	0.041
35	230	6.186	6.190	0.004
35	231	6.162	6.187	0.025
35	232	6.017	6.004	-0.013
35	234	5.910	6.006	0.096
35	237	6.035	6.047	0.012
50	272	6.059	6.179	0.120
50	275	6.100	6.131	0.031
50	276	6.109	6.212	0.103
50	277	6.162	6.165	0.003
50	278	6.076	6.186	0.110
55	279	6.046	6.155	0.109
55	280	6.116	6.167	0.051
55	281	6.184	6.255	0.071
55	282	6.051	6.053	0.002
55	283	6.078	6.061	-0.017
100	317	6.260	6.391	0.131
100	318	6.080	6.090	0.010
100	319	6.038	6.152	0.114
100	320	6.115	6.192	0.077
100	321	5.938	6.082	0.144
100	322	6.124	6.200	0.076
100	323	5.920	6.044	0.124
100	324	6.054	6.144	0.090
100	326	6.209	6.458	0.249
100	327	6.025	6.214	0.189
100	328	6.126	6.269	0.143
100	329	6.174	6.454	0.280
100	332	6.120	6.414	0.294
100	333	5.947	6.043	0.096
100	334	6.175	6.255	0.080
100	335	6.241	6.331	0.090
100	336	6.152	6.401	0.249
100	337	6.154	6.225	0.071
100	338	6.092	6.108	0.016
100	339	6.187	6.471	0.284
100	340	6.080	6.063	-0.017
100	341	6.026	6.104	0.078
100	342	5.943	6.163	0.220
100	343	6.043	6.077	0.034
105	344	6.108	6.125	0.017
105	346	6.101	6.169	0.068
105	349	6.028	6.043	0.015
105	350	6.148	6.133	-0.015
105	351	5.964	6.073	0.109
105	352	6.053	6.171	0.118
	Max	6.260	6.471	0.294
	Average	6.098	6.161	0.063
	Min	5.852	5.833	-0.033
	Std Dev	0.085	0.116	0.077



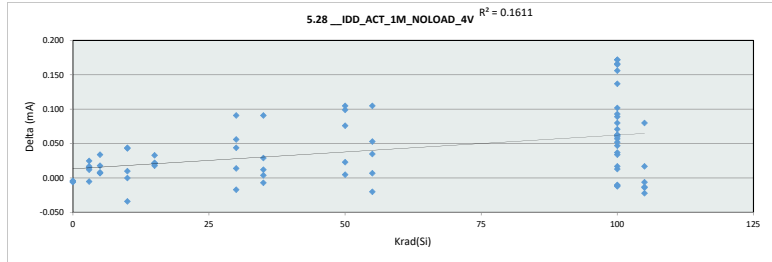
5.27 __IDD_ACT_5											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			8	mA							
				mA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.017	6.103	6.009	5.926	6.067	5.833	6.004	6.131	6.053	6.043	6.043
Average	6.078	6.161	6.154	6.097	6.158	6.123	6.087	6.175	6.138	6.223	6.119
Max	6.125	6.211	6.225	6.254	6.255	6.260	6.190	6.212	6.255	6.471	6.171
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000



HDR TID Report  
TPS7H5004-SP

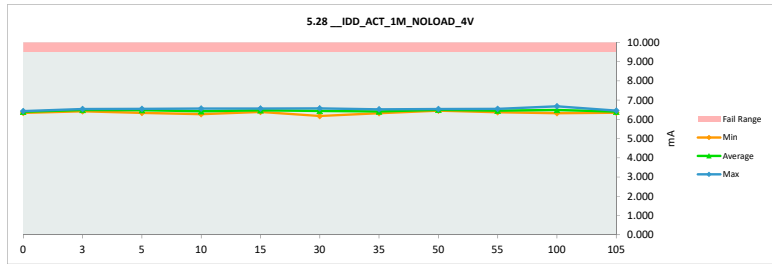
5.28_IDD_ACT_1M_NOLOAD_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	295	6.340	6.336	-0.004
0	297	6.424	6.418	-0.006
0	298	6.435	6.430	-0.005
3	192	6.506	6.531	0.025
3	193	6.421	6.416	-0.005
3	194	6.464	6.481	0.017
3	196	6.519	6.534	0.015
3	197	6.454	6.466	0.012
5	200	6.488	6.522	0.034
5	202	6.498	6.505	0.007
5	203	6.460	6.468	0.008
5	204	6.319	6.337	0.018
5	206	6.536	6.544	0.008
10	207	6.520	6.563	0.043
10	209	6.393	6.403	0.010
10	210	6.415	6.459	0.044
10	211	6.305	6.271	-0.034
10	212	6.406	6.406	0.000
15	214	6.425	6.446	0.021
15	217	6.495	6.517	0.022
15	218	6.363	6.384	0.021
15	219	6.531	6.564	0.033
15	222	6.432	6.450	0.018
30	224	6.432	6.523	0.091
30	225	6.422	6.478	0.056
30	226	6.191	6.174	-0.017
30	227	6.554	6.568	0.014
30	229	6.408	6.452	0.044
35	230	6.512	6.516	0.004
35	231	6.484	6.513	0.029
35	232	6.354	6.347	-0.007
35	234	6.229	6.320	0.091
35	237	6.357	6.369	0.012
50	272	6.376	6.452	0.076
50	275	6.432	6.455	0.023
50	276	6.437	6.536	0.099
50	277	6.496	6.501	0.005
50	278	6.406	6.511	0.105
55	279	6.351	6.456	0.105
55	280	6.467	6.520	0.053
55	281	6.513	6.548	0.035
55	282	6.380	6.387	0.007
55	283	6.393	6.373	-0.020
100	317	6.572	6.643	0.071
100	318	6.397	6.410	0.013
100	319	6.365	6.422	0.057
100	320	6.441	6.504	0.063
100	321	6.288	6.390	0.102
100	322	6.456	6.516	0.060
100	323	6.280	6.369	0.089
100	324	6.378	6.458	0.080
100	326	6.522	6.678	0.156
100	327	6.358	6.451	0.093
100	328	6.446	6.480	0.034
100	329	6.492	6.658	0.166
100	332	6.435	6.607	0.172
100	333	6.271	6.318	0.047
100	334	6.495	6.484	-0.011
100	335	6.549	6.611	0.062
100	336	6.476	6.613	0.137
100	337	6.462	6.514	0.052
100	338	6.412	6.402	-0.010
100	339	6.486	6.651	0.165
100	340	6.386	6.374	-0.012
100	341	6.352	6.389	0.037
100	342	6.268	6.440	0.172
100	343	6.370	6.387	0.017
105	344	6.442	6.420	-0.022
105	346	6.424	6.418	-0.006
105	349	6.358	6.345	-0.013
105	350	6.470	6.456	-0.014
105	351	6.296	6.376	0.080
105	352	6.363	6.380	0.017
	Max	6.572	6.678	0.172
	Average	6.420	6.460	0.041
	Min	6.191	6.174	-0.034
	Std Dev	0.080	0.094	0.050



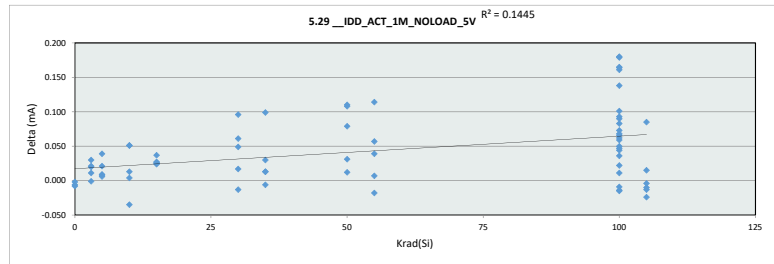
5.28_IDD_ACT_1	
Test Site	
Tester	
Test Number	
Max Limit	9.5 mA
Min Limit	9.5 mA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.336	6.416	6.337	6.271	6.384	6.174	6.320	6.452	6.373	6.318	6.345
Average	6.395	6.486	6.475	6.420	6.472	6.439	6.413	6.491	6.457	6.490	6.399
Max	6.430	6.534	6.544	6.563	6.564	6.568	6.516	6.536	6.548	6.678	6.456
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

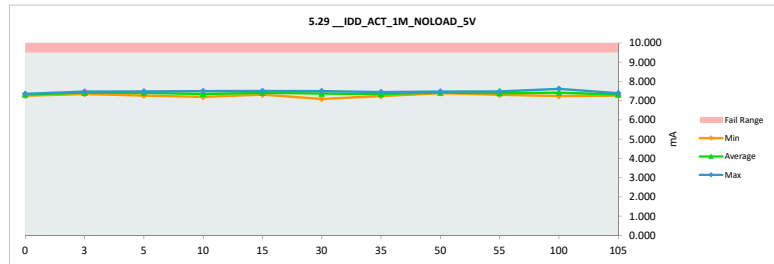


HDR TID Report  
TPS7H5004-SP

5.29_IDD_ACT_1M_NOLOAD_5V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	9.5
			mA	9.5
Krad(Si)	Serial #	PRE	POST	Delta
0	295	7.262	7.254	-0.008
0	297	7.353	7.347	-0.006
0	298	7.357	7.355	-0.002
3	192	7.441	7.471	0.030
3	193	7.350	7.349	-0.001
3	194	7.382	7.403	0.021
3	196	7.440	7.460	0.020
3	197	7.372	7.383	0.011
5	200	7.411	7.450	0.039
5	202	7.429	7.438	0.009
5	203	7.389	7.397	0.008
5	204	7.240	7.261	0.021
5	206	7.474	7.480	0.006
10	207	7.446	7.497	0.051
10	209	7.319	7.332	0.013
10	210	7.336	7.387	0.051
10	211	7.232	7.197	-0.035
10	212	7.332	7.336	0.004
15	214	7.348	7.373	0.025
15	217	7.418	7.445	0.027
15	218	7.283	7.310	0.027
15	219	7.464	7.501	0.037
15	222	7.360	7.384	0.024
30	224	7.362	7.458	0.096
30	225	7.348	7.409	0.061
30	226	7.103	7.090	-0.013
30	227	7.483	7.500	0.017
30	229	7.338	7.387	0.049
35	230	7.437	7.450	0.013
35	231	7.410	7.440	0.030
35	232	7.279	7.273	-0.006
35	234	7.143	7.242	0.099
35	237	7.279	7.292	0.013
50	272	7.306	7.385	0.079
50	275	7.359	7.390	0.031
50	276	7.366	7.474	0.108
50	277	7.422	7.434	0.012
50	278	7.332	7.442	0.110
55	279	7.267	7.381	0.114
55	280	7.394	7.451	0.057
55	281	7.442	7.481	0.039
55	282	7.307	7.314	0.007
55	283	7.325	7.307	-0.018
100	317	7.499	7.572	0.073
100	318	7.316	7.327	0.011
100	319	7.290	7.349	0.059
100	320	7.366	7.434	0.068
100	321	7.216	7.317	0.101
100	322	7.380	7.442	0.062
100	323	7.198	7.291	0.093
100	324	7.294	7.377	0.083
100	326	7.453	7.614	0.161
100	327	7.278	7.368	0.090
100	328	7.371	7.407	0.036
100	329	7.417	7.582	0.165
100	332	7.357	7.537	0.180
100	333	7.193	7.240	0.047
100	334	7.423	7.409	-0.014
100	335	7.482	7.548	0.066
100	336	7.408	7.546	0.138
100	337	7.391	7.441	0.050
100	338	7.339	7.330	-0.009
100	339	7.413	7.577	0.164
100	340	7.312	7.297	-0.015
100	341	7.272	7.316	0.044
100	342	7.183	7.362	0.179
100	343	7.282	7.304	0.022
105	344	7.372	7.348	-0.024
105	346	7.340	7.336	-0.004
105	349	7.275	7.262	-0.013
105	350	7.396	7.386	-0.010
105	351	7.211	7.296	0.085
105	352	7.282	7.297	0.015
	Max	7.499	7.614	0.180
	Average	7.345	7.388	0.043
	Min	7.103	7.090	-0.035
	Std Dev	0.083	0.097	0.051



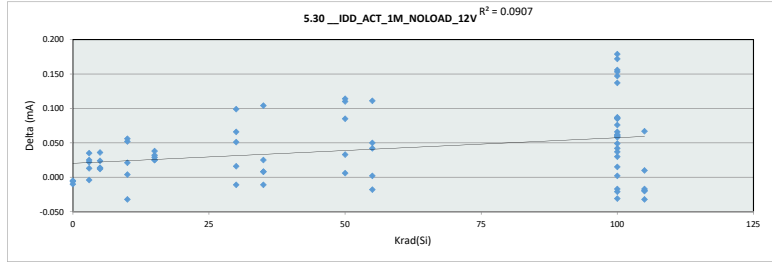
5.29_IDD_ACT_1											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			9.5 mA	mA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	7.254	7.349	7.261	7.197	7.310	7.090	7.242	7.385	7.307	7.240	7.262
Min	7.319	7.413	7.405	7.350	7.403	7.369	7.339	7.425	7.387	7.416	7.321
Average	7.355	7.471	7.480	7.497	7.501	7.500	7.450	7.474	7.481	7.614	7.386
Max	7.355	7.471	7.480	7.497	7.501	7.500	7.450	7.474	7.481	7.614	7.386
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



HDR TID Report  
TPS7H5004-SP

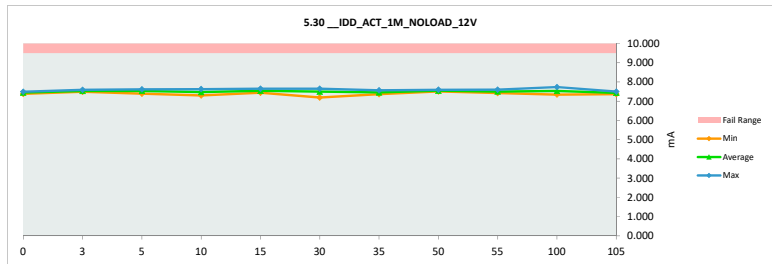
5.30 __IDD_ACT_1M_NOLOAD_12V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	295	7.398	7.388	-0.010
0	297	7.477	7.471	-0.006
0	298	7.499	7.494	-0.005
3	192	7.560	7.595	0.035
3	193	7.502	7.498	-0.004
3	194	7.499	7.521	0.022
3	196	7.542	7.567	0.025
3	197	7.492	7.505	0.013
5	200	7.555	7.591	0.036
5	202	7.561	7.573	0.012
5	203	7.515	7.529	0.014
5	204	7.368	7.392	0.024
5	206	7.606	7.619	0.013
10	207	7.576	7.632	0.056
10	209	7.462	7.483	0.021
10	210	7.463	7.515	0.052
10	211	7.337	7.305	-0.032
10	212	7.464	7.468	0.004
15	214	7.470	7.499	0.029
15	217	7.574	7.599	0.025
15	218	7.416	7.448	0.032
15	219	7.616	7.654	0.038
15	222	7.498	7.523	0.025
30	224	7.522	7.621	0.099
30	225	7.484	7.550	0.066
30	226	7.203	7.192	-0.011
30	227	7.638	7.654	0.016
30	229	7.454	7.505	0.051
35	230	7.561	7.569	0.008
35	231	7.535	7.560	0.025
35	232	7.379	7.368	-0.011
35	234	7.270	7.374	0.104
35	237	7.415	7.423	0.008
50	272	7.426	7.511	0.085
50	275	7.480	7.513	0.033
50	276	7.489	7.599	0.110
50	277	7.543	7.549	0.006
50	278	7.449	7.563	0.114
55	279	7.410	7.521	0.111
55	280	7.482	7.532	0.050
55	281	7.562	7.604	0.042
55	282	7.425	7.427	0.002
55	283	7.472	7.454	-0.018
100	317	7.631	7.697	0.066
100	318	7.448	7.450	0.002
100	319	7.411	7.471	0.060
100	320	7.482	7.544	0.062
100	321	7.304	7.391	0.087
100	322	7.487	7.547	0.060
100	323	7.287	7.373	0.086
100	324	7.428	7.504	0.076
100	326	7.591	7.738	0.147
100	327	7.389	7.474	0.085
100	328	7.491	7.521	0.030
100	329	7.549	7.702	0.153
100	332	7.483	7.655	0.172
100	333	7.307	7.349	0.042
100	334	7.542	7.521	-0.021
100	335	7.632	7.690	0.058
100	336	7.519	7.656	0.137
100	337	7.542	7.591	0.049
100	338	7.475	7.458	-0.017
100	339	7.567	7.723	0.156
100	340	7.465	7.434	-0.031
100	341	7.399	7.436	0.037
100	342	7.306	7.485	0.179
100	343	7.403	7.418	0.015
105	344	7.487	7.455	-0.032
105	346	7.465	7.448	-0.017
105	349	7.390	7.370	-0.020
105	350	7.521	7.502	-0.019
105	351	7.322	7.389	0.067
105	352	7.436	7.446	0.010
Max		7.638	7.738	0.179
Average		7.471	7.512	0.041
Min		7.203	7.192	-0.032
Std Dev		0.090	0.103	0.051



5.30 __IDD_ACT_1	
Test Site	
Tester	
Test Number	
Max Limit	9.5 mA
Min Limit	9.5 mA

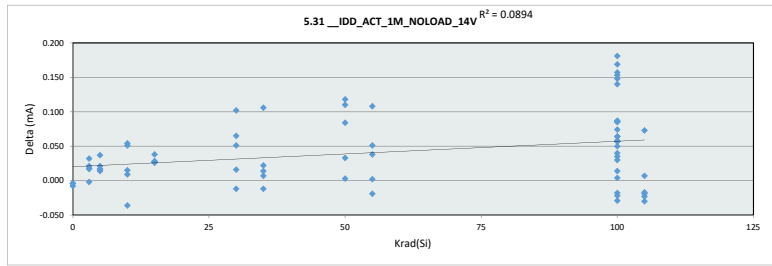
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.388	7.498	7.392	7.305	7.448	7.192	7.368	7.511	7.427	7.349	7.370
Average	7.451	7.537	7.541	7.481	7.545	7.504	7.459	7.547	7.508	7.535	7.435
Max	7.494	7.595	7.619	7.632	7.654	7.654	7.569	7.599	7.604	7.738	7.502
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



HDR TID Report  
TPS7H5004-SP

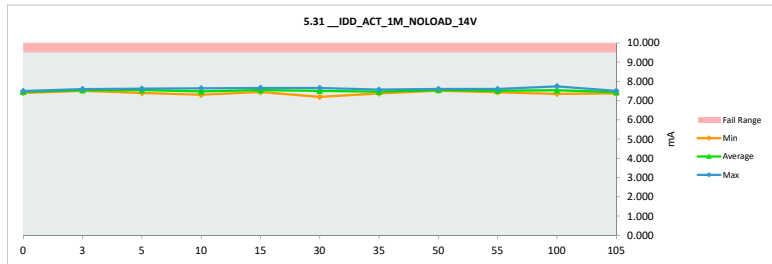
5.31_IDD_ACT_1M_NOLOAD_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	295	7.403	7.395	-0.008
0	297	7.480	7.476	-0.004
0	298	7.503	7.499	-0.004
3	192	7.568	7.600	0.032
3	193	7.505	7.503	-0.002
3	194	7.508	7.526	0.018
3	196	7.548	7.569	0.021
3	197	7.494	7.511	0.017
5	200	7.560	7.597	0.037
5	202	7.564	7.578	0.014
5	203	7.519	7.536	0.017
5	204	7.375	7.396	0.021
5	206	7.608	7.624	0.016
10	207	7.582	7.636	0.054
10	209	7.468	7.483	0.015
10	210	7.469	7.520	0.051
10	211	7.343	7.307	-0.036
10	212	7.464	7.473	0.009
15	214	7.474	7.502	0.028
15	217	7.580	7.606	0.026
15	218	7.422	7.450	0.028
15	219	7.621	7.659	0.038
15	222	7.498	7.525	0.027
30	224	7.525	7.627	0.102
30	225	7.489	7.554	0.065
30	226	7.209	7.197	-0.012
30	227	7.642	7.658	0.016
30	229	7.456	7.507	0.051
35	230	7.567	7.574	0.007
35	231	7.539	7.561	0.022
35	232	7.386	7.374	-0.012
35	234	7.273	7.379	0.106
35	237	7.416	7.430	0.014
50	272	7.434	7.518	0.084
50	275	7.483	7.516	0.033
50	276	7.496	7.606	0.110
50	277	7.551	7.554	0.003
50	278	7.453	7.571	0.118
55	279	7.416	7.524	0.108
55	280	7.486	7.537	0.051
55	281	7.569	7.607	0.038
55	282	7.432	7.434	0.002
55	283	7.477	7.458	-0.019
100	317	7.635	7.699	0.064
100	318	7.449	7.453	0.004
100	319	7.415	7.479	0.064
100	320	7.486	7.543	0.057
100	321	7.309	7.396	0.087
100	322	7.492	7.549	0.057
100	323	7.292	7.378	0.086
100	324	7.432	7.506	0.074
100	326	7.594	7.742	0.148
100	327	7.394	7.479	0.085
100	328	7.494	7.524	0.030
100	329	7.555	7.708	0.153
100	332	7.491	7.660	0.169
100	333	7.312	7.352	0.040
100	334	7.545	7.523	-0.022
100	335	7.638	7.696	0.058
100	336	7.522	7.662	0.140
100	337	7.543	7.593	0.050
100	338	7.480	7.462	-0.018
100	339	7.571	7.728	0.157
100	340	7.469	7.440	-0.029
100	341	7.404	7.439	0.035
100	342	7.310	7.491	0.181
100	343	7.407	7.421	0.014
105	344	7.489	7.459	-0.030
105	346	7.470	7.453	-0.017
105	349	7.394	7.375	-0.019
105	350	7.527	7.504	-0.023
105	351	7.323	7.396	0.073
105	352	7.441	7.448	0.007
	Max	7.642	7.742	0.181
	Average	7.476	7.517	0.041
	Min	7.209	7.197	-0.036
	Std Dev	0.090	0.103	0.051



5.31_IDD_ACT_1	
Test Site	
Tester	
Test Number	
Max Limit	9.5 mA
Min Limit	9.5 mA

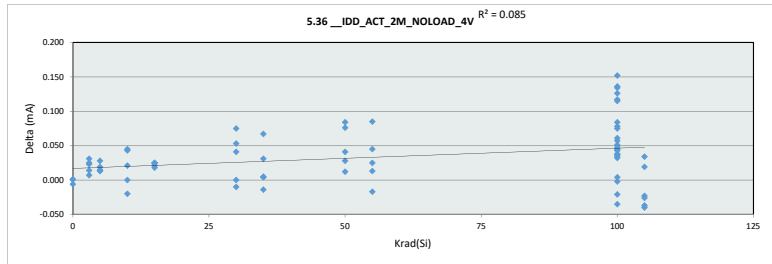
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.395	7.503	7.396	7.307	7.450	7.197	7.374	7.516	7.434	7.352	7.375
Average	7.457	7.542	7.546	7.484	7.548	7.509	7.464	7.553	7.512	7.538	7.439
Max	7.499	7.600	7.624	7.636	7.659	7.658	7.574	7.606	7.607	7.742	7.504
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



HDR TID Report  
TPS7H5004-SP

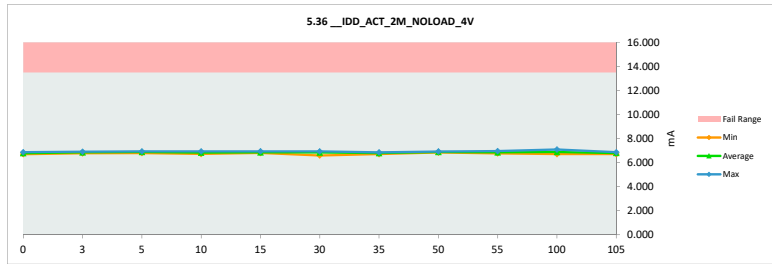
5.36 __IDD_ACT_2M_NOLOAD_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	13.5
Min Limit	13.5

Krad(Si)	Serial #	PRE	POST	Delta
0	295	6.707	6.701	-0.006
0	297	6.783	6.784	0.001
0	298	6.867	6.868	0.001
3	192	6.875	6.906	0.031
3	193	6.849	6.856	0.007
3	194	6.796	6.821	0.025
3	196	6.860	6.883	0.023
3	197	6.761	6.775	0.014
5	200	6.881	6.909	0.028
5	202	6.912	6.931	0.019
5	203	6.826	6.839	0.013
5	204	6.778	6.792	0.014
5	206	6.889	6.904	0.015
10	207	6.890	6.935	0.045
10	209	6.778	6.799	0.021
10	210	6.796	6.839	0.043
10	211	6.742	6.722	-0.020
10	212	6.833	6.833	0.000
15	214	6.813	6.834	0.021
15	217	6.829	6.853	0.024
15	218	6.774	6.799	0.025
15	219	6.912	6.937	0.025
15	222	6.819	6.837	0.018
30	224	6.836	6.911	0.075
30	225	6.856	6.897	0.041
30	226	6.600	6.590	-0.010
30	227	6.930	6.930	0.000
30	229	6.798	6.851	0.053
35	230	6.837	6.841	0.004
35	231	6.817	6.848	0.031
35	232	6.764	6.750	-0.014
35	234	6.637	6.704	0.067
35	237	6.777	6.782	0.005
50	272	6.826	6.867	0.041
50	275	6.813	6.841	0.028
50	276	6.816	6.892	0.076
50	277	6.868	6.880	0.012
50	278	6.843	6.927	0.084
55	279	6.765	6.850	0.085
55	280	6.836	6.881	0.045
55	281	6.933	6.958	0.025
55	282	6.751	6.764	0.013
55	283	6.804	6.787	-0.017
100	317	6.962	7.046	0.084
100	318	6.755	6.759	0.004
100	319	6.796	6.839	0.043
100	320	6.844	6.905	0.061
100	321	6.726	6.804	0.078
100	322	6.803	6.853	0.050
100	323	6.671	6.728	0.057
100	324	6.768	6.843	0.075
100	326	6.971	7.088	0.117
100	327	6.805	6.856	0.051
100	328	6.838	6.885	0.047
100	329	6.875	7.009	0.134
100	332	6.859	7.011	0.152
100	333	6.676	6.708	0.032
100	334	6.896	6.861	-0.035
100	335	6.928	6.965	0.037
100	336	6.876	6.991	0.115
100	337	6.864	6.902	0.038
100	338	6.802	6.800	-0.002
100	339	6.889	7.015	0.126
100	340	6.805	6.784	-0.021
100	341	6.743	6.787	0.044
100	342	6.700	6.836	0.136
100	343	6.741	6.776	0.035
105	344	6.902	6.865	-0.037
105	346	6.869	6.829	-0.040
105	349	6.731	6.708	-0.023
105	350	6.854	6.828	-0.026
105	351	6.705	6.739	0.034
105	352	6.752	6.771	0.019
	Max	6.971	7.088	0.152
	Average	6.815	6.848	0.033
	Min	6.600	6.590	-0.040
	Std Dev	0.074	0.088	0.042



5.36 __IDD_ACT_2	
Test Site	
Tester	
Test Number	
Max Limit	13.5 mA
Min Limit	13.5 mA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.701	6.775	6.792	6.722	6.799	6.590	6.704	6.841	6.764	6.708	6.708
Average	6.784	6.848	6.875	6.826	6.852	6.836	6.785	6.881	6.848	6.877	6.790
Max	6.868	6.906	6.931	6.935	6.937	6.930	6.848	6.927	6.958	7.088	6.865
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

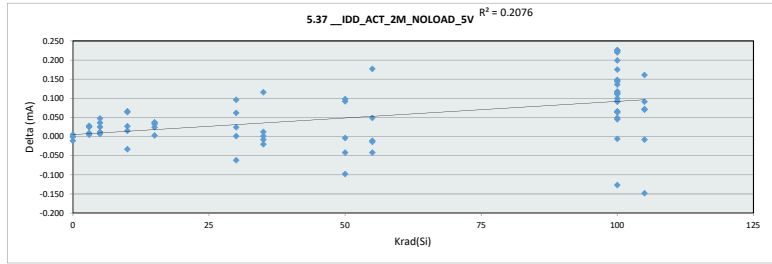




HDR TID Report  
TPS7H5004-SP

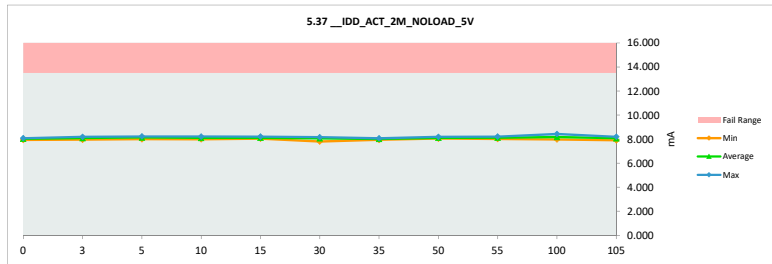
5.37 __IDD_ACT_2M_NOLOAD_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	13.5
Min Limit	13.5

Krad(Si)	Serial #	PRE	POST	Delta
0	295	7.945	7.934	-0.011
0	297	8.048	8.053	0.005
0	298	8.082	8.081	-0.001
3	192	8.168	8.194	0.026
3	193	8.078	8.083	0.005
3	194	8.045	8.073	0.028
3	196	8.108	8.133	0.025
3	197	7.941	7.950	0.009
5	200	8.104	8.151	0.047
5	202	8.198	8.223	0.025
5	203	8.117	8.124	0.007
5	204	7.956	7.992	0.036
5	206	8.178	8.190	0.012
10	207	8.149	8.215	0.066
10	209	8.051	8.078	0.027
10	210	8.058	8.122	0.064
10	211	8.019	7.986	-0.033
10	212	8.078	8.093	0.015
15	214	8.041	8.065	0.024
15	217	8.092	8.095	0.003
15	218	7.999	8.036	0.037
15	219	8.175	8.211	0.036
15	222	8.086	8.118	0.032
30	224	8.062	8.158	0.096
30	225	8.068	8.130	0.062
30	226	7.800	7.801	0.001
30	227	8.225	8.163	-0.062
30	229	8.057	8.081	0.024
35	230	8.054	8.034	-0.020
35	231	8.067	8.068	0.001
35	232	8.012	8.004	-0.008
35	234	7.820	7.936	0.116
35	237	8.027	8.039	0.012
50	272	8.030	8.122	0.092
50	275	8.094	8.052	-0.042
50	276	8.112	8.108	-0.004
50	277	8.145	8.047	-0.098
50	278	8.090	8.188	0.098
55	279	7.962	8.139	0.177
55	280	8.118	8.107	-0.011
55	281	8.222	8.208	-0.014
55	282	8.053	8.011	-0.042
55	283	8.068	8.117	0.049
100	317	8.209	8.356	0.147
100	318	7.999	8.090	0.091
100	319	8.022	8.134	0.112
100	320	8.102	8.213	0.111
100	321	7.958	8.133	0.175
100	322	8.067	8.215	0.148
100	323	7.949	8.085	0.136
100	324	8.030	8.174	0.144
100	326	8.205	8.425	0.220
100	327	8.012	8.130	0.118
100	328	8.091	8.184	0.093
100	329	8.136	8.009	-0.127
100	332	8.094	8.318	0.224
100	333	7.908	7.974	0.066
100	334	8.151	8.200	0.049
100	335	8.201	8.195	-0.006
100	336	8.138	8.337	0.199
100	337	8.128	8.228	0.100
100	338	8.049	8.113	0.064
100	339	8.145	8.371	0.226
100	340	8.017	8.062	0.045
100	341	7.995	8.109	0.114
100	342	7.877	8.103	0.226
100	343	7.979	8.043	0.064
105	344	8.137	8.129	-0.008
105	346	8.045	7.897	-0.148
105	349	7.989	8.059	0.070
105	350	8.127	8.199	0.072
105	351	7.898	8.059	0.161
105	352	8.005	8.096	0.091
	Max	8.225	8.425	0.226
	Average	8.061	8.114	0.053
	Min	7.800	7.801	-0.148
	Std Dev	0.089	0.107	0.078



5.37 __IDD_ACT_2	
Test Site	
Tester	
Test Number	
Max Limit	13.5 mA
Min Limit	13.5 mA

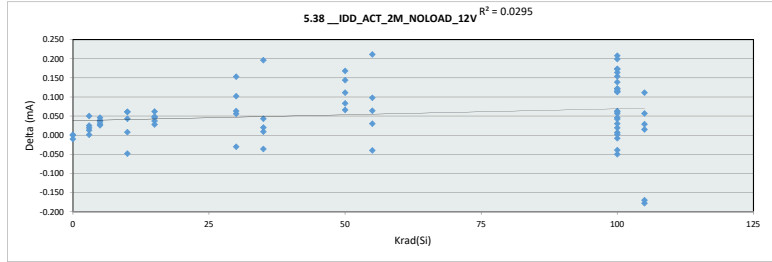
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.934	7.950	7.992	7.986	8.036	7.801	7.936	8.047	8.011	7.974	7.897
Average	8.023	8.087	8.136	8.099	8.105	8.067	8.016	8.103	8.116	8.175	8.073
Max	8.081	8.194	8.223	8.215	8.211	8.163	8.068	8.188	8.208	8.425	8.199
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



HDR TID Report  
TPS7H5004-SP

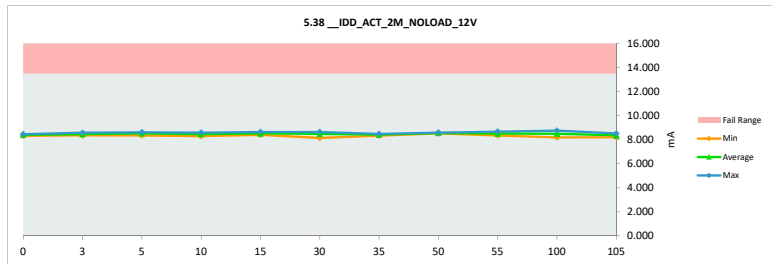
5.38 __IDD_ACT_2M_NOLOAD_12V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	13.5
Min Limit	13.5

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.335	8.325	-0.010
0	297	8.438	8.438	0.000
0	298	8.455	8.456	0.001
3	192	8.534	8.584	0.050
3	193	8.487	8.488	0.001
3	194	8.378	8.403	0.025
3	196	8.425	8.444	0.019
3	197	8.345	8.358	0.013
5	200	8.489	8.535	0.046
5	202	8.577	8.606	0.029
5	203	8.484	8.519	0.035
5	204	8.312	8.351	0.039
5	206	8.594	8.620	0.026
10	207	8.528	8.589	0.061
10	209	8.453	8.496	0.043
10	210	8.407	8.468	0.061
10	211	8.348	8.300	-0.048
10	212	8.442	8.450	0.008
15	214	8.386	8.448	0.062
15	217	8.547	8.575	0.028
15	218	8.368	8.405	0.037
15	219	8.596	8.645	0.049
15	222	8.469	8.515	0.046
30	224	8.493	8.646	0.153
30	225	8.454	8.556	0.102
30	226	8.088	8.144	0.056
30	227	8.637	8.607	-0.030
30	229	8.393	8.456	0.063
35	230	8.488	8.452	-0.036
35	231	8.473	8.482	0.009
35	232	8.315	8.335	0.020
35	234	8.164	8.360	0.196
35	237	8.404	8.447	0.043
50	272	8.391	8.535	0.144
50	275	8.436	8.547	0.111
50	276	8.462	8.528	0.066
50	277	8.499	8.582	0.083
50	278	8.425	8.593	0.168
55	279	8.349	8.560	0.211
55	280	8.394	8.354	-0.040
55	281	8.574	8.672	0.098
55	282	8.385	8.415	0.030
55	283	8.469	8.533	0.064
100	317	8.578	8.691	0.113
100	318	8.365	8.428	0.063
100	319	8.370	8.416	0.046
100	320	8.432	8.462	0.030
100	321	8.237	8.352	0.115
100	322	8.390	8.529	0.139
100	323	8.231	8.353	0.122
100	324	8.401	8.522	0.121
100	326	8.586	8.750	0.164
100	327	8.326	8.328	0.002
100	328	8.433	8.494	0.061
100	329	8.514	8.722	0.208
100	332	8.450	8.604	0.154
100	333	8.230	8.191	-0.039
100	334	8.491	8.510	0.019
100	335	8.615	8.730	0.115
100	336	8.474	8.647	0.173
100	337	8.542	8.584	0.042
100	338	8.431	8.438	0.007
100	339	8.555	8.754	0.199
100	340	8.416	8.366	-0.050
100	341	8.360	8.417	0.057
100	342	8.202	8.375	0.173
100	343	8.322	8.314	-0.008
105	344	8.473	8.303	-0.170
105	346	8.384	8.206	-0.178
105	349	8.317	8.374	0.057
105	350	8.488	8.517	0.029
105	351	8.206	8.317	0.111
105	352	8.423	8.438	0.015
	Max	8.637	8.754	0.211
	Average	8.424	8.479	0.055
	Min	8.088	8.144	-0.178
	Std Dev	0.109	0.131	0.075



5.38 __IDD_ACT_2	
Test Site	
Tester	
Test Number	
Max Limit	13.5 mA
Min Limit	13.5 mA

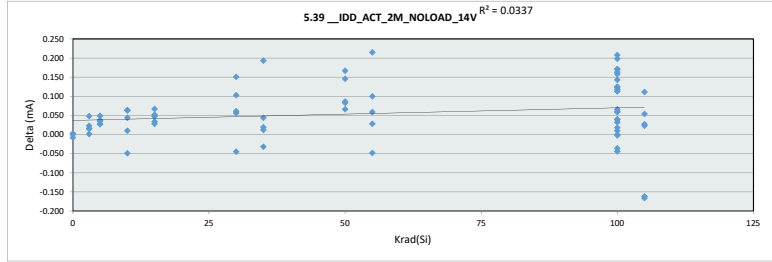
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.325	8.358	8.351	8.300	8.405	8.144	8.335	8.528	8.354	8.191	8.206
Average	8.406	8.455	8.526	8.461	8.518	8.482	8.415	8.557	8.507	8.499	8.359
Max	8.456	8.584	8.620	8.589	8.645	8.646	8.482	8.593	8.672	8.754	8.517
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



HDR TID Report  
TPS7H5004-SP

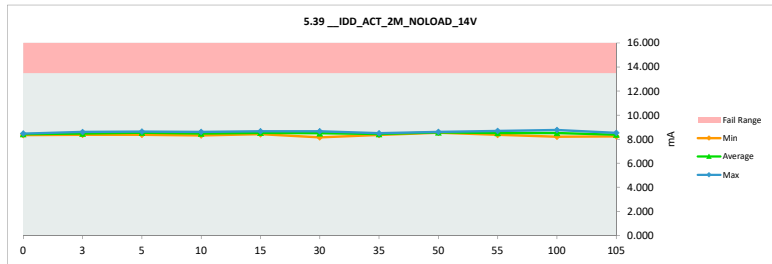
5.39 __IDD_ACT_2M_NOLOAD_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	13.5
Min Limit	13.5

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.338	8.330	-0.008
0	297	8.441	8.444	0.003
0	298	8.462	8.462	0.000
3	192	8.542	8.590	0.048
3	193	8.491	8.492	0.001
3	194	8.384	8.407	0.023
3	196	8.432	8.447	0.015
3	197	8.346	8.362	0.016
5	200	8.490	8.539	0.049
5	202	8.579	8.607	0.028
5	203	8.488	8.523	0.035
5	204	8.318	8.355	0.037
5	206	8.598	8.625	0.027
10	207	8.532	8.595	0.063
10	209	8.454	8.498	0.044
10	210	8.407	8.471	0.064
10	211	8.351	8.302	-0.049
10	212	8.444	8.454	0.010
15	214	8.386	8.453	0.067
15	217	8.551	8.579	0.028
15	218	8.372	8.406	0.034
15	219	8.597	8.649	0.052
15	222	8.472	8.519	0.047
30	224	8.498	8.649	0.151
30	225	8.458	8.561	0.103
30	226	8.092	8.148	0.056
30	227	8.656	8.611	-0.045
30	229	8.397	8.458	0.061
35	230	8.489	8.457	-0.032
35	231	8.475	8.487	0.012
35	232	8.322	8.341	0.019
35	234	8.172	8.365	0.193
35	237	8.408	8.452	0.044
50	272	8.393	8.539	0.146
50	275	8.462	8.548	0.086
50	276	8.465	8.531	0.066
50	277	8.500	8.583	0.083
50	278	8.429	8.596	0.167
55	279	8.350	8.565	0.215
55	280	8.402	8.354	-0.048
55	281	8.575	8.675	0.100
55	282	8.390	8.418	0.028
55	283	8.476	8.535	0.059
100	317	8.580	8.693	0.113
100	318	8.371	8.430	0.059
100	319	8.373	8.413	0.040
100	320	8.437	8.469	0.032
100	321	8.239	8.357	0.118
100	322	8.393	8.536	0.143
100	323	8.234	8.358	0.124
100	324	8.404	8.529	0.125
100	326	8.589	8.752	0.163
100	327	8.331	8.329	-0.002
100	328	8.436	8.502	0.066
100	329	8.518	8.726	0.208
100	332	8.455	8.613	0.158
100	333	8.235	8.199	-0.036
100	334	8.495	8.513	0.018
100	335	8.619	8.737	0.118
100	336	8.480	8.651	0.171
100	337	8.549	8.588	0.039
100	338	8.432	8.442	0.010
100	339	8.559	8.757	0.198
100	340	8.422	8.378	-0.044
100	341	8.364	8.424	0.060
100	342	8.209	8.380	0.171
100	343	8.321	8.321	0.000
105	344	8.477	8.315	-0.162
105	346	8.389	8.223	-0.166
105	349	8.321	8.375	0.054
105	350	8.493	8.520	0.027
105	351	8.211	8.322	0.111
105	352	8.427	8.450	0.023
	Max	8.656	8.757	0.215
	Average	8.428	8.483	0.055
	Min	8.092	8.148	-0.166
	Std Dev	0.109	0.130	0.074



5.39 __IDD_ACT_2	
Test Site	
Tester	
Test Number	
Max Limit	13.5 mA
Min Limit	13.5 mA

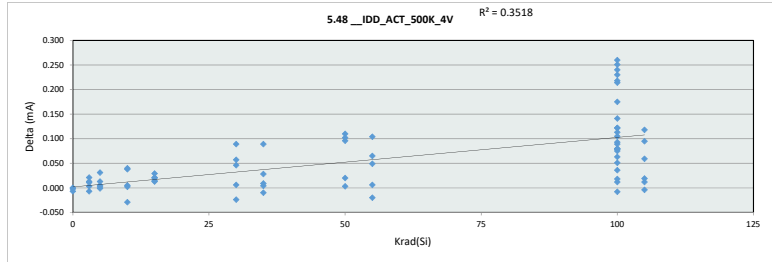
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.330	8.362	8.355	8.302	8.406	8.148	8.341	8.531	8.354	8.199	8.223
Average	8.412	8.460	8.530	8.464	8.521	8.485	8.420	8.559	8.509	8.504	8.368
Max	8.462	8.590	8.625	8.595	8.649	8.649	8.487	8.596	8.675	8.757	8.520
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



HDR TID Report  
TPS7H5004-SP

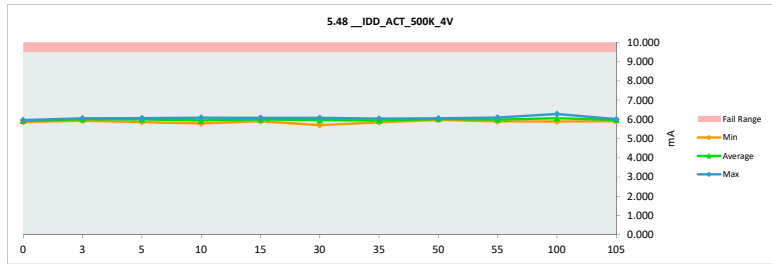
5.48 __IDD_ACT_500K_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.867	5.860	-0.007
0	297	5.945	5.945	0.000
0	298	5.964	5.962	-0.002
3	192	6.028	6.049	0.021
3	193	5.937	5.930	-0.007
3	194	6.000	6.013	0.013
3	196	6.039	6.050	0.011
3	197	5.985	5.988	0.003
5	200	6.017	6.048	0.031
5	202	6.018	6.023	0.005
5	203	5.981	5.983	0.002
5	204	5.838	5.851	0.013
5	206	6.061	6.060	-0.001
10	207	6.044	6.084	0.040
10	209	5.912	5.917	0.005
10	210	5.943	5.981	0.038
10	211	5.819	5.790	-0.029
10	212	5.929	5.931	0.002
15	214	5.949	5.962	0.013
15	217	6.021	6.042	0.021
15	218	5.885	5.905	0.020
15	219	6.052	6.081	0.029
15	222	5.961	5.975	0.014
30	224	5.955	6.044	0.089
30	225	5.940	5.997	0.057
30	226	5.720	5.696	-0.024
30	227	6.074	6.080	0.006
30	229	5.932	5.978	0.046
35	230	6.032	6.036	0.004
35	231	6.005	6.033	0.028
35	232	5.878	5.868	-0.010
35	234	5.753	5.842	0.089
35	237	5.879	5.888	0.009
50	272	5.906	6.016	0.110
50	275	5.953	5.973	0.020
50	276	5.956	6.052	0.096
50	277	6.015	6.018	0.003
50	278	5.929	6.031	0.102
55	279	5.882	5.986	0.104
55	280	5.985	6.034	0.049
55	281	6.032	6.097	0.065
55	282	5.902	5.908	0.006
55	283	5.913	5.893	-0.020
100	317	6.102	6.224	0.122
100	318	5.924	5.936	0.012
100	319	5.884	5.989	0.105
100	320	5.968	6.043	0.075
100	321	5.810	5.951	0.141
100	322	5.985	6.065	0.080
100	323	5.798	5.920	0.122
100	324	5.900	5.989	0.089
100	326	6.047	6.277	0.230
100	327	5.881	6.056	0.175
100	328	5.974	6.087	0.113
100	329	6.015	6.266	0.251
100	332	5.965	6.225	0.260
100	333	5.804	5.883	0.079
100	334	6.025	6.076	0.051
100	335	6.071	6.164	0.093
100	336	6.003	6.221	0.218
100	337	5.988	6.051	0.063
100	338	5.935	5.953	0.018
100	339	6.017	6.257	0.240
100	340	5.907	5.899	-0.008
100	341	5.874	5.953	0.079
100	342	5.790	6.004	0.214
100	343	5.890	5.926	0.036
105	344	5.963	5.975	0.012
105	346	5.949	6.008	0.059
105	349	5.881	5.900	0.019
105	350	5.998	5.994	-0.004
105	351	5.825	5.943	0.118
105	352	5.884	5.979	0.095
	Max	6.102	6.277	0.260
	Average	5.944	6.002	0.058
	Min	5.720	5.696	-0.029
	Std Dev	0.080	0.106	0.070



5.48 __IDD_ACT_5	
Test Site	
Tester	
Test Number	
Max Limit	9.5 mA
Min Limit	

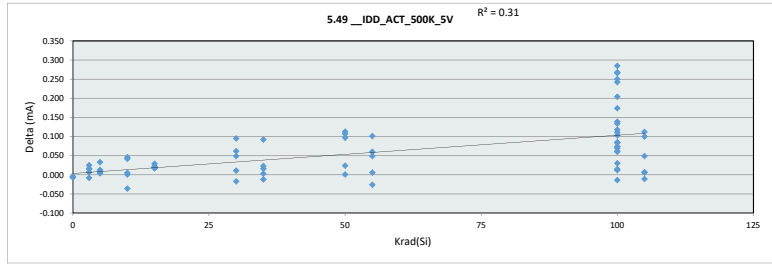
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	5.860	5.930	5.851	5.790	5.905	5.696	5.842	5.973	5.893	5.883	5.900
Average	5.922	6.006	5.993	5.941	5.993	5.959	5.933	6.018	5.984	6.059	5.967
Max	5.962	6.050	6.060	6.084	6.081	6.080	6.036	6.052	6.097	6.277	6.008
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



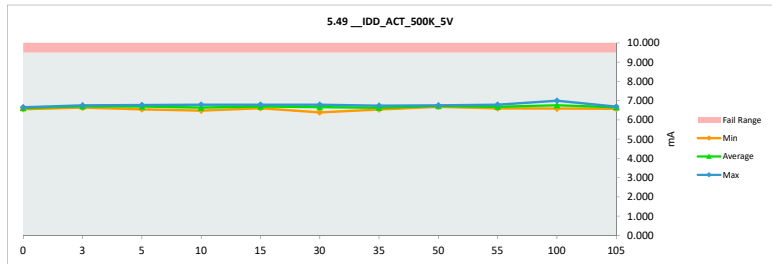
HDR TID Report  
TPS7H5004-SP

5.49 __IDD_ACT_500K_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	295	6.563	6.558	-0.005
0	297	6.645	6.640	-0.005
0	298	6.662	6.655	-0.007
3	192	6.731	6.756	0.025
3	193	6.643	6.635	-0.008
3	194	6.692	6.708	0.016
3	196	6.737	6.753	0.016
3	197	6.675	6.682	0.007
5	200	6.718	6.751	0.033
5	202	6.722	6.730	0.008
5	203	6.682	6.686	0.004
5	204	6.537	6.550	0.013
5	206	6.771	6.776	0.005
10	207	6.746	6.788	0.042
10	209	6.615	6.620	0.005
10	210	6.636	6.682	0.046
10	211	6.520	6.484	-0.036
10	212	6.637	6.638	0.001
15	214	6.649	6.669	0.020
15	217	6.723	6.741	0.018
15	218	6.582	6.604	0.022
15	219	6.757	6.786	0.029
15	222	6.663	6.680	0.017
30	224	6.658	6.753	0.095
30	225	6.644	6.706	0.062
30	226	6.406	6.389	-0.017
30	227	6.775	6.786	0.011
30	229	6.634	6.683	0.049
35	230	6.734	6.737	0.003
35	231	6.704	6.727	0.023
35	232	6.579	6.567	-0.012
35	234	6.445	6.537	0.092
35	237	6.575	6.591	0.016
50	272	6.610	6.723	0.113
50	275	6.654	6.678	0.024
50	276	6.661	6.758	0.097
50	277	6.721	6.722	0.001
50	278	6.630	6.737	0.107
55	279	6.582	6.683	0.101
55	280	6.684	6.733	0.049
55	281	6.732	6.792	0.060
55	282	6.604	6.610	0.006
55	283	6.621	6.595	-0.026
100	317	6.804	6.922	0.118
100	318	6.616	6.628	0.012
100	319	6.583	6.686	0.103
100	320	6.666	6.739	0.073
100	321	6.509	6.648	0.139
100	322	6.683	6.753	0.070
100	323	6.494	6.605	0.111
100	324	6.592	6.676	0.084
100	326	6.753	6.995	0.242
100	327	6.580	6.754	0.174
100	328	6.670	6.804	0.134
100	329	6.711	6.979	0.268
100	332	6.662	6.947	0.285
100	333	6.500	6.585	0.085
100	334	6.721	6.783	0.062
100	335	6.777	6.861	0.084
100	336	6.706	6.956	0.250
100	337	6.689	6.749	0.060
100	338	6.636	6.651	0.015
100	339	6.717	6.983	0.266
100	340	6.607	6.593	-0.014
100	341	6.567	6.641	0.074
100	342	6.485	6.689	0.204
100	343	6.581	6.611	0.030
105	344	6.664	6.671	0.007
105	346	6.643	6.692	0.049
105	349	6.574	6.580	0.006
105	350	6.700	6.689	-0.011
105	351	6.519	6.619	0.100
105	352	6.575	6.687	0.112
	Max	6.804	6.995	0.285
	Average	6.643	6.702	0.059
	Min	6.406	6.389	-0.036
	Std Dev	0.083	0.112	0.074

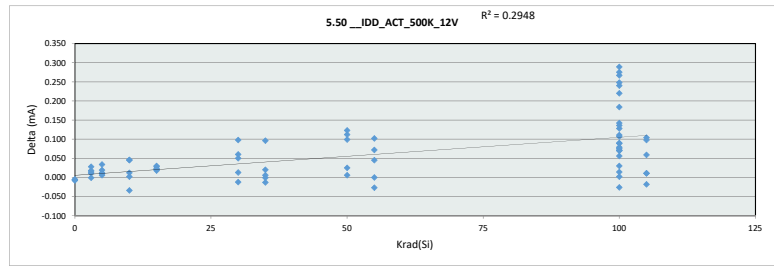


5.49 __IDD_ACT_5												
Test Site												
Tester												
Test Number												
Max Limit	9.5	mA										
Min Limit		mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	6.558	6.635	6.550	6.484	6.604	6.389	6.537	6.678	6.595	6.585	6.580	
Average	6.618	6.707	6.699	6.642	6.696	6.663	6.632	6.724	6.683	6.760	6.656	
Max	6.655	6.756	6.776	6.788	6.786	6.786	6.737	6.758	6.792	6.995	6.692	
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	

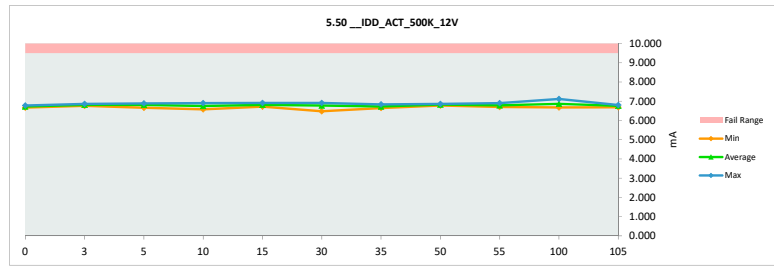


HDR TID Report  
TPS7H5004-SP

5.50 __IDD_ACT_500K_12V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	mA
			9.5	9.5
Krad(Si)	Serial #	PRE	POST	Delta
0	295	6.678	6.670	-0.008
0	297	6.751	6.746	-0.005
0	298	6.781	6.775	-0.006
3	192	6.835	6.863	0.028
3	193	6.763	6.762	-0.001
3	194	6.791	6.809	0.018
3	196	6.826	6.840	0.014
3	197	6.774	6.785	0.011
5	200	6.833	6.867	0.034
5	202	6.831	6.843	0.012
5	203	6.790	6.796	0.006
5	204	6.641	6.660	0.019
5	206	6.880	6.890	0.010
10	207	6.856	6.902	0.046
10	209	6.732	6.744	0.012
10	210	6.739	6.784	0.045
10	211	6.611	6.577	-0.034
10	212	6.740	6.742	0.002
15	214	6.748	6.769	0.021
15	217	6.848	6.871	0.023
15	218	6.692	6.720	0.028
15	219	6.882	6.912	0.030
15	222	6.774	6.792	0.018
30	224	6.789	6.887	0.098
30	225	6.759	6.819	0.060
30	226	6.488	6.476	-0.012
30	227	6.902	6.915	0.013
30	229	6.734	6.784	0.050
35	230	6.835	6.834	-0.001
35	231	6.810	6.830	0.020
35	232	6.663	6.650	-0.013
35	234	6.553	6.649	0.096
35	237	6.689	6.695	0.006
50	272	6.716	6.839	0.123
50	275	6.753	6.778	0.025
50	276	6.763	6.862	0.099
50	277	6.816	6.822	0.006
50	278	6.724	6.836	0.112
55	279	6.695	6.797	0.102
55	280	6.762	6.807	0.045
55	281	6.834	6.906	0.072
55	282	6.704	6.704	0.000
55	283	6.738	6.711	-0.027
100	317	6.915	7.043	0.128
100	318	6.725	6.727	0.002
100	319	6.689	6.800	0.111
100	320	6.763	6.834	0.071
100	321	6.587	6.729	0.142
100	322	6.775	6.846	0.071
100	323	6.572	6.679	0.107
100	324	6.700	6.779	0.079
100	326	6.864	7.112	0.248
100	327	6.671	6.855	0.184
100	328	6.769	6.905	0.136
100	329	6.825	7.092	0.267
100	332	6.770	7.059	0.289
100	333	6.597	6.686	0.089
100	334	6.820	6.890	0.070
100	335	6.899	6.989	0.090
100	336	6.807	7.047	0.240
100	337	6.814	6.870	0.056
100	338	6.748	6.762	0.014
100	339	6.844	7.119	0.275
100	340	6.731	6.705	-0.026
100	341	6.675	6.751	0.076
100	342	6.585	6.805	0.220
100	343	6.683	6.713	0.030
105	344	6.761	6.772	0.011
105	346	6.745	6.804	0.059
105	349	6.673	6.684	0.011
105	350	6.802	6.784	-0.018
105	351	6.610	6.708	0.098
105	352	6.703	6.807	0.104
	Max	6.915	7.119	0.289
	Average	6.749	6.809	0.061
	Min	6.488	6.476	-0.034
	Std Dev	0.088	0.117	0.075



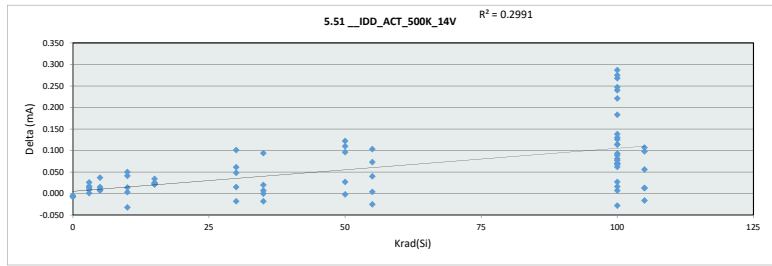
5.50 __IDD_ACT_5											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			9.5 mA	mA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	6.670	6.762	6.660	6.577	6.720	6.476	6.649	6.778	6.704	6.679	6.684
Average	6.730	6.812	6.811	6.750	6.813	6.776	6.732	6.827	6.785	6.867	6.760
Max	6.775	6.863	6.890	6.902	6.912	6.915	6.834	6.862	6.906	7.119	6.807
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



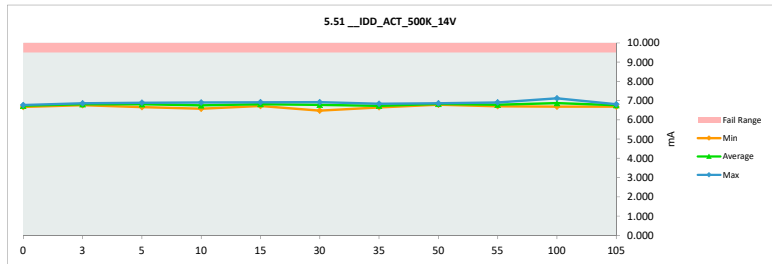
HDR TID Report  
TPS7H5004-SP

5.51_IDD_ACT_500K_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	6.680	6.675	-0.005
0	297	6.756	6.750	-0.006
0	298	6.784	6.776	-0.008
3	192	6.841	6.867	0.026
3	193	6.767	6.768	0.001
3	194	6.796	6.810	0.014
3	196	6.829	6.846	0.017
3	197	6.781	6.790	0.009
5	200	6.837	6.874	0.037
5	202	6.838	6.847	0.009
5	203	6.790	6.797	0.007
5	204	6.649	6.664	0.015
5	206	6.884	6.893	0.009
10	207	6.862	6.903	0.041
10	209	6.736	6.750	0.014
10	210	6.742	6.792	0.050
10	211	6.615	6.583	-0.032
10	212	6.747	6.750	0.003
15	214	6.751	6.776	0.025
15	217	6.856	6.877	0.021
15	218	6.699	6.722	0.023
15	219	6.884	6.918	0.034
15	222	6.776	6.798	0.022
30	224	6.792	6.893	0.101
30	225	6.764	6.825	0.061
30	226	6.497	6.479	-0.018
30	227	6.905	6.920	0.015
30	229	6.740	6.788	0.048
35	230	6.841	6.841	0.000
35	231	6.815	6.835	0.020
35	232	6.670	6.652	-0.018
35	234	6.559	6.653	0.094
35	237	6.693	6.700	0.007
50	272	6.721	6.843	0.122
50	275	6.759	6.786	0.027
50	276	6.772	6.868	0.096
50	277	6.827	6.825	-0.002
50	278	6.730	6.840	0.110
55	279	6.702	6.805	0.103
55	280	6.769	6.809	0.040
55	281	6.838	6.911	0.073
55	282	6.707	6.711	0.004
55	283	6.744	6.719	-0.025
100	317	6.920	7.046	0.126
100	318	6.727	6.734	0.007
100	319	6.690	6.804	0.114
100	320	6.769	6.839	0.070
100	321	6.593	6.731	0.138
100	322	6.779	6.849	0.070
100	323	6.574	6.688	0.114
100	324	6.704	6.785	0.081
100	326	6.872	7.119	0.247
100	327	6.678	6.861	0.183
100	328	6.778	6.908	0.130
100	329	6.826	7.094	0.268
100	332	6.773	7.060	0.287
100	333	6.603	6.696	0.093
100	334	6.826	6.894	0.068
100	335	6.906	6.995	0.089
100	336	6.811	7.051	0.240
100	337	6.817	6.879	0.062
100	338	6.754	6.770	0.016
100	339	6.847	7.122	0.275
100	340	6.736	6.708	-0.028
100	341	6.678	6.755	0.077
100	342	6.589	6.810	0.221
100	343	6.687	6.714	0.027
105	344	6.766	6.779	0.013
105	346	6.751	6.807	0.056
105	349	6.676	6.689	0.013
105	350	6.806	6.790	-0.016
105	351	6.615	6.713	0.098
105	352	6.707	6.814	0.107
	Max	6.920	7.122	0.287
	Average	6.753	6.814	0.061
	Min	6.497	6.479	-0.032
	Std Dev	0.088	0.117	0.075

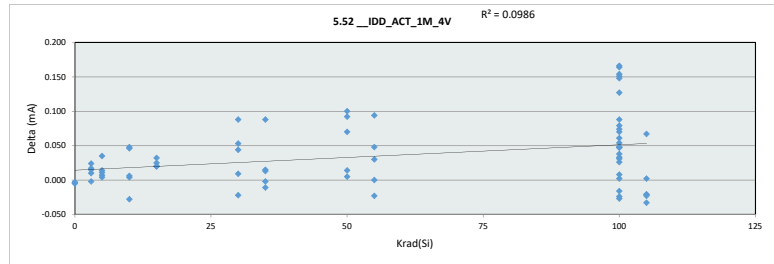


5.51_IDD_ACT_5		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		6.675	6.768	6.664	6.583	6.722	6.479	6.652	6.786	6.711	6.688	6.689
Average		6.734	6.816	6.815	6.756	6.818	6.781	6.736	6.832	6.791	6.871	6.765
Max		6.776	6.867	6.893	6.903	6.918	6.920	6.841	6.868	6.911	7.122	6.814
UL		9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

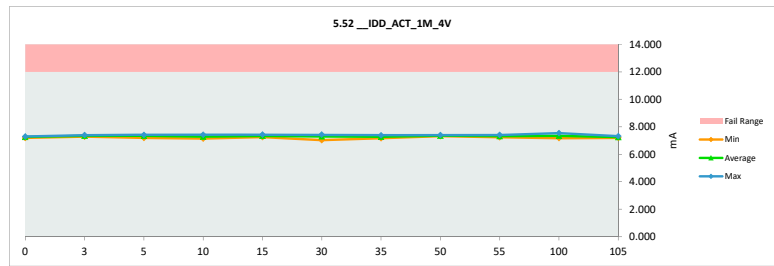


HDR TID Report  
TPS7H5004-SP

5.52_IDD_ACT_1M_4V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	mA
			12	12
Krad(Si)	Serial #	PRE	POST	Delta
0	295	7.204	7.199	-0.005
0	297	7.293	7.290	-0.003
0	298	7.300	7.296	-0.004
3	192	7.375	7.399	0.024
3	193	7.291	7.289	-0.002
3	194	7.329	7.346	0.017
3	196	7.378	7.393	0.015
3	197	7.312	7.322	0.010
5	200	7.354	7.389	0.035
5	202	7.369	7.376	0.007
5	203	7.329	7.340	0.011
5	204	7.184	7.198	0.014
5	206	7.411	7.415	0.004
10	207	7.384	7.430	0.046
10	209	7.261	7.267	0.006
10	210	7.281	7.329	0.048
10	211	7.175	7.147	-0.028
10	212	7.277	7.281	0.004
15	214	7.290	7.310	0.020
15	217	7.365	7.385	0.020
15	218	7.227	7.252	0.025
15	219	7.403	7.435	0.032
15	222	7.305	7.325	0.020
30	224	7.303	7.391	0.088
30	225	7.289	7.342	0.053
30	226	7.055	7.033	-0.022
30	227	7.425	7.434	0.009
30	229	7.278	7.322	0.044
35	230	7.383	7.381	-0.002
35	231	7.349	7.364	0.015
35	232	7.223	7.212	-0.011
35	234	7.086	7.174	0.088
35	237	7.224	7.237	0.013
50	272	7.250	7.320	0.070
50	275	7.304	7.318	0.014
50	276	7.309	7.401	0.092
50	277	7.365	7.370	0.005
50	278	7.277	7.377	0.100
55	279	7.217	7.311	0.094
55	280	7.333	7.381	0.048
55	281	7.382	7.412	0.030
55	282	7.253	7.253	0.000
55	283	7.264	7.241	-0.023
100	317	7.444	7.505	0.061
100	318	7.258	7.260	0.002
100	319	7.232	7.280	0.048
100	320	7.309	7.357	0.048
100	321	7.162	7.250	0.088
100	322	7.328	7.375	0.047
100	323	7.154	7.228	0.074
100	324	7.242	7.312	0.070
100	326	7.395	7.543	0.148
100	327	7.224	7.303	0.079
100	328	7.315	7.341	0.026
100	329	7.361	7.515	0.154
100	332	7.302	7.466	0.164
100	333	7.139	7.177	0.038
100	334	7.362	7.338	-0.024
100	335	7.423	7.477	0.054
100	336	7.346	7.473	0.127
100	337	7.335	7.368	0.033
100	338	7.283	7.267	-0.016
100	339	7.358	7.509	0.151
100	340	7.254	7.227	-0.027
100	341	7.216	7.247	0.031
100	342	7.126	7.292	0.166
100	343	7.228	7.236	0.008
105	344	7.315	7.282	-0.033
105	346	7.284	7.263	-0.021
105	349	7.221	7.200	-0.021
105	350	7.345	7.322	-0.023
105	351	7.161	7.228	0.067
105	352	7.228	7.230	0.002
	Max	7.444	7.543	0.166
	Average	7.288	7.323	0.035
	Min	7.055	7.033	-0.033
	Std Dev	0.082	0.095	0.049



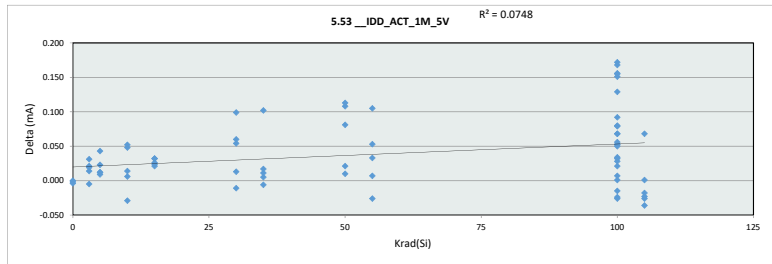
5.52_IDD_ACT_1											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			12 mA	mA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.199	7.289	7.198	7.147	7.252	7.033	7.174	7.318	7.241	7.177	7.200
Average	7.262	7.350	7.344	7.291	7.341	7.304	7.274	7.357	7.320	7.348	7.254
Max	7.296	7.399	7.415	7.430	7.435	7.434	7.381	7.401	7.412	7.543	7.322
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000



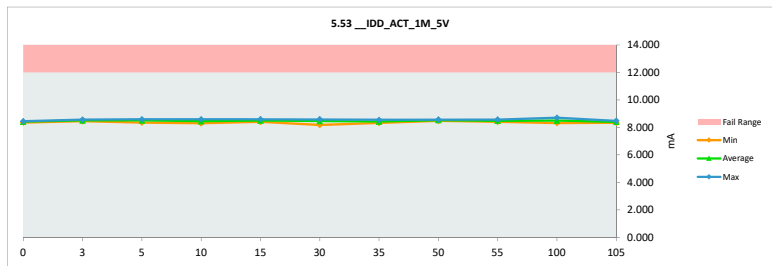


HDR TID Report  
TPS7H5004-SP

5.53_IDD_ACT_1M_5V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			mA	12	12
Krad(Si)	Serial #	PRE	POST	Delta	
0	295	8.351	8.347	-0.004	
0	297	8.446	8.446	0.000	
0	298	8.440	8.438	-0.002	
3	192	8.535	8.566	0.031	
3	193	8.449	8.444	-0.005	
3	194	8.472	8.493	0.021	
3	196	8.524	8.543	0.019	
3	197	8.453	8.467	0.014	
5	200	8.500	8.543	0.043	
5	202	8.526	8.538	0.012	
5	203	8.487	8.499	0.012	
5	204	8.327	8.350	0.023	
5	206	8.578	8.587	0.009	
10	207	8.536	8.584	0.048	
10	209	8.411	8.425	0.014	
10	210	8.427	8.479	0.052	
10	211	8.325	8.296	-0.029	
10	212	8.436	8.442	0.006	
15	214	8.439	8.460	0.021	
15	217	8.516	8.540	0.024	
15	218	8.371	8.403	0.032	
15	219	8.562	8.594	0.032	
15	222	8.458	8.484	0.026	
30	224	8.456	8.555	0.099	
30	225	8.439	8.499	0.060	
30	226	8.186	8.175	-0.011	
30	227	8.579	8.592	0.013	
30	229	8.434	8.488	0.054	
35	230	8.534	8.539	0.005	
35	231	8.499	8.516	0.017	
35	232	8.366	8.360	-0.006	
35	234	8.221	8.323	0.102	
35	237	8.374	8.385	0.011	
50	272	8.406	8.487	0.081	
50	275	8.461	8.482	0.021	
50	276	8.462	8.570	0.108	
50	277	8.520	8.530	0.010	
50	278	8.427	8.540	0.113	
55	279	8.353	8.458	0.105	
55	280	8.488	8.541	0.053	
55	281	8.536	8.569	0.033	
55	282	8.406	8.413	0.007	
55	283	8.427	8.401	-0.026	
100	317	8.591	8.659	0.068	
100	318	8.395	8.396	0.001	
100	319	8.379	8.432	0.053	
100	320	8.453	8.509	0.056	
100	321	8.311	8.403	0.092	
100	322	8.475	8.527	0.052	
100	323	8.299	8.379	0.080	
100	324	8.383	8.451	0.068	
100	326	8.551	8.702	0.151	
100	327	8.369	8.448	0.079	
100	328	8.462	8.483	0.021	
100	329	8.508	8.663	0.155	
100	332	8.446	8.618	0.172	
100	333	8.286	8.314	0.028	
100	334	8.509	8.483	-0.026	
100	335	8.585	8.635	0.050	
100	336	8.500	8.629	0.129	
100	337	8.487	8.521	0.034	
100	338	8.433	8.418	-0.015	
100	339	8.506	8.662	0.156	
100	340	8.398	8.374	-0.024	
100	341	8.361	8.393	0.032	
100	342	8.265	8.433	0.168	
100	343	8.363	8.370	0.007	
105	344	8.467	8.431	-0.036	
105	346	8.421	8.398	-0.023	
105	349	8.363	8.337	-0.026	
105	350	8.495	8.477	-0.018	
105	351	8.298	8.366	0.068	
105	352	8.369	8.370	0.001	
	Max	8.591	8.702	0.172	
	Average	8.437	8.475	0.038	
	Min	8.186	8.175	-0.036	
	Std Dev	0.086	0.099	0.050	



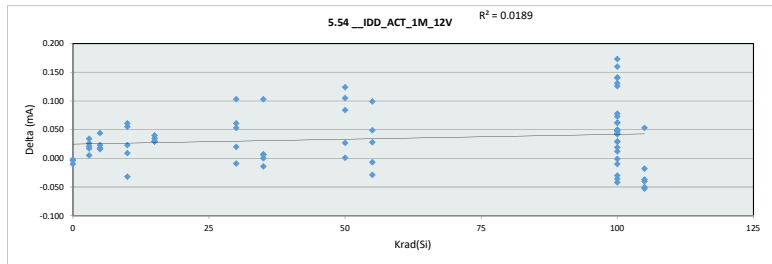
5.53_IDD_ACT_1											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			12	12							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	8.347	8.444	8.350	8.296	8.403	8.175	8.323	8.482	8.401	8.314	8.337
Min	8.410	8.503	8.503	8.445	8.496	8.462	8.425	8.522	8.476	8.496	8.397
Average	8.446	8.566	8.587	8.584	8.594	8.592	8.539	8.570	8.569	8.702	8.477
Max	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000



HDR TID Report  
TPS7H5004-SP

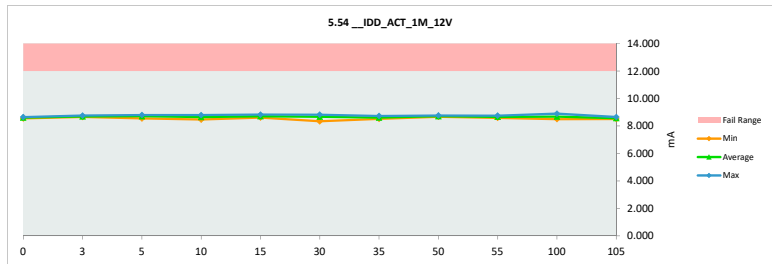
5.54_IDD_ACT_1M_12V	
Test Site	
Tester	
Test Number	
Unit	mA      mA
Max Limit	12      12
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.561	8.551	-0.010
0	297	8.642	8.640	-0.002
0	298	8.654	8.650	-0.004
3	192	8.726	8.760	0.034
3	193	8.670	8.675	0.005
3	194	8.653	8.679	0.026
3	196	8.690	8.711	0.021
3	197	8.639	8.656	0.017
5	200	8.715	8.759	0.044
5	202	8.727	8.745	0.018
5	203	8.683	8.700	0.017
5	204	8.528	8.551	0.023
5	206	8.783	8.799	0.016
10	207	8.739	8.794	0.055
10	209	8.626	8.649	0.023
10	210	8.615	8.676	0.061
10	211	8.499	8.467	-0.032
10	212	8.630	8.639	0.009
15	214	8.624	8.653	0.029
15	217	8.746	8.776	0.030
15	218	8.575	8.610	0.035
15	219	8.787	8.827	0.040
15	222	8.662	8.692	0.030
30	224	8.695	8.798	0.103
30	225	8.652	8.713	0.061
30	226	8.348	8.339	-0.009
30	227	8.803	8.823	0.020
30	229	8.617	8.670	0.053
35	230	8.727	8.727	0.000
35	231	8.693	8.700	0.007
35	232	8.534	8.520	-0.014
35	234	8.419	8.522	0.103
35	237	8.576	8.582	0.006
50	272	8.600	8.684	0.084
50	275	8.643	8.670	0.027
50	276	8.657	8.762	0.105
50	277	8.709	8.710	0.001
50	278	8.607	8.731	0.124
55	279	8.569	8.668	0.099
55	280	8.633	8.682	0.049
55	281	8.726	8.754	0.028
55	282	8.595	8.588	-0.007
55	283	8.644	8.615	-0.029
100	317	8.793	8.843	0.050
100	318	8.596	8.586	-0.010
100	319	8.571	8.620	0.049
100	320	8.638	8.680	0.042
100	321	8.461	8.539	0.078
100	322	8.644	8.691	0.047
100	323	8.449	8.511	0.062
100	324	8.582	8.644	0.062
100	326	8.762	8.893	0.131
100	327	8.544	8.617	0.073
100	328	8.648	8.660	0.012
100	329	8.713	8.853	0.140
100	332	8.643	8.803	0.160
100	333	8.464	8.493	0.029
100	334	8.695	8.659	-0.036
100	335	8.810	8.853	0.043
100	336	8.682	8.808	0.126
100	337	8.714	8.743	0.029
100	338	8.646	8.616	-0.030
100	339	8.737	8.878	0.141
100	340	8.627	8.585	-0.042
100	341	8.561	8.580	0.019
100	342	8.454	8.627	0.173
100	343	8.551	8.550	-0.001
105	344	8.648	8.595	-0.053
105	346	8.615	8.565	-0.050
105	349	8.544	8.507	-0.037
105	350	8.689	8.649	-0.040
105	351	8.473	8.526	0.053
105	352	8.600	8.582	-0.018
	Max	8.810	8.893	0.173
	Average	8.633	8.667	0.034
	Min	8.348	8.339	-0.053
	Std Dev	0.095	0.108	0.051



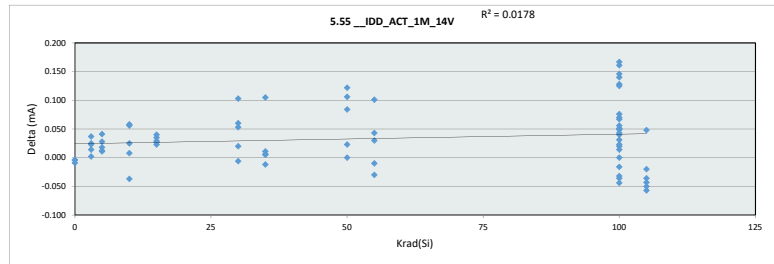
5.54_IDD_ACT_1	
Test Site	
Tester	
Test Number	
Max Limit	12 mA
Min Limit	mA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.551	8.656	8.551	8.467	8.610	8.339	8.520	8.670	8.588	8.493	8.507
Average	8.614	8.696	8.711	8.645	8.712	8.669	8.610	8.711	8.661	8.681	8.571
Max	8.650	8.760	8.799	8.794	8.827	8.823	8.727	8.762	8.754	8.893	8.649
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

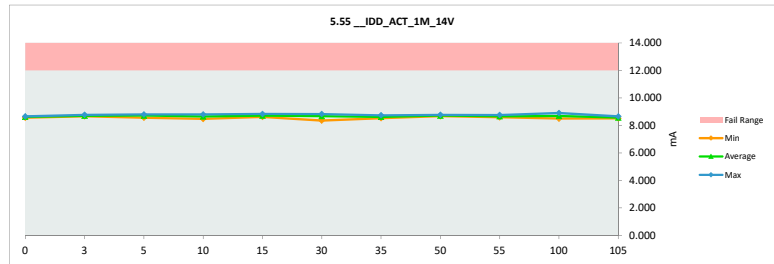


HDR TID Report  
TPS7H5004-SP

5.55_IDD_ACT_1M_14V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	mA
			12	12
Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.565	8.556	-0.009
0	297	8.649	8.645	-0.004
0	298	8.658	8.654	-0.004
3	192	8.732	8.769	0.037
3	193	8.678	8.680	0.002
3	194	8.657	8.682	0.025
3	196	8.694	8.717	0.023
3	197	8.649	8.663	0.014
5	200	8.723	8.764	0.041
5	202	8.732	8.750	0.018
5	203	8.690	8.701	0.011
5	204	8.530	8.558	0.028
5	206	8.788	8.800	0.012
10	207	8.741	8.799	0.058
10	209	8.632	8.657	0.025
10	210	8.620	8.676	0.056
10	211	8.508	8.471	-0.037
10	212	8.636	8.644	0.008
15	214	8.629	8.656	0.027
15	217	8.750	8.779	0.029
15	218	8.581	8.616	0.035
15	219	8.793	8.833	0.040
15	222	8.670	8.693	0.023
30	224	8.699	8.802	0.103
30	225	8.655	8.715	0.060
30	226	8.354	8.348	-0.006
30	227	8.809	8.829	0.020
30	229	8.624	8.677	0.053
35	230	8.728	8.733	0.005
35	231	8.695	8.706	0.011
35	232	8.536	8.524	-0.012
35	234	8.425	8.530	0.105
35	237	8.581	8.587	0.006
50	272	8.603	8.687	0.084
50	275	8.649	8.672	0.023
50	276	8.661	8.767	0.106
50	277	8.715	8.715	0.000
50	278	8.613	8.735	0.122
55	279	8.573	8.674	0.101
55	280	8.641	8.684	0.043
55	281	8.730	8.760	0.030
55	282	8.600	8.590	-0.010
55	283	8.652	8.622	-0.030
100	317	8.796	8.846	0.050
100	318	8.604	8.588	-0.016
100	319	8.575	8.626	0.051
100	320	8.644	8.684	0.040
100	321	8.465	8.541	0.076
100	322	8.651	8.700	0.049
100	323	8.450	8.517	0.067
100	324	8.589	8.645	0.056
100	326	8.768	8.896	0.128
100	327	8.550	8.620	0.070
100	328	8.652	8.666	0.014
100	329	8.715	8.855	0.140
100	332	8.648	8.809	0.161
100	333	8.472	8.495	0.023
100	334	8.702	8.666	-0.036
100	335	8.813	8.856	0.043
100	336	8.687	8.812	0.125
100	337	8.718	8.749	0.031
100	338	8.651	8.619	-0.032
100	339	8.742	8.888	0.146
100	340	8.631	8.587	-0.044
100	341	8.567	8.587	0.020
100	342	8.461	8.628	0.167
100	343	8.556	8.556	0.000
105	344	8.656	8.599	-0.057
105	346	8.619	8.569	-0.050
105	349	8.548	8.512	-0.036
105	350	8.697	8.654	-0.043
105	351	8.481	8.529	0.048
105	352	8.605	8.585	-0.020
	Max	8.813	8.896	0.167
	Average	8.638	8.671	0.033
	Min	8.354	8.348	-0.006
	Std Dev	0.094	0.107	0.051

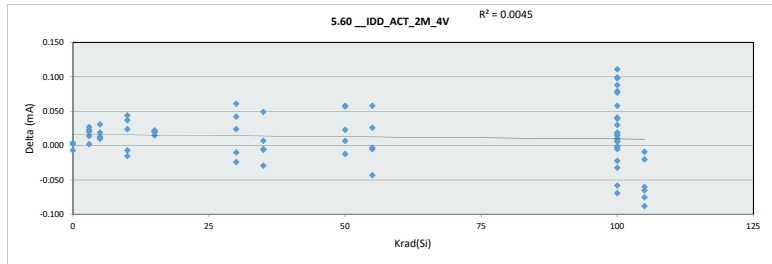


5.55_IDD_ACT_1											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			12 mA	mA							
			12 mA	mA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.556	8.663	8.558	8.471	8.616	8.348	8.524	8.672	8.590	8.495	8.512
Average	8.618	8.702	8.715	8.649	8.715	8.674	8.616	8.715	8.666	8.685	8.575
Max	8.654	8.769	8.800	8.799	8.833	8.829	8.733	8.767	8.760	8.896	8.654
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

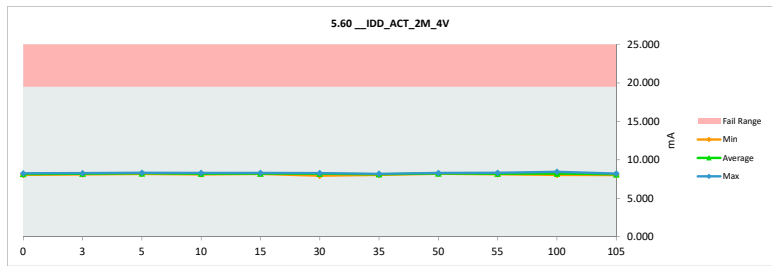


HDR TID Report  
TPS7H5004-SP

5.60_IDD_ACT_2M_4V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.048	8.041	-0.007
0	297	8.137	8.139	0.002
0	298	8.226	8.230	0.004
3	192	8.230	8.257	0.027
3	193	8.217	8.219	0.002
3	194	8.134	8.154	0.020
3	196	8.191	8.214	0.023
3	197	8.079	8.093	0.014
5	200	8.226	8.257	0.031
5	202	8.286	8.305	0.019
5	203	8.176	8.186	0.010
5	204	8.143	8.156	0.013
5	206	8.252	8.263	0.011
10	207	8.235	8.279	0.044
10	209	8.127	8.151	0.024
10	210	8.148	8.185	0.037
10	211	8.116	8.101	-0.015
10	212	8.204	8.197	-0.007
15	214	8.157	8.179	0.022
15	217	8.173	8.193	0.020
15	218	8.129	8.149	0.020
15	219	8.263	8.284	0.021
15	222	8.180	8.195	0.015
30	224	8.192	8.253	0.061
30	225	8.221	8.245	0.024
30	226	7.948	7.924	-0.024
30	227	8.280	8.270	-0.010
30	229	8.158	8.200	0.042
35	230	8.181	8.176	-0.005
35	231	8.155	8.162	0.007
35	232	8.125	8.096	-0.029
35	234	7.981	8.030	0.049
35	237	8.141	8.135	-0.006
50	272	8.197	8.220	0.023
50	275	8.164	8.171	0.007
50	276	8.175	8.232	0.057
50	277	8.223	8.211	-0.012
50	278	8.218	8.276	0.058
55	279	8.125	8.183	0.058
55	280	8.182	8.208	0.026
55	281	8.300	8.297	-0.003
55	282	8.110	8.105	-0.005
55	283	8.169	8.126	-0.043
100	317	8.316	8.374	0.058
100	318	8.084	8.062	-0.022
100	319	8.153	8.163	0.010
100	320	8.197	8.227	0.030
100	321	8.101	8.140	0.039
100	322	8.147	8.165	0.018
100	323	8.035	8.054	0.019
100	324	8.116	8.157	0.041
100	326	8.352	8.429	0.077
100	327	8.170	8.176	0.006
100	328	8.195	8.210	0.015
100	329	8.226	8.325	0.099
100	332	8.226	8.337	0.111
100	333	8.033	8.031	-0.002
100	334	8.254	8.185	-0.069
100	335	8.289	8.288	-0.001
100	336	8.237	8.316	0.079
100	337	8.229	8.224	-0.005
100	338	8.157	8.125	-0.032
100	339	8.248	8.336	0.088
100	340	8.161	8.103	-0.058
100	341	8.091	8.106	0.015
100	342	8.052	8.150	0.098
100	343	8.080	8.086	0.006
105	344	8.280	8.205	-0.075
105	346	8.227	8.139	-0.088
105	349	8.078	8.018	-0.060
105	350	8.215	8.150	-0.065
105	351	8.059	8.050	-0.009
105	352	8.106	8.086	-0.020
	Max	8.352	8.429	0.111
	Average	8.170	8.183	0.013
	Min	7.948	7.924	-0.088
	Std Dev	0.078	0.091	0.040



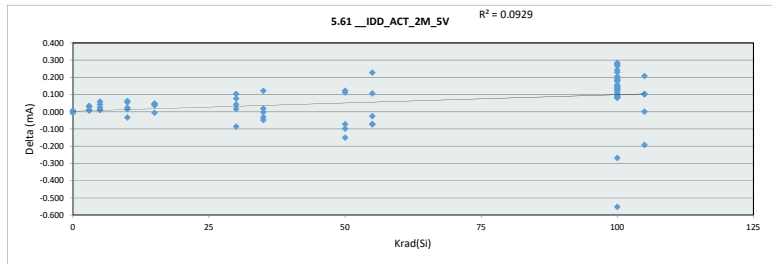
5.60_IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	8.041	8.093	8.156	8.101	8.149	7.924	8.030	8.171	8.105	8.031	8.018
Average	8.137	8.187	8.233	8.183	8.200	8.178	8.120	8.222	8.184	8.199	8.108
Max	8.230	8.257	8.305	8.279	8.284	8.270	8.176	8.276	8.297	8.429	8.205
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500



HDR TID Report  
TPS7H5004-SP

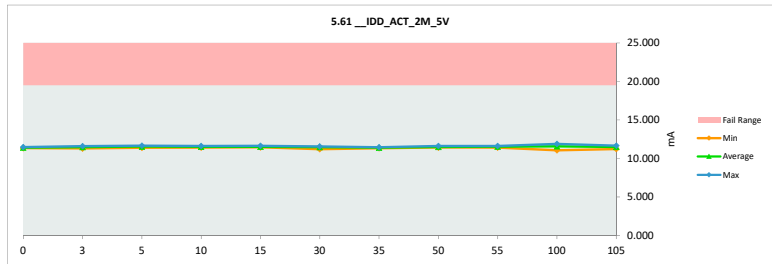
5.61_IDD_ACT_2M_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	19.5
Min Limit	19.5

Krad(Si)	Serial #	PRE	POST	Delta
0	295	11.332	11.325	-0.007
0	297	11.428	11.435	0.007
0	298	11.457	11.459	0.002
3	192	11.554	11.588	0.034
3	193	11.466	11.476	0.010
3	194	11.433	11.463	0.030
3	196	11.467	11.497	0.030
3	197	11.277	11.284	0.007
5	200	11.484	11.543	0.059
5	202	11.620	11.645	0.025
5	203	11.504	11.515	0.011
5	204	11.332	11.374	0.042
5	206	11.580	11.593	0.013
10	207	11.559	11.614	0.055
10	209	11.466	11.490	0.024
10	210	11.468	11.531	0.063
10	211	11.435	11.402	-0.033
10	212	11.489	11.504	0.015
15	214	11.427	11.475	0.048
15	217	11.482	11.476	-0.006
15	218	11.388	11.428	0.040
15	219	11.588	11.627	0.039
15	222	11.499	11.545	0.046
30	224	11.448	11.552	0.104
30	225	11.451	11.528	0.077
30	226	11.179	11.195	0.016
30	227	11.612	11.526	-0.086
30	229	11.463	11.505	0.042
35	230	11.420	11.372	-0.048
35	231	11.435	11.404	-0.031
35	232	11.407	11.405	-0.002
35	234	11.186	11.307	0.121
35	237	11.432	11.450	0.018
50	272	11.428	11.550	0.122
50	275	11.512	11.414	-0.098
50	276	11.538	11.466	-0.072
50	277	11.559	11.409	-0.150
50	278	11.496	11.608	0.112
55	279	11.346	11.573	0.227
55	280	11.531	11.458	-0.073
55	281	11.639	11.613	-0.026
55	282	11.461	11.390	-0.071
55	283	11.472	11.578	0.106
100	317	11.595	11.786	0.191
100	318	11.386	11.508	0.122
100	319	11.395	11.552	0.157
100	320	11.488	11.632	0.144
100	321	11.346	11.576	0.230
100	322	11.447	11.652	0.205
100	323	11.362	11.539	0.177
100	324	11.431	11.613	0.182
100	326	11.605	11.884	0.279
100	327	11.394	11.541	0.147
100	328	11.484	11.615	0.131
100	329	11.539	11.271	-0.268
100	332	11.484	11.751	0.267
100	333	11.290	11.380	0.090
100	334	11.545	11.640	0.095
100	335	11.603	11.050	-0.553
100	336	11.534	11.777	0.243
100	337	11.530	11.663	0.133
100	338	11.434	11.544	0.110
100	339	11.539	11.823	0.284
100	340	11.384	11.467	0.083
100	341	11.387	11.535	0.148
100	342	11.234	11.505	0.271
100	343	11.361	11.441	0.080
105	344	11.528	11.528	0.000
105	346	11.407	11.214	-0.193
105	349	11.392	11.494	0.102
105	350	11.539	11.642	0.103
105	351	11.262	11.470	0.208
105	352	11.406	11.509	0.103
	Max	11.639	11.884	0.284
	Average	11.453	11.510	0.056
	Min	11.179	11.050	-0.553
	Std Dev	0.098	0.140	0.129



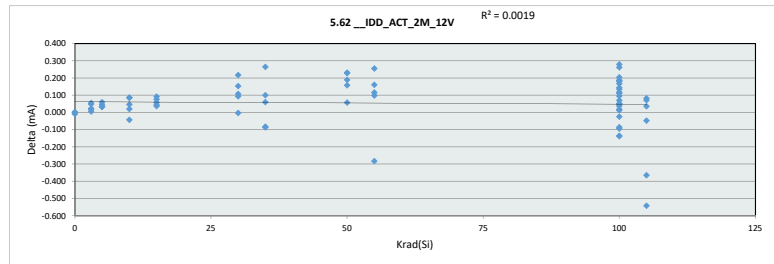
5.61_IDD_ACT_2	
Test Site	
Tester	
Test Number	
Max Limit	19.5 mA
Min Limit	19.5 mA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	11.325	11.284	11.374	11.402	11.428	11.195	11.307	11.409	11.390	11.050	11.214
Average	11.406	11.462	11.534	11.508	11.510	11.461	11.388	11.489	11.522	11.573	11.476
Max	11.459	11.588	11.645	11.614	11.627	11.552	11.450	11.608	11.613	11.884	11.642
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

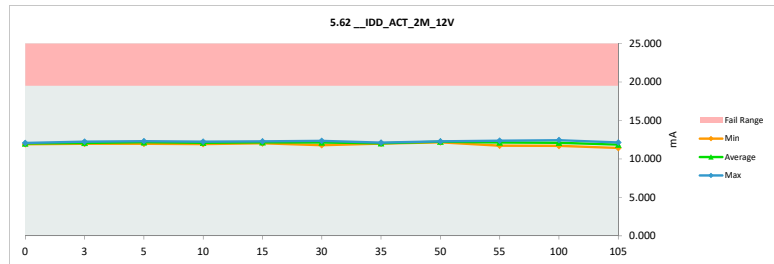


HDR TID Report  
TPS7H5004-SP

5.62_IDD_ACT_2M_12V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	295	11.922	11.913	-0.009
0	297	12.079	12.081	0.002
0	298	12.059	12.059	0.000
3	192	12.187	12.241	0.054
3	193	12.109	12.114	0.005
3	194	11.996	12.019	0.023
3	196	11.994	12.042	0.048
3	197	11.950	11.967	0.017
5	200	12.099	12.158	0.059
5	202	12.222	12.255	0.033
5	203	12.127	12.160	0.033
5	204	11.916	11.962	0.046
5	206	12.275	12.309	0.034
10	207	12.150	12.235	0.085
10	209	12.085	12.131	0.046
10	210	12.026	12.112	0.086
10	211	11.976	11.933	-0.043
10	212	12.075	12.095	0.020
15	214	11.986	12.078	0.092
15	217	12.214	12.251	0.037
15	218	11.980	12.023	0.043
15	219	12.226	12.283	0.057
15	222	12.104	12.180	0.076
30	224	12.119	12.336	0.217
30	225	12.063	12.216	0.153
30	226	11.675	11.782	0.107
30	227	12.283	12.280	-0.003
30	229	12.014	12.107	0.093
35	230	12.140	12.057	-0.083
35	231	12.111	12.024	-0.087
35	232	11.926	11.986	0.060
35	234	11.750	12.014	0.264
35	237	12.026	12.126	0.100
50	272	12.017	12.245	0.228
50	275	12.064	12.253	0.189
50	276	12.103	12.160	0.057
50	277	12.118	12.276	0.158
50	278	12.052	12.283	0.231
55	279	11.967	12.222	0.255
55	280	11.999	11.716	-0.283
55	281	12.205	12.365	0.160
55	282	12.038	12.136	0.098
55	283	12.108	12.224	0.116
100	317	12.181	12.276	0.095
100	318	11.970	12.040	0.070
100	319	11.963	11.976	0.013
100	320	12.036	12.011	-0.025
100	321	11.821	11.956	0.135
100	322	12.002	12.183	0.181
100	323	11.842	12.009	0.167
100	324	12.009	12.153	0.144
100	326	12.205	12.389	0.184
100	327	11.918	11.824	-0.094
100	328	12.033	12.084	0.051
100	329	12.127	12.406	0.279
100	332	12.058	12.176	0.118
100	333	11.826	11.690	-0.136
100	334	12.096	12.146	0.050
100	335	12.237	12.424	0.187
100	336	12.084	12.288	0.204
100	337	12.168	12.206	0.038
100	338	12.036	12.054	0.018
100	339	12.179	12.441	0.262
100	340	12.014	11.876	-0.138
100	341	11.961	12.011	0.050
100	342	11.776	11.887	0.111
100	343	11.909	11.823	-0.086
105	344	12.078	11.713	-0.365
105	346	11.959	11.417	-0.542
105	349	11.923	11.994	0.071
105	350	12.111	12.147	0.036
105	351	11.784	11.866	0.082
105	352	12.037	11.989	-0.048
Max		12.283	12.441	0.279
Average		12.039	12.094	0.055
Min		11.675	11.417	-0.542
Std Dev		0.124	0.189	0.134

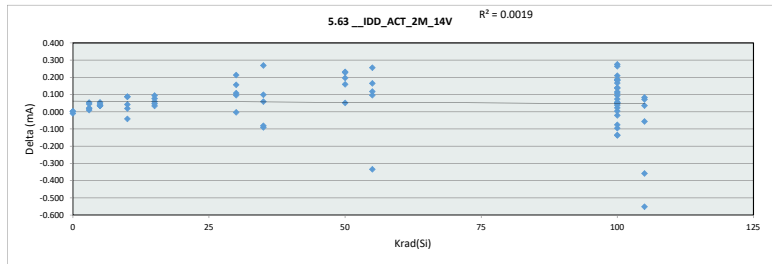


5.62_IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	11.913	11.967	11.962	11.933	12.023	11.782	11.986	12.160	11.716	11.690	11.417
Average	12.018	12.077	12.169	12.101	12.163	12.144	12.041	12.243	12.133	12.097	11.854
Max	12.081	12.241	12.309	12.235	12.283	12.336	12.126	12.283	12.365	12.441	12.147
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

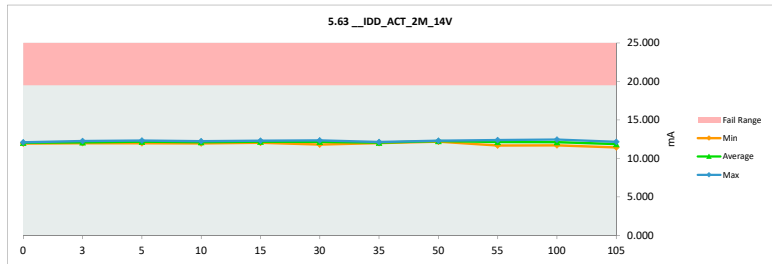


HDR TID Report  
TPS7H5004-SP

5.63 __IDD_ACT_2M_14V				
Test Site				
Tester				
Test Number				
Unit	mA	mA		
Max Limit	19.5	19.5		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	295	11.928	11.918	-0.010
0	297	12.083	12.086	0.003
0	298	12.063	12.065	0.002
3	192	12.193	12.246	0.053
3	193	12.111	12.121	0.010
3	194	12.001	12.023	0.022
3	196	11.999	12.044	0.045
3	197	11.954	11.972	0.018
5	200	12.105	12.159	0.054
5	202	12.228	12.263	0.035
5	203	12.133	12.169	0.036
5	204	11.922	11.967	0.045
5	206	12.278	12.313	0.035
10	207	12.151	12.239	0.088
10	209	12.091	12.133	0.042
10	210	12.030	12.117	0.087
10	211	11.977	11.936	-0.041
10	212	12.081	12.100	0.019
15	214	11.988	12.082	0.094
15	217	12.220	12.254	0.034
15	218	11.983	12.030	0.047
15	219	12.234	12.293	0.059
15	222	12.106	12.183	0.077
30	224	12.128	12.341	0.213
30	225	12.068	12.225	0.157
30	226	11.679	11.787	0.108
30	227	12.284	12.281	-0.003
30	229	12.018	12.114	0.096
35	230	12.141	12.060	-0.081
35	231	12.115	12.024	-0.091
35	232	11.929	11.988	0.059
35	234	11.753	12.022	0.269
35	237	12.033	12.132	0.099
50	272	12.023	12.251	0.228
50	275	12.065	12.262	0.197
50	276	12.109	12.161	0.052
50	277	12.125	12.284	0.159
50	278	12.054	12.286	0.232
55	279	11.971	12.227	0.256
55	280	12.007	11.673	-0.334
55	281	12.207	12.372	0.165
55	282	12.043	12.139	0.096
55	283	12.112	12.230	0.118
100	317	12.185	12.279	0.094
100	318	11.974	12.048	0.074
100	319	11.968	11.972	0.004
100	320	12.040	12.019	-0.021
100	321	11.827	11.964	0.137
100	322	12.005	12.189	0.184
100	323	11.844	12.011	0.167
100	324	12.018	12.158	0.140
100	326	12.206	12.389	0.183
100	327	11.921	11.845	-0.076
100	328	12.039	12.086	0.047
100	329	12.131	12.407	0.276
100	332	12.064	12.180	0.116
100	333	11.831	11.694	-0.137
100	334	12.099	12.153	0.054
100	335	12.242	12.431	0.189
100	336	12.088	12.298	0.210
100	337	12.174	12.213	0.039
100	338	12.039	12.062	0.023
100	339	12.183	12.447	0.264
100	340	12.017	11.882	-0.135
100	341	11.968	12.023	0.055
100	342	11.784	11.892	0.108
100	343	11.913	11.818	-0.095
105	344	12.081	11.723	-0.358
105	346	11.967	11.415	-0.552
105	349	11.926	11.998	0.072
105	350	12.114	12.150	0.036
105	351	11.791	11.874	0.083
105	352	12.047	11.991	-0.056
Max		12.284	12.447	0.276
Average		12.044	12.098	0.054
Min		11.679	11.415	-0.552
Std Dev		0.124	0.190	0.136

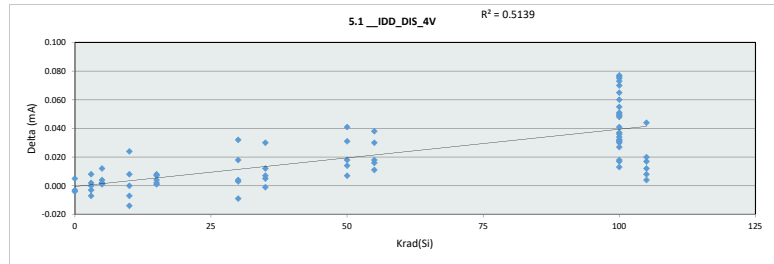


5.63 __IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	11.918	11.972	11.967	11.936	12.030	11.787	11.988	12.161	11.673	11.694	11.415
Average	12.023	12.081	12.174	12.105	12.168	12.150	12.045	12.249	12.128	12.103	11.859
Max	12.086	12.246	12.313	12.239	12.293	12.341	12.132	12.286	12.372	12.447	12.150
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

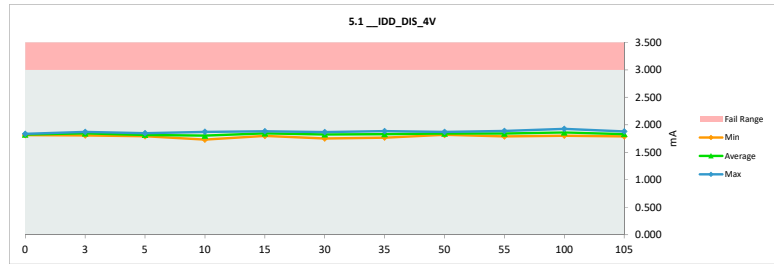


HDR TID Report  
TPS7H5004-SP

5.1 __IDD_DIS_4V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	3
			mA	3
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.815	1.812	-0.003
0	297	1.840	1.836	-0.004
0	298	1.824	1.829	0.005
3	192	1.821	1.829	0.008
3	193	1.811	1.808	-0.003
3	194	1.866	1.868	0.002
3	196	1.879	1.872	-0.007
3	197	1.851	1.851	0.000
5	200	1.837	1.849	0.012
5	202	1.813	1.815	0.002
5	203	1.812	1.814	0.002
5	204	1.789	1.793	0.004
5	206	1.815	1.816	0.001
10	207	1.873	1.873	0.000
10	209	1.803	1.811	0.008
10	210	1.792	1.816	0.024
10	211	1.745	1.731	-0.014
10	212	1.801	1.794	-0.007
15	214	1.840	1.844	0.004
15	217	1.876	1.883	0.007
15	218	1.834	1.842	0.008
15	219	1.853	1.855	0.002
15	222	1.798	1.799	0.001
30	224	1.836	1.868	0.032
30	225	1.826	1.844	0.018
30	226	1.761	1.752	-0.009
30	227	1.848	1.852	0.004
30	229	1.813	1.816	0.003
35	230	1.874	1.881	0.007
35	231	1.882	1.887	0.005
35	232	1.821	1.833	0.012
35	234	1.738	1.768	0.030
35	237	1.788	1.787	-0.001
50	272	1.806	1.837	0.031
50	275	1.810	1.817	0.007
50	276	1.806	1.824	0.018
50	277	1.858	1.872	0.014
50	278	1.816	1.857	0.041
55	279	1.850	1.888	0.038
55	280	1.828	1.858	0.030
55	281	1.836	1.854	0.018
55	282	1.805	1.821	0.016
55	283	1.778	1.789	0.011
100	317	1.871	1.926	0.055
100	318	1.850	1.881	0.031
100	319	1.802	1.843	0.041
100	320	1.841	1.890	0.049
100	321	1.744	1.809	0.065
100	322	1.820	1.852	0.032
100	323	1.800	1.851	0.051
100	324	1.813	1.847	0.034
100	326	1.820	1.897	0.077
100	327	1.757	1.807	0.050
100	328	1.801	1.838	0.037
100	329	1.848	1.923	0.075
100	332	1.837	1.907	0.070
100	333	1.771	1.803	0.032
100	334	1.815	1.832	0.017
100	335	1.871	1.907	0.036
100	336	1.807	1.867	0.060
100	337	1.825	1.873	0.048
100	338	1.844	1.862	0.018
100	339	1.832	1.908	0.076
100	340	1.824	1.837	0.013
100	341	1.820	1.850	0.030
100	342	1.753	1.826	0.073
100	343	1.803	1.830	0.027
105	344	1.816	1.828	0.012
105	346	1.828	1.832	0.004
105	349	1.800	1.808	0.008
105	350	1.861	1.881	0.020
105	351	1.809	1.853	0.044
105	352	1.774	1.791	0.017
	Max	1.882	1.926	0.077
	Average	1.820	1.841	0.022
	Min	1.738	1.731	-0.014
	Std Dev	0.033	0.038	0.023



5.1 __IDD_DIS_4V											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			3 mA	3 mA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.812	1.808	1.793	1.731	1.799	1.752	1.768	1.817	1.789	1.803	1.791
Average	1.826	1.846	1.817	1.805	1.845	1.826	1.831	1.841	1.842	1.861	1.832
Max	1.836	1.872	1.849	1.873	1.883	1.868	1.887	1.872	1.888	1.926	1.881
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

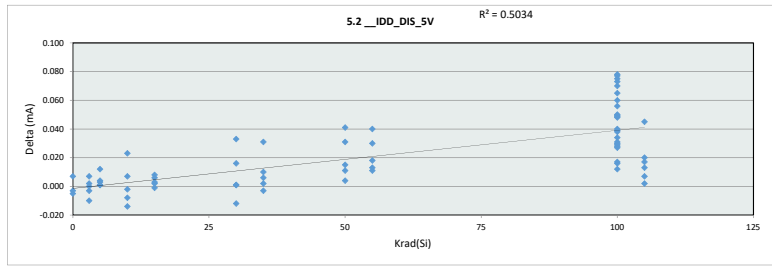




HDR TID Report  
TPS7H5004-SP

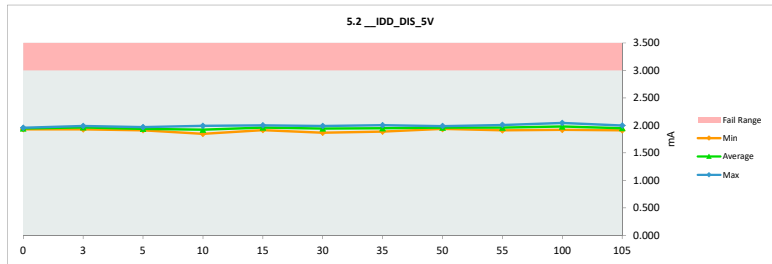
5.2 __IDD_DIS_5V	
Test Site	
Tester	
Test Number	
Unit	mA      mA
Max Limit	3            3
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.933	1.930	-0.003
0	297	1.961	1.956	-0.005
0	298	1.942	1.949	0.007
3	192	1.940	1.947	0.007
3	193	1.931	1.928	-0.003
3	194	1.984	1.986	0.002
3	196	2.000	1.990	-0.010
3	197	1.971	1.971	0.000
5	200	1.957	1.969	0.012
5	202	1.932	1.935	0.003
5	203	1.932	1.933	0.001
5	204	1.908	1.912	0.004
5	206	1.933	1.934	0.001
10	207	1.994	1.992	-0.002
10	209	1.923	1.930	0.007
10	210	1.909	1.932	0.023
10	211	1.861	1.847	-0.014
10	212	1.920	1.912	-0.008
15	214	1.957	1.960	0.003
15	217	1.996	2.002	0.006
15	218	1.951	1.959	0.008
15	219	1.973	1.972	-0.001
15	222	1.913	1.915	0.002
30	224	1.956	1.989	0.033
30	225	1.946	1.962	0.016
30	226	1.879	1.867	-0.012
30	227	1.969	1.970	0.001
30	229	1.933	1.934	0.001
35	230	1.992	1.998	0.006
35	231	2.002	2.004	0.002
35	232	1.938	1.948	0.010
35	234	1.856	1.887	0.031
35	237	1.907	1.904	-0.003
50	272	1.926	1.957	0.031
50	275	1.931	1.935	0.004
50	276	1.926	1.941	0.015
50	277	1.977	1.988	0.011
50	278	1.937	1.978	0.041
55	279	1.968	2.008	0.040
55	280	1.946	1.976	0.030
55	281	1.955	1.973	0.018
55	282	1.925	1.938	0.013
55	283	1.899	1.910	0.011
100	317	1.988	2.044	0.056
100	318	1.969	1.999	0.030
100	319	1.922	1.962	0.040
100	320	1.961	2.009	0.048
100	321	1.864	1.929	0.065
100	322	1.936	1.967	0.031
100	323	1.917	1.966	0.049
100	324	1.932	1.966	0.034
100	326	1.937	2.015	0.078
100	327	1.875	1.925	0.050
100	328	1.918	1.956	0.038
100	329	1.966	2.041	0.075
100	332	1.957	2.027	0.070
100	333	1.891	1.920	0.029
100	334	1.932	1.948	0.016
100	335	1.988	2.027	0.039
100	336	1.927	1.987	0.060
100	337	1.944	1.993	0.049
100	338	1.964	1.981	0.017
100	339	1.953	2.030	0.077
100	340	1.943	1.955	0.012
100	341	1.940	1.968	0.028
100	342	1.871	1.944	0.073
100	343	1.919	1.946	0.027
105	344	1.935	1.948	0.013
105	346	1.947	1.949	0.002
105	349	1.919	1.926	0.007
105	350	1.979	1.999	0.020
105	351	1.925	1.970	0.045
105	352	1.893	1.910	0.017
	Max	2.002	2.044	0.078
	Average	1.938	1.959	0.021
	Min	1.856	1.847	-0.014
	Std Dev	0.033	0.039	0.024



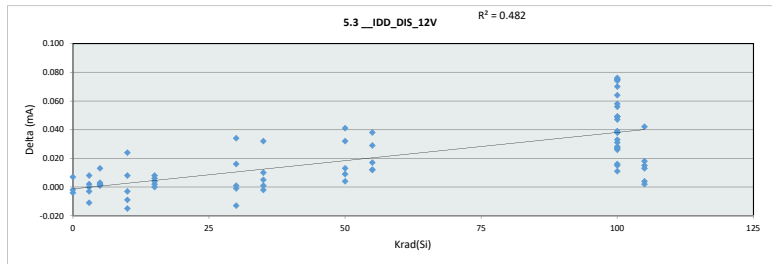
5.2 __IDD_DIS_5V	
Test Site	
Tester	
Test Number	
Max Limit	3      mA
Min Limit	

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.930	1.928	1.912	1.847	1.915	1.867	1.887	1.935	1.910	1.920	1.910
Average	1.945	1.964	1.937	1.923	1.962	1.944	1.948	1.960	1.961	1.979	1.950
Max	1.956	1.990	1.969	1.992	2.002	1.989	2.004	1.988	2.008	2.044	1.999
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

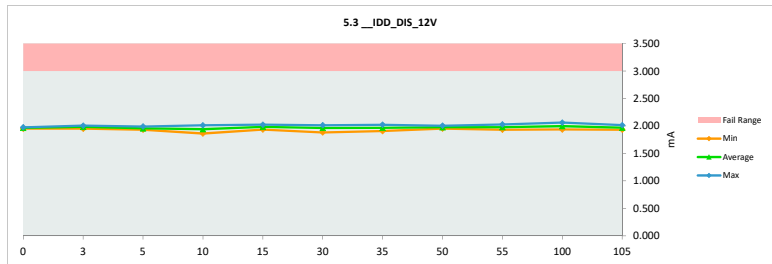


HDR TID Report  
TPS7H5004-SP

5.3_IDD_DIS_12V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			mA	3	3
Krad(Si)	Serial #	PRE	POST	Delta	
0	295	1.952	1.950	-0.002	
0	297	1.979	1.975	-0.004	
0	298	1.962	1.969	0.007	
3	192	1.957	1.965	0.008	
3	193	1.953	1.950	-0.003	
3	194	2.002	2.004	0.002	
3	196	2.016	2.005	-0.011	
3	197	1.989	1.989	0.000	
5	200	1.977	1.990	0.013	
5	202	1.952	1.954	0.002	
5	203	1.951	1.952	0.001	
5	204	1.928	1.931	0.003	
5	206	1.952	1.954	0.002	
10	207	2.014	2.011	-0.003	
10	209	1.944	1.952	0.008	
10	210	1.926	1.950	0.024	
10	211	1.878	1.863	-0.015	
10	212	1.940	1.931	-0.009	
15	214	1.974	1.978	0.004	
15	217	2.018	2.024	0.006	
15	218	1.971	1.979	0.008	
15	219	1.994	1.994	0.000	
15	222	1.932	1.934	0.002	
30	224	1.979	2.013	0.034	
30	225	1.967	1.983	0.016	
30	226	1.895	1.882	-0.013	
30	227	1.991	1.992	0.001	
30	229	1.951	1.950	-0.001	
35	230	2.010	2.015	0.005	
35	231	2.021	2.022	0.001	
35	232	1.954	1.964	0.010	
35	234	1.874	1.906	0.032	
35	237	1.926	1.924	-0.002	
50	272	1.944	1.976	0.032	
50	275	1.948	1.952	0.004	
50	276	1.945	1.958	0.013	
50	277	1.995	2.004	0.009	
50	278	1.955	1.996	0.041	
55	279	1.990	2.028	0.038	
55	280	1.959	1.988	0.029	
55	281	1.973	1.990	0.017	
55	282	1.943	1.955	0.012	
55	283	1.919	1.931	0.012	
100	317	2.006	2.062	0.056	
100	318	1.989	2.017	0.028	
100	319	1.941	1.980	0.039	
100	320	1.978	2.025	0.047	
100	321	1.877	1.941	0.064	
100	322	1.951	1.982	0.031	
100	323	1.930	1.979	0.049	
100	324	1.952	1.985	0.033	
100	326	1.956	2.031	0.075	
100	327	1.892	1.941	0.049	
100	328	1.935	1.973	0.038	
100	329	1.984	2.058	0.074	
100	332	1.975	2.045	0.070	
100	333	1.908	1.936	0.028	
100	334	1.949	1.964	0.015	
100	335	2.009	2.047	0.038	
100	336	1.944	2.002	0.058	
100	337	1.966	2.015	0.049	
100	338	1.984	2.000	0.016	
100	339	1.975	2.051	0.076	
100	340	1.965	1.976	0.011	
100	341	1.959	1.986	0.027	
100	342	1.888	1.963	0.075	
100	343	1.938	1.964	0.026	
105	344	1.952	1.965	0.013	
105	346	1.965	1.967	0.002	
105	349	1.937	1.941	0.004	
105	350	1.997	2.015	0.018	
105	351	1.942	1.984	0.042	
105	352	1.916	1.931	0.015	
Max		2.021	2.062	0.076	
Average		1.957	1.978	0.021	
Min		1.874	1.863	-0.015	
Std Dev		0.034	0.039	0.023	

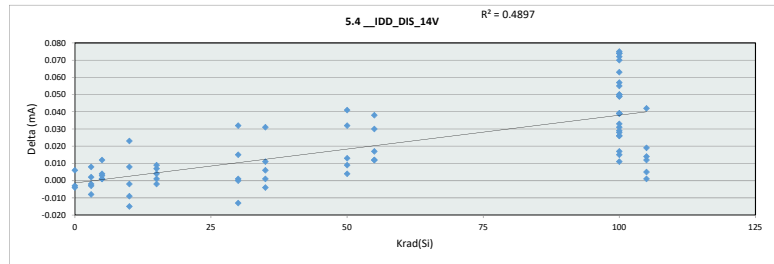


5.3_IDD_DIS_12											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			3	mA							
			3	mA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.950	1.950	1.931	1.863	1.934	1.882	1.906	1.952	1.931	1.936	1.931
Min	1.965	1.983	1.956	1.941	1.982	1.964	1.966	1.977	1.978	1.997	1.967
Average	1.975	2.005	1.990	2.011	2.024	2.013	2.022	2.004	2.028	2.062	2.015
Max	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

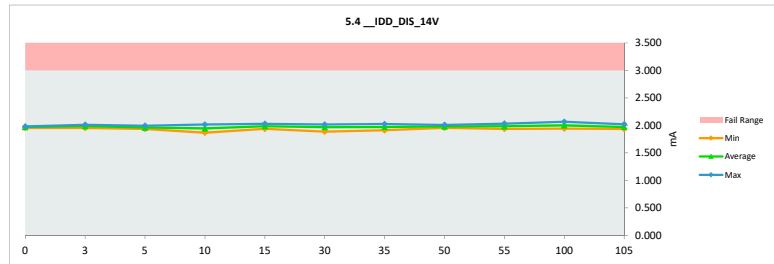


HDR TID Report  
TPS7H5004-SP

5.4_IDD_DIS_14V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	3
			mA	3
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.957	1.953	-0.004
0	297	1.983	1.980	-0.003
0	298	1.967	1.973	0.006
3	192	1.962	1.970	0.008
3	193	1.957	1.954	-0.003
3	194	2.006	2.008	0.002
3	196	2.019	2.011	-0.008
3	197	1.994	1.992	-0.002
5	200	1.982	1.994	0.012
5	202	1.956	1.959	0.003
5	203	1.955	1.956	0.001
5	204	1.932	1.936	0.004
5	206	1.957	1.958	0.001
10	207	2.018	2.016	-0.002
10	209	1.949	1.957	0.008
10	210	1.931	1.954	0.023
10	211	1.882	1.867	-0.015
10	212	1.943	1.934	-0.009
15	214	1.978	1.982	0.004
15	217	2.022	2.029	0.007
15	218	1.974	1.983	0.009
15	219	1.999	1.997	-0.002
15	222	1.936	1.937	0.001
30	224	1.984	2.016	0.032
30	225	1.972	1.987	0.015
30	226	1.899	1.886	-0.013
30	227	1.995	1.996	0.001
30	229	1.955	1.955	0.000
35	230	2.013	2.019	0.006
35	231	2.024	2.025	0.001
35	232	1.958	1.969	0.011
35	234	1.880	1.911	0.031
35	237	1.931	1.927	-0.004
50	272	1.948	1.980	0.032
50	275	1.953	1.957	0.004
50	276	1.949	1.962	0.013
50	277	1.999	2.008	0.009
50	278	1.958	1.999	0.041
55	279	1.994	2.032	0.038
55	280	1.964	1.994	0.030
55	281	1.977	1.994	0.017
55	282	1.948	1.960	0.012
55	283	1.924	1.936	0.012
100	317	2.011	2.066	0.055
100	318	1.993	2.021	0.028
100	319	1.944	1.983	0.039
100	320	1.982	2.031	0.049
100	321	1.882	1.945	0.063
100	322	1.955	1.986	0.031
100	323	1.934	1.984	0.050
100	324	1.956	1.989	0.033
100	326	1.961	2.036	0.075
100	327	1.896	1.946	0.050
100	328	1.939	1.978	0.039
100	329	1.989	2.061	0.072
100	332	1.979	2.049	0.070
100	333	1.912	1.941	0.029
100	334	1.954	1.969	0.015
100	335	2.013	2.052	0.039
100	336	1.949	2.006	0.057
100	337	1.970	2.019	0.049
100	338	1.989	2.006	0.017
100	339	1.980	2.054	0.074
100	340	1.969	1.980	0.011
100	341	1.963	1.989	0.026
100	342	1.893	1.967	0.074
100	343	1.941	1.967	0.026
105	344	1.956	1.968	0.012
105	346	1.970	1.971	0.001
105	349	1.940	1.945	0.005
105	350	2.001	2.020	0.019
105	351	1.946	1.988	0.042
105	352	1.920	1.934	0.014
Max		2.024	2.066	0.075
Average		1.961	1.982	0.020
Min		1.880	1.867	-0.015
Std Dev		0.034	0.039	0.023

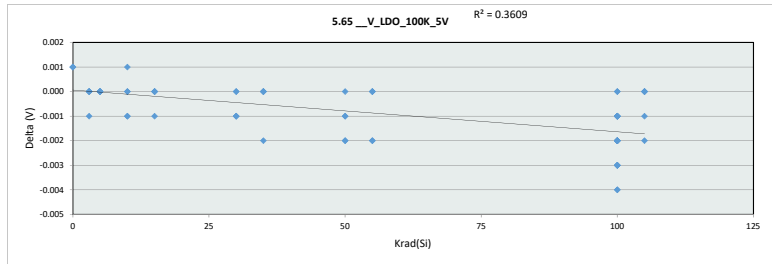


5.4_IDD_DIS_14											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			3	mA							
			3	mA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.953	1.954	1.936	1.867	1.937	1.886	1.911	1.957	1.936	1.941	1.934
Average	1.969	1.987	1.961	1.946	1.986	1.968	1.970	1.981	1.983	2.001	1.971
Max	1.980	2.011	1.994	2.016	2.029	2.016	2.025	2.008	2.032	2.066	2.020
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

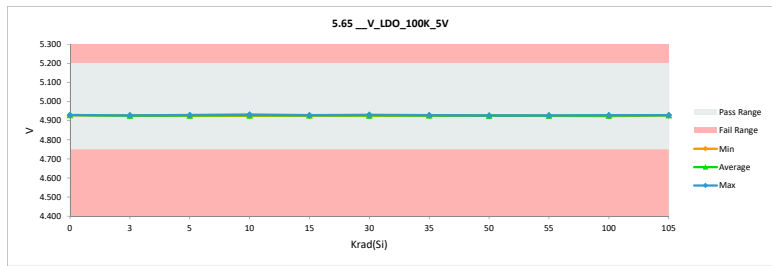


HDR TID Report  
TPS7H5004-SP

5.65_V_LDO_100K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	4.929	4.930	0.001
0	297	4.928	4.929	0.001
0	298	4.928	4.929	0.001
3	192	4.926	4.926	0.000
3	193	4.928	4.928	0.000
3	194	4.928	4.927	-0.001
3	196	4.926	4.926	0.000
3	197	4.927	4.927	0.000
5	200	4.926	4.926	0.000
5	202	4.928	4.928	0.000
5	203	4.927	4.927	0.000
5	204	4.930	4.930	0.000
5	206	4.926	4.926	0.000
10	207	4.926	4.925	-0.001
10	209	4.928	4.928	0.000
10	210	4.929	4.928	-0.001
10	211	4.931	4.932	0.001
10	212	4.928	4.928	0.000
15	214	4.928	4.928	0.000
15	217	4.927	4.927	0.000
15	218	4.929	4.929	0.000
15	219	4.927	4.926	-0.001
15	222	4.929	4.929	0.000
30	224	4.927	4.926	-0.001
30	225	4.928	4.927	-0.001
30	226	4.931	4.931	0.000
30	227	4.926	4.926	0.000
30	229	4.928	4.927	-0.001
35	230	4.927	4.927	0.000
35	231	4.927	4.927	0.000
35	232	4.929	4.929	0.000
35	234	4.931	4.929	-0.002
35	237	4.929	4.929	0.000
50	272	4.928	4.927	-0.001
50	275	4.928	4.927	-0.001
50	276	4.927	4.925	-0.002
50	277	4.927	4.927	0.000
50	278	4.929	4.927	-0.002
55	279	4.929	4.927	-0.002
55	280	4.927	4.925	-0.002
55	281	4.927	4.927	0.000
55	282	4.928	4.928	0.000
55	283	4.928	4.928	0.000
100	317	4.927	4.926	-0.001
100	318	4.929	4.928	-0.001
100	319	4.930	4.928	-0.002
100	320	4.928	4.927	-0.001
100	321	4.931	4.929	-0.002
100	322	4.929	4.927	-0.002
100	323	4.932	4.929	-0.003
100	324	4.929	4.927	-0.002
100	326	4.928	4.925	-0.003
100	327	4.929	4.927	-0.002
100	328	4.928	4.927	-0.001
100	329	4.928	4.925	-0.003
100	332	4.929	4.925	-0.004
100	333	4.931	4.929	-0.002
100	334	4.929	4.928	-0.001
100	335	4.927	4.925	-0.002
100	336	4.927	4.924	-0.003
100	337	4.928	4.926	-0.002
100	338	4.928	4.928	0.000
100	339	4.928	4.925	-0.003
100	340	4.929	4.929	0.000
100	341	4.929	4.928	-0.001
100	342	4.931	4.927	-0.004
100	343	4.930	4.929	-0.001
105	344	4.929	4.929	0.000
105	346	4.928	4.928	0.000
105	349	4.929	4.929	0.000
105	350	4.929	4.929	0.000
105	351	4.931	4.929	-0.002
105	352	4.930	4.929	-0.001
Max		4.932	4.932	0.001
Average		4.928	4.927	-0.001
Min		4.926	4.924	-0.004
Std Dev		0.001	0.002	0.001

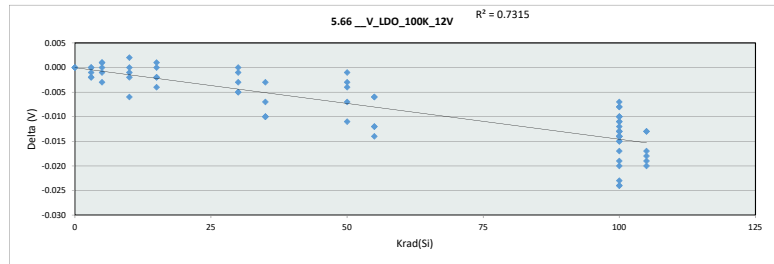


5.65_V_LDO_100											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.929	4.926	4.926	4.925	4.926	4.926	4.927	4.925	4.925	4.924	4.928
Average	4.929	4.927	4.927	4.928	4.928	4.928	4.928	4.927	4.927	4.927	4.929
Max	4.930	4.928	4.930	4.932	4.929	4.931	4.929	4.927	4.928	4.929	4.929
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

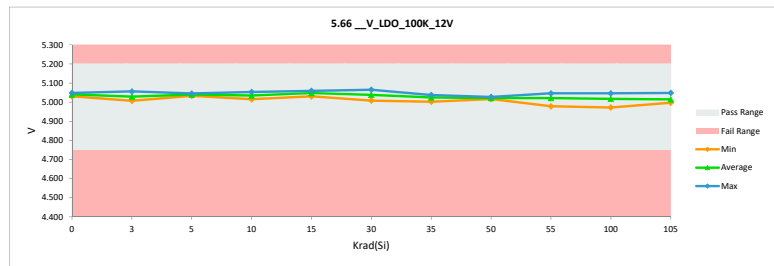


HDR TID Report  
TPS7H5004-SP

5.66 _V_LDO_100K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.042	5.042	0.000
0	297	5.030	5.030	0.000
0	298	5.048	5.048	0.000
3	192	5.029	5.029	0.000
3	193	5.056	5.056	0.000
3	194	5.027	5.025	-0.002
3	196	5.008	5.007	-0.001
3	197	5.028	5.026	-0.002
5	200	5.048	5.045	-0.003
5	202	5.042	5.042	0.000
5	203	5.034	5.033	-0.001
5	204	5.039	5.040	0.001
5	206	5.038	5.039	0.001
10	207	5.040	5.039	-0.001
10	209	5.051	5.053	0.002
10	210	5.033	5.031	-0.002
10	211	5.021	5.015	-0.006
10	212	5.034	5.034	0.000
15	214	5.030	5.030	0.000
15	217	5.063	5.059	-0.004
15	218	5.044	5.045	0.001
15	219	5.061	5.059	-0.002
15	222	5.047	5.045	-0.002
30	224	5.070	5.065	-0.005
30	225	5.047	5.044	-0.003
30	226	5.008	5.008	0.000
30	227	5.057	5.056	-0.001
30	229	5.022	5.017	-0.005
35	230	5.031	5.024	-0.007
35	231	5.035	5.025	-0.010
35	232	5.012	5.002	-0.010
35	234	5.040	5.037	-0.003
35	237	5.044	5.034	-0.010
50	272	5.030	5.027	-0.003
50	275	5.023	5.019	-0.004
50	276	5.029	5.022	-0.007
50	277	5.027	5.016	-0.011
50	278	5.024	5.023	-0.001
55	279	5.057	5.045	-0.012
55	280	4.990	4.978	-0.012
55	281	5.029	5.023	-0.006
55	282	5.029	5.015	-0.014
55	283	5.052	5.046	-0.006
100	317	5.037	5.024	-0.013
100	318	5.039	5.025	-0.014
100	319	5.030	5.022	-0.008
100	320	5.019	5.006	-0.013
100	321	4.995	4.972	-0.023
100	322	5.013	5.003	-0.010
100	323	4.998	4.984	-0.014
100	324	5.040	5.025	-0.015
100	326	5.044	5.020	-0.024
100	327	5.014	5.002	-0.012
100	328	5.025	5.014	-0.011
100	329	5.040	5.016	-0.024
100	332	5.035	5.015	-0.020
100	333	5.020	5.010	-0.010
100	334	5.025	5.012	-0.013
100	335	5.054	5.040	-0.014
100	336	5.018	5.007	-0.011
100	337	5.056	5.046	-0.010
100	338	5.041	5.027	-0.014
100	339	5.060	5.043	-0.017
100	340	5.061	5.042	-0.019
100	341	5.034	5.019	-0.015
100	342	5.029	5.022	-0.007
100	343	5.027	5.019	-0.008
105	344	5.021	5.008	-0.013
105	346	5.031	5.012	-0.019
105	349	5.022	5.009	-0.013
105	350	5.033	5.013	-0.020
105	351	5.015	4.997	-0.018
105	352	5.065	5.048	-0.017
Max		5.070	5.065	0.002
Average		5.034	5.026	-0.008
Min		4.990	4.972	-0.024
Std Dev		0.016	0.019	0.007

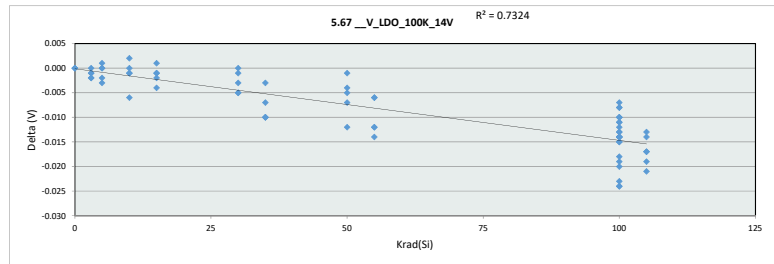


5.66 _V_LDO_100											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.030	5.007	5.033	5.015	5.030	5.008	5.002	5.016	4.978	4.972	4.997
Average	5.040	5.029	5.040	5.034	5.048	5.038	5.024	5.021	5.021	5.017	5.015
Max	5.048	5.056	5.045	5.053	5.059	5.065	5.037	5.027	5.046	5.046	5.048
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

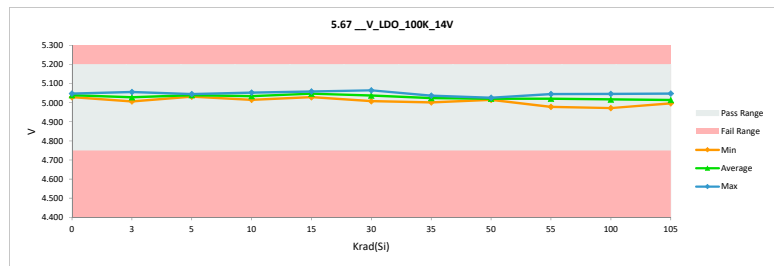


HDR TID Report  
TPS7H5004-SP

5.67 _V_LDO_100K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.042	5.042	0.000
0	297	5.029	5.029	0.000
0	298	5.048	5.048	0.000
3	192	5.029	5.028	-0.001
3	193	5.057	5.056	-0.001
3	194	5.027	5.025	-0.002
3	196	5.007	5.007	0.000
3	197	5.028	5.026	-0.002
5	200	5.048	5.045	-0.003
5	202	5.042	5.042	0.000
5	203	5.034	5.032	-0.002
5	204	5.039	5.040	0.001
5	206	5.038	5.038	0.000
10	207	5.040	5.039	-0.001
10	209	5.051	5.053	0.002
10	210	5.033	5.032	-0.001
10	211	5.021	5.015	-0.006
10	212	5.034	5.034	0.000
15	214	5.030	5.029	-0.001
15	217	5.063	5.059	-0.004
15	218	5.044	5.045	0.001
15	219	5.060	5.059	-0.001
15	222	5.047	5.045	-0.002
30	224	5.070	5.065	-0.005
30	225	5.047	5.044	-0.003
30	226	5.008	5.008	0.000
30	227	5.057	5.056	-0.001
30	229	5.022	5.017	-0.005
35	230	5.031	5.024	-0.007
35	231	5.035	5.025	-0.010
35	232	5.012	5.002	-0.010
35	234	5.040	5.037	-0.003
35	237	5.044	5.034	-0.010
50	272	5.030	5.026	-0.004
50	275	5.023	5.018	-0.005
50	276	5.029	5.022	-0.007
50	277	5.027	5.015	-0.012
50	278	5.024	5.023	-0.001
55	279	5.057	5.045	-0.012
55	280	4.990	4.978	-0.012
55	281	5.029	5.023	-0.006
55	282	5.029	5.015	-0.014
55	283	5.051	5.045	-0.006
100	317	5.037	5.024	-0.013
100	318	5.039	5.025	-0.014
100	319	5.030	5.022	-0.008
100	320	5.019	5.006	-0.013
100	321	4.995	4.972	-0.023
100	322	5.013	5.003	-0.010
100	323	4.998	4.984	-0.014
100	324	5.039	5.025	-0.014
100	326	5.044	5.020	-0.024
100	327	5.014	5.002	-0.012
100	328	5.025	5.014	-0.011
100	329	5.040	5.016	-0.024
100	332	5.035	5.015	-0.020
100	333	5.020	5.010	-0.010
100	334	5.025	5.011	-0.014
100	335	5.054	5.040	-0.014
100	336	5.018	5.007	-0.011
100	337	5.056	5.046	-0.010
100	338	5.042	5.027	-0.015
100	339	5.060	5.042	-0.018
100	340	5.061	5.042	-0.019
100	341	5.034	5.019	-0.015
100	342	5.029	5.022	-0.007
100	343	5.027	5.019	-0.008
105	344	5.021	5.008	-0.013
105	346	5.031	5.012	-0.019
105	349	5.022	5.008	-0.014
105	350	5.034	5.013	-0.021
105	351	5.014	4.997	-0.017
105	352	5.065	5.048	-0.017
Max		5.070	5.065	0.002
Average		5.034	5.026	-0.008
Min		4.990	4.972	-0.024
Std Dev		0.017	0.019	0.007

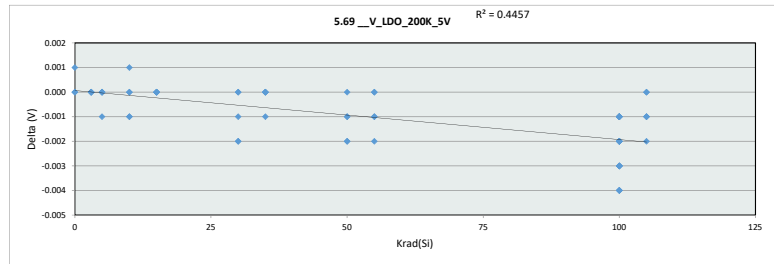


5.67 _V_LDO_100											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.029	5.007	5.032	5.015	5.029	5.008	5.002	5.015	4.978	4.972	4.997
Average	5.040	5.028	5.039	5.035	5.047	5.038	5.024	5.021	5.021	5.017	5.014
Max	5.048	5.056	5.045	5.053	5.059	5.065	5.037	5.026	5.045	5.046	5.048
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

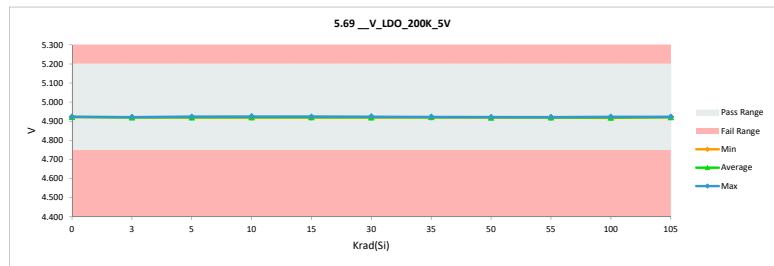


HDR TID Report  
TPS7H5004-SP

5.69 _V_LDO_200K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	4.923	4.923	0.000
0	297	4.922	4.922	0.000
0	298	4.922	4.923	0.001
3	192	4.920	4.920	0.000
3	193	4.922	4.922	0.000
3	194	4.922	4.922	0.000
3	196	4.920	4.920	0.000
3	197	4.921	4.921	0.000
5	200	4.920	4.920	0.000
5	202	4.921	4.921	0.000
5	203	4.921	4.921	0.000
5	204	4.924	4.923	-0.001
5	206	4.920	4.920	0.000
10	207	4.919	4.918	-0.001
10	209	4.921	4.921	0.000
10	210	4.923	4.922	-0.001
10	211	4.925	4.926	0.001
10	212	4.922	4.922	0.000
15	214	4.922	4.922	0.000
15	217	4.921	4.921	0.000
15	218	4.923	4.923	0.000
15	219	4.920	4.920	0.000
15	222	4.923	4.923	0.000
30	224	4.922	4.920	-0.002
30	225	4.922	4.921	-0.001
30	226	4.925	4.925	0.000
30	227	4.919	4.919	0.000
30	229	4.922	4.920	-0.002
35	230	4.921	4.921	0.000
35	231	4.921	4.921	0.000
35	232	4.923	4.923	0.000
35	234	4.924	4.923	-0.001
35	237	4.923	4.923	0.000
50	272	4.921	4.920	-0.001
50	275	4.922	4.921	-0.001
50	276	4.921	4.919	-0.002
50	277	4.921	4.921	0.000
50	278	4.922	4.920	-0.002
55	279	4.923	4.921	-0.002
55	280	4.920	4.919	-0.001
55	281	4.921	4.920	-0.001
55	282	4.922	4.922	0.000
55	283	4.922	4.922	0.000
100	317	4.921	4.920	-0.001
100	318	4.923	4.922	-0.001
100	319	4.923	4.922	-0.001
100	320	4.922	4.921	-0.001
100	321	4.925	4.922	-0.003
100	322	4.922	4.920	-0.002
100	323	4.926	4.923	-0.003
100	324	4.923	4.921	-0.002
100	326	4.922	4.918	-0.004
100	327	4.923	4.920	-0.003
100	328	4.922	4.920	-0.002
100	329	4.922	4.918	-0.004
100	332	4.922	4.918	-0.004
100	333	4.924	4.923	-0.001
100	334	4.922	4.921	-0.001
100	335	4.921	4.918	-0.003
100	336	4.920	4.917	-0.003
100	337	4.922	4.920	-0.002
100	338	4.922	4.921	-0.001
100	339	4.921	4.918	-0.003
100	340	4.923	4.922	-0.001
100	341	4.923	4.921	-0.002
100	342	4.924	4.920	-0.004
100	343	4.924	4.922	-0.002
105	344	4.922	4.922	0.000
105	346	4.922	4.921	-0.001
105	349	4.923	4.922	-0.001
105	350	4.923	4.923	0.000
105	351	4.924	4.922	-0.002
105	352	4.923	4.922	-0.001
Max		4.926	4.926	0.001
Average		4.922	4.921	-0.001
Min		4.919	4.917	-0.004
Std Dev		0.001	0.002	0.001

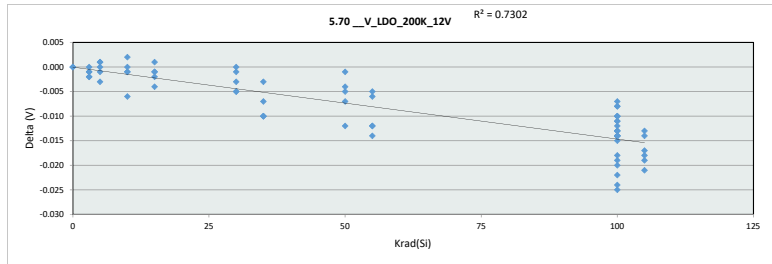


5.69 _V_LDO_200K_5V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.922	4.920	4.920	4.918	4.920	4.919	4.921	4.919	4.919	4.917	4.921
Average	4.923	4.921	4.921	4.922	4.922	4.921	4.922	4.920	4.921	4.920	4.922
Max	4.923	4.922	4.923	4.926	4.923	4.925	4.923	4.921	4.922	4.923	4.923
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

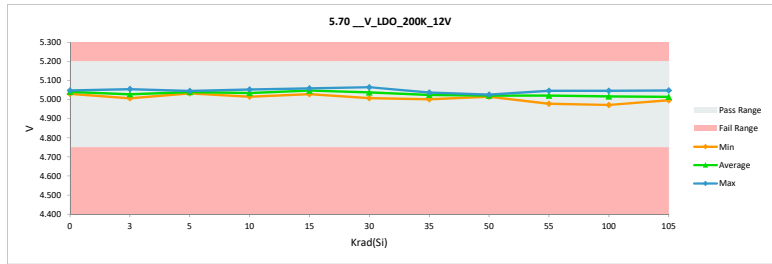


HDR TID Report  
TPS7H5004-SP

5.70 __V_LDO_200K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.042	5.042	0.000
0	297	5.030	5.030	0.000
0	298	5.048	5.048	0.000
3	192	5.029	5.028	-0.001
3	193	5.056	5.055	-0.001
3	194	5.027	5.025	-0.002
3	196	5.007	5.007	0.000
3	197	5.028	5.026	-0.002
5	200	5.048	5.045	-0.003
5	202	5.042	5.041	-0.001
5	203	5.033	5.033	0.000
5	204	5.039	5.040	0.001
5	206	5.037	5.038	0.001
10	207	5.040	5.039	-0.001
10	209	5.051	5.053	0.002
10	210	5.033	5.032	-0.001
10	211	5.021	5.015	-0.006
10	212	5.034	5.034	0.000
15	214	5.030	5.029	-0.001
15	217	5.063	5.059	-0.004
15	218	5.044	5.045	0.001
15	219	5.060	5.059	-0.001
15	222	5.047	5.045	-0.002
30	224	5.070	5.065	-0.005
30	225	5.047	5.044	-0.003
30	226	5.008	5.008	0.000
30	227	5.057	5.056	-0.001
30	229	5.022	5.017	-0.005
35	230	5.031	5.024	-0.007
35	231	5.035	5.025	-0.010
35	232	5.012	5.002	-0.010
35	234	5.040	5.037	-0.003
35	237	5.043	5.033	-0.010
50	272	5.030	5.026	-0.004
50	275	5.023	5.018	-0.005
50	276	5.029	5.022	-0.007
50	277	5.027	5.015	-0.012
50	278	5.024	5.023	-0.001
55	279	5.057	5.045	-0.012
55	280	4.990	4.978	-0.012
55	281	5.029	5.023	-0.006
55	282	5.029	5.015	-0.014
55	283	5.051	5.046	-0.005
100	317	5.037	5.024	-0.013
100	318	5.039	5.025	-0.014
100	319	5.030	5.022	-0.008
100	320	5.019	5.006	-0.013
100	321	4.994	4.972	-0.022
100	322	5.013	5.003	-0.010
100	323	4.998	4.984	-0.014
100	324	5.039	5.025	-0.014
100	326	5.044	5.019	-0.025
100	327	5.014	5.002	-0.012
100	328	5.025	5.014	-0.011
100	329	5.040	5.016	-0.024
100	332	5.035	5.015	-0.020
100	333	5.020	5.010	-0.010
100	334	5.025	5.012	-0.013
100	335	5.054	5.040	-0.014
100	336	5.018	5.007	-0.011
100	337	5.056	5.046	-0.010
100	338	5.041	5.027	-0.014
100	339	5.060	5.042	-0.018
100	340	5.061	5.042	-0.019
100	341	5.034	5.019	-0.015
100	342	5.029	5.022	-0.007
100	343	5.027	5.019	-0.008
105	344	5.021	5.008	-0.013
105	346	5.031	5.012	-0.019
105	349	5.022	5.008	-0.014
105	350	5.034	5.013	-0.021
105	351	5.015	4.997	-0.018
105	352	5.065	5.048	-0.017
Max		5.070	5.065	0.002
Average		5.034	5.026	-0.008
Min		4.990	4.972	-0.025
Std Dev		0.016	0.019	0.007



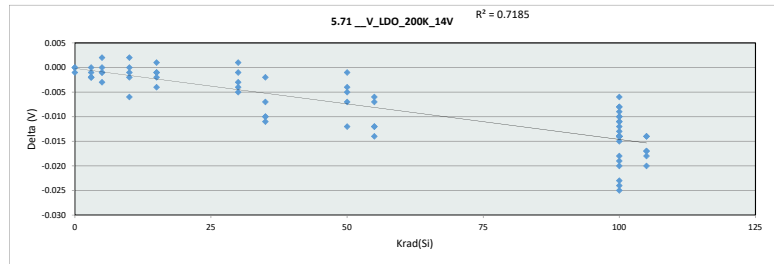
5.70 __V_LDO_200K_12V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.030	5.007	5.033	5.015	5.029	5.008	5.002	5.015	4.978	4.972	4.997
Average	5.040	5.028	5.039	5.035	5.047	5.038	5.024	5.021	5.021	5.017	5.014
Max	5.048	5.055	5.045	5.053	5.059	5.065	5.037	5.026	5.046	5.046	5.048
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



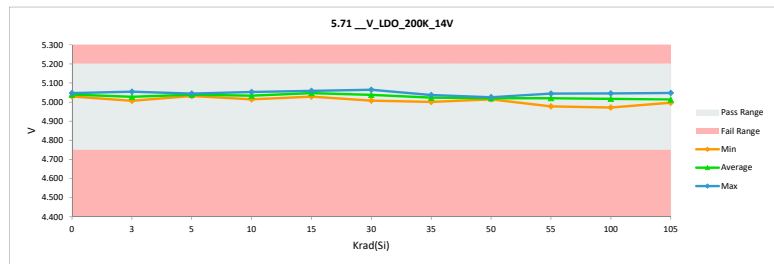


HDR TID Report  
TPS7H5004-SP

5.71 _V_LDO_200K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.042	5.042	0.000
0	297	5.030	5.030	0.000
0	298	5.048	5.047	-0.001
3	192	5.029	5.028	-0.001
3	193	5.057	5.055	-0.002
3	194	5.027	5.025	-0.002
3	196	5.007	5.007	0.000
3	197	5.028	5.026	-0.002
5	200	5.048	5.045	-0.003
5	202	5.042	5.041	-0.001
5	203	5.033	5.032	-0.001
5	204	5.038	5.040	0.002
5	206	5.038	5.038	0.000
10	207	5.040	5.039	-0.001
10	209	5.051	5.053	0.002
10	210	5.033	5.031	-0.002
10	211	5.021	5.015	-0.006
10	212	5.034	5.034	0.000
15	214	5.030	5.029	-0.001
15	217	5.063	5.059	-0.004
15	218	5.044	5.045	0.001
15	219	5.060	5.059	-0.001
15	222	5.047	5.045	-0.002
30	224	5.070	5.065	-0.005
30	225	5.047	5.044	-0.003
30	226	5.007	5.008	0.001
30	227	5.057	5.056	-0.001
30	229	5.021	5.017	-0.004
35	230	5.031	5.024	-0.007
35	231	5.035	5.025	-0.010
35	232	5.012	5.001	-0.011
35	234	5.039	5.037	-0.002
35	237	5.043	5.033	-0.010
50	272	5.030	5.026	-0.004
50	275	5.023	5.018	-0.005
50	276	5.029	5.022	-0.007
50	277	5.027	5.015	-0.012
50	278	5.024	5.023	-0.001
55	279	5.057	5.045	-0.012
55	280	4.990	4.978	-0.012
55	281	5.029	5.022	-0.007
55	282	5.029	5.015	-0.014
55	283	5.051	5.045	-0.006
100	317	5.037	5.023	-0.014
100	318	5.039	5.025	-0.014
100	319	5.030	5.022	-0.008
100	320	5.019	5.006	-0.013
100	321	4.995	4.972	-0.023
100	322	5.013	5.004	-0.009
100	323	4.998	4.984	-0.014
100	324	5.039	5.025	-0.014
100	326	5.044	5.019	-0.025
100	327	5.014	5.002	-0.012
100	328	5.025	5.014	-0.011
100	329	5.040	5.016	-0.024
100	332	5.035	5.015	-0.020
100	333	5.020	5.010	-0.010
100	334	5.025	5.011	-0.014
100	335	5.054	5.040	-0.014
100	336	5.018	5.007	-0.011
100	337	5.056	5.046	-0.010
100	338	5.041	5.027	-0.014
100	339	5.060	5.042	-0.018
100	340	5.061	5.042	-0.019
100	341	5.034	5.019	-0.015
100	342	5.028	5.022	-0.006
100	343	5.027	5.019	-0.008
105	344	5.021	5.007	-0.014
105	346	5.030	5.012	-0.018
105	349	5.022	5.008	-0.014
105	350	5.033	5.013	-0.020
105	351	5.014	4.997	-0.017
105	352	5.065	5.048	-0.017
Max		5.070	5.065	0.002
Average		5.034	5.026	-0.008
Min		4.990	4.972	-0.025
Std Dev		0.017	0.019	0.007

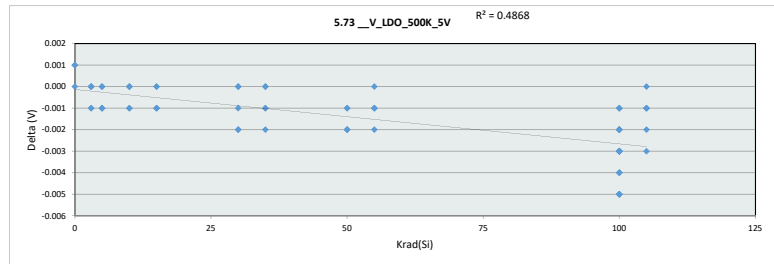


5.71 _V_LDO_200K_14V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.030	5.007	5.032	5.015	5.029	5.008	5.001	5.015	4.978	4.972	4.997
Average	5.040	5.028	5.039	5.034	5.047	5.038	5.024	5.021	5.021	5.017	5.014
Max	5.047	5.055	5.045	5.053	5.059	5.065	5.037	5.026	5.045	5.046	5.048
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

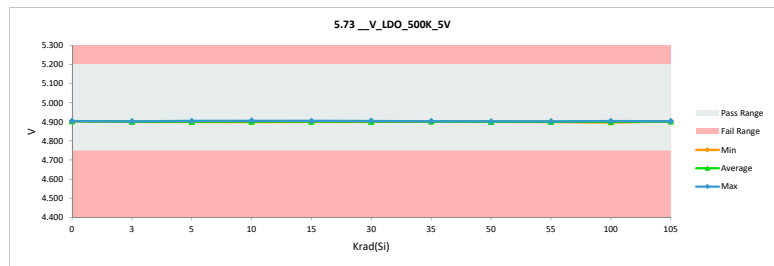


HDR TID Report  
TPS7H5004-SP

5.73 __V_LDO_500K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	4.904	4.904	0.000
0	297	4.902	4.903	0.001
0	298	4.903	4.904	0.001
3	192	4.901	4.900	-0.001
3	193	4.902	4.902	0.000
3	194	4.903	4.902	-0.001
3	196	4.900	4.900	0.000
3	197	4.902	4.902	0.000
5	200	4.901	4.900	-0.001
5	202	4.903	4.902	-0.001
5	203	4.902	4.901	-0.001
5	204	4.904	4.904	0.000
5	206	4.901	4.901	0.000
10	207	4.900	4.899	-0.001
10	209	4.902	4.902	0.000
10	210	4.904	4.903	-0.001
10	211	4.906	4.906	0.000
10	212	4.902	4.902	0.000
15	214	4.903	4.903	0.000
15	217	4.902	4.901	-0.001
15	218	4.904	4.904	0.000
15	219	4.901	4.900	-0.001
15	222	4.905	4.904	-0.001
30	224	4.902	4.900	-0.002
30	225	4.903	4.901	-0.002
30	226	4.905	4.905	0.000
30	227	4.900	4.900	0.000
30	229	4.902	4.901	-0.001
35	230	4.902	4.901	-0.001
35	231	4.902	4.902	0.000
35	232	4.904	4.904	0.000
35	234	4.905	4.903	-0.002
35	237	4.904	4.903	-0.001
50	272	4.902	4.900	-0.002
50	275	4.902	4.901	-0.001
50	276	4.902	4.900	-0.002
50	277	4.902	4.901	-0.001
50	278	4.903	4.901	-0.002
55	279	4.904	4.902	-0.002
55	280	4.901	4.900	-0.001
55	281	4.902	4.901	-0.001
55	282	4.903	4.902	-0.001
55	283	4.902	4.902	0.000
100	317	4.902	4.900	-0.002
100	318	4.904	4.903	-0.001
100	319	4.905	4.902	-0.003
100	320	4.903	4.901	-0.002
100	321	4.906	4.902	-0.004
100	322	4.904	4.901	-0.003
100	323	4.907	4.904	-0.003
100	324	4.904	4.901	-0.003
100	326	4.903	4.898	-0.005
100	327	4.904	4.900	-0.004
100	328	4.903	4.900	-0.003
100	329	4.903	4.898	-0.005
100	332	4.903	4.898	-0.005
100	333	4.905	4.903	-0.002
100	334	4.904	4.901	-0.003
100	335	4.901	4.899	-0.002
100	336	4.901	4.896	-0.005
100	337	4.903	4.900	-0.003
100	338	4.902	4.901	-0.001
100	339	4.902	4.897	-0.005
100	340	4.904	4.903	-0.001
100	341	4.904	4.901	-0.003
100	342	4.905	4.901	-0.004
100	343	4.905	4.903	-0.002
105	344	4.903	4.903	0.000
105	346	4.903	4.902	-0.001
105	349	4.904	4.903	-0.001
105	350	4.904	4.903	-0.001
105	351	4.905	4.903	-0.002
105	352	4.905	4.902	-0.003
Max		4.907	4.906	0.001
Average		4.903	4.901	-0.002
Min		4.900	4.896	-0.005
Std Dev		0.001	0.002	0.001

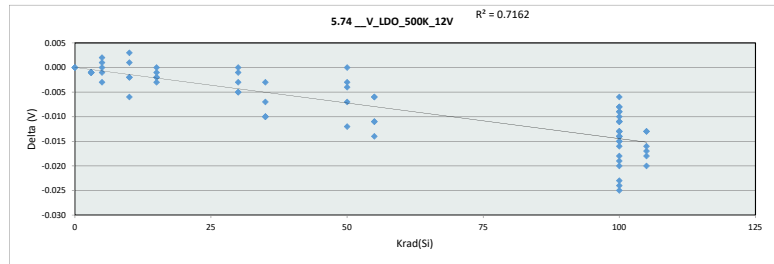


5.73 __V_LDO_500											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.903	4.900	4.900	4.899	4.900	4.900	4.901	4.900	4.900	4.896	4.902
Average	4.904	4.901	4.902	4.902	4.902	4.901	4.903	4.901	4.901	4.901	4.903
Max	4.904	4.902	4.904	4.906	4.904	4.905	4.904	4.901	4.902	4.904	4.903
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

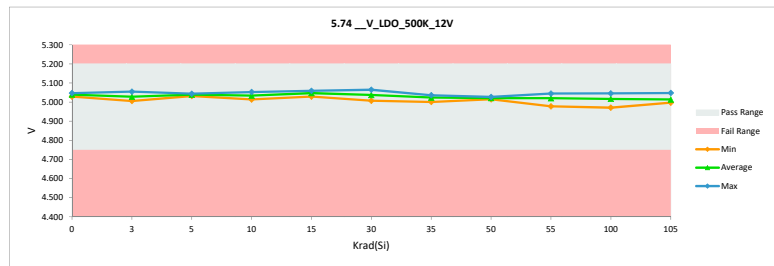


HDR TID Report  
TPS7H5004-SP

5.74 __V_LDO_500K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.042	5.042	0.000
0	297	5.029	5.029	0.000
0	298	5.047	5.047	0.000
3	192	5.029	5.028	-0.001
3	193	5.056	5.055	-0.001
3	194	5.026	5.025	-0.001
3	196	5.007	5.006	-0.001
3	197	5.027	5.026	-0.001
5	200	5.047	5.044	-0.003
5	202	5.041	5.041	0.000
5	203	5.033	5.032	-0.001
5	204	5.038	5.040	0.002
5	206	5.037	5.038	0.001
10	207	5.040	5.038	-0.002
10	209	5.050	5.053	0.003
10	210	5.033	5.031	-0.002
10	211	5.020	5.014	-0.006
10	212	5.033	5.034	0.001
15	214	5.030	5.029	-0.001
15	217	5.062	5.059	-0.003
15	218	5.044	5.044	0.000
15	219	5.060	5.058	-0.002
15	222	5.047	5.045	-0.002
30	224	5.070	5.065	-0.005
30	225	5.046	5.043	-0.003
30	226	5.007	5.007	0.000
30	227	5.057	5.056	-0.001
30	229	5.021	5.016	-0.005
35	230	5.031	5.024	-0.007
35	231	5.035	5.025	-0.010
35	232	5.011	5.001	-0.010
35	234	5.039	5.036	-0.003
35	237	5.043	5.033	-0.010
50	272	5.030	5.027	-0.003
50	275	5.022	5.018	-0.004
50	276	5.029	5.022	-0.007
50	277	5.027	5.015	-0.012
50	278	5.023	5.023	0.000
55	279	5.056	5.045	-0.011
55	280	4.989	4.978	-0.011
55	281	5.028	5.022	-0.006
55	282	5.028	5.014	-0.014
55	283	5.051	5.045	-0.006
100	317	5.036	5.023	-0.013
100	318	5.039	5.025	-0.014
100	319	5.030	5.022	-0.008
100	320	5.019	5.006	-0.013
100	321	4.994	4.971	-0.023
100	322	5.013	5.003	-0.010
100	323	4.998	4.984	-0.014
100	324	5.039	5.024	-0.015
100	326	5.043	5.019	-0.024
100	327	5.013	5.002	-0.011
100	328	5.024	5.013	-0.011
100	329	5.040	5.015	-0.025
100	332	5.035	5.015	-0.020
100	333	5.019	5.010	-0.009
100	334	5.024	5.011	-0.013
100	335	5.054	5.039	-0.015
100	336	5.017	5.006	-0.011
100	337	5.055	5.046	-0.009
100	338	5.041	5.027	-0.014
100	339	5.060	5.042	-0.018
100	340	5.060	5.041	-0.019
100	341	5.034	5.018	-0.016
100	342	5.028	5.022	-0.006
100	343	5.027	5.019	-0.008
105	344	5.020	5.007	-0.013
105	346	5.030	5.012	-0.018
105	349	5.021	5.008	-0.013
105	350	5.033	5.013	-0.020
105	351	5.014	4.997	-0.017
105	352	5.064	5.048	-0.016
Max		5.070	5.065	0.003
Average		5.033	5.025	-0.008
Min		4.989	4.971	-0.025
Std Dev		0.017	0.019	0.007

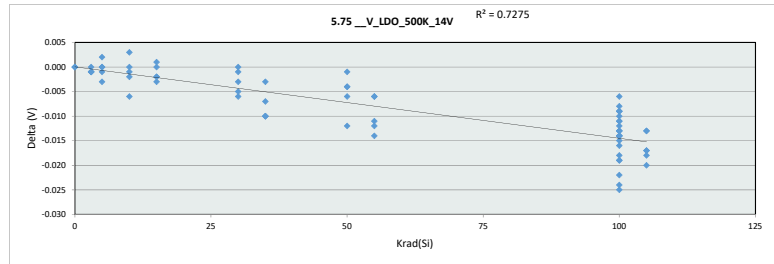


5.74 __V_LDO_500K_12V											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.029	5.006	5.032	5.014	5.029	5.007	5.001	5.015	4.978	4.971	4.997
Average	5.039	5.028	5.039	5.034	5.047	5.037	5.024	5.021	5.021	5.017	5.014
Max	5.047	5.055	5.044	5.053	5.059	5.065	5.036	5.027	5.045	5.046	5.048
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

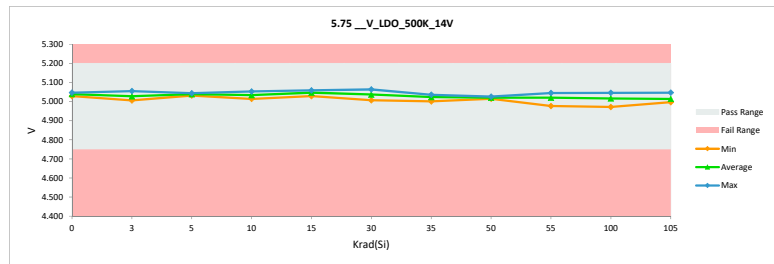


HDR TID Report  
TPS7H5004-SP

5.75 _V_LDO_500K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.042	5.042	0.000
0	297	5.029	5.029	0.000
0	298	5.047	5.047	0.000
3	192	5.028	5.028	0.000
3	193	5.056	5.055	-0.001
3	194	5.026	5.025	-0.001
3	196	5.007	5.006	-0.001
3	197	5.027	5.026	-0.001
5	200	5.047	5.044	-0.003
5	202	5.041	5.041	0.000
5	203	5.033	5.032	-0.001
5	204	5.038	5.040	0.002
5	206	5.037	5.037	0.000
10	207	5.040	5.038	-0.002
10	209	5.050	5.053	0.003
10	210	5.032	5.031	-0.001
10	211	5.020	5.014	-0.006
10	212	5.033	5.033	0.000
15	214	5.029	5.029	0.000
15	217	5.062	5.059	-0.003
15	218	5.043	5.044	0.001
15	219	5.060	5.058	-0.002
15	222	5.047	5.045	-0.002
30	224	5.070	5.064	-0.006
30	225	5.046	5.043	-0.003
30	226	5.007	5.007	0.000
30	227	5.057	5.056	-0.001
30	229	5.021	5.016	-0.005
35	230	5.031	5.024	-0.007
35	231	5.035	5.025	-0.010
35	232	5.011	5.001	-0.010
35	234	5.039	5.036	-0.003
35	237	5.043	5.033	-0.010
50	272	5.030	5.026	-0.004
50	275	5.022	5.018	-0.004
50	276	5.028	5.022	-0.006
50	277	5.027	5.015	-0.012
50	278	5.023	5.022	-0.001
55	279	5.056	5.045	-0.011
55	280	4.989	4.977	-0.012
55	281	5.028	5.022	-0.006
55	282	5.028	5.014	-0.014
55	283	5.051	5.045	-0.006
100	317	5.036	5.023	-0.013
100	318	5.038	5.024	-0.014
100	319	5.030	5.021	-0.009
100	320	5.019	5.006	-0.013
100	321	4.994	4.972	-0.022
100	322	5.012	5.002	-0.010
100	323	4.998	4.984	-0.014
100	324	5.039	5.024	-0.015
100	326	5.043	5.019	-0.024
100	327	5.013	5.001	-0.012
100	328	5.024	5.013	-0.011
100	329	5.040	5.015	-0.025
100	332	5.034	5.015	-0.019
100	333	5.019	5.010	-0.009
100	334	5.024	5.011	-0.013
100	335	5.053	5.039	-0.014
100	336	5.017	5.006	-0.011
100	337	5.055	5.046	-0.009
100	338	5.041	5.027	-0.014
100	339	5.060	5.042	-0.018
100	340	5.060	5.041	-0.019
100	341	5.034	5.018	-0.016
100	342	5.028	5.022	-0.006
100	343	5.027	5.019	-0.008
105	344	5.020	5.007	-0.013
105	346	5.030	5.012	-0.018
105	349	5.021	5.008	-0.013
105	350	5.033	5.013	-0.020
105	351	5.014	4.997	-0.017
105	352	5.064	5.047	-0.017
Max		5.070	5.064	0.003
Average		5.033	5.025	-0.008
Min		4.989	4.972	-0.025
Std Dev		0.017	0.019	0.007

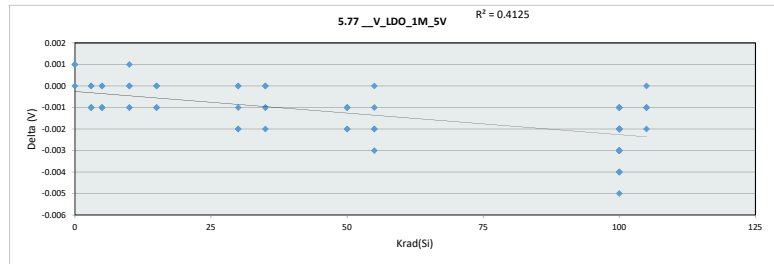


5.75 _V_LDO_500											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.029	5.006	5.032	5.014	5.029	5.007	5.001	5.015	4.977	4.972	4.997
Average	5.039	5.028	5.039	5.034	5.047	5.037	5.024	5.021	5.021	5.017	5.014
Max	5.047	5.055	5.044	5.053	5.059	5.064	5.036	5.026	5.045	5.046	5.047
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

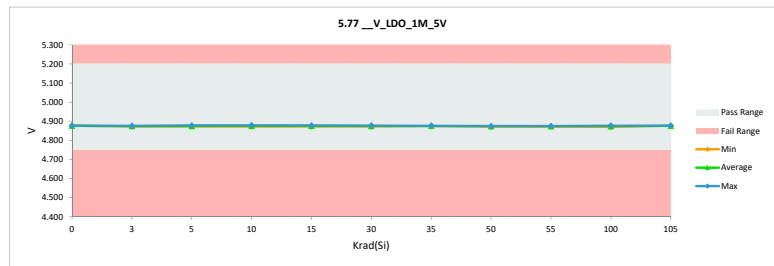


HDR TID Report  
TPS7H5004-SP

5.77 __V_LDO_1M_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	4.876	4.877	0.001
0	297	4.875	4.876	0.001
0	298	4.877	4.877	0.000
3	192	4.874	4.873	-0.001
3	193	4.875	4.875	0.000
3	194	4.877	4.876	-0.001
3	196	4.874	4.873	-0.001
3	197	4.875	4.875	0.000
5	200	4.874	4.873	-0.001
5	202	4.876	4.876	0.000
5	203	4.875	4.874	-0.001
5	204	4.878	4.877	-0.001
5	206	4.873	4.873	0.000
10	207	4.873	4.872	-0.001
10	209	4.875	4.875	0.000
10	210	4.878	4.877	-0.001
10	211	4.879	4.880	0.001
10	212	4.875	4.875	0.000
15	214	4.877	4.877	0.000
15	217	4.875	4.874	-0.001
15	218	4.878	4.877	-0.001
15	219	4.874	4.873	-0.001
15	222	4.878	4.878	0.000
30	224	4.875	4.873	-0.002
30	225	4.876	4.874	-0.002
30	226	4.878	4.878	0.000
30	227	4.873	4.873	0.000
30	229	4.875	4.874	-0.001
35	230	4.875	4.875	0.000
35	231	4.876	4.875	-0.001
35	232	4.877	4.877	0.000
35	234	4.878	4.876	-0.002
35	237	4.877	4.876	-0.001
50	272	4.875	4.874	-0.001
50	275	4.875	4.874	-0.001
50	276	4.874	4.872	-0.002
50	277	4.875	4.874	-0.001
50	278	4.876	4.874	-0.002
55	279	4.878	4.875	-0.003
55	280	4.874	4.872	-0.002
55	281	4.876	4.874	-0.002
55	282	4.876	4.875	-0.001
55	283	4.875	4.875	0.000
100	317	4.875	4.874	-0.001
100	318	4.877	4.876	-0.001
100	319	4.877	4.875	-0.002
100	320	4.876	4.874	-0.002
100	321	4.879	4.876	-0.003
100	322	4.877	4.874	-0.003
100	323	4.880	4.877	-0.003
100	324	4.877	4.874	-0.003
100	326	4.877	4.873	-0.004
100	327	4.877	4.874	-0.003
100	328	4.876	4.874	-0.002
100	329	4.876	4.872	-0.004
100	332	4.877	4.872	-0.005
100	333	4.879	4.877	-0.002
100	334	4.877	4.876	-0.001
100	335	4.874	4.872	-0.002
100	336	4.874	4.871	-0.003
100	337	4.876	4.873	-0.003
100	338	4.875	4.874	-0.001
100	339	4.876	4.872	-0.004
100	340	4.877	4.875	-0.002
100	341	4.877	4.874	-0.003
100	342	4.878	4.874	-0.004
100	343	4.878	4.877	-0.001
105	344	4.877	4.877	0.000
105	346	4.877	4.876	-0.001
105	349	4.877	4.876	-0.001
105	350	4.878	4.877	-0.001
105	351	4.878	4.876	-0.002
105	352	4.878	4.877	-0.001
Max		4.880	4.880	0.001
Average		4.876	4.875	-0.001
Min		4.873	4.871	-0.005
Std Dev		0.002	0.002	0.001

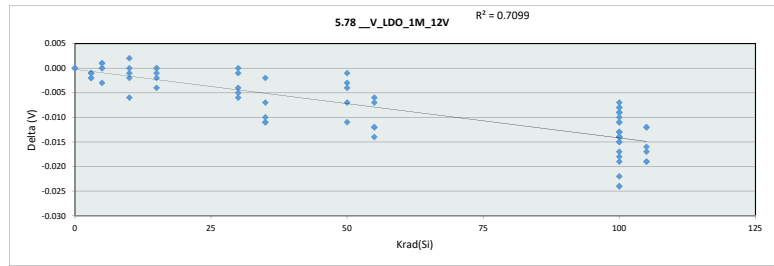


5.77 __V_LDO_1M											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.876	4.873	4.873	4.872	4.873	4.873	4.875	4.872	4.872	4.871	4.876
Average	4.877	4.874	4.875	4.876	4.876	4.874	4.876	4.874	4.874	4.874	4.877
Max	4.877	4.876	4.877	4.880	4.878	4.878	4.877	4.874	4.875	4.877	4.877
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

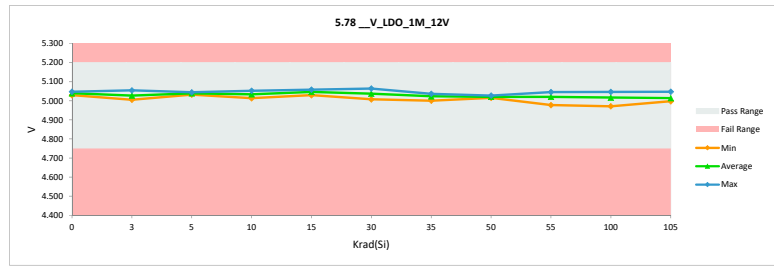


HDR TID Report  
TPS7H5004-SP

5.78 __V_LDO_1M_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.041	5.041	0.000
0	297	5.029	5.029	0.000
0	298	5.047	5.047	0.000
3	192	5.028	5.027	-0.001
3	193	5.055	5.054	-0.001
3	194	5.026	5.024	-0.002
3	196	5.006	5.005	-0.001
3	197	5.027	5.025	-0.002
5	200	5.047	5.044	-0.003
5	202	5.041	5.041	0.000
5	203	5.032	5.032	0.000
5	204	5.038	5.039	0.001
5	206	5.036	5.037	0.001
10	207	5.039	5.037	-0.002
10	209	5.050	5.052	0.002
10	210	5.032	5.031	-0.001
10	211	5.019	5.013	-0.006
10	212	5.033	5.033	0.000
15	214	5.029	5.029	0.000
15	217	5.062	5.058	-0.004
15	218	5.043	5.043	0.000
15	219	5.059	5.058	-0.001
15	222	5.046	5.044	-0.002
30	224	5.070	5.064	-0.006
30	225	5.046	5.042	-0.004
30	226	5.007	5.007	0.000
30	227	5.056	5.055	-0.001
30	229	5.021	5.016	-0.005
35	230	5.030	5.023	-0.007
35	231	5.035	5.024	-0.011
35	232	5.011	5.000	-0.011
35	234	5.038	5.036	-0.002
35	237	5.042	5.032	-0.010
50	272	5.030	5.027	-0.003
50	275	5.021	5.017	-0.004
50	276	5.028	5.021	-0.007
50	277	5.026	5.015	-0.011
50	278	5.022	5.021	-0.001
55	279	5.056	5.044	-0.012
55	280	4.989	4.977	-0.012
55	281	5.028	5.021	-0.007
55	282	5.027	5.013	-0.014
55	283	5.051	5.045	-0.006
100	317	5.036	5.023	-0.013
100	318	5.038	5.024	-0.014
100	319	5.029	5.021	-0.008
100	320	5.019	5.006	-0.013
100	321	4.993	4.971	-0.022
100	322	5.011	5.002	-0.009
100	323	4.997	4.983	-0.014
100	324	5.038	5.024	-0.014
100	326	5.043	5.019	-0.024
100	327	5.012	5.001	-0.011
100	328	5.023	5.012	-0.011
100	329	5.039	5.015	-0.024
100	332	5.033	5.014	-0.019
100	333	5.018	5.009	-0.009
100	334	5.023	5.010	-0.013
100	335	5.053	5.038	-0.015
100	336	5.016	5.006	-0.010
100	337	5.055	5.046	-0.009
100	338	5.040	5.026	-0.014
100	339	5.059	5.042	-0.017
100	340	5.059	5.041	-0.018
100	341	5.033	5.018	-0.015
100	342	5.028	5.021	-0.007
100	343	5.027	5.019	-0.008
105	344	5.019	5.007	-0.012
105	346	5.030	5.011	-0.019
105	349	5.020	5.008	-0.012
105	350	5.032	5.013	-0.019
105	351	5.014	4.997	-0.017
105	352	5.063	5.047	-0.016
Max		5.070	5.064	0.002
Average		5.033	5.025	-0.008
Min		4.989	4.971	-0.024
Std Dev		0.017	0.019	0.007

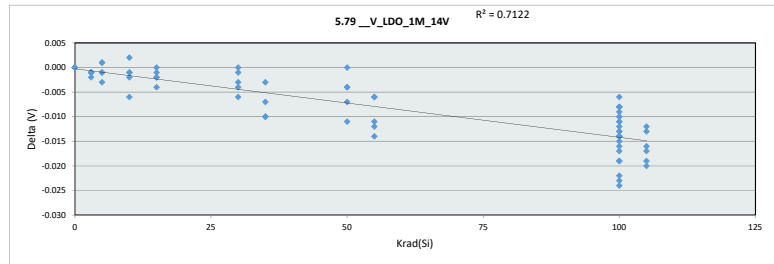


5.78 __V_LDO_1M											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.029	5.005	5.032	5.013	5.029	5.007	5.000	5.015	4.977	4.971	4.997
Average	5.039	5.027	5.039	5.033	5.046	5.037	5.023	5.020	5.020	5.016	5.014
Max	5.047	5.054	5.044	5.052	5.058	5.064	5.036	5.027	5.045	5.046	5.047
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

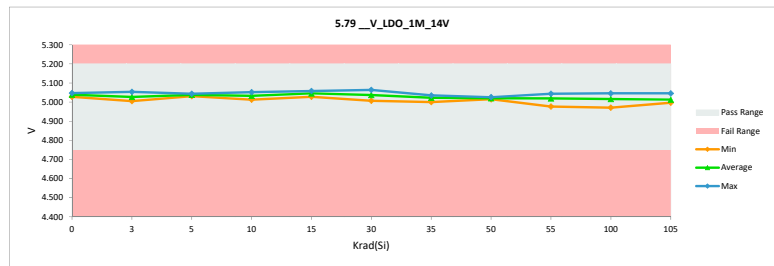


HDR TID Report  
TPS7H5004-SP

5.79 _V_LDO_1M_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.041	5.041	0.000
0	297	5.028	5.028	0.000
0	298	5.047	5.047	0.000
3	192	5.028	5.027	-0.001
3	193	5.055	5.054	-0.001
3	194	5.025	5.024	-0.001
3	196	5.006	5.005	-0.001
3	197	5.027	5.025	-0.002
5	200	5.047	5.044	-0.003
5	202	5.041	5.040	-0.001
5	203	5.032	5.031	-0.001
5	204	5.038	5.039	0.001
5	206	5.036	5.037	0.001
10	207	5.038	5.037	-0.001
10	209	5.050	5.052	0.002
10	210	5.032	5.030	-0.002
10	211	5.019	5.013	-0.006
10	212	5.033	5.032	-0.001
15	214	5.029	5.028	-0.001
15	217	5.062	5.058	-0.004
15	218	5.043	5.043	0.000
15	219	5.059	5.057	-0.002
15	222	5.046	5.044	-0.002
30	224	5.070	5.064	-0.006
30	225	5.045	5.042	-0.003
30	226	5.007	5.007	0.000
30	227	5.056	5.055	-0.001
30	229	5.020	5.016	-0.004
35	230	5.030	5.023	-0.007
35	231	5.034	5.024	-0.010
35	232	5.010	5.000	-0.010
35	234	5.038	5.035	-0.003
35	237	5.042	5.032	-0.010
50	272	5.030	5.026	-0.004
50	275	5.021	5.017	-0.004
50	276	5.028	5.021	-0.007
50	277	5.026	5.015	-0.011
50	278	5.022	5.022	0.000
55	279	5.056	5.044	-0.012
55	280	4.988	4.977	-0.011
55	281	5.027	5.021	-0.006
55	282	5.027	5.013	-0.014
55	283	5.050	5.044	-0.006
100	317	5.036	5.023	-0.013
100	318	5.038	5.024	-0.014
100	319	5.029	5.021	-0.008
100	320	5.018	5.006	-0.012
100	321	4.993	4.971	-0.022
100	322	5.011	5.001	-0.010
100	323	4.997	4.983	-0.014
100	324	5.038	5.024	-0.014
100	326	5.042	5.019	-0.023
100	327	5.012	5.001	-0.011
100	328	5.023	5.012	-0.011
100	329	5.039	5.015	-0.024
100	332	5.033	5.014	-0.019
100	333	5.018	5.009	-0.009
100	334	5.023	5.010	-0.013
100	335	5.053	5.038	-0.015
100	336	5.016	5.006	-0.010
100	337	5.054	5.046	-0.008
100	338	5.040	5.026	-0.014
100	339	5.059	5.042	-0.017
100	340	5.059	5.040	-0.019
100	341	5.033	5.017	-0.016
100	342	5.027	5.021	-0.006
100	343	5.026	5.018	-0.008
105	344	5.019	5.007	-0.012
105	346	5.030	5.011	-0.019
105	349	5.020	5.007	-0.013
105	350	5.032	5.012	-0.020
105	351	5.014	4.997	-0.017
105	352	5.062	5.046	-0.016
Max		5.070	5.064	0.002
Average		5.033	5.025	-0.008
Min		4.988	4.971	-0.024
Std Dev		0.017	0.019	0.007

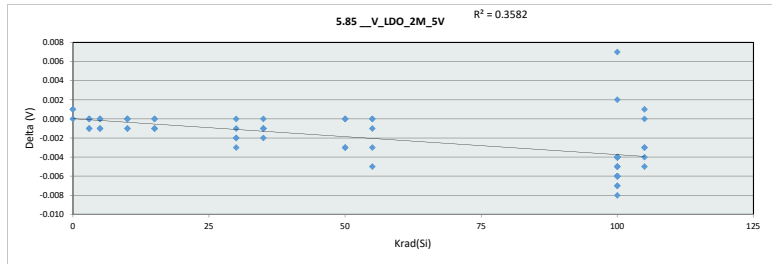


5.79 _V_LDO_1M											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.028	5.005	5.031	5.013	5.028	5.007	5.000	5.015	4.977	4.971	4.997
Average	5.039	5.027	5.038	5.033	5.046	5.037	5.023	5.020	5.020	5.016	5.013
Max	5.047	5.054	5.044	5.052	5.058	5.064	5.035	5.026	5.044	5.046	5.046
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

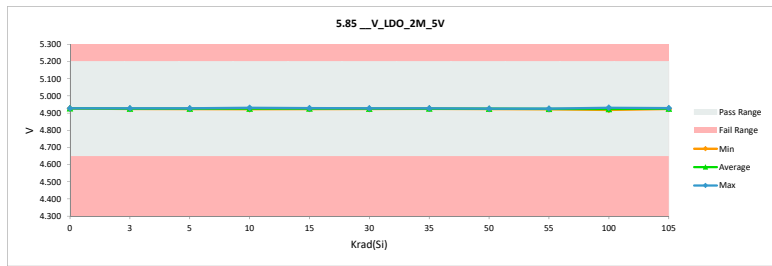


HDR TID Report  
TPS7H5004-SP

5.85_V_LDO_2M_5V					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			V	5.2	4.65
			V	5.2	4.65
Krad(Si)	Serial #	PRE	POST	Delta	
0	295	4.927	4.928	0.001	
0	297	4.926	4.927	0.001	
0	298	4.928	4.928	0.000	
3	192	4.925	4.924	-0.001	
3	193	4.925	4.925	0.000	
3	194	4.928	4.928	0.000	
3	196	4.925	4.924	-0.001	
3	197	4.928	4.928	0.000	
5	200	4.925	4.924	-0.001	
5	202	4.927	4.926	-0.001	
5	203	4.925	4.925	0.000	
5	204	4.928	4.928	0.000	
5	206	4.924	4.924	0.000	
10	207	4.923	4.922	-0.001	
10	209	4.925	4.925	0.000	
10	210	4.929	4.928	-0.001	
10	211	4.930	4.930	0.000	
10	212	4.925	4.925	0.000	
15	214	4.928	4.928	0.000	
15	217	4.926	4.926	0.000	
15	218	4.929	4.928	-0.001	
15	219	4.925	4.924	-0.001	
15	222	4.930	4.929	-0.001	
30	224	4.926	4.923	-0.003	
30	225	4.926	4.924	-0.002	
30	226	4.929	4.928	-0.001	
30	227	4.924	4.924	0.000	
30	229	4.926	4.924	-0.002	
35	230	4.928	4.928	0.000	
35	231	4.928	4.927	-0.001	
35	232	4.928	4.927	-0.001	
35	234	4.929	4.927	-0.002	
35	237	4.927	4.926	-0.001	
50	272	4.926	4.923	-0.003	
50	275	4.925	4.925	0.000	
50	276	4.924	4.924	0.000	
50	277	4.926	4.926	0.000	
50	278	4.927	4.924	-0.003	
55	279	4.929	4.924	-0.005	
55	280	4.923	4.923	0.000	
55	281	4.926	4.925	-0.001	
55	282	4.926	4.926	0.000	
55	283	4.925	4.922	-0.003	
100	317	4.927	4.923	-0.004	
100	318	4.929	4.925	-0.004	
100	319	4.928	4.924	-0.004	
100	320	4.927	4.923	-0.004	
100	321	4.929	4.924	-0.005	
100	322	4.929	4.923	-0.006	
100	323	4.932	4.926	-0.006	
100	324	4.927	4.921	-0.006	
100	326	4.928	4.921	-0.007	
100	327	4.927	4.923	-0.004	
100	328	4.927	4.923	-0.004	
100	329	4.927	4.929	0.002	
100	332	4.927	4.921	-0.006	
100	333	4.930	4.926	-0.004	
100	334	4.929	4.925	-0.004	
100	335	4.924	4.931	0.007	
100	336	4.924	4.918	-0.006	
100	337	4.926	4.921	-0.005	
100	338	4.926	4.922	-0.004	
100	339	4.927	4.919	-0.008	
100	340	4.928	4.924	-0.004	
100	341	4.927	4.922	-0.005	
100	342	4.929	4.922	-0.007	
100	343	4.930	4.926	-0.004	
105	344	4.927	4.927	0.000	
105	346	4.928	4.929	0.001	
105	349	4.928	4.924	-0.004	
105	350	4.929	4.926	-0.003	
105	351	4.930	4.925	-0.005	
105	352	4.929	4.926	-0.003	
	Max	4.932	4.931	0.007	
	Average	4.927	4.925	-0.002	
	Min	4.923	4.918	-0.008	
	Std Dev	0.002	0.003	0.003	



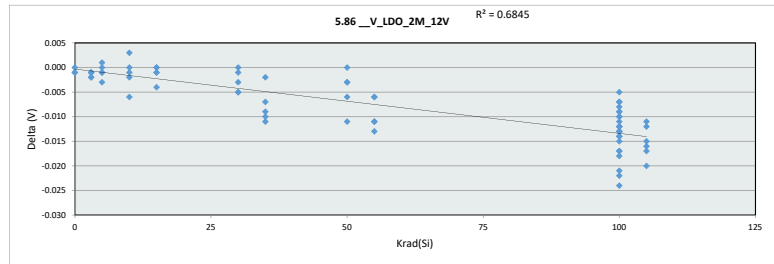
5.85_V_LDO_2M											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			5.2	4.65	V						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	4.927	4.924	4.924	4.922	4.924	4.923	4.926	4.923	4.922	4.918	4.924
Average	4.928	4.926	4.925	4.926	4.927	4.925	4.927	4.924	4.924	4.923	4.926
Max	4.928	4.928	4.928	4.930	4.929	4.928	4.928	4.926	4.926	4.931	4.929
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



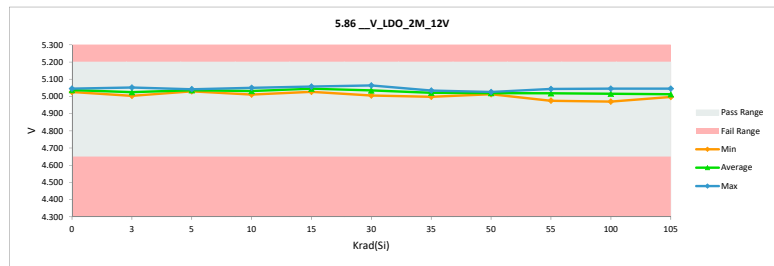


HDR TID Report  
TPS7H5004-SP

5.86 __V_LDO_2M_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.65	4.65		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.040	5.039	-0.001
0	297	5.027	5.026	-0.001
0	298	5.045	5.045	0.000
3	192	5.026	5.025	-0.001
3	193	5.053	5.052	-0.001
3	194	5.024	5.022	-0.002
3	196	5.004	5.003	-0.001
3	197	5.026	5.024	-0.002
5	200	5.045	5.042	-0.003
5	202	5.039	5.038	-0.001
5	203	5.030	5.029	-0.001
5	204	5.036	5.037	0.001
5	206	5.034	5.034	0.000
10	207	5.036	5.034	-0.002
10	209	5.047	5.050	0.003
10	210	5.030	5.029	-0.001
10	211	5.017	5.011	-0.006
10	212	5.031	5.031	0.000
15	214	5.027	5.027	0.000
15	217	5.061	5.057	-0.004
15	218	5.041	5.041	0.000
15	219	5.057	5.056	-0.001
15	222	5.044	5.043	-0.001
30	224	5.069	5.064	-0.005
30	225	5.043	5.040	-0.003
30	226	5.005	5.005	0.000
30	227	5.054	5.053	-0.001
30	229	5.019	5.014	-0.005
35	230	5.029	5.022	-0.007
35	231	5.033	5.023	-0.010
35	232	5.009	4.998	-0.011
35	234	5.036	5.034	-0.002
35	237	5.039	5.030	-0.009
50	272	5.029	5.026	-0.003
50	275	5.019	5.016	-0.003
50	276	5.026	5.020	-0.006
50	277	5.024	5.013	-0.011
50	278	5.020	5.020	0.000
55	279	5.054	5.043	-0.011
55	280	4.986	4.975	-0.011
55	281	5.025	5.019	-0.006
55	282	5.024	5.011	-0.013
55	283	5.049	5.043	-0.006
100	317	5.034	5.022	-0.012
100	318	5.036	5.024	-0.012
100	319	5.028	5.021	-0.007
100	320	5.018	5.006	-0.012
100	321	4.991	4.970	-0.021
100	322	5.009	4.999	-0.010
100	323	4.996	4.982	-0.014
100	324	5.035	5.022	-0.013
100	326	5.040	5.018	-0.022
100	327	5.011	5.001	-0.010
100	328	5.021	5.010	-0.011
100	329	5.037	5.013	-0.024
100	332	5.031	5.013	-0.018
100	333	5.016	5.007	-0.009
100	334	5.021	5.008	-0.013
100	335	5.051	5.037	-0.014
100	336	5.014	5.005	-0.009
100	337	5.053	5.045	-0.008
100	338	5.038	5.025	-0.013
100	339	5.058	5.041	-0.017
100	340	5.056	5.039	-0.017
100	341	5.031	5.016	-0.015
100	342	5.025	5.020	-0.005
100	343	5.025	5.018	-0.007
105	344	5.017	5.006	-0.011
105	346	5.027	5.010	-0.017
105	349	5.018	5.006	-0.012
105	350	5.031	5.011	-0.020
105	351	5.013	4.997	-0.016
105	352	5.060	5.045	-0.015
Max		5.069	5.064	0.003
Average		5.031	5.023	-0.008
Min		4.986	4.970	-0.024
Std Dev		0.017	0.019	0.007

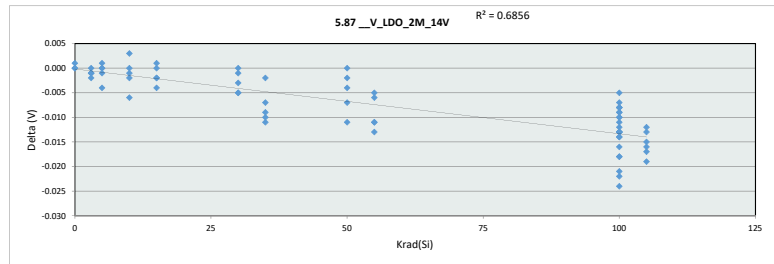


5.86 __V_LDO_2M											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.65	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.026	5.003	5.029	5.011	5.027	5.005	4.998	5.013	4.975	4.970	4.997
Average	5.037	5.025	5.036	5.031	5.045	5.035	5.021	5.019	5.018	5.015	5.013
Max	5.045	5.052	5.042	5.050	5.057	5.064	5.034	5.026	5.043	5.045	5.045
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

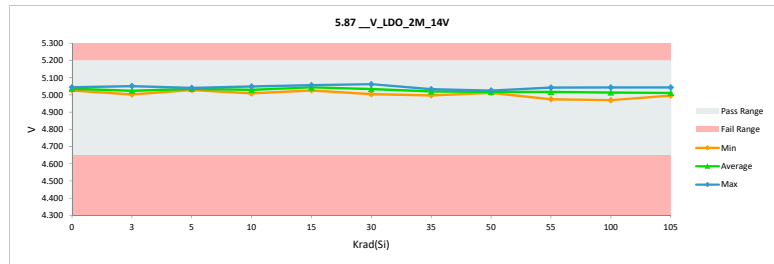


HDR TID Report  
TPS7H5004-SP

5.87 __V_LDO_2M_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.65	4.65		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	5.039	5.039	0.000
0	297	5.026	5.027	0.001
0	298	5.045	5.045	0.000
3	192	5.026	5.025	-0.001
3	193	5.053	5.052	-0.001
3	194	5.023	5.022	-0.001
3	196	5.003	5.003	0.000
3	197	5.025	5.023	-0.002
5	200	5.045	5.041	-0.004
5	202	5.038	5.038	0.000
5	203	5.030	5.029	-0.001
5	204	5.036	5.037	0.001
5	206	5.034	5.034	0.000
10	207	5.036	5.034	-0.002
10	209	5.047	5.050	0.003
10	210	5.030	5.029	-0.001
10	211	5.016	5.010	-0.006
10	212	5.030	5.030	0.000
15	214	5.027	5.027	0.000
15	217	5.061	5.057	-0.004
15	218	5.040	5.041	0.001
15	219	5.057	5.055	-0.002
15	222	5.044	5.042	-0.002
30	224	5.068	5.063	-0.005
30	225	5.042	5.039	-0.003
30	226	5.005	5.005	0.000
30	227	5.054	5.053	-0.001
30	229	5.019	5.014	-0.005
35	230	5.029	5.022	-0.007
35	231	5.033	5.022	-0.011
35	232	5.008	4.998	-0.010
35	234	5.036	5.034	-0.002
35	237	5.039	5.030	-0.009
50	272	5.028	5.026	-0.002
50	275	5.019	5.015	-0.004
50	276	5.026	5.019	-0.007
50	277	5.024	5.013	-0.011
50	278	5.019	5.019	0.000
55	279	5.053	5.042	-0.011
55	280	4.986	4.975	-0.011
55	281	5.025	5.019	-0.006
55	282	5.024	5.011	-0.013
55	283	5.048	5.043	-0.005
100	317	5.034	5.022	-0.012
100	318	5.036	5.023	-0.013
100	319	5.027	5.020	-0.007
100	320	5.017	5.006	-0.011
100	321	4.991	4.970	-0.021
100	322	5.008	4.999	-0.009
100	323	4.996	4.982	-0.014
100	324	5.035	5.022	-0.013
100	326	5.040	5.018	-0.022
100	327	5.010	5.000	-0.010
100	328	5.020	5.010	-0.010
100	329	5.037	5.013	-0.024
100	332	5.030	5.012	-0.018
100	333	5.015	5.007	-0.008
100	334	5.020	5.007	-0.013
100	335	5.050	5.037	-0.013
100	336	5.014	5.005	-0.009
100	337	5.052	5.044	-0.008
100	338	5.038	5.025	-0.013
100	339	5.057	5.041	-0.016
100	340	5.056	5.038	-0.018
100	341	5.030	5.016	-0.014
100	342	5.025	5.020	-0.005
100	343	5.025	5.017	-0.008
105	344	5.017	5.005	-0.012
105	346	5.027	5.010	-0.017
105	349	5.018	5.005	-0.013
105	350	5.030	5.011	-0.019
105	351	5.013	4.997	-0.016
105	352	5.059	5.044	-0.015
Max		5.068	5.063	0.003
Average		5.030	5.023	-0.007
Min		4.986	4.970	-0.024
Std Dev		0.017	0.019	0.007



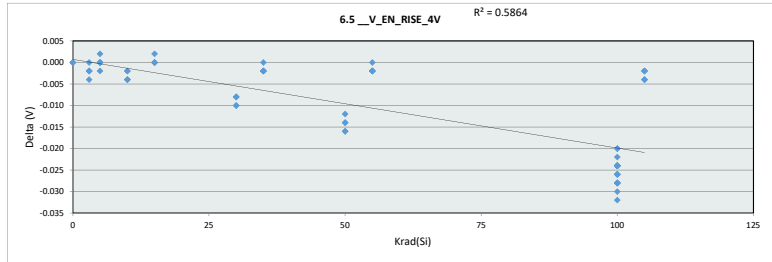
5.87 __V_LDO_2M											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.65	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.027	5.003	5.029	5.010	5.027	5.005	4.998	5.013	4.975	4.970	4.997
Average	5.037	5.025	5.036	5.031	5.044	5.035	5.021	5.018	5.018	5.015	5.012
Max	5.045	5.052	5.041	5.050	5.057	5.063	5.034	5.026	5.043	5.044	5.044
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



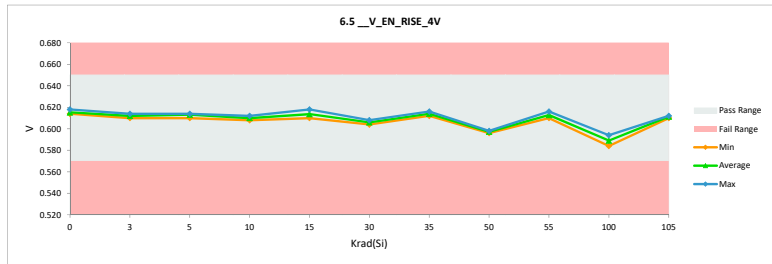
HDR TID Report  
TPS7H5004-SP

6.5_V_EN_RISE_4V				
Test Site				
Tester				
Test Number				
Unit	V		V	
Max Limit	0.65		0.65	
Min Limit	0.57		0.57	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.618	0.618	0.000
0	297	0.614	0.614	0.000
0	298	0.614	0.614	0.000
3	192	0.614	0.610	-0.004
3	193	0.614	0.612	-0.002
3	194	0.612	0.612	0.000
3	196	0.614	0.612	-0.002
3	197	0.616	0.614	-0.002
5	200	0.614	0.614	0.000
5	202	0.612	0.610	-0.002
5	203	0.614	0.614	0.000
5	204	0.612	0.614	0.002
5	206	0.614	0.614	0.000
10	207	0.612	0.610	-0.002
10	209	0.612	0.608	-0.004
10	210	0.614	0.610	-0.004
10	211	0.612	0.610	-0.002
10	212	0.616	0.612	-0.004
15	214	0.608	0.610	0.002
15	217	0.612	0.612	0.000
15	218	0.616	0.616	0.000
15	219	0.618	0.618	0.000
15	222	0.612	0.612	0.000
30	224	0.616	0.608	-0.008
30	225	0.612	0.604	-0.008
30	226	0.614	0.604	-0.010
30	227	0.616	0.606	-0.010
30	229	0.616	0.608	-0.008
35	230	0.616	0.614	-0.002
35	231	0.614	0.614	0.000
35	232	0.616	0.614	-0.002
35	234	0.614	0.612	-0.002
35	237	0.618	0.616	-0.002
50	272	0.614	0.598	-0.016
50	275	0.608	0.596	-0.012
50	276	0.614	0.598	-0.016
50	277	0.612	0.598	-0.014
50	278	0.610	0.596	-0.014
55	279	0.612	0.610	-0.002
55	280	0.614	0.612	-0.002
55	281	0.614	0.614	0.000
55	282	0.614	0.612	-0.002
55	283	0.618	0.616	-0.002
100	317	0.616	0.586	-0.030
100	318	0.614	0.590	-0.024
100	319	0.614	0.586	-0.028
100	320	0.616	0.592	-0.024
100	321	0.610	0.590	-0.020
100	322	0.612	0.588	-0.024
100	323	0.616	0.588	-0.028
100	324	0.616	0.592	-0.024
100	326	0.612	0.586	-0.026
100	327	0.616	0.588	-0.028
100	328	0.612	0.584	-0.028
100	329	0.614	0.588	-0.026
100	332	0.616	0.590	-0.026
100	333	0.614	0.586	-0.028
100	334	0.616	0.592	-0.024
100	335	0.614	0.594	-0.020
100	336	0.616	0.592	-0.024
100	337	0.616	0.584	-0.032
100	338	0.614	0.590	-0.024
100	339	0.614	0.590	-0.024
100	340	0.614	0.588	-0.026
100	341	0.616	0.592	-0.024
100	342	0.612	0.588	-0.024
100	343	0.616	0.594	-0.022
105	344	0.614	0.610	-0.004
105	346	0.614	0.612	-0.002
105	349	0.616	0.612	-0.004
105	350	0.612	0.610	-0.002
105	351	0.614	0.612	-0.002
105	352	0.616	0.612	-0.004
Max		0.618	0.618	0.002
Average		0.614	0.603	-0.011
Min		0.608	0.584	-0.032
Std Dev		0.002	0.011	0.011

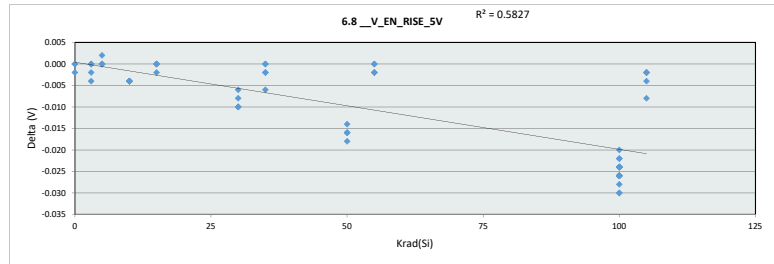


6.5_V_EN_RISE											
Test Site											
Tester											
Test Number											
Max Limit	0.65										
Min Limit	0.57										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.614	0.610	0.610	0.608	0.610	0.604	0.612	0.596	0.610	0.584	0.610
Average	0.615	0.612	0.613	0.610	0.614	0.606	0.614	0.597	0.613	0.589	0.611
Max	0.618	0.614	0.614	0.612	0.618	0.608	0.616	0.598	0.616	0.594	0.612
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

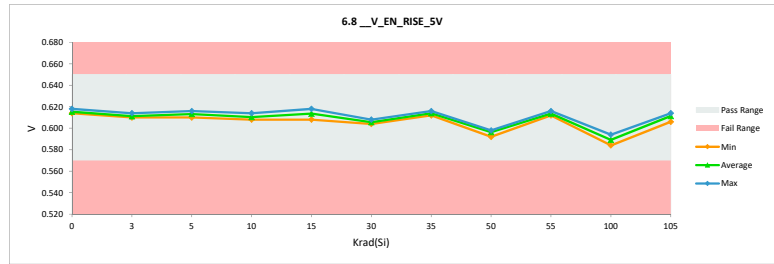


HDR TID Report  
TPS7H5004-SP

6.8 __V_EN_RISE_5V				
Test Site	Tester	Test Number	Unit	
			V	V
			Max Limit	0.65
			Min Limit	0.57
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.618	0.618	0.000
0	297	0.614	0.614	0.000
0	298	0.616	0.614	-0.002
3	192	0.614	0.610	-0.004
3	193	0.614	0.614	0.000
3	194	0.612	0.612	0.000
3	196	0.610	0.610	0.000
3	197	0.612	0.610	-0.002
5	200	0.610	0.610	0.000
5	202	0.612	0.612	0.000
5	203	0.614	0.614	0.000
5	204	0.612	0.614	0.002
5	206	0.616	0.616	0.000
10	207	0.612	0.608	-0.004
10	209	0.614	0.610	-0.004
10	210	0.614	0.610	-0.004
10	211	0.614	0.610	-0.004
10	212	0.618	0.614	-0.004
15	214	0.610	0.608	-0.002
15	217	0.612	0.612	0.000
15	218	0.616	0.616	0.000
15	219	0.618	0.618	0.000
15	222	0.614	0.614	0.000
30	224	0.618	0.608	-0.010
30	225	0.612	0.604	-0.008
30	226	0.610	0.604	-0.006
30	227	0.616	0.606	-0.010
30	229	0.616	0.606	-0.010
35	230	0.616	0.614	-0.002
35	231	0.616	0.616	0.000
35	232	0.616	0.614	-0.002
35	234	0.614	0.614	0.000
35	237	0.618	0.612	-0.006
50	272	0.614	0.598	-0.016
50	275	0.610	0.592	-0.018
50	276	0.614	0.598	-0.016
50	277	0.612	0.598	-0.014
50	278	0.612	0.596	-0.016
55	279	0.614	0.612	-0.002
55	280	0.614	0.614	0.000
55	281	0.614	0.614	0.000
55	282	0.614	0.612	-0.002
55	283	0.618	0.616	-0.002
100	317	0.612	0.586	-0.026
100	318	0.614	0.590	-0.024
100	319	0.610	0.586	-0.024
100	320	0.616	0.592	-0.024
100	321	0.612	0.586	-0.026
100	322	0.612	0.588	-0.024
100	323	0.614	0.590	-0.024
100	324	0.616	0.592	-0.024
100	326	0.612	0.588	-0.024
100	327	0.616	0.588	-0.028
100	328	0.614	0.584	-0.030
100	329	0.614	0.588	-0.026
100	332	0.616	0.590	-0.026
100	333	0.614	0.588	-0.026
100	334	0.616	0.594	-0.022
100	335	0.614	0.594	-0.020
100	336	0.618	0.592	-0.026
100	337	0.614	0.584	-0.030
100	338	0.616	0.590	-0.026
100	339	0.614	0.590	-0.024
100	340	0.614	0.588	-0.026
100	341	0.616	0.592	-0.024
100	342	0.612	0.590	-0.022
100	343	0.616	0.590	-0.026
105	344	0.614	0.612	-0.002
105	346	0.614	0.612	-0.002
105	349	0.616	0.612	-0.004
105	350	0.614	0.612	-0.002
105	351	0.614	0.606	-0.008
105	352	0.616	0.614	-0.002
	Max	0.618	0.618	0.002
	Average	0.614	0.603	-0.011
	Min	0.610	0.584	-0.030
	Std Dev	0.002	0.011	0.011



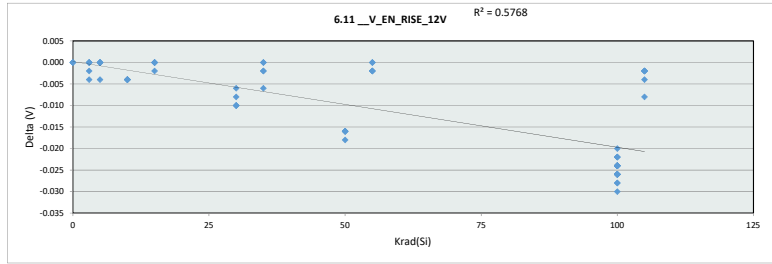
6.8 __V_EN_RISE															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
			0.65	0.57	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
			V	V	0.614	0.610	0.610	0.608	0.608	0.604	0.612	0.592	0.612	0.584	0.606
					0.615	0.611	0.613	0.610	0.614	0.606	0.614	0.596	0.614	0.589	0.611
					0.618	0.614	0.616	0.614	0.618	0.608	0.616	0.598	0.616	0.594	0.614
					0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



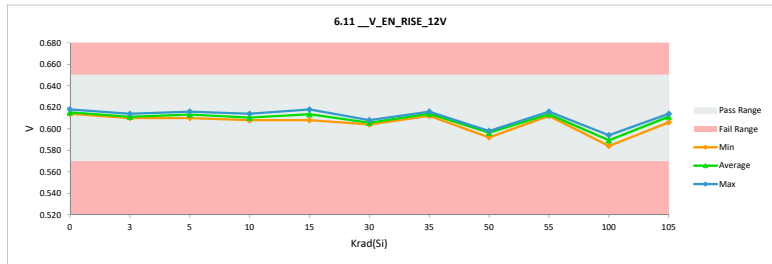
HDR TID Report  
TPS7H5004-SP

6.11 __V_EN_RISE_12V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	0.65	0.65
Min Limit	0.57	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.618	0.618	0.000
0	297	0.614	0.614	0.000
0	298	0.614	0.614	0.000
3	192	0.614	0.610	-0.004
3	193	0.614	0.614	0.000
3	194	0.612	0.612	0.000
3	196	0.610	0.610	0.000
3	197	0.612	0.610	-0.002
5	200	0.614	0.610	-0.004
5	202	0.612	0.612	0.000
5	203	0.614	0.614	0.000
5	204	0.614	0.614	0.000
5	206	0.616	0.616	0.000
10	207	0.612	0.608	-0.004
10	209	0.614	0.610	-0.004
10	210	0.614	0.610	-0.004
10	211	0.614	0.610	-0.004
10	212	0.618	0.614	-0.004
15	214	0.610	0.608	-0.002
15	217	0.612	0.612	0.000
15	218	0.616	0.616	0.000
15	219	0.618	0.618	0.000
15	222	0.614	0.614	0.000
30	224	0.618	0.608	-0.010
30	225	0.612	0.604	-0.008
30	226	0.610	0.604	-0.006
30	227	0.616	0.606	-0.010
30	229	0.616	0.606	-0.010
35	230	0.616	0.614	-0.002
35	231	0.616	0.616	0.000
35	232	0.616	0.614	-0.002
35	234	0.614	0.614	0.000
35	237	0.618	0.612	-0.006
50	272	0.614	0.598	-0.016
50	275	0.610	0.592	-0.018
50	276	0.614	0.598	-0.016
50	277	0.614	0.598	-0.016
50	278	0.612	0.596	-0.016
55	279	0.614	0.612	-0.002
55	280	0.614	0.614	0.000
55	281	0.614	0.614	0.000
55	282	0.614	0.612	-0.002
55	283	0.618	0.616	-0.002
100	317	0.612	0.586	-0.026
100	318	0.614	0.592	-0.022
100	319	0.610	0.586	-0.024
100	320	0.616	0.592	-0.024
100	321	0.612	0.586	-0.026
100	322	0.612	0.588	-0.024
100	323	0.614	0.590	-0.024
100	324	0.616	0.592	-0.024
100	326	0.612	0.588	-0.024
100	327	0.616	0.588	-0.028
100	328	0.612	0.584	-0.028
100	329	0.614	0.588	-0.026
100	332	0.616	0.590	-0.026
100	333	0.614	0.588	-0.026
100	334	0.616	0.594	-0.022
100	335	0.614	0.594	-0.020
100	336	0.618	0.592	-0.026
100	337	0.614	0.584	-0.030
100	338	0.616	0.590	-0.026
100	339	0.614	0.590	-0.024
100	340	0.614	0.590	-0.024
100	341	0.616	0.592	-0.024
100	342	0.612	0.590	-0.022
100	343	0.616	0.590	-0.026
105	344	0.614	0.612	-0.002
105	346	0.614	0.612	-0.002
105	349	0.616	0.612	-0.004
105	350	0.614	0.612	-0.002
105	351	0.614	0.606	-0.008
105	352	0.616	0.614	-0.002
Max		0.618	0.618	0.000
Average		0.614	0.603	-0.011
Min		0.610	0.584	-0.030
Std Dev		0.002	0.011	0.011



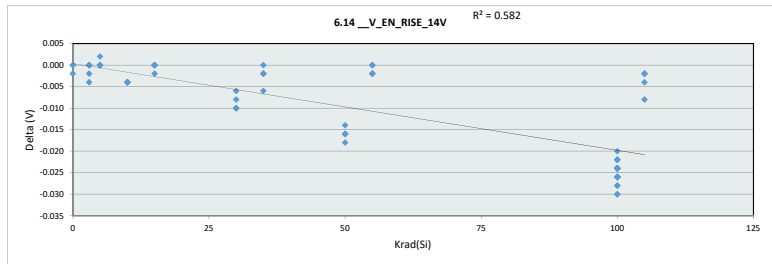
6.11 __V_EN_RISE											
Test Site											
Tester											
Test Number											
Max Limit	0.65	V									
Min Limit	0.57	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.614	0.610	0.610	0.608	0.608	0.604	0.612	0.592	0.612	0.584	0.606
Average	0.615	0.611	0.613	0.610	0.614	0.606	0.614	0.596	0.614	0.589	0.611
Max	0.618	0.614	0.616	0.614	0.618	0.608	0.616	0.598	0.616	0.594	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



HDR TID Report  
TPS7H5004-SP

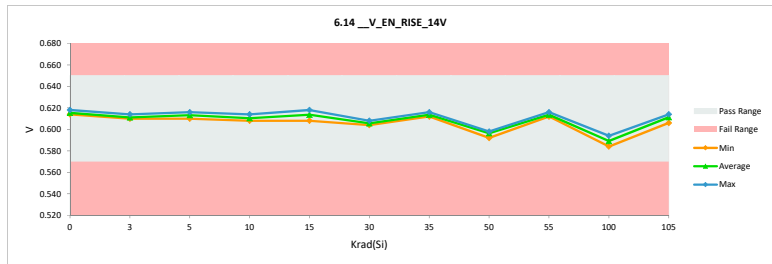
6.14 __V_EN_RISE_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.618	0.618	0.000
0	297	0.614	0.614	0.000
0	298	0.616	0.614	-0.002
3	192	0.614	0.610	-0.004
3	193	0.614	0.614	0.000
3	194	0.612	0.612	0.000
3	196	0.610	0.610	0.000
3	197	0.612	0.610	-0.002
5	200	0.610	0.610	0.000
5	202	0.612	0.612	0.000
5	203	0.614	0.614	0.000
5	204	0.612	0.614	0.002
5	206	0.616	0.616	0.000
10	207	0.612	0.608	-0.004
10	209	0.614	0.610	-0.004
10	210	0.614	0.610	-0.004
10	211	0.614	0.610	-0.004
10	212	0.618	0.614	-0.004
15	214	0.610	0.608	-0.002
15	217	0.612	0.612	0.000
15	218	0.616	0.616	0.000
15	219	0.618	0.618	0.000
15	222	0.614	0.614	0.000
30	224	0.618	0.608	-0.010
30	225	0.612	0.604	-0.008
30	226	0.610	0.604	-0.006
30	227	0.616	0.606	-0.010
30	229	0.616	0.606	-0.010
35	230	0.616	0.614	-0.002
35	231	0.616	0.616	0.000
35	232	0.616	0.614	-0.002
35	234	0.614	0.612	-0.002
35	237	0.618	0.612	-0.006
50	272	0.614	0.598	-0.016
50	275	0.610	0.592	-0.018
50	276	0.614	0.598	-0.016
50	277	0.612	0.598	-0.014
50	278	0.612	0.596	-0.016
55	279	0.614	0.612	-0.002
55	280	0.614	0.614	0.000
55	281	0.614	0.614	0.000
55	282	0.614	0.612	-0.002
55	283	0.618	0.616	-0.002
100	317	0.612	0.586	-0.026
100	318	0.614	0.592	-0.022
100	319	0.610	0.586	-0.024
100	320	0.616	0.592	-0.024
100	321	0.612	0.586	-0.026
100	322	0.612	0.588	-0.024
100	323	0.614	0.590	-0.024
100	324	0.616	0.592	-0.024
100	326	0.612	0.586	-0.026
100	327	0.616	0.588	-0.028
100	328	0.614	0.584	-0.030
100	329	0.614	0.588	-0.026
100	332	0.616	0.590	-0.026
100	333	0.614	0.586	-0.028
100	334	0.616	0.594	-0.022
100	335	0.614	0.594	-0.020
100	336	0.616	0.592	-0.024
100	337	0.614	0.584	-0.030
100	338	0.616	0.590	-0.026
100	339	0.614	0.590	-0.024
100	340	0.614	0.590	-0.024
100	341	0.616	0.592	-0.024
100	342	0.612	0.590	-0.022
100	343	0.616	0.590	-0.026
105	344	0.614	0.612	-0.002
105	346	0.614	0.612	-0.002
105	349	0.616	0.612	-0.004
105	350	0.614	0.612	-0.002
105	351	0.614	0.606	-0.008
105	352	0.616	0.614	-0.002
Max		0.618	0.618	0.002
Average		0.614	0.603	-0.011
Min		0.610	0.584	-0.030
Std Dev		0.002	0.011	0.011



6.14 __V_EN_RISE	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

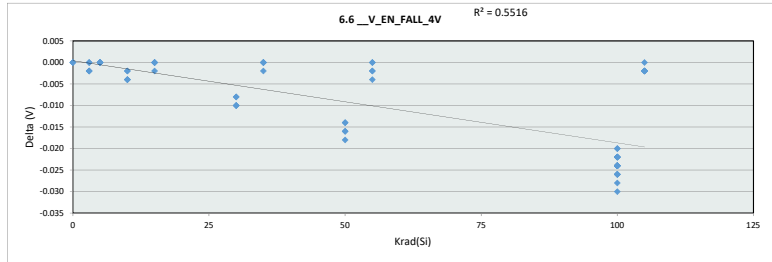
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.614	0.610	0.610	0.608	0.608	0.604	0.612	0.592	0.612	0.584	0.606
Average	0.615	0.611	0.613	0.610	0.614	0.606	0.614	0.596	0.614	0.589	0.611
Max	0.618	0.614	0.616	0.614	0.618	0.608	0.616	0.598	0.616	0.594	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



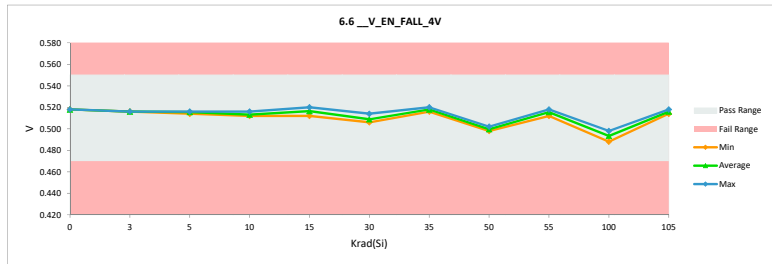
HDR TID Report  
TPS7H5004-SP

6.6_V_EN_FALL_4V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	0.55	0.55
Min Limit	0.47	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.518	0.518	0.000
0	297	0.518	0.518	0.000
0	298	0.518	0.518	0.000
3	192	0.518	0.516	-0.002
3	193	0.518	0.516	-0.002
3	194	0.516	0.516	0.000
3	196	0.516	0.516	0.000
3	197	0.518	0.516	-0.002
5	200	0.516	0.516	0.000
5	202	0.514	0.514	0.000
5	203	0.516	0.516	0.000
5	204	0.516	0.516	0.000
5	206	0.516	0.516	0.000
10	207	0.516	0.512	-0.004
10	209	0.516	0.512	-0.004
10	210	0.516	0.514	-0.002
10	211	0.516	0.512	-0.004
10	212	0.518	0.516	-0.002
15	214	0.512	0.512	0.000
15	217	0.516	0.516	0.000
15	218	0.520	0.518	-0.002
15	219	0.520	0.520	0.000
15	222	0.516	0.516	0.000
30	224	0.522	0.514	-0.008
30	225	0.516	0.506	-0.010
30	226	0.516	0.506	-0.010
30	227	0.518	0.508	-0.010
30	229	0.518	0.510	-0.008
35	230	0.518	0.518	0.000
35	231	0.518	0.518	0.000
35	232	0.520	0.520	0.000
35	234	0.516	0.516	0.000
35	237	0.520	0.518	-0.002
50	272	0.518	0.500	-0.018
50	275	0.512	0.498	-0.014
50	276	0.518	0.502	-0.016
50	277	0.516	0.500	-0.016
50	278	0.512	0.498	-0.014
55	279	0.516	0.512	-0.004
55	280	0.516	0.516	0.000
55	281	0.518	0.516	-0.002
55	282	0.516	0.516	0.000
55	283	0.520	0.518	-0.002
100	317	0.516	0.490	-0.026
100	318	0.516	0.496	-0.020
100	319	0.516	0.492	-0.024
100	320	0.518	0.494	-0.024
100	321	0.516	0.492	-0.024
100	322	0.516	0.492	-0.024
100	323	0.518	0.492	-0.026
100	324	0.518	0.496	-0.022
100	326	0.514	0.490	-0.024
100	327	0.518	0.490	-0.028
100	328	0.514	0.488	-0.026
100	329	0.516	0.492	-0.024
100	332	0.520	0.494	-0.026
100	333	0.516	0.492	-0.024
100	334	0.520	0.496	-0.024
100	335	0.518	0.496	-0.022
100	336	0.520	0.496	-0.024
100	337	0.520	0.490	-0.030
100	338	0.518	0.496	-0.022
100	339	0.516	0.492	-0.024
100	340	0.518	0.496	-0.022
100	341	0.518	0.496	-0.022
100	342	0.516	0.494	-0.022
100	343	0.518	0.498	-0.020
105	344	0.516	0.514	-0.002
105	346	0.520	0.518	-0.002
105	349	0.520	0.518	-0.002
105	350	0.516	0.516	0.000
105	351	0.516	0.514	-0.002
105	352	0.518	0.516	-0.002
	Max	0.522	0.520	0.000
	Average	0.517	0.507	-0.010
	Min	0.512	0.488	-0.030
	Std Dev	0.002	0.011	0.011

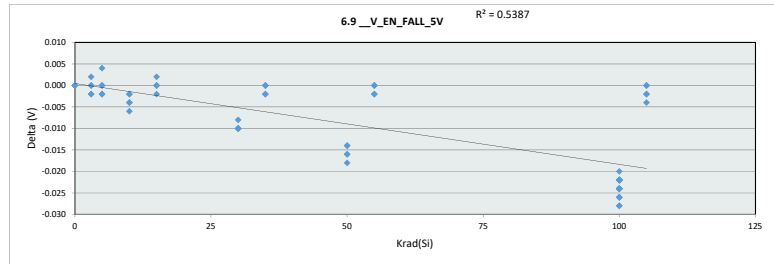


6.6_V_EN_FALL													
Test Site													
Tester													
Test Number													
Max Limit	0.55	V											
Min Limit	0.47	V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.512	0.512	0.506	0.516	0.498	0.512	0.488	0.514	0.518	0.518
Average	0.518	0.516	0.516	0.513	0.516	0.509	0.518	0.500	0.516	0.493	0.516	0.518	0.518
Max	0.518	0.516	0.516	0.516	0.520	0.514	0.520	0.502	0.518	0.498	0.518	0.518	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

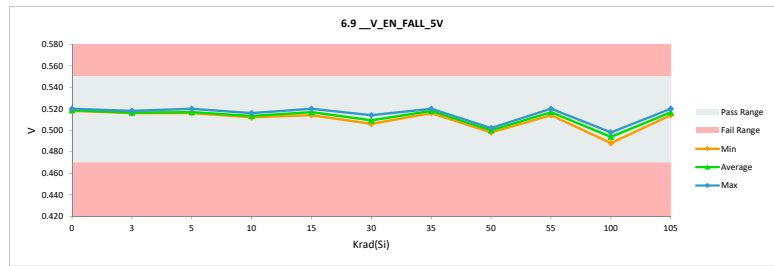


HDR TID Report  
TPS7H5004-SP

6.9 __V_EN_FALL_5V				
Test Site	Tester	Test Number	Unit	
			V	V
			Max Limit	0.55
			Min Limit	0.47
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.518	0.518	0.000
0	297	0.520	0.520	0.000
0	298	0.518	0.518	0.000
3	192	0.518	0.516	-0.002
3	193	0.518	0.516	-0.002
3	194	0.516	0.516	0.000
3	196	0.516	0.516	0.000
3	197	0.516	0.518	0.002
5	200	0.518	0.516	-0.002
5	202	0.516	0.516	0.000
5	203	0.516	0.516	0.000
5	204	0.518	0.516	-0.002
5	206	0.516	0.520	0.004
10	207	0.516	0.512	-0.004
10	209	0.518	0.512	-0.006
10	210	0.516	0.514	-0.002
10	211	0.516	0.512	-0.004
10	212	0.518	0.516	-0.002
15	214	0.512	0.514	0.002
15	217	0.516	0.516	0.000
15	218	0.520	0.518	-0.002
15	219	0.520	0.520	0.000
15	222	0.516	0.516	0.000
30	224	0.522	0.514	-0.008
30	225	0.516	0.506	-0.010
30	226	0.518	0.508	-0.010
30	227	0.518	0.508	-0.010
30	229	0.520	0.510	-0.010
35	230	0.518	0.518	0.000
35	231	0.520	0.518	-0.002
35	232	0.520	0.520	0.000
35	234	0.516	0.516	0.000
35	237	0.520	0.518	-0.002
50	272	0.518	0.500	-0.018
50	275	0.512	0.498	-0.014
50	276	0.518	0.502	-0.016
50	277	0.516	0.502	-0.014
50	278	0.514	0.498	-0.016
55	279	0.516	0.514	-0.002
55	280	0.518	0.516	-0.002
55	281	0.518	0.518	0.000
55	282	0.516	0.516	0.000
55	283	0.520	0.520	0.000
100	317	0.516	0.490	-0.026
100	318	0.516	0.496	-0.020
100	319	0.516	0.492	-0.024
100	320	0.518	0.494	-0.024
100	321	0.516	0.494	-0.022
100	322	0.516	0.494	-0.022
100	323	0.518	0.492	-0.026
100	324	0.518	0.496	-0.022
100	326	0.514	0.490	-0.024
100	327	0.520	0.492	-0.028
100	328	0.514	0.488	-0.026
100	329	0.516	0.492	-0.024
100	332	0.520	0.496	-0.024
100	333	0.516	0.492	-0.024
100	334	0.520	0.498	-0.022
100	335	0.518	0.496	-0.022
100	336	0.520	0.496	-0.024
100	337	0.520	0.492	-0.028
100	338	0.518	0.496	-0.022
100	339	0.516	0.492	-0.024
100	340	0.518	0.496	-0.022
100	341	0.518	0.496	-0.022
100	342	0.516	0.494	-0.022
100	343	0.520	0.498	-0.022
105	344	0.516	0.514	-0.002
105	346	0.520	0.518	-0.002
105	349	0.520	0.520	0.000
105	350	0.516	0.516	0.000
105	351	0.518	0.514	-0.004
105	352	0.518	0.518	0.000
	Max	0.522	0.520	0.004
	Average	0.517	0.507	-0.010
	Min	0.512	0.488	-0.028
	Std Dev	0.002	0.011	0.011



6.9 __V_EN_FALL											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			0.55	0.47							
			V	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.516	0.512	0.514	0.506	0.516	0.498	0.514	0.488	0.514
Average	0.519	0.516	0.517	0.513	0.517	0.509	0.518	0.500	0.517	0.494	0.517
Max	0.520	0.518	0.520	0.516	0.520	0.514	0.520	0.502	0.520	0.498	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

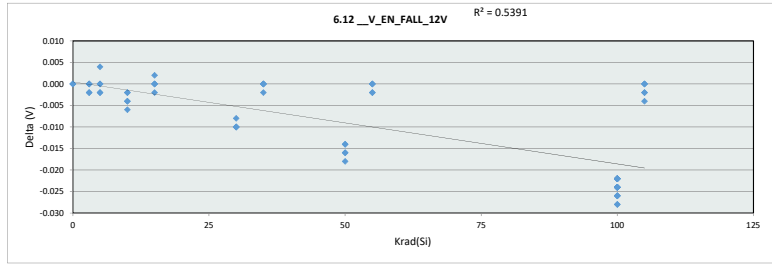




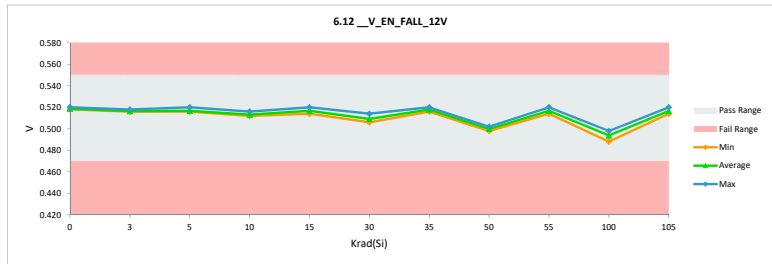
HDR TID Report  
TPS7H5004-SP

6.12 __V_EN_FALL_12V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	0.55	0.55
Min Limit	0.47	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.518	0.518	0.000
0	297	0.520	0.520	0.000
0	298	0.518	0.518	0.000
3	192	0.518	0.516	-0.002
3	193	0.518	0.516	-0.002
3	194	0.516	0.516	0.000
3	196	0.516	0.516	0.000
3	197	0.518	0.518	0.000
5	200	0.518	0.516	-0.002
5	202	0.516	0.516	0.000
5	203	0.516	0.516	0.000
5	204	0.518	0.516	-0.002
5	206	0.516	0.520	0.004
10	207	0.516	0.512	-0.004
10	209	0.518	0.512	-0.006
10	210	0.516	0.514	-0.002
10	211	0.516	0.512	-0.004
10	212	0.518	0.516	-0.002
15	214	0.512	0.514	0.002
15	217	0.516	0.516	0.000
15	218	0.520	0.518	-0.002
15	219	0.520	0.520	0.000
15	222	0.516	0.516	0.000
30	224	0.522	0.514	-0.008
30	225	0.516	0.506	-0.010
30	226	0.518	0.508	-0.010
30	227	0.518	0.508	-0.010
30	229	0.520	0.510	-0.010
35	230	0.518	0.518	0.000
35	231	0.518	0.518	0.000
35	232	0.520	0.520	0.000
35	234	0.516	0.516	0.000
35	237	0.520	0.518	-0.002
50	272	0.518	0.500	-0.018
50	275	0.512	0.498	-0.014
50	276	0.518	0.502	-0.016
50	277	0.516	0.502	-0.014
50	278	0.514	0.498	-0.016
55	279	0.516	0.514	-0.002
55	280	0.518	0.516	-0.002
55	281	0.518	0.518	0.000
55	282	0.516	0.516	0.000
55	283	0.520	0.520	0.000
100	317	0.516	0.490	-0.026
100	318	0.520	0.496	-0.024
100	319	0.516	0.492	-0.024
100	320	0.518	0.494	-0.024
100	321	0.516	0.494	-0.022
100	322	0.518	0.494	-0.024
100	323	0.518	0.492	-0.026
100	324	0.518	0.496	-0.022
100	326	0.514	0.490	-0.024
100	327	0.520	0.492	-0.028
100	328	0.514	0.488	-0.026
100	329	0.516	0.492	-0.024
100	332	0.522	0.496	-0.026
100	333	0.516	0.492	-0.024
100	334	0.520	0.496	-0.024
100	335	0.518	0.496	-0.022
100	336	0.520	0.496	-0.024
100	337	0.520	0.492	-0.028
100	338	0.518	0.496	-0.022
100	339	0.516	0.494	-0.022
100	340	0.518	0.496	-0.022
100	341	0.518	0.496	-0.022
100	342	0.516	0.494	-0.022
100	343	0.520	0.498	-0.022
105	344	0.516	0.514	-0.002
105	346	0.520	0.518	-0.002
105	349	0.520	0.520	0.000
105	350	0.516	0.516	0.000
105	351	0.518	0.514	-0.004
105	352	0.518	0.518	0.000
Max		0.522	0.520	0.004
Average		0.518	0.507	-0.010
Min		0.512	0.488	-0.028
Std Dev		0.002	0.011	0.011



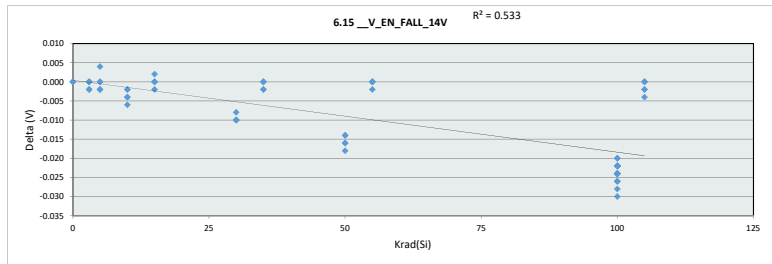
6.12 __V_EN_FALL											
Test Site											
Tester											
Test Number											
Max Limit	0.55	V									
Min Limit	0.47	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.516	0.512	0.514	0.506	0.516	0.498	0.514	0.488	0.514
Average	0.519	0.516	0.517	0.513	0.517	0.509	0.518	0.500	0.517	0.494	0.517
Max	0.520	0.518	0.520	0.516	0.520	0.514	0.520	0.502	0.520	0.498	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



HDR TID Report  
TPS7H5004-SP

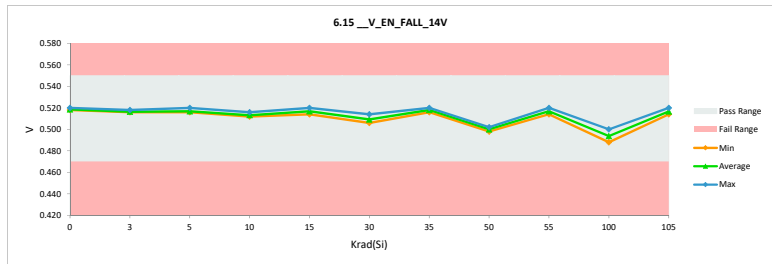
6.15 __V_EN_FALL_14V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	0.55	0.55
Min Limit	0.47	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.518	0.518	0.000
0	297	0.520	0.520	0.000
0	298	0.518	0.518	0.000
3	192	0.518	0.516	-0.002
3	193	0.518	0.516	-0.002
3	194	0.516	0.516	0.000
3	196	0.516	0.516	0.000
3	197	0.518	0.518	0.000
5	200	0.518	0.516	-0.002
5	202	0.516	0.516	0.000
5	203	0.516	0.516	0.000
5	204	0.518	0.516	-0.002
5	206	0.516	0.520	0.004
10	207	0.516	0.512	-0.004
10	209	0.518	0.512	-0.006
10	210	0.516	0.514	-0.002
10	211	0.516	0.512	-0.004
10	212	0.518	0.516	-0.002
15	214	0.512	0.514	0.002
15	217	0.516	0.516	0.000
15	218	0.520	0.518	-0.002
15	219	0.520	0.520	0.000
15	222	0.516	0.516	0.000
30	224	0.522	0.514	-0.008
30	225	0.516	0.506	-0.010
30	226	0.518	0.508	-0.010
30	227	0.518	0.508	-0.010
30	229	0.520	0.510	-0.010
35	230	0.518	0.518	0.000
35	231	0.520	0.518	-0.002
35	232	0.520	0.520	0.000
35	234	0.516	0.516	0.000
35	237	0.520	0.518	-0.002
50	272	0.518	0.500	-0.018
50	275	0.512	0.498	-0.014
50	276	0.518	0.502	-0.016
50	277	0.516	0.502	-0.014
50	278	0.514	0.498	-0.016
55	279	0.516	0.514	-0.002
55	280	0.516	0.516	0.000
55	281	0.518	0.518	0.000
55	282	0.516	0.516	0.000
55	283	0.520	0.520	0.000
100	317	0.516	0.490	-0.026
100	318	0.516	0.496	-0.020
100	319	0.516	0.492	-0.024
100	320	0.518	0.494	-0.024
100	321	0.516	0.494	-0.022
100	322	0.516	0.494	-0.022
100	323	0.518	0.492	-0.026
100	324	0.518	0.496	-0.022
100	326	0.514	0.490	-0.024
100	327	0.520	0.490	-0.030
100	328	0.514	0.488	-0.026
100	329	0.516	0.492	-0.024
100	332	0.520	0.496	-0.024
100	333	0.516	0.492	-0.024
100	334	0.520	0.496	-0.024
100	335	0.518	0.496	-0.022
100	336	0.520	0.496	-0.024
100	337	0.520	0.492	-0.028
100	338	0.518	0.496	-0.022
100	339	0.516	0.492	-0.024
100	340	0.518	0.496	-0.022
100	341	0.518	0.496	-0.022
100	342	0.516	0.494	-0.022
100	343	0.520	0.500	-0.020
105	344	0.516	0.514	-0.002
105	346	0.520	0.518	-0.002
105	349	0.520	0.520	0.000
105	350	0.516	0.516	0.000
105	351	0.518	0.514	-0.004
105	352	0.518	0.518	0.000
Max		0.522	0.520	0.004
Average		0.517	0.507	-0.010
Min		0.512	0.488	-0.030
Std Dev		0.002	0.011	0.011



6.15 __V_EN_FALL		
Test Site		
Tester		
Test Number		
Max Limit	0.55	V
Min Limit	0.47	V

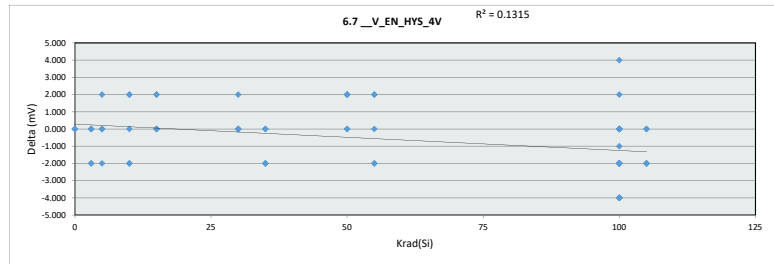
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.516	0.512	0.514	0.506	0.516	0.498	0.514	0.488	0.514
Average	0.519	0.516	0.517	0.513	0.517	0.509	0.518	0.500	0.517	0.494	0.517
Max	0.520	0.518	0.520	0.516	0.520	0.514	0.520	0.502	0.520	0.500	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



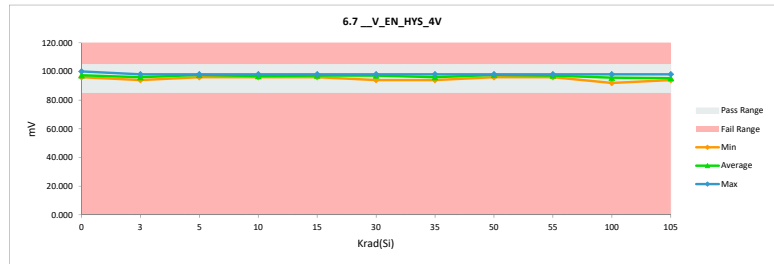
HDR TID Report  
TPS7H5004-SP

6.7_V_EN_HYS_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

Krad(Si)	Serial #	PRE	POST	Delta
0	295	100.000	100.000	0.000
0	297	96.000	96.000	0.000
0	298	96.000	96.000	0.000
3	192	96.000	94.000	-2.000
3	193	96.000	96.000	0.000
3	194	96.000	96.000	0.000
3	196	98.000	96.000	-2.000
3	197	98.000	98.000	0.000
5	200	98.000	98.000	0.000
5	202	98.000	96.000	-2.000
5	203	98.000	98.000	0.000
5	204	96.000	98.000	2.000
5	206	98.000	98.000	0.000
10	207	96.000	98.000	2.000
10	209	96.000	96.000	0.000
10	210	98.000	96.000	-2.000
10	211	96.000	98.000	2.000
10	212	98.000	96.000	-2.000
15	214	96.000	98.000	2.000
15	217	96.000	96.000	0.000
15	218	96.000	98.000	2.000
15	219	98.000	98.000	0.000
15	222	96.000	96.000	0.000
30	224	94.000	94.000	0.000
30	225	96.000	98.000	2.000
30	226	98.000	98.000	0.000
30	227	98.000	98.000	0.000
30	229	98.000	98.000	0.000
35	230	98.000	96.000	-2.000
35	231	96.000	96.000	0.000
35	232	96.000	94.000	-2.000
35	234	98.000	96.000	-2.000
35	237	98.000	98.000	0.000
50	272	96.000	98.000	2.000
50	275	96.000	98.000	2.000
50	276	96.000	96.000	0.000
50	277	96.000	98.000	2.000
50	278	98.000	98.000	0.000
55	279	96.000	98.000	2.000
55	280	98.000	96.000	-2.000
55	281	96.000	98.000	2.000
55	282	98.000	96.000	-2.000
55	283	98.000	98.000	0.000
100	317	100.000	96.000	-4.000
100	318	98.000	94.000	-4.000
100	319	98.000	94.000	-4.000
100	320	98.000	98.000	0.000
100	321	94.000	98.000	4.000
100	322	96.000	96.000	0.000
100	323	98.000	96.000	-2.000
100	324	98.000	96.000	-2.000
100	326	98.000	96.000	-2.000
100	327	98.000	98.000	0.000
100	328	98.000	96.000	-2.000
100	329	98.000	96.000	-2.000
100	332	96.000	96.000	0.000
100	333	98.000	94.000	-4.000
100	334	96.000	96.000	0.000
100	335	96.000	98.000	2.000
100	336	96.000	95.000	-1.000
100	337	96.000	94.000	-2.000
100	338	96.000	94.000	-2.000
100	339	98.000	98.000	0.000
100	340	96.000	92.000	-4.000
100	341	98.000	96.000	-2.000
100	342	96.000	94.000	-2.000
100	343	98.000	96.000	-2.000
105	344	98.000	96.000	-2.000
105	346	94.000	94.000	0.000
105	349	96.000	94.000	-2.000
105	350	96.000	94.000	-2.000
105	351	98.000	98.000	0.000
105	352	98.000	96.000	-2.000
Max		100.000	100.000	4.000
Average		96.986	96.425	-0.562
Min		94.000	92.000	-4.000
Std Dev		1.253	1.580	1.740

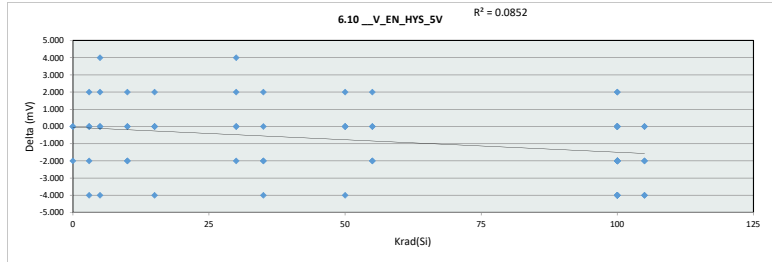


6.7_V_EN_HYS_4V		0	3	5	10	15	30	35	50	55	100	105
LL		85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min		96.000	94.000	96.000	96.000	96.000	94.000	94.000	96.000	96.000	92.000	94.000
Average		97.333	96.000	97.600	96.800	97.200	97.200	96.000	97.600	97.200	95.708	95.333
Max		100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL		105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

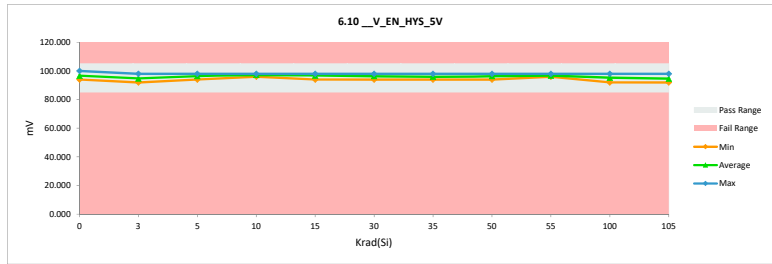


HDR TID Report  
TPS7H5004-SP

6.10 __V_EN_HYS_5V				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	105		105	
Min Limit	85		85	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	100.000	100.000	0.000
0	297	94.000	94.000	0.000
0	298	98.000	96.000	-2.000
3	192	96.000	94.000	-2.000
3	193	96.000	98.000	2.000
3	194	96.000	96.000	0.000
3	196	94.000	94.000	0.000
3	197	96.000	92.000	-4.000
5	200	92.000	94.000	2.000
5	202	96.000	96.000	0.000
5	203	98.000	98.000	0.000
5	204	94.000	98.000	4.000
5	206	100.000	96.000	-4.000
10	207	96.000	96.000	0.000
10	209	96.000	98.000	2.000
10	210	98.000	96.000	-2.000
10	211	98.000	98.000	0.000
10	212	100.000	98.000	-2.000
15	214	98.000	94.000	-4.000
15	217	96.000	96.000	0.000
15	218	96.000	98.000	2.000
15	219	98.000	98.000	0.000
15	222	98.000	98.000	0.000
30	224	96.000	94.000	-2.000
30	225	96.000	98.000	2.000
30	226	92.000	96.000	4.000
30	227	98.000	98.000	0.000
30	229	96.000	96.000	0.000
35	230	98.000	96.000	-2.000
35	231	96.000	98.000	2.000
35	232	96.000	94.000	-2.000
35	234	98.000	98.000	0.000
35	237	98.000	94.000	-4.000
50	272	96.000	98.000	2.000
50	275	98.000	94.000	-4.000
50	276	96.000	96.000	0.000
50	277	96.000	96.000	0.000
50	278	98.000	98.000	0.000
55	279	98.000	98.000	0.000
55	280	96.000	98.000	2.000
55	281	96.000	96.000	0.000
55	282	98.000	96.000	-2.000
55	283	98.000	96.000	-2.000
100	317	96.000	96.000	0.000
100	318	98.000	94.000	-4.000
100	319	94.000	94.000	0.000
100	320	98.000	98.000	0.000
100	321	96.000	92.000	-4.000
100	322	96.000	94.000	-2.000
100	323	96.000	98.000	2.000
100	324	98.000	96.000	-2.000
100	326	98.000	98.000	0.000
100	327	96.000	96.000	0.000
100	328	100.000	96.000	-4.000
100	329	98.000	96.000	-2.000
100	332	96.000	94.000	-2.000
100	333	98.000	96.000	-2.000
100	334	96.000	96.000	0.000
100	335	96.000	98.000	2.000
100	336	98.000	96.000	-2.000
100	337	94.000	92.000	-2.000
100	338	98.000	94.000	-4.000
100	339	98.000	98.000	0.000
100	340	96.000	92.000	-4.000
100	341	98.000	96.000	-2.000
100	342	96.000	96.000	0.000
100	343	96.000	92.000	-4.000
105	344	98.000	98.000	0.000
105	346	94.000	94.000	0.000
105	349	96.000	92.000	-4.000
105	350	98.000	96.000	-2.000
105	351	96.000	92.000	-4.000
105	352	98.000	96.000	-2.000
Max		100.000	100.000	4.000
Average		96.740	95.890	-0.849
Min		92.000	92.000	-4.000
Std Dev		1.650	1.969	2.053



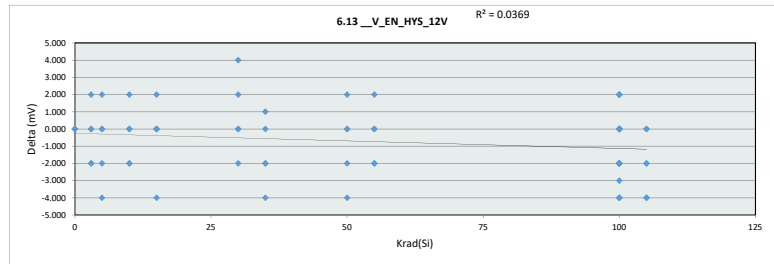
6.10 __V_EN_HYS											
Test Site											
Tester											
Test Number											
Max Limit	105										
Min Limit	85										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	92.000	94.000	96.000	94.000	94.000	94.000	94.000	96.000	92.000	92.000
Average	96.667	94.800	96.400	97.200	96.800	96.400	96.000	96.400	96.800	95.333	94.667
Max	100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000



HDR TID Report  
TPS7H5004-SP

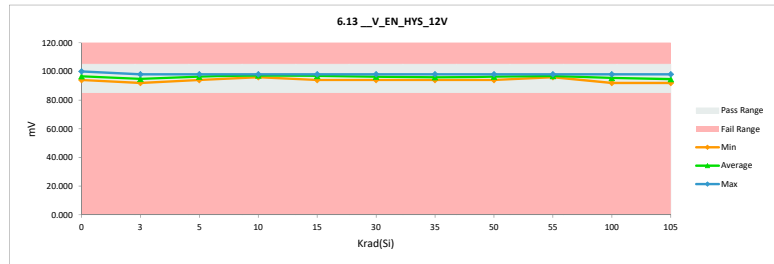
6.13 __V_EN_HYS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

Krad(Si)	Serial #	PRE	POST	Delta
0	295	100.000	100.000	0.000
0	297	94.000	94.000	0.000
0	298	96.000	96.000	0.000
3	192	96.000	94.000	-2.000
3	193	96.000	98.000	2.000
3	194	96.000	96.000	0.000
3	196	94.000	94.000	0.000
3	197	94.000	92.000	-2.000
5	200	96.000	94.000	-2.000
5	202	96.000	96.000	0.000
5	203	98.000	98.000	0.000
5	204	96.000	98.000	2.000
5	206	100.000	96.000	-4.000
10	207	96.000	96.000	0.000
10	209	96.000	98.000	2.000
10	210	98.000	96.000	-2.000
10	211	98.000	98.000	0.000
10	212	100.000	98.000	-2.000
15	214	98.000	94.000	-4.000
15	217	96.000	96.000	0.000
15	218	96.000	98.000	2.000
15	219	98.000	98.000	0.000
15	222	98.000	98.000	0.000
30	224	96.000	94.000	-2.000
30	225	96.000	98.000	2.000
30	226	92.000	96.000	4.000
30	227	98.000	98.000	0.000
30	229	96.000	96.000	0.000
35	230	98.000	96.000	-2.000
35	231	97.000	98.000	1.000
35	232	96.000	94.000	-2.000
35	234	98.000	98.000	0.000
35	237	98.000	94.000	-4.000
50	272	96.000	98.000	2.000
50	275	98.000	94.000	-4.000
50	276	96.000	96.000	0.000
50	277	98.000	96.000	-2.000
50	278	98.000	98.000	0.000
55	279	98.000	98.000	0.000
55	280	96.000	98.000	2.000
55	281	96.000	96.000	0.000
55	282	98.000	96.000	-2.000
55	283	98.000	96.000	-2.000
100	317	96.000	96.000	0.000
100	318	94.000	96.000	2.000
100	319	94.000	94.000	0.000
100	320	98.000	98.000	0.000
100	321	96.000	92.000	-4.000
100	322	94.000	94.000	0.000
100	323	96.000	98.000	2.000
100	324	98.000	95.000	-3.000
100	326	98.000	98.000	0.000
100	327	96.000	96.000	0.000
100	328	98.000	96.000	-2.000
100	329	98.000	96.000	-2.000
100	332	94.000	94.000	0.000
100	333	98.000	96.000	-2.000
100	334	96.000	98.000	2.000
100	335	96.000	98.000	2.000
100	336	98.000	96.000	-2.000
100	337	94.000	92.000	-2.000
100	338	98.000	94.000	-4.000
100	339	98.000	96.000	-2.000
100	340	96.000	94.000	-2.000
100	341	98.000	96.000	-2.000
100	342	96.000	96.000	0.000
100	343	96.000	92.000	-4.000
105	344	98.000	98.000	0.000
105	346	94.000	94.000	0.000
105	349	96.000	92.000	-4.000
105	350	98.000	96.000	-2.000
105	351	96.000	92.000	-4.000
105	352	98.000	96.000	-2.000
Max		100.000	100.000	4.000
Average		96.671	95.932	-0.740
Min		92.000	92.000	-4.000
Std Dev		1.599	1.917	1.893



6.13 __V_EN_HYS	
Test Site	
Tester	
Test Number	
Max Limit	105 mV
Min Limit	85 mV

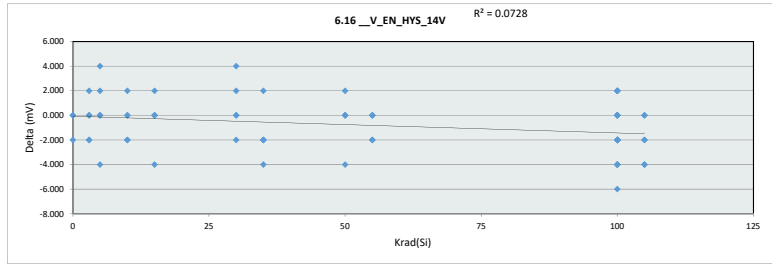
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	92.000	94.000	96.000	94.000	94.000	94.000	94.000	96.000	92.000	92.000
Average	96.667	94.800	96.400	97.200	96.800	96.400	96.000	96.400	96.800	95.458	94.667
Max	100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000



HDR TID Report  
TPS7H5004-SP

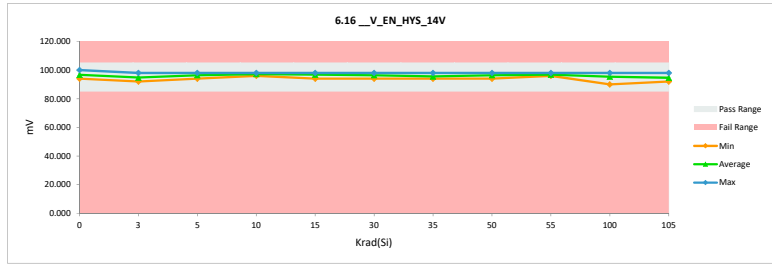
6.16 __V_EN_HYS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

Krad(Si)	Serial #	PRE	POST	Delta
0	295	100.000	100.000	0.000
0	297	94.000	94.000	0.000
0	298	98.000	96.000	-2.000
3	192	96.000	94.000	-2.000
3	193	96.000	98.000	2.000
3	194	96.000	96.000	0.000
3	196	94.000	94.000	0.000
3	197	94.000	92.000	-2.000
5	200	92.000	94.000	2.000
5	202	96.000	96.000	0.000
5	203	98.000	98.000	0.000
5	204	94.000	98.000	4.000
5	206	100.000	96.000	-4.000
10	207	96.000	96.000	0.000
10	209	96.000	98.000	2.000
10	210	98.000	96.000	-2.000
10	211	98.000	98.000	0.000
10	212	100.000	98.000	-2.000
15	214	98.000	94.000	-4.000
15	217	96.000	96.000	0.000
15	218	96.000	98.000	2.000
15	219	98.000	98.000	0.000
15	222	98.000	98.000	0.000
30	224	96.000	94.000	-2.000
30	225	96.000	98.000	2.000
30	226	92.000	96.000	4.000
30	227	98.000	98.000	0.000
30	229	96.000	96.000	0.000
35	230	98.000	96.000	-2.000
35	231	96.000	98.000	2.000
35	232	96.000	94.000	-2.000
35	234	98.000	96.000	-2.000
35	237	98.000	94.000	-4.000
50	272	96.000	98.000	2.000
50	275	98.000	94.000	-4.000
50	276	96.000	96.000	0.000
50	277	96.000	96.000	0.000
50	278	98.000	98.000	0.000
55	279	98.000	98.000	0.000
55	280	98.000	98.000	0.000
55	281	96.000	96.000	0.000
55	282	98.000	96.000	-2.000
55	283	98.000	96.000	-2.000
100	317	96.000	96.000	0.000
100	318	98.000	96.000	-2.000
100	319	94.000	94.000	0.000
100	320	98.000	98.000	0.000
100	321	96.000	92.000	-4.000
100	322	96.000	94.000	-2.000
100	323	96.000	98.000	2.000
100	324	98.000	96.000	-2.000
100	326	98.000	96.000	-2.000
100	327	96.000	98.000	2.000
100	328	100.000	96.000	-4.000
100	329	98.000	96.000	-2.000
100	332	96.000	94.000	-2.000
100	333	98.000	94.000	-4.000
100	334	96.000	98.000	2.000
100	335	96.000	98.000	2.000
100	336	96.000	96.000	0.000
100	337	94.000	92.000	-2.000
100	338	98.000	94.000	-4.000
100	339	98.000	98.000	0.000
100	340	96.000	94.000	-2.000
100	341	98.000	96.000	-2.000
100	342	96.000	96.000	0.000
100	343	96.000	90.000	-6.000
105	344	98.000	98.000	0.000
105	346	94.000	94.000	0.000
105	349	96.000	92.000	-4.000
105	350	98.000	96.000	-2.000
105	351	96.000	92.000	-4.000
105	352	98.000	96.000	-2.000
Max		100.000	100.000	4.000
Average		96.712	95.890	-0.822
Min		92.000	90.000	-6.000
Std Dev		1.679	1.997	2.077



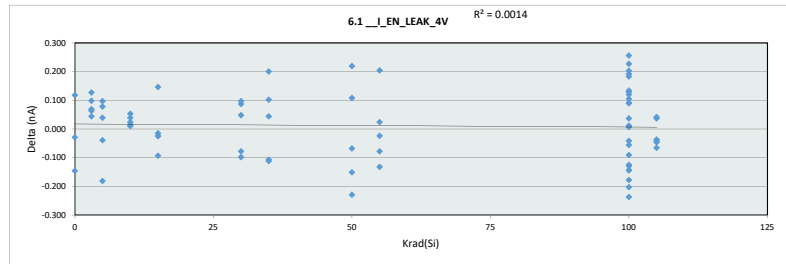
6.16 __V_EN_HYS	
Test Site	
Tester	
Test Number	
Max Limit	105 mV
Min Limit	85 mV

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	92.000	94.000	96.000	94.000	94.000	94.000	94.000	96.000	90.000	92.000
Average	96.667	94.800	96.400	97.200	96.800	96.400	95.600	96.400	96.800	95.417	94.667
Max	100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000

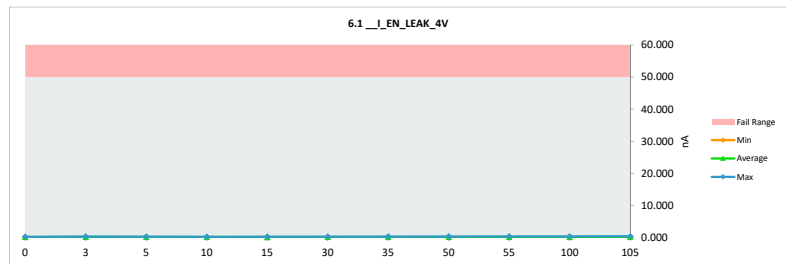


HDR TID Report  
TPS7H5004-SP

6.1_I_EN_LEAK_4V				
Test Site	Tester	Test Number	Unit	
			nA	nA
			50	50
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.161	0.132	-0.029
0	297	0.024	0.142	0.118
0	298	0.190	0.044	-0.146
3	192	0.100	0.163	0.063
3	193	0.124	0.222	0.098
3	194	0.007	0.051	0.044
3	196	0.041	0.110	0.069
3	197	0.022	0.149	0.127
5	200	0.022	0.119	0.097
5	202	0.183	0.002	-0.181
5	203	0.163	0.202	0.039
5	204	0.022	0.100	0.078
5	206	0.149	0.110	-0.039
10	207	0.051	0.066	0.015
10	209	0.076	0.115	0.039
10	210	0.110	0.163	0.053
10	211	0.080	0.090	0.010
10	212	0.027	0.051	0.024
15	214	0.056	0.202	0.146
15	217	0.037	0.012	-0.025
15	218	0.149	0.056	-0.093
15	219	0.032	0.017	-0.015
15	222	0.051	0.027	-0.024
30	224	0.071	0.119	0.048
30	225	0.183	0.085	-0.098
30	226	0.183	0.105	-0.078
30	227	0.022	0.119	0.097
30	229	0.115	0.202	0.087
35	230	0.100	0.144	0.044
35	231	0.012	0.212	0.200
35	232	0.139	0.241	0.102
35	234	0.168	0.061	-0.107
35	237	0.139	0.027	-0.112
50	272	0.251	0.022	-0.229
50	275	0.007	0.115	0.108
50	276	0.188	0.037	-0.151
50	277	0.032	0.251	0.219
50	278	0.090	0.022	-0.068
55	279	0.197	0.173	-0.024
55	280	0.037	0.241	0.204
55	281	0.144	0.066	-0.078
55	282	0.183	0.051	-0.132
55	283	0.139	0.163	0.024
100	317	0.242	0.005	-0.237
100	318	0.183	0.054	-0.129
100	319	0.051	0.278	0.227
100	320	0.032	0.288	0.256
100	321	0.193	0.015	-0.178
100	322	0.154	0.063	-0.091
100	323	0.159	0.034	-0.125
100	324	0.120	0.210	0.090
100	326	0.129	0.073	-0.056
100	327	0.144	0.181	0.037
100	328	0.037	0.142	0.105
100	329	0.032	0.044	0.012
100	332	0.129	0.220	0.091
100	333	0.095	0.054	-0.041
100	334	0.227	0.024	-0.203
100	335	0.149	0.005	-0.144
100	336	0.100	0.229	0.129
100	337	0.168	0.024	-0.144
100	338	0.144	0.151	0.007
100	339	0.012	0.132	0.120
100	340	0.007	0.190	0.183
100	341	0.027	0.161	0.134
100	342	0.037	0.229	0.192
100	343	0.007	0.210	0.203
105	344	0.046	0.083	0.037
105	346	0.383	0.337	-0.046
105	349	0.310	0.268	-0.042
105	350	0.100	0.063	-0.037
105	351	0.178	0.220	0.042
105	352	0.149	0.083	-0.066
	Max	0.383	0.337	0.256
	Average	0.110	0.122	0.012
	Min	0.007	0.002	-0.237
	Std Dev	0.078	0.083	0.118



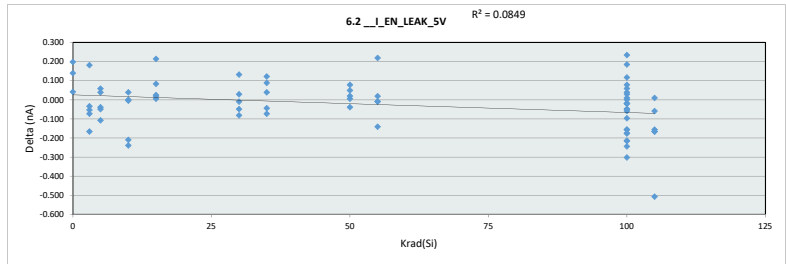
6.1_I_EN_LEAK											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			50	nA							
				nA							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.044	0.051	0.002	0.051	0.012	0.085	0.027	0.022	0.051	0.005	0.063
Average	0.106	0.139	0.107	0.097	0.063	0.126	0.137	0.089	0.139	0.126	0.176
Max	0.142	0.222	0.202	0.163	0.202	0.202	0.241	0.251	0.241	0.288	0.337
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



HDR TID Report  
TPS7H5004-SP

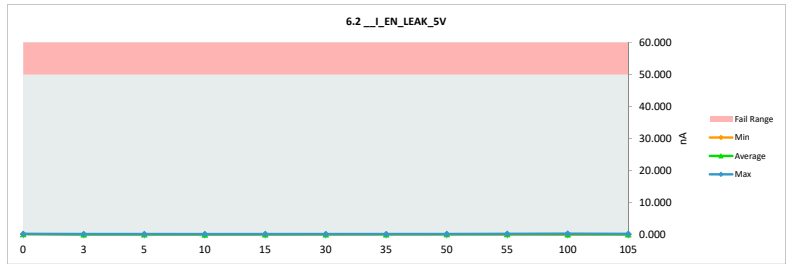
6.2_I_EN_LEAK_5V	
Test Site	
Tester	
Test Number	
Unit	nA
Max Limit	50
Min Limit	50

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.027	0.225	0.198
0	297	0.115	0.156	0.041
0	298	0.027	0.166	0.139
3	192	0.315	0.149	-0.166
3	193	0.139	0.105	-0.034
3	194	0.100	0.027	-0.073
3	196	0.002	0.183	0.181
3	197	0.080	0.027	-0.053
5	200	0.168	0.119	-0.049
5	202	0.076	0.115	0.039
5	203	0.110	0.071	-0.039
5	204	0.276	0.168	-0.108
5	206	0.066	0.124	0.058
10	207	0.334	0.124	-0.210
10	209	0.305	0.066	-0.239
10	210	0.110	0.149	0.039
10	211	0.061	0.056	-0.005
10	212	0.007	0.007	0.000
15	214	0.002	0.027	0.025
15	217	0.227	0.232	0.005
15	218	0.032	0.051	0.019
15	219	0.022	0.236	0.214
15	222	0.027	0.110	0.083
30	224	0.056	0.188	0.132
30	225	0.188	0.178	-0.010
30	226	0.158	0.076	-0.082
30	227	0.027	0.056	0.029
30	229	0.188	0.139	-0.049
35	230	0.188	0.115	-0.073
35	231	0.032	0.154	0.122
35	232	0.129	0.085	-0.044
35	234	0.002	0.090	0.088
35	237	0.110	0.149	0.039
50	272	0.022	0.071	0.049
50	275	0.197	0.217	0.020
50	276	0.129	0.207	0.078
50	277	0.246	0.207	-0.039
50	278	0.110	0.115	0.005
55	279	0.227	0.217	-0.010
55	280	0.037	0.256	0.219
55	281	0.188	0.207	0.019
55	282	0.041	0.032	-0.009
55	283	0.168	0.027	-0.141
100	317	0.088	0.039	-0.049
100	318	0.098	0.078	-0.020
100	319	0.283	0.127	-0.156
100	320	0.225	0.010	-0.215
100	321	0.068	0.020	-0.048
100	322	0.107	0.117	0.010
100	323	0.381	0.166	-0.215
100	324	0.098	0.176	0.078
100	326	0.225	0.254	0.029
100	327	0.439	0.137	-0.302
100	328	0.195	0.020	-0.175
100	329	0.176	0.020	-0.156
100	332	0.010	0.195	0.185
100	333	0.185	0.244	0.059
100	334	0.283	0.322	0.039
100	335	0.283	0.039	-0.244
100	336	0.049	0.029	-0.020
100	337	0.166	0.166	0.000
100	338	0.088	0.068	-0.020
100	339	0.312	0.215	-0.097
100	340	0.244	0.185	-0.059
100	341	0.020	0.254	0.234
100	342	0.059	0.176	0.117
100	343	0.264	0.088	-0.176
105	344	0.303	0.137	-0.166
105	346	0.566	0.059	-0.507
105	349	0.234	0.078	-0.156
105	350	0.244	0.254	0.010
105	351	0.098	0.039	-0.059
105	352	0.293	0.127	-0.166
Max		0.566	0.322	0.234
Average		0.153	0.128	-0.025
Min		0.002	0.007	-0.507
Std Dev		0.116	0.076	0.130



6.2_I_EN_LEAK	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.156	0.027	0.071	0.007	0.027	0.056	0.085	0.071	0.027	0.010	0.039
Average	0.182	0.098	0.119	0.080	0.131	0.127	0.119	0.163	0.148	0.131	0.116
Max	0.225	0.183	0.168	0.149	0.236	0.188	0.154	0.217	0.256	0.322	0.254
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000

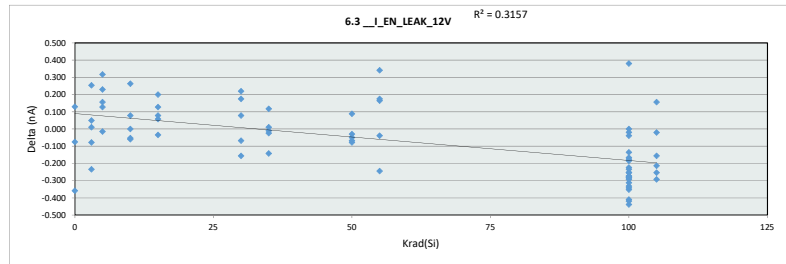




HDR TID Report  
TPS7H5004-SP

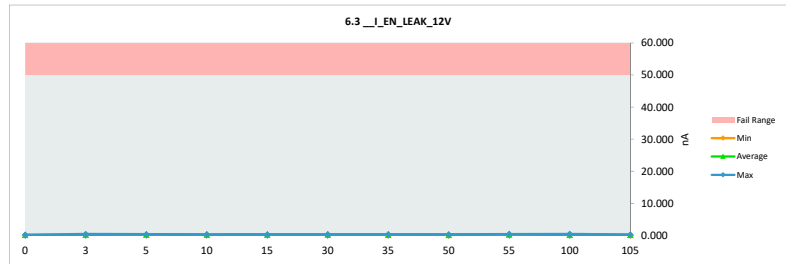
6.3_I_EN_LEAK_12V	
Test Site	
Tester	
Test Number	
Unit	nA
Max Limit	50
Min Limit	50

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.173	0.098	-0.075
0	297	0.027	0.156	0.129
0	298	0.388	0.029	-0.359
3	192	0.222	0.271	0.049
3	193	0.046	0.056	0.010
3	194	0.183	0.105	-0.078
3	196	0.241	0.007	-0.234
3	197	0.134	0.388	0.254
5	200	0.061	0.046	-0.015
5	202	0.046	0.173	0.127
5	203	0.012	0.329	0.317
5	204	0.115	0.271	0.156
5	206	0.041	0.271	0.230
10	207	0.046	0.046	0.000
10	209	0.158	0.105	-0.053
10	210	0.027	0.290	0.263
10	211	0.232	0.310	0.078
10	212	0.144	0.085	-0.059
15	214	0.144	0.222	0.078
15	217	0.071	0.271	0.200
15	218	0.183	0.241	0.058
15	219	0.066	0.032	-0.034
15	222	0.124	0.251	0.127
30	224	0.212	0.290	0.078
30	225	0.061	0.280	0.219
30	226	0.134	0.066	-0.068
30	227	0.271	0.115	-0.156
30	229	0.154	0.329	0.175
35	230	0.183	0.300	0.117
35	231	0.080	0.056	-0.024
35	232	0.183	0.041	-0.142
35	234	0.261	0.251	-0.010
35	237	0.261	0.271	0.010
50	272	0.076	0.046	-0.030
50	275	0.154	0.105	-0.049
50	276	0.183	0.271	0.088
50	277	0.241	0.163	-0.078
50	278	0.085	0.017	-0.068
55	279	0.183	0.358	0.175
55	280	0.280	0.241	-0.039
55	281	0.154	0.319	0.165
55	282	0.076	0.417	0.341
55	283	0.290	0.046	-0.244
100	317	0.234	0.098	-0.136
100	318	0.283	0.010	-0.273
100	319	0.381	0.098	-0.283
100	320	0.478	0.059	-0.419
100	321	0.390	0.156	-0.234
100	322	0.400	0.234	-0.166
100	323	0.342	0.010	-0.332
100	324	0.010	0.390	0.380
100	326	0.195	0.176	-0.019
100	327	0.351	0.098	-0.253
100	328	0.527	0.215	-0.312
100	329	0.517	0.078	-0.439
100	332	0.166	0.166	0.000
100	333	0.322	0.029	-0.293
100	334	0.420	0.078	-0.342
100	335	0.273	0.049	-0.224
100	336	0.195	0.156	-0.039
100	337	0.283	0.107	-0.176
100	338	0.205	0.020	-0.185
100	339	0.410	0.000	-0.410
100	340	0.381	0.029	-0.352
100	341	0.342	0.088	-0.254
100	342	0.303	0.020	-0.283
100	343	0.342	0.068	-0.274
105	344	0.400	0.107	-0.293
105	346	0.078	0.234	0.156
105	349	0.205	0.185	-0.020
105	350	0.195	0.039	-0.156
105	351	0.303	0.049	-0.254
105	352	0.312	0.098	-0.214
	Max	0.527	0.417	0.380
	Average	0.214	0.153	-0.061
	Min	0.010	0.000	-0.439
	Std Dev	0.128	0.114	0.200



6.3_I_EN_LEAK	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA

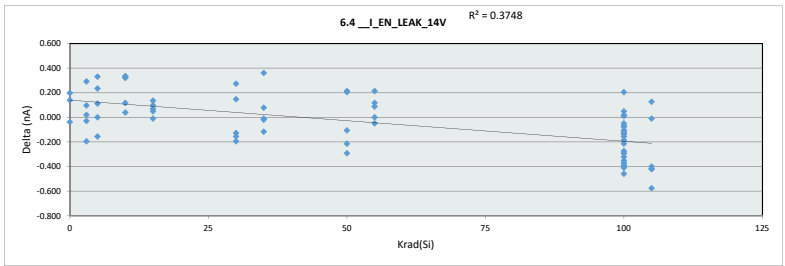
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.029	0.007	0.046	0.046	0.032	0.066	0.041	0.017	0.046	0.010	0.039
Average	0.094	0.165	0.218	0.167	0.203	0.216	0.184	0.120	0.276	0.101	0.119
Max	0.156	0.388	0.329	0.310	0.271	0.329	0.300	0.271	0.417	0.390	0.234
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



HDR TID Report  
TPS7H5004-SP

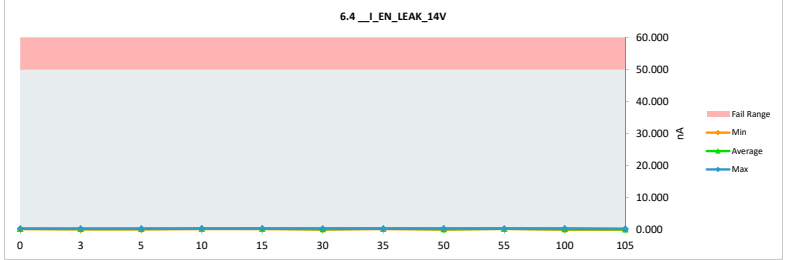
6.4_I_EN_LEAK_14V	
Test Site	
Tester	
Test Number	
Unit	nA
Max Limit	50
Min Limit	50

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.222	0.420	0.198
0	297	0.222	0.185	-0.037
0	298	0.232	0.371	0.139
3	192	0.407	0.427	0.020
3	193	0.027	0.124	0.097
3	194	0.105	0.397	0.292
3	196	0.436	0.241	-0.195
3	197	0.241	0.212	-0.029
5	200	0.154	0.388	0.234
5	202	0.012	0.124	0.112
5	203	0.241	0.085	-0.156
5	204	0.232	0.232	0.000
5	206	0.105	0.436	0.331
10	207	0.076	0.397	0.321
10	209	0.071	0.407	0.336
10	210	0.163	0.202	0.039
10	211	0.173	0.290	0.117
10	212	0.144	0.475	0.331
15	214	0.251	0.349	0.098
15	217	0.144	0.212	0.068
15	218	0.349	0.339	-0.010
15	219	0.310	0.446	0.136
15	222	0.339	0.388	0.049
30	224	0.280	0.427	0.147
30	225	0.349	0.222	-0.127
30	226	0.027	0.300	0.273
30	227	0.232	0.076	-0.156
30	229	0.290	0.095	-0.195
35	230	0.466	0.349	-0.117
35	231	0.007	0.368	0.361
35	232	0.358	0.339	-0.019
35	234	0.358	0.436	0.078
35	237	0.232	0.222	-0.010
50	272	0.358	0.144	-0.214
50	275	0.124	0.329	0.205
50	276	0.144	0.037	-0.107
50	277	0.271	0.485	0.214
50	278	0.368	0.076	-0.292
55	279	0.339	0.290	-0.049
55	280	0.300	0.417	0.117
55	281	0.173	0.261	0.088
55	282	0.144	0.358	0.214
55	283	0.251	0.251	0.000
100	317	0.264	0.156	-0.108
100	318	0.547	0.273	-0.274
100	319	0.498	0.205	-0.293
100	320	0.088	0.098	0.010
100	321	0.556	0.146	-0.410
100	322	0.400	0.244	-0.156
100	323	0.264	0.215	-0.049
100	324	0.254	0.146	-0.108
100	326	0.166	0.088	-0.078
100	327	0.264	0.312	0.048
100	328	0.400	0.000	-0.400
100	329	0.332	0.146	-0.186
100	332	0.469	0.342	-0.127
100	333	0.469	0.146	-0.323
100	334	0.498	0.039	-0.459
100	335	0.469	0.488	0.019
100	336	0.234	0.439	0.205
100	337	0.498	0.283	-0.215
100	338	0.586	0.234	-0.352
100	339	0.244	0.107	-0.137
100	340	0.429	0.039	-0.390
100	341	0.459	0.166	-0.293
100	342	0.429	0.361	-0.068
100	343	0.400	0.029	-0.371
105	344	0.225	0.351	0.126
105	346	0.488	0.068	-0.420
105	349	0.625	0.049	-0.576
105	350	0.537	0.117	-0.420
105	351	0.322	0.312	-0.010
105	352	0.400	0.000	-0.400
Max		0.625	0.488	0.361
Average		0.295	0.250	-0.045
Min		0.007	0.000	-0.576
Std Dev		0.150	0.137	0.225



6.4_I_EN_LEAK	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	50 nA

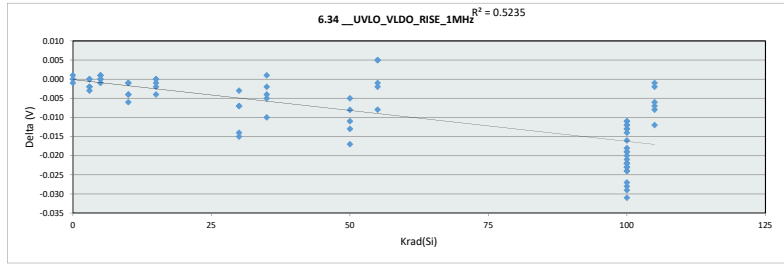
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.185	0.124	0.085	0.202	0.212	0.076	0.222	0.037	0.251	0.029	0.049
Average	0.325	0.280	0.253	0.354	0.347	0.224	0.343	0.214	0.315	0.196	0.150
Max	0.420	0.427	0.436	0.475	0.446	0.427	0.436	0.485	0.417	0.488	0.351
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



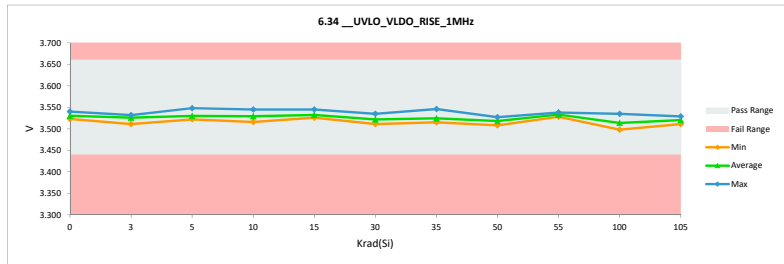
HDR TID Report  
TPS7H5004-SP

6.34 __UVLO_VLDO_RISE_1MHz		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	3.66	3.66
Min Limit	3.44	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	295	3.540	3.540	0.000
0	297	3.522	3.523	0.001
0	298	3.529	3.528	-0.001
3	192	3.529	3.527	-0.002
3	193	3.532	3.529	-0.003
3	194	3.532	3.532	0.000
3	196	3.511	3.511	0.000
3	197	3.532	3.530	-0.002
5	200	3.526	3.527	0.001
5	202	3.521	3.522	0.001
5	203	3.525	3.524	-0.001
5	204	3.529	3.529	0.000
5	206	3.548	3.548	0.000
10	207	3.537	3.533	-0.004
10	209	3.546	3.545	-0.001
10	210	3.517	3.516	-0.001
10	211	3.527	3.521	-0.006
10	212	3.535	3.531	-0.004
15	214	3.528	3.528	0.000
15	217	3.531	3.531	0.000
15	218	3.546	3.545	-0.001
15	219	3.530	3.526	-0.004
15	222	3.533	3.531	-0.002
30	224	3.534	3.527	-0.007
30	225	3.529	3.522	-0.007
30	226	3.525	3.511	-0.014
30	227	3.538	3.535	-0.003
30	229	3.529	3.514	-0.015
35	230	3.550	3.546	-0.004
35	231	3.514	3.515	0.001
35	232	3.526	3.524	-0.002
35	234	3.523	3.518	-0.005
35	237	3.529	3.519	-0.010
50	272	3.526	3.518	-0.008
50	275	3.525	3.508	-0.017
50	276	3.516	3.511	-0.005
50	277	3.540	3.527	-0.013
50	278	3.538	3.527	-0.011
55	279	3.546	3.538	-0.008
55	280	3.528	3.533	0.005
55	281	3.523	3.528	0.005
55	282	3.531	3.530	-0.001
55	283	3.539	3.537	-0.002
100	317	3.543	3.522	-0.021
100	318	3.541	3.518	-0.023
100	319	3.532	3.509	-0.023
100	320	3.546	3.535	-0.011
100	321	3.526	3.510	-0.016
100	322	3.531	3.519	-0.012
100	323	3.531	3.507	-0.024
100	324	3.514	3.503	-0.011
100	326	3.523	3.501	-0.022
100	327	3.532	3.518	-0.014
100	328	3.520	3.498	-0.022
100	329	3.530	3.511	-0.019
100	332	3.539	3.520	-0.019
100	333	3.537	3.515	-0.022
100	334	3.531	3.513	-0.018
100	335	3.533	3.520	-0.013
100	336	3.541	3.517	-0.024
100	337	3.534	3.503	-0.031
100	338	3.526	3.506	-0.020
100	339	3.539	3.511	-0.028
100	340	3.535	3.522	-0.013
100	341	3.538	3.509	-0.029
100	342	3.534	3.522	-0.012
100	343	3.543	3.516	-0.027
105	344	3.523	3.511	-0.012
105	346	3.528	3.522	-0.006
105	349	3.534	3.527	-0.007
105	350	3.518	3.516	-0.002
105	351	3.518	3.517	-0.001
105	352	3.537	3.529	-0.008
Max		3.550	3.548	0.005
Average		3.531	3.522	-0.009
Min		3.511	3.498	-0.031
Std Dev		0.009	0.011	0.009

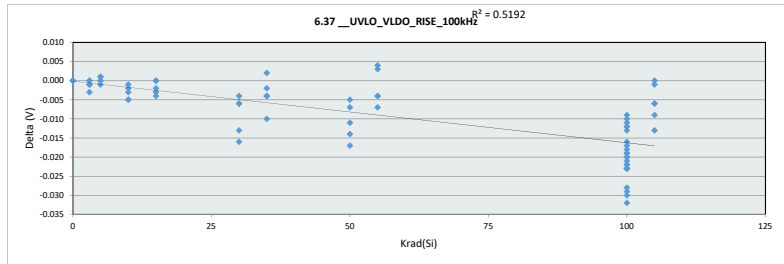


6.34 __UVLO_VLDO		0	3	5	10	15	30	35	50	55	100	105
LL		3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min		3.523	3.511	3.522	3.516	3.526	3.511	3.515	3.508	3.528	3.498	3.511
Average		3.530	3.526	3.530	3.529	3.532	3.522	3.524	3.518	3.533	3.514	3.520
Max		3.540	3.532	3.548	3.545	3.545	3.535	3.546	3.527	3.538	3.535	3.529
UL		3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

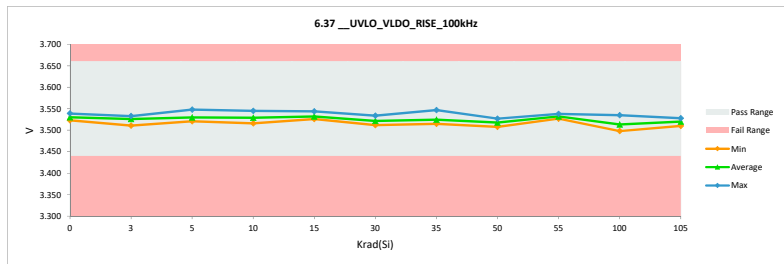


HDR TID Report  
TPS7H5004-SP

6.37 __UVLO_VLDO_RISE_100kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.66	3.66		
Min Limit	3.44	3.44		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	3.539	3.539	0.000
0	297	3.523	3.523	0.000
0	298	3.529	3.529	0.000
3	192	3.528	3.527	-0.001
3	193	3.532	3.529	-0.003
3	194	3.533	3.533	0.000
3	196	3.511	3.511	0.000
3	197	3.531	3.530	-0.001
5	200	3.526	3.526	0.000
5	202	3.522	3.521	-0.001
5	203	3.525	3.525	0.000
5	204	3.529	3.530	0.001
5	206	3.547	3.548	0.001
10	207	3.536	3.533	-0.003
10	209	3.547	3.545	-0.002
10	210	3.517	3.516	-0.001
10	211	3.526	3.521	-0.005
10	212	3.535	3.530	-0.005
15	214	3.528	3.528	0.000
15	217	3.531	3.531	0.000
15	218	3.546	3.544	-0.002
15	219	3.530	3.526	-0.004
15	222	3.534	3.531	-0.003
30	224	3.533	3.527	-0.006
30	225	3.529	3.523	-0.006
30	226	3.525	3.512	-0.013
30	227	3.538	3.534	-0.004
30	229	3.529	3.513	-0.016
35	230	3.551	3.547	-0.004
35	231	3.513	3.515	0.002
35	232	3.527	3.525	-0.002
35	234	3.523	3.519	-0.004
35	237	3.528	3.518	-0.010
50	272	3.525	3.518	-0.007
50	275	3.525	3.508	-0.017
50	276	3.515	3.510	-0.005
50	277	3.540	3.526	-0.014
50	278	3.538	3.527	-0.011
55	279	3.545	3.538	-0.007
55	280	3.529	3.532	0.003
55	281	3.523	3.527	0.004
55	282	3.532	3.528	-0.004
55	283	3.540	3.536	-0.004
100	317	3.543	3.522	-0.021
100	318	3.541	3.518	-0.023
100	319	3.532	3.509	-0.023
100	320	3.545	3.535	-0.010
100	321	3.527	3.510	-0.017
100	322	3.531	3.519	-0.012
100	323	3.530	3.507	-0.023
100	324	3.512	3.503	-0.009
100	326	3.523	3.503	-0.020
100	327	3.533	3.517	-0.016
100	328	3.520	3.498	-0.022
100	329	3.531	3.511	-0.020
100	332	3.539	3.520	-0.019
100	333	3.537	3.515	-0.022
100	334	3.530	3.512	-0.018
100	335	3.532	3.520	-0.012
100	336	3.540	3.517	-0.023
100	337	3.535	3.503	-0.032
100	338	3.526	3.507	-0.019
100	339	3.540	3.511	-0.029
100	340	3.534	3.521	-0.013
100	341	3.538	3.508	-0.030
100	342	3.534	3.523	-0.011
100	343	3.543	3.515	-0.028
105	344	3.523	3.510	-0.013
105	346	3.527	3.521	-0.006
105	349	3.534	3.528	-0.006
105	350	3.517	3.517	0.000
105	351	3.518	3.517	-0.001
105	352	3.537	3.528	-0.009
Max		3.551	3.548	0.004
Average		3.531	3.522	-0.009
Min		3.511	3.498	-0.032
Std Dev		0.009	0.011	0.009



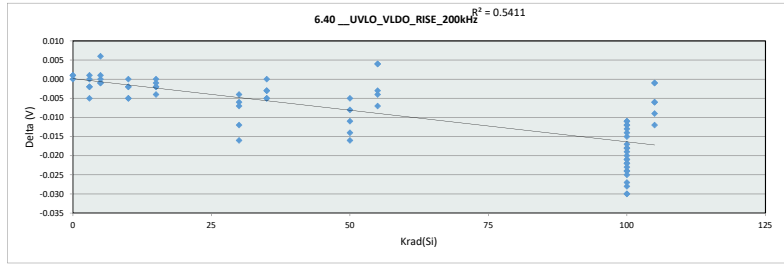
6.37 __UVLO_VLDO											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.523	3.511	3.521	3.516	3.526	3.512	3.515	3.508	3.527	3.498	3.510
Average	3.530	3.526	3.530	3.529	3.532	3.522	3.525	3.518	3.532	3.514	3.520
Max	3.539	3.533	3.548	3.545	3.544	3.534	3.547	3.527	3.538	3.535	3.528
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



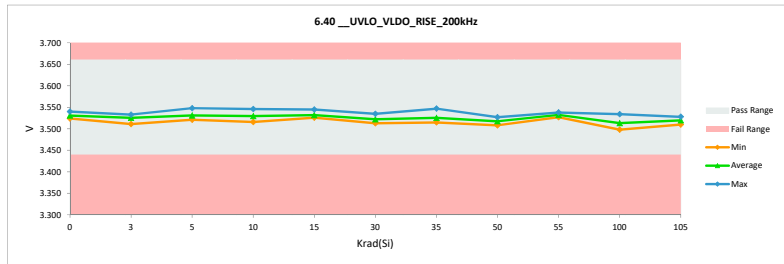
HDR TID Report  
TPS7H5004-SP

6.40 __UVLO_VLDO_RISE_200kHz		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	3.66	3.66
Min Limit	3.44	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	295	3.539	3.540	0.001
0	297	3.523	3.524	0.001
0	298	3.529	3.529	0.000
3	192	3.529	3.527	-0.002
3	193	3.532	3.527	-0.005
3	194	3.532	3.533	0.001
3	196	3.511	3.511	0.000
3	197	3.532	3.530	-0.002
5	200	3.527	3.526	-0.001
5	202	3.522	3.521	-0.001
5	203	3.525	3.531	0.006
5	204	3.530	3.530	0.000
5	206	3.547	3.548	0.001
10	207	3.536	3.534	-0.002
10	209	3.546	3.546	0.000
10	210	3.518	3.516	-0.002
10	211	3.526	3.521	-0.005
10	212	3.536	3.531	-0.005
15	214	3.529	3.528	-0.001
15	217	3.531	3.531	0.000
15	218	3.547	3.545	-0.002
15	219	3.530	3.526	-0.004
15	222	3.532	3.530	-0.002
30	224	3.534	3.527	-0.007
30	225	3.529	3.523	-0.006
30	226	3.525	3.513	-0.012
30	227	3.539	3.535	-0.004
30	229	3.529	3.513	-0.016
35	230	3.550	3.547	-0.003
35	231	3.515	3.515	0.000
35	232	3.528	3.525	-0.003
35	234	3.523	3.518	-0.005
35	237	3.528	3.523	-0.005
50	272	3.525	3.517	-0.008
50	275	3.524	3.508	-0.016
50	276	3.515	3.510	-0.005
50	277	3.540	3.526	-0.014
50	278	3.538	3.527	-0.011
55	279	3.545	3.538	-0.007
55	280	3.529	3.533	0.004
55	281	3.523	3.527	0.004
55	282	3.531	3.528	-0.003
55	283	3.540	3.536	-0.004
100	317	3.543	3.522	-0.021
100	318	3.541	3.518	-0.023
100	319	3.532	3.508	-0.024
100	320	3.545	3.534	-0.011
100	321	3.527	3.510	-0.017
100	322	3.531	3.518	-0.013
100	323	3.530	3.506	-0.024
100	324	3.513	3.502	-0.011
100	326	3.522	3.501	-0.021
100	327	3.532	3.517	-0.015
100	328	3.520	3.498	-0.022
100	329	3.530	3.510	-0.020
100	332	3.539	3.520	-0.019
100	333	3.537	3.515	-0.022
100	334	3.531	3.513	-0.018
100	335	3.532	3.520	-0.012
100	336	3.541	3.516	-0.025
100	337	3.534	3.504	-0.030
100	338	3.525	3.507	-0.018
100	339	3.540	3.512	-0.028
100	340	3.535	3.521	-0.014
100	341	3.538	3.508	-0.030
100	342	3.534	3.522	-0.012
100	343	3.543	3.516	-0.027
105	344	3.522	3.510	-0.012
105	346	3.527	3.521	-0.006
105	349	3.533	3.527	-0.006
105	350	3.517	3.516	-0.001
105	351	3.518	3.517	-0.001
105	352	3.537	3.528	-0.009
Max		3.550	3.548	0.006
Average		3.531	3.522	-0.009
Min		3.511	3.498	-0.030
Std Dev		0.009	0.011	0.009



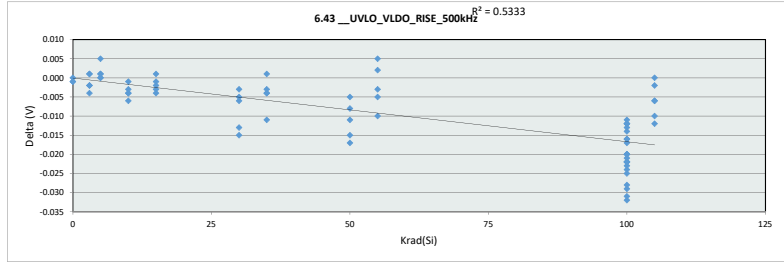
6.40 __UVLO_VLDO		0	3	5	10	15	30	35	50	55	100	105
LL		3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min		3.524	3.511	3.521	3.516	3.526	3.513	3.515	3.508	3.527	3.498	3.510
Average		3.531	3.526	3.531	3.530	3.532	3.522	3.526	3.518	3.532	3.513	3.520
Max		3.540	3.533	3.548	3.546	3.545	3.535	3.547	3.527	3.538	3.534	3.528
UL		3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



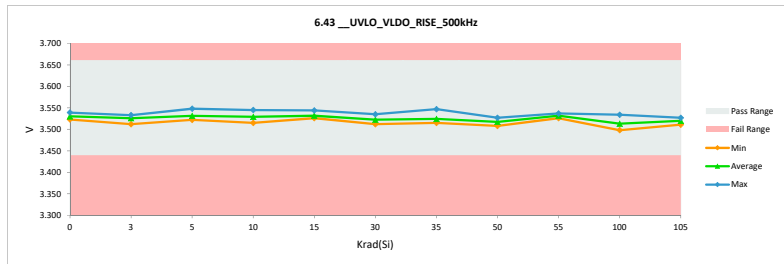
HDR TID Report  
TPS7H5004-SP

6.43 __UVLO_VLDO_RISE_500kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	295	3.540	3.539	-0.001
0	297	3.523	3.523	0.000
0	298	3.530	3.529	-0.001
3	192	3.529	3.527	-0.002
3	193	3.532	3.528	-0.004
3	194	3.532	3.533	0.001
3	196	3.511	3.512	0.001
3	197	3.532	3.530	-0.002
5	200	3.526	3.526	0.000
5	202	3.522	3.522	0.000
5	203	3.525	3.530	0.005
5	204	3.530	3.531	0.001
5	206	3.547	3.548	0.001
10	207	3.537	3.533	-0.004
10	209	3.546	3.545	-0.001
10	210	3.518	3.515	-0.003
10	211	3.527	3.521	-0.006
10	212	3.536	3.532	-0.004
15	214	3.529	3.528	-0.001
15	217	3.530	3.531	0.001
15	218	3.546	3.544	-0.002
15	219	3.530	3.526	-0.004
15	222	3.533	3.530	-0.003
30	224	3.534	3.528	-0.006
30	225	3.528	3.523	-0.005
30	226	3.525	3.512	-0.013
30	227	3.538	3.535	-0.003
30	229	3.529	3.514	-0.015
35	230	3.551	3.547	-0.004
35	231	3.514	3.515	0.001
35	232	3.527	3.524	-0.003
35	234	3.522	3.518	-0.004
35	237	3.528	3.517	-0.011
50	272	3.525	3.517	-0.008
50	275	3.525	3.508	-0.017
50	276	3.515	3.510	-0.005
50	277	3.540	3.525	-0.015
50	278	3.538	3.527	-0.011
55	279	3.546	3.536	-0.010
55	280	3.528	3.533	0.005
55	281	3.524	3.526	0.002
55	282	3.532	3.527	-0.005
55	283	3.540	3.537	-0.003
100	317	3.543	3.521	-0.022
100	318	3.541	3.519	-0.022
100	319	3.532	3.509	-0.023
100	320	3.546	3.534	-0.012
100	321	3.526	3.510	-0.016
100	322	3.531	3.519	-0.012
100	323	3.531	3.506	-0.025
100	324	3.513	3.502	-0.011
100	326	3.522	3.501	-0.021
100	327	3.533	3.517	-0.016
100	328	3.520	3.498	-0.022
100	329	3.530	3.510	-0.020
100	332	3.540	3.520	-0.020
100	333	3.537	3.515	-0.022
100	334	3.530	3.513	-0.017
100	335	3.534	3.521	-0.013
100	336	3.541	3.517	-0.024
100	337	3.534	3.502	-0.032
100	338	3.526	3.506	-0.020
100	339	3.540	3.511	-0.029
100	340	3.535	3.521	-0.014
100	341	3.539	3.508	-0.031
100	342	3.534	3.522	-0.012
100	343	3.543	3.515	-0.028
105	344	3.523	3.511	-0.012
105	346	3.527	3.521	-0.006
105	349	3.533	3.527	-0.006
105	350	3.516	3.516	0.000
105	351	3.519	3.517	-0.002
105	352	3.537	3.527	-0.010
Max		3.551	3.548	0.005
Average		3.531	3.522	-0.009
Min		3.511	3.498	-0.032
Std Dev		0.009	0.011	0.009



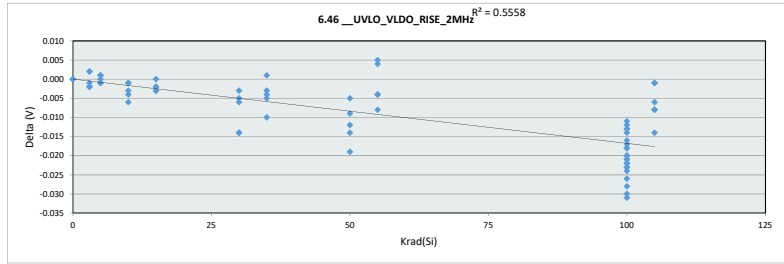
6.43 __UVLO_VLDO		0	3	5	10	15	30	35	50	55	100	105
LL		3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min		3.523	3.512	3.522	3.515	3.526	3.512	3.515	3.508	3.526	3.498	3.511
Average		3.530	3.526	3.531	3.529	3.532	3.522	3.524	3.517	3.532	3.513	3.520
Max		3.539	3.533	3.548	3.545	3.544	3.535	3.547	3.527	3.537	3.534	3.527
UL		3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



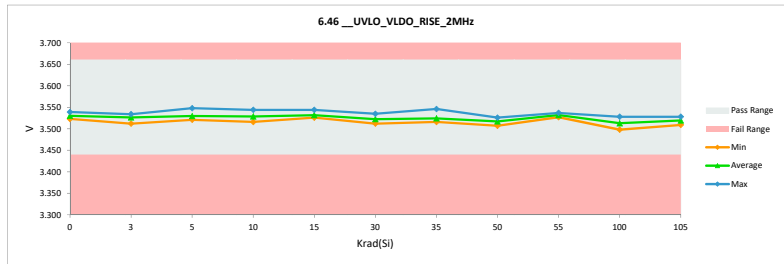
HDR TID Report  
TPS7H5004-SP

6.46 __UVLO_VLDO_RISE_2MHz		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	3.66	3.66
Min Limit	3.44	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	295	3.539	3.539	0.000
0	297	3.523	3.523	0.000
0	298	3.529	3.529	0.000
3	192	3.529	3.527	-0.002
3	193	3.532	3.530	-0.002
3	194	3.532	3.534	0.002
3	196	3.510	3.512	0.002
3	197	3.531	3.530	-0.001
5	200	3.526	3.526	0.000
5	202	3.522	3.521	-0.001
5	203	3.525	3.524	-0.001
5	204	3.529	3.530	0.001
5	206	3.547	3.548	0.001
10	207	3.536	3.533	-0.003
10	209	3.545	3.544	-0.001
10	210	3.517	3.516	-0.001
10	211	3.526	3.520	-0.006
10	212	3.535	3.531	-0.004
15	214	3.529	3.527	-0.002
15	217	3.531	3.531	0.000
15	218	3.547	3.544	-0.003
15	219	3.529	3.526	-0.003
15	222	3.533	3.531	-0.002
30	224	3.533	3.528	-0.005
30	225	3.529	3.523	-0.006
30	226	3.526	3.512	-0.014
30	227	3.538	3.535	-0.003
30	229	3.528	3.514	-0.014
35	230	3.550	3.546	-0.004
35	231	3.515	3.516	0.001
35	232	3.527	3.524	-0.003
35	234	3.523	3.518	-0.005
35	237	3.528	3.518	-0.010
50	272	3.526	3.517	-0.009
50	275	3.526	3.507	-0.019
50	276	3.515	3.510	-0.005
50	277	3.540	3.526	-0.014
50	278	3.538	3.526	-0.012
55	279	3.545	3.537	-0.008
55	280	3.528	3.533	0.005
55	281	3.523	3.527	0.004
55	282	3.531	3.527	-0.004
55	283	3.540	3.536	-0.004
100	317	3.543	3.522	-0.021
100	318	3.541	3.518	-0.023
100	319	3.531	3.509	-0.022
100	320	3.545	3.528	-0.017
100	321	3.527	3.509	-0.018
100	322	3.531	3.518	-0.013
100	323	3.530	3.507	-0.023
100	324	3.514	3.503	-0.011
100	326	3.522	3.502	-0.020
100	327	3.533	3.517	-0.016
100	328	3.520	3.498	-0.022
100	329	3.530	3.509	-0.021
100	332	3.540	3.520	-0.020
100	333	3.537	3.514	-0.023
100	334	3.531	3.513	-0.018
100	335	3.533	3.520	-0.013
100	336	3.541	3.517	-0.024
100	337	3.534	3.503	-0.031
100	338	3.526	3.508	-0.018
100	339	3.539	3.511	-0.028
100	340	3.534	3.520	-0.014
100	341	3.538	3.508	-0.030
100	342	3.534	3.522	-0.012
100	343	3.542	3.516	-0.026
105	344	3.523	3.509	-0.014
105	346	3.527	3.521	-0.006
105	349	3.534	3.526	-0.008
105	350	3.517	3.516	-0.001
105	351	3.518	3.517	-0.001
105	352	3.536	3.528	-0.008
Max		3.550	3.548	0.005
Average		3.531	3.522	-0.009
Min		3.510	3.498	-0.031
Std Dev		0.009	0.011	0.009

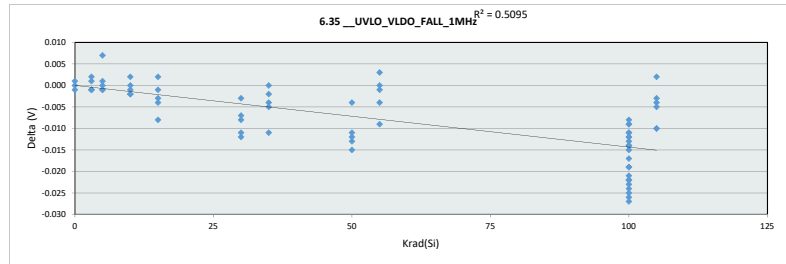


6.46 __UVLO_VLDO		0	3	5	10	15	30	35	50	55	100	105
LL		3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min		3.523	3.512	3.521	3.516	3.526	3.512	3.516	3.507	3.527	3.498	3.509
Average		3.530	3.527	3.530	3.529	3.532	3.522	3.524	3.517	3.532	3.513	3.520
Max		3.539	3.534	3.548	3.544	3.544	3.535	3.546	3.526	3.537	3.528	3.528
UL		3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

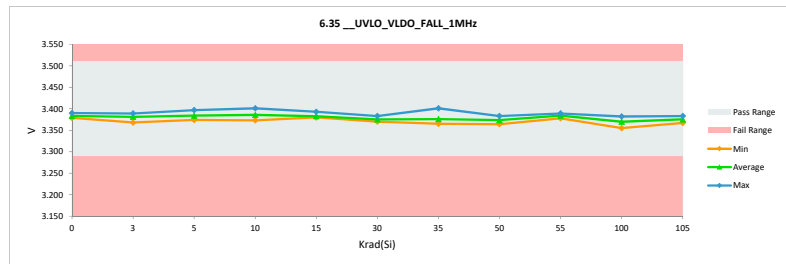


HDR TID Report  
TPS7H5004-SP

6.35 __UVLO_VLDO_FALL_1MHz					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	3.51	3.51			
Min Limit	3.29	3.29			
Krad(Si)	Serial #	PRE	POST	Delta	
0	295	3.390	3.390	0.000	
0	297	3.380	3.379	-0.001	
0	298	3.380	3.381	0.001	
3	192	3.381	3.382	0.001	
3	193	3.383	3.382	-0.001	
3	194	3.386	3.385	-0.001	
3	196	3.369	3.368	-0.001	
3	197	3.387	3.389	0.002	
5	200	3.375	3.374	-0.001	
5	202	3.373	3.374	0.001	
5	203	3.383	3.382	-0.001	
5	204	3.386	3.393	0.007	
5	206	3.397	3.397	0.000	
10	207	3.388	3.387	-0.001	
10	209	3.403	3.401	-0.002	
10	210	3.373	3.373	0.000	
10	211	3.382	3.384	0.002	
10	212	3.386	3.384	-0.002	
15	214	3.381	3.380	-0.001	
15	217	3.379	3.381	0.002	
15	218	3.397	3.393	-0.004	
15	219	3.383	3.380	-0.003	
15	222	3.388	3.380	-0.008	
30	224	3.393	3.381	-0.012	
30	225	3.378	3.371	-0.007	
30	226	3.374	3.371	-0.003	
30	227	3.391	3.383	-0.008	
30	229	3.381	3.370	-0.011	
35	230	3.401	3.401	0.000	
35	231	3.370	3.365	-0.005	
35	232	3.375	3.373	-0.002	
35	234	3.379	3.368	-0.011	
35	237	3.377	3.373	-0.004	
50	272	3.390	3.379	-0.011	
50	275	3.377	3.364	-0.013	
50	276	3.382	3.367	-0.015	
50	277	3.387	3.375	-0.012	
50	278	3.387	3.383	-0.004	
55	279	3.398	3.389	-0.009	
55	280	3.388	3.384	-0.004	
55	281	3.379	3.378	-0.001	
55	282	3.379	3.382	0.003	
55	283	3.388	3.388	0.000	
100	317	3.390	3.376	-0.014	
100	318	3.399	3.373	-0.026	
100	319	3.384	3.376	-0.008	
100	320	3.395	3.382	-0.013	
100	321	3.384	3.362	-0.022	
100	322	3.379	3.368	-0.011	
100	323	3.396	3.375	-0.021	
100	324	3.382	3.368	-0.014	
100	326	3.377	3.368	-0.009	
100	327	3.385	3.368	-0.017	
100	328	3.370	3.355	-0.015	
100	329	3.380	3.361	-0.019	
100	332	3.395	3.373	-0.022	
100	333	3.391	3.366	-0.025	
100	334	3.386	3.363	-0.023	
100	335	3.387	3.376	-0.011	
100	336	3.394	3.367	-0.027	
100	337	3.389	3.370	-0.019	
100	338	3.376	3.364	-0.012	
100	339	3.390	3.366	-0.024	
100	340	3.382	3.370	-0.012	
100	341	3.387	3.373	-0.014	
100	342	3.391	3.382	-0.009	
100	343	3.394	3.372	-0.022	
105	344	3.387	3.377	-0.010	
105	346	3.376	3.372	-0.004	
105	349	3.388	3.383	-0.005	
105	350	3.370	3.367	-0.003	
105	351	3.372	3.374	0.002	
105	352	3.390	3.380	-0.010	
Max		3.403	3.401	0.007	
Average		3.385	3.377	-0.008	
Min		3.369	3.355	-0.027	
Std Dev		0.008	0.009	0.008	



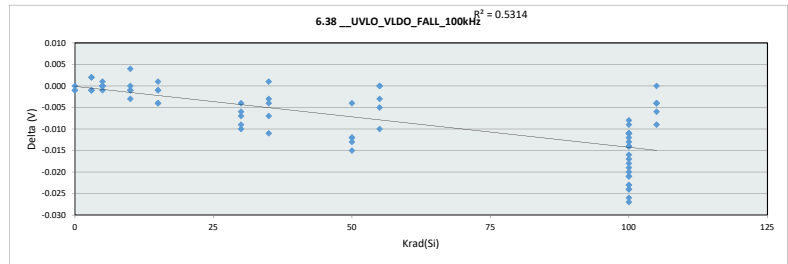
6.35 __UVLO_VLDO											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.379	3.368	3.374	3.373	3.380	3.370	3.365	3.364	3.378	3.355	3.367
Average	3.383	3.381	3.384	3.386	3.383	3.375	3.376	3.374	3.384	3.370	3.376
Max	3.390	3.389	3.397	3.401	3.393	3.383	3.401	3.383	3.389	3.382	3.383
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



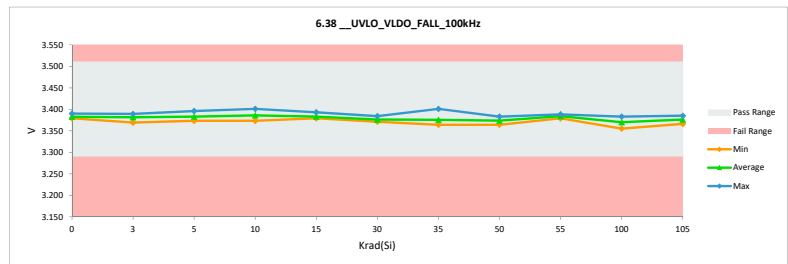


HDR TID Report  
TPS7H5004-SP

6.38 __UVLO_VLDO_FALL_100kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	3.390	3.390	0.000
0	297	3.380	3.379	-0.001
0	298	3.380	3.379	-0.001
3	192	3.381	3.383	0.002
3	193	3.383	3.382	-0.001
3	194	3.385	3.384	-0.001
3	196	3.370	3.369	-0.001
3	197	3.387	3.389	0.002
5	200	3.375	3.375	0.000
5	202	3.373	3.373	0.000
5	203	3.384	3.383	-0.001
5	204	3.386	3.387	0.001
5	206	3.396	3.396	0.000
10	207	3.389	3.386	-0.003
10	209	3.402	3.401	-0.001
10	210	3.373	3.373	0.000
10	211	3.381	3.385	0.004
10	212	3.386	3.385	-0.001
15	214	3.381	3.380	-0.001
15	217	3.380	3.381	0.001
15	218	3.397	3.393	-0.004
15	219	3.382	3.381	-0.001
15	222	3.383	3.379	-0.004
30	224	3.393	3.383	-0.010
30	225	3.378	3.372	-0.006
30	226	3.375	3.371	-0.004
30	227	3.391	3.384	-0.007
30	229	3.380	3.371	-0.009
35	230	3.400	3.401	0.001
35	231	3.371	3.364	-0.007
35	232	3.376	3.373	-0.003
35	234	3.378	3.367	-0.011
35	237	3.377	3.373	-0.004
50	272	3.391	3.379	-0.012
50	275	3.376	3.364	-0.012
50	276	3.382	3.367	-0.015
50	277	3.388	3.375	-0.013
50	278	3.387	3.383	-0.004
55	279	3.398	3.388	-0.010
55	280	3.387	3.384	-0.003
55	281	3.379	3.379	0.000
55	282	3.386	3.381	-0.005
55	283	3.388	3.388	0.000
100	317	3.390	3.376	-0.014
100	318	3.400	3.374	-0.026
100	319	3.385	3.377	-0.008
100	320	3.395	3.383	-0.012
100	321	3.383	3.362	-0.021
100	322	3.379	3.368	-0.011
100	323	3.396	3.376	-0.020
100	324	3.382	3.368	-0.014
100	326	3.377	3.368	-0.009
100	327	3.386	3.368	-0.018
100	328	3.369	3.355	-0.014
100	329	3.379	3.360	-0.019
100	332	3.394	3.371	-0.023
100	333	3.391	3.367	-0.024
100	334	3.386	3.363	-0.023
100	335	3.387	3.376	-0.011
100	336	3.394	3.367	-0.027
100	337	3.387	3.370	-0.017
100	338	3.375	3.364	-0.011
100	339	3.391	3.367	-0.024
100	340	3.384	3.371	-0.013
100	341	3.388	3.372	-0.016
100	342	3.392	3.381	-0.011
100	343	3.394	3.373	-0.021
105	344	3.387	3.378	-0.009
105	346	3.377	3.371	-0.006
105	349	3.387	3.383	-0.004
105	350	3.370	3.366	-0.004
105	351	3.374	3.374	0.000
105	352	3.389	3.385	-0.004
Max		3.402	3.401	0.004
Average		3.385	3.377	-0.008
Min		3.369	3.355	-0.027
Std Dev		0.008	0.009	0.008



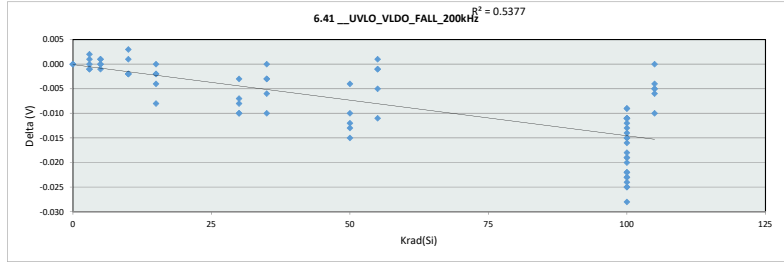
6.38 __UVLO_VLDO											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.379	3.369	3.373	3.373	3.379	3.371	3.364	3.364	3.379	3.355	3.366
Average	3.383	3.381	3.383	3.386	3.383	3.376	3.376	3.374	3.384	3.370	3.376
Max	3.390	3.389	3.396	3.401	3.393	3.384	3.401	3.383	3.388	3.383	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



HDR TID Report  
TPS7H5004-SP

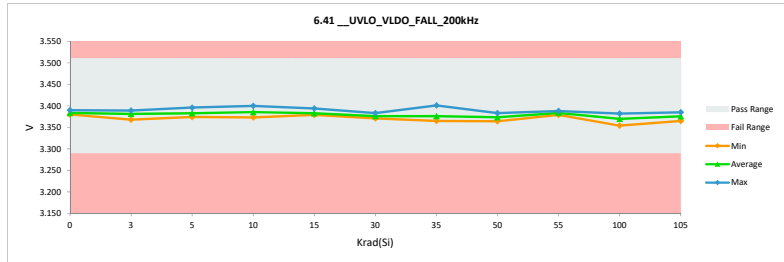
6.41 __UVLO_VLDO_FALL_200kHz	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	3.51 3.51
Min Limit	3.29 3.29

Krad(Si)	Serial #	PRE	POST	Delta
0	295	3.390	3.390	0.000
0	297	3.380	3.380	0.000
0	298	3.380	3.380	0.000
3	192	3.381	3.382	0.001
3	193	3.383	3.383	0.000
3	194	3.385	3.384	-0.001
3	196	3.369	3.368	-0.001
3	197	3.387	3.389	0.002
5	200	3.375	3.375	0.000
5	202	3.373	3.374	0.001
5	203	3.383	3.382	-0.001
5	204	3.386	3.387	0.001
5	206	3.396	3.396	0.000
10	207	3.389	3.387	-0.002
10	209	3.402	3.400	-0.002
10	210	3.372	3.373	0.001
10	211	3.381	3.384	0.003
10	212	3.386	3.384	-0.002
15	214	3.382	3.380	-0.002
15	217	3.380	3.380	0.000
15	218	3.398	3.394	-0.004
15	219	3.383	3.381	-0.002
15	222	3.387	3.379	-0.008
30	224	3.393	3.383	-0.010
30	225	3.379	3.372	-0.007
30	226	3.374	3.371	-0.003
30	227	3.391	3.383	-0.008
30	229	3.381	3.371	-0.010
35	230	3.401	3.401	0.000
35	231	3.371	3.365	-0.006
35	232	3.376	3.373	-0.003
35	234	3.378	3.368	-0.010
35	237	3.376	3.373	-0.003
50	272	3.389	3.379	-0.010
50	275	3.376	3.364	-0.012
50	276	3.381	3.368	-0.013
50	277	3.389	3.374	-0.015
50	278	3.387	3.383	-0.004
55	279	3.399	3.388	-0.011
55	280	3.388	3.383	-0.005
55	281	3.380	3.379	-0.001
55	282	3.380	3.381	0.001
55	283	3.388	3.387	-0.001
100	317	3.391	3.376	-0.015
100	318	3.399	3.374	-0.025
100	319	3.385	3.376	-0.009
100	320	3.394	3.382	-0.012
100	321	3.384	3.362	-0.022
100	322	3.379	3.368	-0.011
100	323	3.395	3.376	-0.019
100	324	3.382	3.368	-0.014
100	326	3.377	3.368	-0.009
100	327	3.386	3.367	-0.019
100	328	3.369	3.354	-0.015
100	329	3.380	3.360	-0.020
100	332	3.394	3.372	-0.022
100	333	3.391	3.368	-0.023
100	334	3.386	3.363	-0.023
100	335	3.387	3.376	-0.011
100	336	3.395	3.367	-0.028
100	337	3.388	3.370	-0.018
100	338	3.376	3.365	-0.011
100	339	3.392	3.367	-0.025
100	340	3.383	3.370	-0.013
100	341	3.388	3.372	-0.016
100	342	3.392	3.381	-0.011
100	343	3.395	3.371	-0.024
105	344	3.387	3.377	-0.010
105	346	3.376	3.371	-0.005
105	349	3.388	3.383	-0.005
105	350	3.371	3.365	-0.006
105	351	3.374	3.374	0.000
105	352	3.389	3.385	-0.004
Max		3.402	3.401	0.003
Average		3.385	3.377	-0.008
Min		3.369	3.354	-0.028
Std Dev		0.008	0.009	0.008



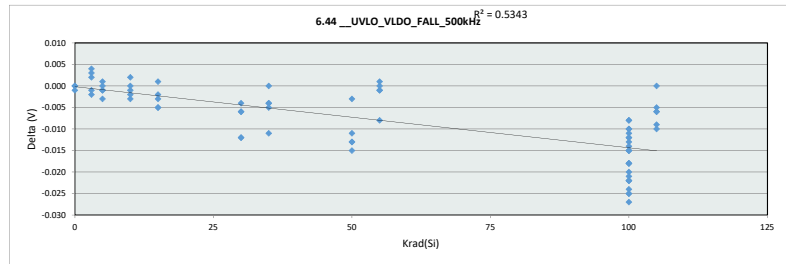
6.41 __UVLO_VLDO	
Test Site	
Tester	
Test Number	
Max Limit	3.51 V
Min Limit	3.29 V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.380	3.368	3.374	3.373	3.379	3.371	3.365	3.364	3.379	3.354	3.365
Average	3.383	3.381	3.383	3.386	3.383	3.376	3.376	3.374	3.384	3.370	3.376
Max	3.390	3.389	3.396	3.400	3.394	3.383	3.401	3.383	3.388	3.382	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

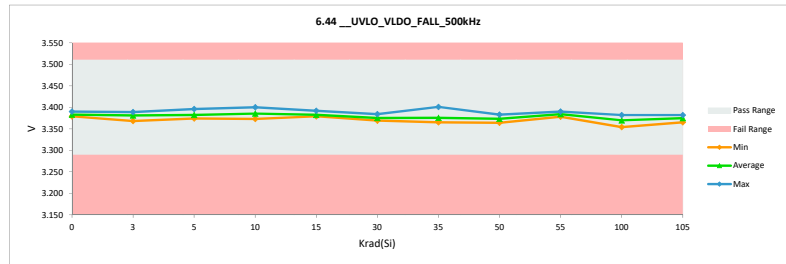


HDR TID Report  
TPS7H5004-SP

6.44 __UVLO_VLDO_FALL_500kHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	3.390	3.390	0.000
0	297	3.380	3.380	0.000
0	298	3.380	3.379	-0.001
3	192	3.380	3.383	0.003
3	193	3.383	3.381	-0.002
3	194	3.381	3.385	0.004
3	196	3.369	3.368	-0.001
3	197	3.387	3.389	0.002
5	200	3.375	3.374	-0.001
5	202	3.373	3.374	0.001
5	203	3.384	3.381	-0.003
5	204	3.386	3.386	0.000
5	206	3.397	3.396	-0.001
10	207	3.388	3.387	-0.001
10	209	3.402	3.400	-0.002
10	210	3.373	3.373	0.000
10	211	3.381	3.383	0.002
10	212	3.387	3.384	-0.003
15	214	3.382	3.380	-0.002
15	217	3.379	3.380	0.001
15	218	3.397	3.392	-0.005
15	219	3.384	3.381	-0.003
15	222	3.384	3.379	-0.005
30	224	3.394	3.382	-0.012
30	225	3.377	3.371	-0.006
30	226	3.374	3.370	-0.004
30	227	3.390	3.384	-0.006
30	229	3.381	3.369	-0.012
35	230	3.401	3.401	0.000
35	231	3.370	3.365	-0.005
35	232	3.376	3.372	-0.004
35	234	3.378	3.367	-0.011
35	237	3.376	3.372	-0.004
50	272	3.390	3.379	-0.011
50	275	3.377	3.364	-0.013
50	276	3.382	3.367	-0.015
50	277	3.387	3.374	-0.013
50	278	3.386	3.383	-0.003
55	279	3.398	3.390	-0.008
55	280	3.384	3.383	-0.001
55	281	3.379	3.378	-0.001
55	282	3.380	3.381	0.001
55	283	3.388	3.388	0.000
100	317	3.391	3.376	-0.015
100	318	3.398	3.373	-0.025
100	319	3.384	3.376	-0.008
100	320	3.394	3.382	-0.012
100	321	3.383	3.362	-0.021
100	322	3.379	3.369	-0.010
100	323	3.394	3.376	-0.018
100	324	3.382	3.368	-0.014
100	326	3.376	3.368	-0.008
100	327	3.386	3.368	-0.018
100	328	3.369	3.354	-0.015
100	329	3.380	3.360	-0.020
100	332	3.394	3.372	-0.022
100	333	3.391	3.367	-0.024
100	334	3.386	3.364	-0.022
100	335	3.386	3.376	-0.010
100	336	3.394	3.367	-0.027
100	337	3.388	3.370	-0.018
100	338	3.376	3.364	-0.012
100	339	3.391	3.366	-0.025
100	340	3.383	3.370	-0.013
100	341	3.387	3.372	-0.015
100	342	3.392	3.381	-0.011
100	343	3.394	3.372	-0.022
105	344	3.387	3.377	-0.010
105	346	3.377	3.371	-0.006
105	349	3.387	3.382	-0.005
105	350	3.371	3.365	-0.006
105	351	3.373	3.373	0.000
105	352	3.389	3.380	-0.009
Max		3.402	3.401	0.004
Average		3.384	3.376	-0.008
Min		3.369	3.354	-0.027
Std Dev		0.008	0.009	0.008

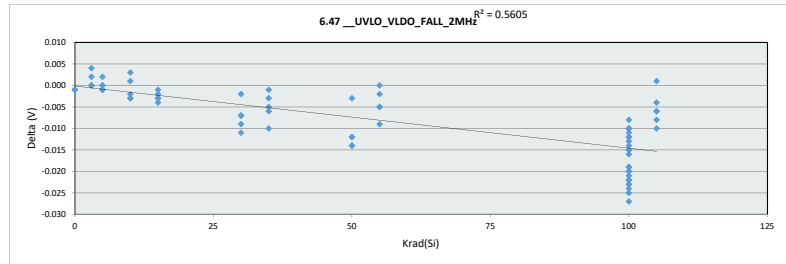


6.44 __UVLO_VLDO											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.379	3.368	3.374	3.373	3.379	3.369	3.365	3.364	3.378	3.354	3.365
Average	3.383	3.381	3.382	3.385	3.382	3.375	3.375	3.373	3.384	3.370	3.375
Max	3.390	3.389	3.396	3.400	3.392	3.384	3.401	3.383	3.390	3.382	3.382
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

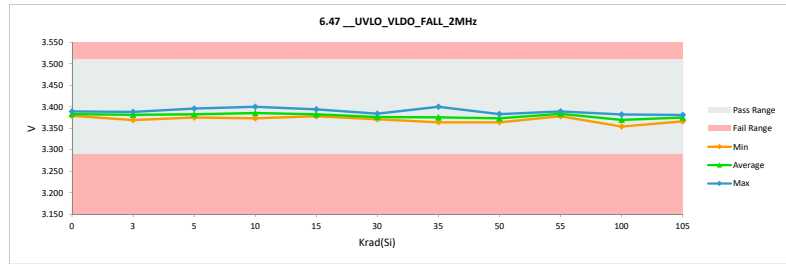


HDR TID Report  
TPS7H5004-SP

6.47 __UVLO_VLDO_FALL_2MHz				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	3.51	3.51		
Min Limit	3.29	3.29		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	3.390	3.389	-0.001
0	297	3.381	3.380	-0.001
0	298	3.380	3.379	-0.001
3	192	3.380	3.382	0.002
3	193	3.383	3.383	0.000
3	194	3.381	3.385	0.004
3	196	3.369	3.369	0.000
3	197	3.388	3.388	0.000
5	200	3.375	3.375	0.000
5	202	3.373	3.375	0.002
5	203	3.382	3.382	0.000
5	204	3.386	3.385	-0.001
5	206	3.397	3.396	-0.001
10	207	3.389	3.386	-0.003
10	209	3.402	3.400	-0.002
10	210	3.372	3.373	0.001
10	211	3.381	3.384	0.003
10	212	3.387	3.384	-0.003
15	214	3.381	3.379	-0.002
15	217	3.379	3.378	-0.001
15	218	3.397	3.394	-0.003
15	219	3.384	3.381	-0.003
15	222	3.384	3.380	-0.004
30	224	3.393	3.382	-0.011
30	225	3.378	3.371	-0.007
30	226	3.374	3.372	-0.002
30	227	3.391	3.384	-0.007
30	229	3.380	3.371	-0.009
35	230	3.401	3.400	-0.001
35	231	3.370	3.364	-0.006
35	232	3.376	3.373	-0.003
35	234	3.378	3.368	-0.010
35	237	3.377	3.372	-0.005
50	272	3.390	3.378	-0.012
50	275	3.376	3.364	-0.012
50	276	3.381	3.367	-0.014
50	277	3.388	3.374	-0.014
50	278	3.386	3.383	-0.003
55	279	3.398	3.389	-0.009
55	280	3.388	3.383	-0.005
55	281	3.380	3.378	-0.002
55	282	3.385	3.380	-0.005
55	283	3.388	3.388	0.000
100	317	3.391	3.375	-0.016
100	318	3.399	3.374	-0.025
100	319	3.384	3.376	-0.008
100	320	3.394	3.382	-0.012
100	321	3.384	3.362	-0.022
100	322	3.380	3.368	-0.012
100	323	3.395	3.375	-0.020
100	324	3.382	3.368	-0.014
100	326	3.377	3.367	-0.010
100	327	3.386	3.367	-0.019
100	328	3.369	3.354	-0.015
100	329	3.379	3.359	-0.020
100	332	3.394	3.372	-0.022
100	333	3.390	3.367	-0.023
100	334	3.386	3.365	-0.021
100	335	3.387	3.376	-0.011
100	336	3.394	3.367	-0.027
100	337	3.388	3.369	-0.019
100	338	3.375	3.365	-0.010
100	339	3.390	3.366	-0.024
100	340	3.382	3.369	-0.013
100	341	3.387	3.372	-0.015
100	342	3.391	3.381	-0.010
100	343	3.394	3.371	-0.023
105	344	3.387	3.377	-0.010
105	346	3.376	3.370	-0.006
105	349	3.387	3.381	-0.006
105	350	3.370	3.366	-0.004
105	351	3.373	3.374	0.001
105	352	3.389	3.381	-0.008
Max		3.402	3.400	0.004
Average		3.384	3.376	-0.008
Min		3.369	3.354	-0.027
Std Dev		0.008	0.009	0.008



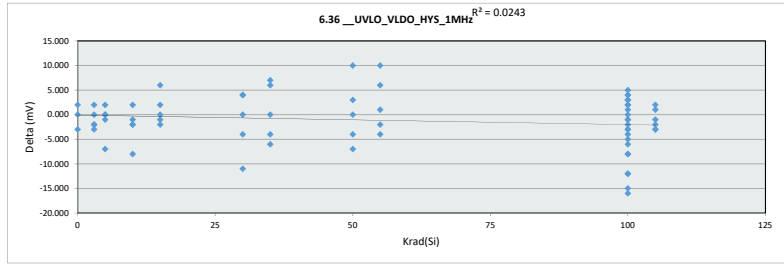
6.47 __UVLO_VLDO											
Test Site											
Tester											
Test Number											
Max Limit	3.51	V									
Min Limit	3.29	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.379	3.369	3.375	3.373	3.378	3.371	3.364	3.364	3.378	3.354	3.366
Average	3.383	3.381	3.383	3.385	3.382	3.376	3.375	3.373	3.384	3.369	3.375
Max	3.389	3.388	3.396	3.400	3.394	3.384	3.400	3.383	3.389	3.382	3.381
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



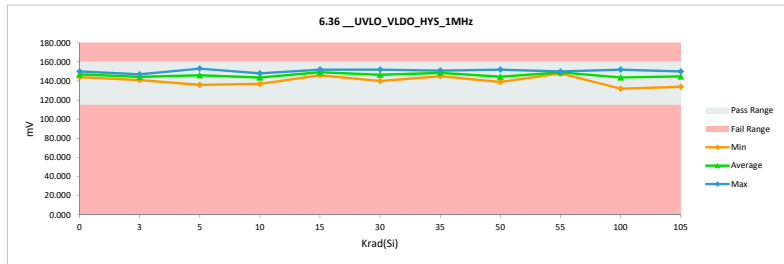
HDR TID Report  
TPS7H5004-SP

6.36 __UVLO_VLDO_HYS_1MHz	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	160
Min Limit	115

Krad(Si)	Serial #	PRE	POST	Delta
0	295	150.000	150.000	0.000
0	297	142.000	144.000	2.000
0	298	150.000	147.000	-3.000
3	192	147.000	145.000	-2.000
3	193	149.000	147.000	-2.000
3	194	145.000	147.000	2.000
3	196	142.000	142.000	0.000
3	197	144.000	141.000	-3.000
5	200	151.000	153.000	2.000
5	202	149.000	148.000	-1.000
5	203	142.000	142.000	0.000
5	204	143.000	136.000	-7.000
5	206	151.000	151.000	0.000
10	207	148.000	146.000	-2.000
10	209	143.000	145.000	2.000
10	210	145.000	143.000	-2.000
10	211	145.000	137.000	-8.000
10	212	149.000	148.000	-1.000
15	214	147.000	147.000	0.000
15	217	152.000	150.000	-2.000
15	218	150.000	152.000	2.000
15	219	147.000	146.000	-1.000
15	222	145.000	151.000	6.000
30	224	142.000	146.000	4.000
30	225	151.000	151.000	0.000
30	226	151.000	140.000	-11.000
30	227	148.000	152.000	4.000
30	229	148.000	144.000	-4.000
35	230	149.000	145.000	-4.000
35	231	144.000	150.000	6.000
35	232	151.000	151.000	0.000
35	234	144.000	151.000	7.000
35	237	152.000	146.000	-6.000
50	272	136.000	139.000	3.000
50	275	148.000	144.000	-4.000
50	276	134.000	144.000	10.000
50	277	152.000	152.000	0.000
50	278	151.000	144.000	-7.000
55	279	148.000	149.000	1.000
55	280	139.000	149.000	10.000
55	281	144.000	150.000	6.000
55	282	152.000	148.000	-4.000
55	283	151.000	149.000	-2.000
100	317	154.000	146.000	-8.000
100	318	142.000	144.000	2.000
100	319	149.000	133.000	-16.000
100	320	151.000	152.000	1.000
100	321	143.000	147.000	4.000
100	322	152.000	151.000	-1.000
100	323	135.000	132.000	-3.000
100	324	132.000	136.000	4.000
100	326	145.000	133.000	-12.000
100	327	147.000	150.000	3.000
100	328	150.000	144.000	-6.000
100	329	150.000	150.000	0.000
100	332	145.000	147.000	2.000
100	333	146.000	149.000	3.000
100	334	145.000	150.000	5.000
100	335	146.000	144.000	-2.000
100	336	148.000	151.000	3.000
100	337	145.000	133.000	-12.000
100	338	150.000	142.000	-8.000
100	339	149.000	145.000	-4.000
100	340	153.000	152.000	-1.000
100	341	151.000	136.000	-15.000
100	342	143.000	140.000	-3.000
100	343	149.000	144.000	-5.000
105	344	137.000	134.000	-3.000
105	346	151.000	150.000	-1.000
105	349	146.000	144.000	-2.000
105	350	148.000	149.000	1.000
105	351	146.000	143.000	-3.000
105	352	147.000	149.000	2.000
Max		154.000	153.000	10.000
Average		146.658	145.507	-1.151
Min		132.000	132.000	-16.000
Std Dev		4.616	5.362	5.095

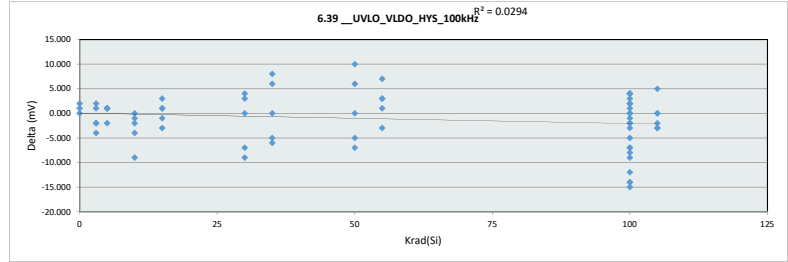


6.36 __UVLO_VLDO		0	3	5	10	15	30	35	50	55	100	105
LL		115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min		144.000	141.000	136.000	137.000	146.000	140.000	145.000	139.000	148.000	132.000	134.000
Average		147.000	144.400	146.000	143.800	149.200	146.600	148.600	144.600	149.000	143.792	144.833
Max		150.000	147.000	153.000	148.000	152.000	152.000	151.000	152.000	150.000	152.000	150.000
UL		160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

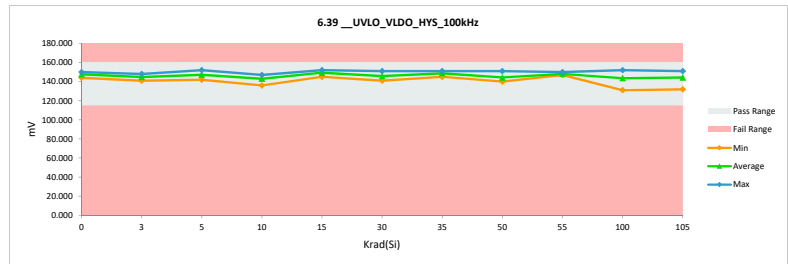


HDR TID Report  
TPS7H5004-SP

6.39 __UVLO_VLDO_HYS_100kHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	149.000	149.000	0.000
0	297	142.000	144.000	2.000
0	298	149.000	150.000	1.000
3	192	147.000	145.000	-2.000
3	193	149.000	147.000	-2.000
3	194	147.000	148.000	1.000
3	196	141.000	143.000	2.000
3	197	145.000	141.000	-4.000
5	200	150.000	151.000	1.000
5	202	149.000	147.000	-2.000
5	203	141.000	142.000	1.000
5	204	143.000	144.000	1.000
5	206	151.000	152.000	1.000
10	207	147.000	147.000	0.000
10	209	145.000	144.000	-1.000
10	210	145.000	143.000	-2.000
10	211	145.000	136.000	-9.000
10	212	149.000	145.000	-4.000
15	214	147.000	148.000	1.000
15	217	151.000	150.000	-1.000
15	218	149.000	152.000	3.000
15	219	148.000	145.000	-3.000
15	222	151.000	152.000	1.000
30	224	140.000	144.000	4.000
30	225	151.000	151.000	0.000
30	226	150.000	141.000	-9.000
30	227	148.000	151.000	3.000
30	229	149.000	142.000	-7.000
35	230	151.000	145.000	-6.000
35	231	143.000	151.000	8.000
35	232	151.000	151.000	0.000
35	234	145.000	151.000	6.000
35	237	151.000	146.000	-5.000
50	272	134.000	140.000	6.000
50	275	149.000	144.000	-5.000
50	276	133.000	143.000	10.000
50	277	151.000	151.000	0.000
50	278	151.000	144.000	-7.000
55	279	147.000	150.000	3.000
55	280	141.000	148.000	7.000
55	281	144.000	147.000	3.000
55	282	146.000	147.000	1.000
55	283	151.000	148.000	-3.000
100	317	153.000	146.000	-7.000
100	318	142.000	144.000	2.000
100	319	147.000	132.000	-15.000
100	320	150.000	152.000	2.000
100	321	145.000	148.000	3.000
100	322	153.000	151.000	-2.000
100	323	134.000	131.000	-3.000
100	324	131.000	135.000	4.000
100	326	146.000	134.000	-12.000
100	327	148.000	149.000	1.000
100	328	150.000	143.000	-7.000
100	329	151.000	151.000	0.000
100	332	145.000	149.000	4.000
100	333	146.000	148.000	2.000
100	334	145.000	149.000	4.000
100	335	145.000	144.000	-1.000
100	336	146.000	150.000	4.000
100	337	148.000	134.000	-14.000
100	338	152.000	143.000	-9.000
100	339	149.000	144.000	-5.000
100	340	150.000	150.000	0.000
100	341	150.000	136.000	-14.000
100	342	143.000	141.000	-2.000
100	343	150.000	142.000	-8.000
105	344	135.000	132.000	-3.000
105	346	150.000	150.000	0.000
105	349	147.000	145.000	-2.000
105	350	146.000	151.000	5.000
105	351	144.000	144.000	0.000
105	352	147.000	144.000	-3.000
Max		153.000	152.000	10.000
Average		146.493	145.370	-1.123
Min		131.000	131.000	-15.000
Std Dev		4.735	5.277	5.077

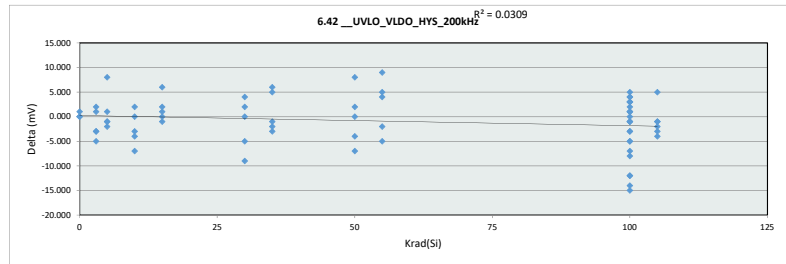


6.39 __UVLO_VLDO											
Test Site											
Tester											
Test Number											
Max Limit	160										
Min Limit	115										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	144.000	141.000	142.000	136.000	145.000	141.000	145.000	140.000	147.000	131.000	132.000
Average	147.667	144.800	147.200	143.000	149.400	145.800	148.800	144.400	148.000	143.583	144.333
Max	150.000	148.000	152.000	147.000	152.000	151.000	151.000	151.000	150.000	152.000	151.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

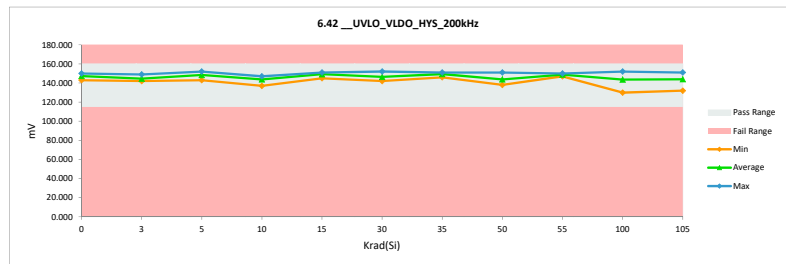


HDR TID Report  
TPS7H5004-SP

6.42 __UVLO_VLDO_HYS_200kHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	149.000	150.000	1.000
0	297	143.000	143.000	0.000
0	298	149.000	149.000	0.000
3	192	148.000	145.000	-3.000
3	193	149.000	144.000	-5.000
3	194	147.000	149.000	2.000
3	196	142.000	143.000	1.000
3	197	145.000	142.000	-3.000
5	200	152.000	151.000	-1.000
5	202	149.000	147.000	-2.000
5	203	141.000	149.000	8.000
5	204	144.000	143.000	-1.000
5	206	151.000	152.000	1.000
10	207	147.000	147.000	0.000
10	209	144.000	146.000	2.000
10	210	146.000	143.000	-3.000
10	211	144.000	137.000	-7.000
10	212	150.000	146.000	-4.000
15	214	147.000	148.000	1.000
15	217	151.000	151.000	0.000
15	218	149.000	151.000	2.000
15	219	146.000	145.000	-1.000
15	222	145.000	151.000	6.000
30	224	142.000	144.000	2.000
30	225	151.000	151.000	0.000
30	226	151.000	142.000	-9.000
30	227	148.000	152.000	4.000
30	229	148.000	143.000	-5.000
35	230	149.000	146.000	-3.000
35	231	144.000	150.000	6.000
35	232	152.000	151.000	-1.000
35	234	145.000	150.000	5.000
35	237	152.000	150.000	-2.000
50	272	136.000	138.000	2.000
50	275	148.000	144.000	-4.000
50	276	134.000	142.000	8.000
50	277	151.000	151.000	0.000
50	278	151.000	144.000	-7.000
55	279	146.000	150.000	4.000
55	280	141.000	150.000	9.000
55	281	143.000	148.000	5.000
55	282	152.000	147.000	-5.000
55	283	151.000	149.000	-2.000
100	317	151.000	146.000	-5.000
100	318	142.000	144.000	2.000
100	319	147.000	132.000	-15.000
100	320	151.000	152.000	1.000
100	321	144.000	148.000	4.000
100	322	152.000	151.000	-1.000
100	323	135.000	130.000	-5.000
100	324	131.000	134.000	3.000
100	326	145.000	133.000	-12.000
100	327	146.000	150.000	4.000
100	328	151.000	144.000	-7.000
100	329	150.000	151.000	1.000
100	332	145.000	148.000	3.000
100	333	146.000	147.000	1.000
100	334	145.000	150.000	5.000
100	335	145.000	144.000	-1.000
100	336	146.000	149.000	3.000
100	337	146.000	134.000	-12.000
100	338	150.000	142.000	-8.000
100	339	148.000	145.000	-3.000
100	340	151.000	151.000	0.000
100	341	150.000	136.000	-14.000
100	342	142.000	141.000	-1.000
100	343	148.000	145.000	-3.000
105	344	135.000	132.000	-3.000
105	346	152.000	150.000	-2.000
105	349	145.000	144.000	-1.000
105	350	146.000	151.000	5.000
105	351	144.000	143.000	-1.000
105	352	148.000	144.000	-4.000
Max		152.000	152.000	9.000
Average		146.438	145.548	-0.890
Min		131.000	130.000	-15.000
Std Dev		4.567	5.414	4.869



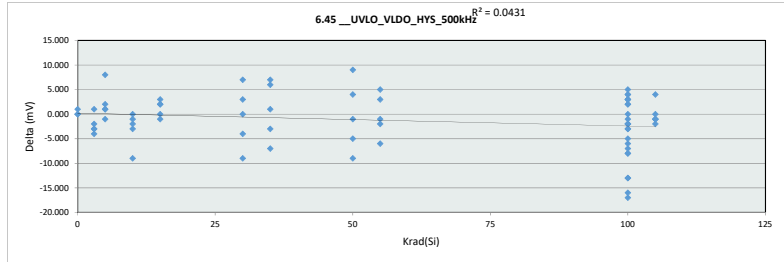
6.42 __UVLO_VLDO											
Test Site											
Tester											
Test Number											
Max Limit	160										
Min Limit	115										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	143.000	142.000	143.000	137.000	145.000	142.000	146.000	138.000	147.000	130.000	132.000
Average	147.333	144.600	148.400	143.800	149.200	146.400	149.400	143.800	148.800	143.625	144.000
Max	150.000	149.000	152.000	147.000	151.000	152.000	151.000	151.000	150.000	152.000	151.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



HDR TID Report  
TPS7H5004-SP

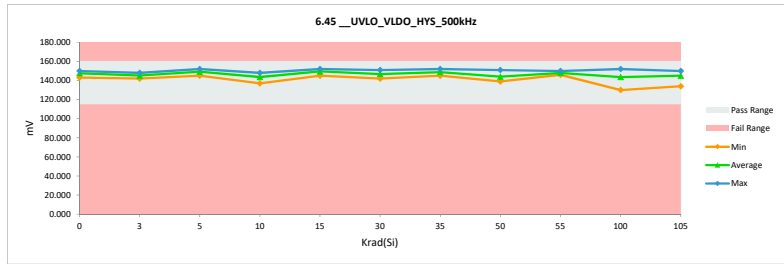
6.45 __UVLO_VLDO_HYS_500kHz			
Test Site			
Tester			
Test Number			
Unit	mV	mV	
Max Limit	160	160	
Min Limit	115	115	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	149.000	150.000	1.000
0	297	143.000	143.000	0.000
0	298	150.000	150.000	0.000
3	192	149.000	145.000	-4.000
3	193	149.000	147.000	-2.000
3	194	151.000	148.000	-3.000
3	196	143.000	144.000	1.000
3	197	145.000	142.000	-3.000
5	200	151.000	152.000	1.000
5	202	149.000	148.000	-1.000
5	203	141.000	149.000	8.000
5	204	144.000	145.000	1.000
5	206	150.000	152.000	2.000
10	207	149.000	147.000	-2.000
10	209	144.000	144.000	0.000
10	210	145.000	142.000	-3.000
10	211	146.000	137.000	-9.000
10	212	149.000	148.000	-1.000
15	214	147.000	149.000	2.000
15	217	151.000	151.000	0.000
15	218	149.000	152.000	3.000
15	219	146.000	145.000	-1.000
15	222	149.000	151.000	2.000
30	224	139.000	146.000	7.000
30	225	151.000	151.000	0.000
30	226	151.000	142.000	-9.000
30	227	148.000	151.000	3.000
30	229	148.000	144.000	-4.000
35	230	149.000	146.000	-3.000
35	231	144.000	150.000	6.000
35	232	151.000	152.000	1.000
35	234	144.000	151.000	7.000
35	237	152.000	145.000	-7.000
50	272	135.000	139.000	4.000
50	275	149.000	144.000	-5.000
50	276	134.000	143.000	9.000
50	277	152.000	151.000	-1.000
50	278	152.000	143.000	-9.000
55	279	148.000	147.000	-1.000
55	280	145.000	150.000	5.000
55	281	145.000	148.000	3.000
55	282	152.000	146.000	-6.000
55	283	151.000	149.000	-2.000
100	317	153.000	145.000	-8.000
100	318	143.000	146.000	3.000
100	319	149.000	132.000	-17.000
100	320	152.000	152.000	0.000
100	321	143.000	148.000	5.000
100	322	152.000	150.000	-2.000
100	323	137.000	130.000	-7.000
100	324	132.000	135.000	3.000
100	326	146.000	133.000	-13.000
100	327	147.000	150.000	3.000
100	328	150.000	144.000	-6.000
100	329	151.000	149.000	-2.000
100	332	146.000	148.000	2.000
100	333	146.000	148.000	2.000
100	334	145.000	149.000	4.000
100	335	148.000	145.000	-3.000
100	336	147.000	151.000	4.000
100	337	146.000	133.000	-13.000
100	338	150.000	142.000	-8.000
100	339	149.000	146.000	-3.000
100	340	152.000	151.000	-1.000
100	341	152.000	136.000	-16.000
100	342	142.000	140.000	-2.000
100	343	149.000	144.000	-5.000
105	344	136.000	134.000	-2.000
105	346	150.000	150.000	0.000
105	349	146.000	145.000	-1.000
105	350	146.000	150.000	4.000
105	351	145.000	144.000	-1.000
105	352	148.000	147.000	-1.000
Max		153.000	152.000	9.000
Average		146.945	145.699	-1.247
Min		132.000	130.000	-17.000
Std Dev		4.537	5.304	5.212



6.45 __UVLO_VLDO			
Test Site			
Tester			
Test Number			
Max Limit	160	mV	
Min Limit	115	mV	

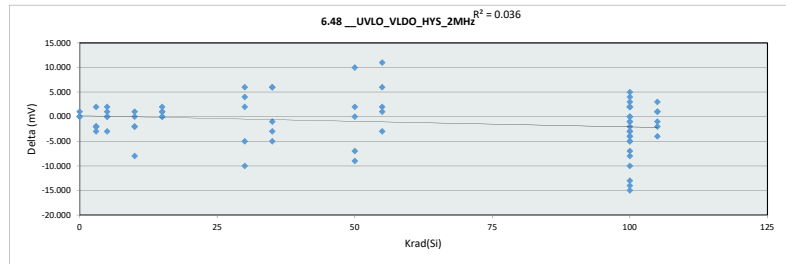
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	143.000	142.000	145.000	137.000	145.000	142.000	145.000	139.000	146.000	130.000	134.000
Average	147.667	145.200	149.200	143.600	149.600	148.800	148.800	144.000	148.000	143.625	145.000
Max	150.000	148.000	152.000	148.000	152.000	151.000	152.000	151.000	150.000	152.000	150.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



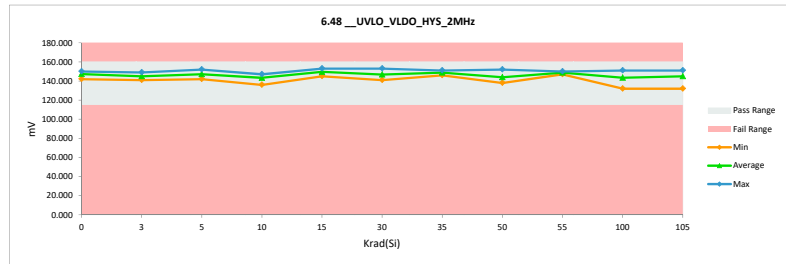


HDR TID Report  
TPS7H5004-SP

6.48 __UVLO_VLDO_HYS_2MHz				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	160		160	
Min Limit	115		115	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	149.000	150.000	1.000
0	297	142.000	142.000	0.000
0	298	150.000	150.000	0.000
3	192	148.000	145.000	-3.000
3	193	149.000	147.000	-2.000
3	194	151.000	149.000	-2.000
3	196	141.000	143.000	2.000
3	197	143.000	141.000	-2.000
5	200	151.000	151.000	0.000
5	202	149.000	146.000	-3.000
5	203	142.000	142.000	0.000
5	204	144.000	145.000	1.000
5	206	150.000	152.000	2.000
10	207	147.000	147.000	0.000
10	209	143.000	144.000	1.000
10	210	145.000	143.000	-2.000
10	211	144.000	136.000	-8.000
10	212	149.000	147.000	-2.000
15	214	148.000	148.000	0.000
15	217	152.000	153.000	1.000
15	218	150.000	151.000	1.000
15	219	145.000	145.000	0.000
15	222	149.000	151.000	2.000
30	224	140.000	146.000	6.000
30	225	151.000	153.000	2.000
30	226	151.000	141.000	-10.000
30	227	147.000	151.000	4.000
30	229	148.000	143.000	-5.000
35	230	149.000	146.000	-3.000
35	231	145.000	151.000	6.000
35	232	151.000	150.000	-1.000
35	234	145.000	151.000	6.000
35	237	151.000	146.000	-5.000
50	272	136.000	138.000	2.000
50	275	150.000	143.000	-7.000
50	276	134.000	144.000	10.000
50	277	152.000	152.000	0.000
50	278	152.000	143.000	-9.000
55	279	147.000	149.000	2.000
55	280	139.000	150.000	11.000
55	281	143.000	149.000	6.000
55	282	146.000	147.000	1.000
55	283	152.000	149.000	-3.000
100	317	152.000	147.000	-5.000
100	318	142.000	144.000	2.000
100	319	147.000	133.000	-14.000
100	320	151.000	146.000	-5.000
100	321	143.000	148.000	5.000
100	322	151.000	150.000	-1.000
100	323	135.000	132.000	-3.000
100	324	132.000	135.000	3.000
100	326	145.000	135.000	-10.000
100	327	148.000	150.000	2.000
100	328	151.000	144.000	-7.000
100	329	150.000	150.000	0.000
100	332	146.000	148.000	2.000
100	333	147.000	147.000	0.000
100	334	145.000	149.000	4.000
100	335	146.000	143.000	-3.000
100	336	147.000	149.000	2.000
100	337	146.000	133.000	-13.000
100	338	151.000	143.000	-8.000
100	339	149.000	145.000	-4.000
100	340	152.000	151.000	-1.000
100	341	151.000	136.000	-15.000
100	342	143.000	141.000	-2.000
100	343	149.000	145.000	-4.000
105	344	136.000	132.000	-4.000
105	346	150.000	151.000	1.000
105	349	147.000	146.000	-1.000
105	350	147.000	150.000	3.000
105	351	145.000	143.000	-2.000
105	352	147.000	148.000	1.000
Max		152.000	153.000	11.000
Average		146.589	145.534	-1.055
Min		132.000	132.000	-15.000
Std Dev		4.621	5.263	4.913

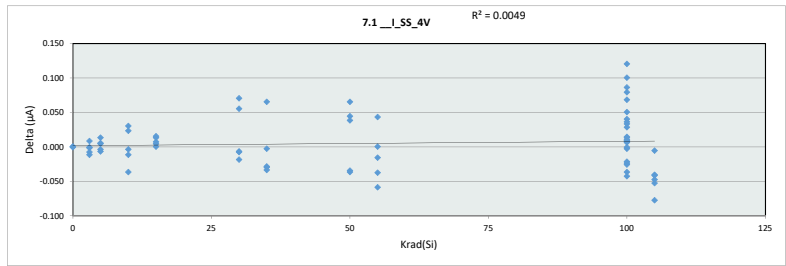


6.48 __UVLO_VLDO_HYS_2MHz											
Test Site											
Tester											
Test Number											
Max Limit	160		mV								
Min Limit	115		mV								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	142.000	141.000	142.000	136.000	145.000	141.000	146.000	138.000	147.000	132.000	132.000
Average	147.333	145.000	147.200	143.400	149.600	146.800	148.800	144.000	148.800	143.500	145.000
Max	150.000	149.000	152.000	147.000	153.000	151.000	152.000	150.000	151.000	151.000	151.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000

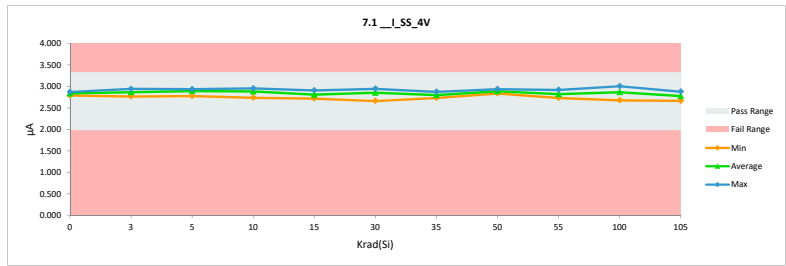


HDR TID Report  
TPS7H5004-SP

7.1_I_SS_4V				
Test Site	Tester	Test Number	Unit	
			µA	µA
			Max Limit	3.32
			Min Limit	1.98
Krad(Si)	Serial #	PRE	POST	Delta
0	295	2.788	2.788	0.000
0	297	2.870	2.870	0.000
0	298	2.852	2.851	-0.001
3	192	2.932	2.940	0.008
3	193	2.856	2.844	-0.012
3	194	2.764	2.763	-0.001
3	196	2.928	2.920	-0.008
3	197	2.859	2.857	-0.002
5	200	2.920	2.933	0.013
5	202	2.900	2.896	-0.004
5	203	2.913	2.917	0.004
5	204	2.768	2.773	0.005
5	206	2.940	2.933	-0.007
10	207	2.914	2.937	0.023
10	209	2.874	2.870	-0.004
10	210	2.922	2.952	0.030
10	211	2.771	2.734	-0.037
10	212	2.926	2.914	-0.012
15	214	2.714	2.727	0.013
15	217	2.906	2.906	0.000
15	218	2.711	2.715	0.004
15	219	2.891	2.906	0.015
15	222	2.781	2.788	0.007
30	224	2.849	2.919	0.070
30	225	2.846	2.901	0.055
30	226	2.677	2.658	-0.019
30	227	2.948	2.941	-0.007
30	229	2.847	2.839	-0.008
35	230	2.859	2.829	-0.030
35	231	2.874	2.871	-0.003
35	232	2.791	2.757	-0.034
35	234	2.665	2.730	0.065
35	237	2.828	2.799	-0.029
50	272	2.868	2.912	0.044
50	275	2.880	2.845	-0.035
50	276	2.870	2.908	0.038
50	277	2.872	2.835	-0.037
50	278	2.869	2.934	0.065
55	279	2.687	2.730	0.043
55	280	2.915	2.915	0.000
55	281	2.897	2.881	-0.016
55	282	2.821	2.783	-0.038
55	283	2.836	2.777	-0.059
100	317	2.891	2.888	-0.003
100	318	2.803	2.779	-0.024
100	319	2.831	2.871	0.040
100	320	2.827	2.855	0.028
100	321	2.766	2.799	0.033
100	322	2.851	2.858	0.007
100	323	2.662	2.675	0.013
100	324	2.945	2.955	0.010
100	326	2.876	2.996	0.120
100	327	2.821	2.857	0.036
100	328	2.890	2.889	-0.001
100	329	2.876	2.976	0.100
100	332	2.802	2.881	0.079
100	333	2.757	2.765	0.008
100	334	2.904	2.882	-0.022
100	335	2.927	2.933	0.006
100	336	2.954	3.004	0.050
100	337	2.887	2.901	0.014
100	338	2.879	2.836	-0.043
100	339	2.889	2.975	0.086
100	340	2.770	2.733	-0.037
100	341	2.738	2.747	0.009
100	342	2.806	2.874	0.068
100	343	2.818	2.792	-0.026
105	344	2.924	2.876	-0.048
105	346	2.771	2.729	-0.042
105	349	2.860	2.782	-0.078
105	350	2.870	2.829	-0.041
105	351	2.667	2.661	-0.006
105	352	2.832	2.779	-0.053
	Max	2.954	3.004	0.120
	Average	2.842	2.848	0.005
	Min	2.662	2.658	-0.078
	Std Dev	0.074	0.083	0.038

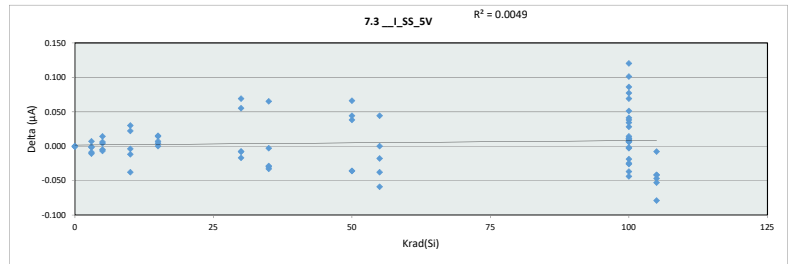


7.1_I_SS_4V															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL			3.32	1.98	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min					2.788	2.763	2.773	2.734	2.715	2.658	2.730	2.835	2.730	2.675	2.661
Average					2.836	2.865	2.890	2.881	2.808	2.852	2.797	2.887	2.817	2.863	2.776
Max					2.870	2.940	2.933	2.952	2.906	2.941	2.871	2.934	2.915	3.004	2.876
UL					3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

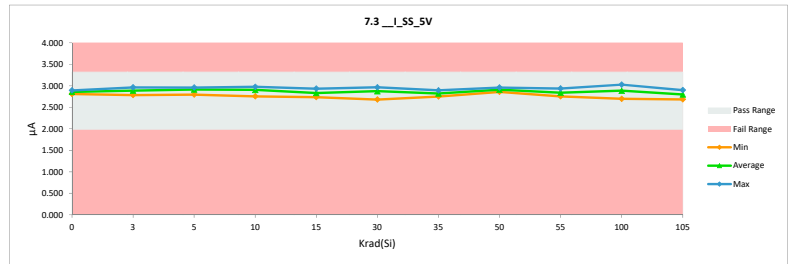


HDR TID Report  
TPS7H5004-SP

7.3_I_SS_5V				
Test Site	Tester	Test Number	Unit	
			µA	µA
			Max Limit	3.32
			Min Limit	1.98
Krad(Si)	Serial #	PRE	POST	Delta
0	295	2.810	2.810	0.000
0	297	2.892	2.892	0.000
0	298	2.873	2.872	-0.001
3	192	2.956	2.963	0.007
3	193	2.877	2.866	-0.011
3	194	2.785	2.784	-0.001
3	196	2.951	2.942	-0.009
3	197	2.880	2.878	-0.002
5	200	2.942	2.956	0.014
5	202	2.923	2.918	-0.005
5	203	2.935	2.939	0.004
5	204	2.789	2.795	0.006
5	206	2.964	2.957	-0.007
10	207	2.937	2.959	0.022
10	209	2.898	2.894	-0.004
10	210	2.945	2.975	0.030
10	211	2.793	2.755	-0.038
10	212	2.950	2.938	-0.012
15	214	2.736	2.750	0.014
15	217	2.931	2.931	0.000
15	218	2.731	2.735	0.004
15	219	2.913	2.928	0.015
15	222	2.803	2.810	0.007
30	224	2.873	2.942	0.069
30	225	2.869	2.924	0.055
30	226	2.697	2.680	-0.017
30	227	2.972	2.964	-0.008
30	229	2.870	2.862	-0.008
35	230	2.882	2.852	-0.030
35	231	2.896	2.893	-0.003
35	232	2.813	2.780	-0.033
35	234	2.686	2.751	0.065
35	237	2.851	2.822	-0.029
50	272	2.890	2.934	0.044
50	275	2.903	2.867	-0.036
50	276	2.894	2.932	0.038
50	277	2.894	2.858	-0.036
50	278	2.892	2.958	0.066
55	279	2.709	2.753	0.044
55	280	2.936	2.936	0.000
55	281	2.920	2.902	-0.018
55	282	2.843	2.805	-0.038
55	283	2.859	2.800	-0.059
100	317	2.914	2.911	-0.003
100	318	2.825	2.800	-0.025
100	319	2.853	2.894	0.041
100	320	2.849	2.877	0.028
100	321	2.788	2.822	0.034
100	322	2.873	2.880	0.007
100	323	2.685	2.696	0.011
100	324	2.968	2.979	0.011
100	326	2.899	3.019	0.120
100	327	2.843	2.881	0.038
100	328	2.914	2.912	-0.002
100	329	2.898	2.999	0.101
100	332	2.825	2.902	0.077
100	333	2.778	2.785	0.007
100	334	2.928	2.909	-0.019
100	335	2.950	2.956	0.006
100	336	2.976	3.027	0.051
100	337	2.909	2.923	0.014
100	338	2.901	2.857	-0.044
100	339	2.912	2.998	0.086
100	340	2.792	2.755	-0.037
100	341	2.758	2.768	0.010
100	342	2.827	2.896	0.069
100	343	2.841	2.815	-0.026
105	344	2.946	2.899	-0.047
105	346	2.792	2.750	-0.042
105	349	2.882	2.803	-0.079
105	350	2.895	2.853	-0.042
105	351	2.690	2.682	-0.008
105	352	2.854	2.801	-0.053
	Max	2.976	3.027	0.120
	Average	2.865	2.870	0.005
	Min	2.685	2.680	-0.079
	Std Dev	0.074	0.083	0.038

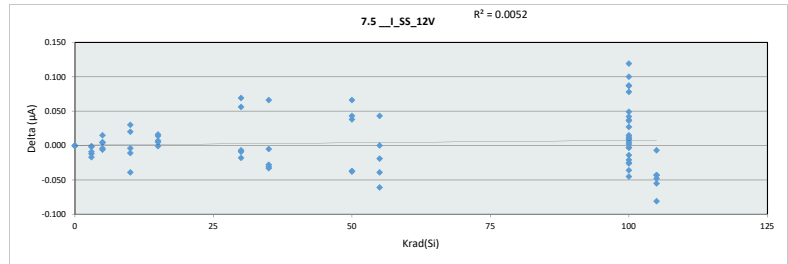


7.3_I_SS_5V															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL			3.32	1.98	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min					2.810	2.784	2.795	2.755	2.735	2.680	2.751	2.858	2.753	2.696	2.682
Average					2.858	2.887	2.913	2.904	2.831	2.874	2.820	2.910	2.839	2.886	2.798
Max					2.892	2.963	2.957	2.975	2.931	2.964	2.893	2.958	2.936	3.027	2.899
UL					3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

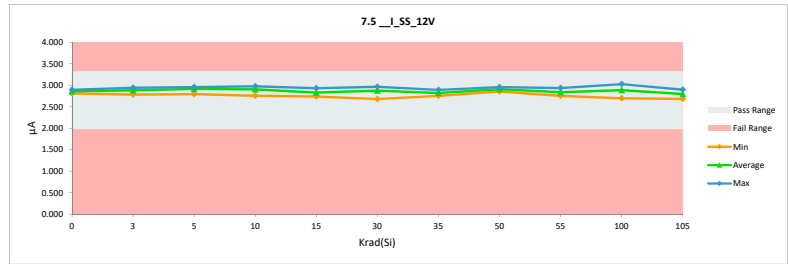


HDR TID Report  
TPS7H5004-SP

7.5_I_SS_12V				
Test Site	Tester	Test Number	Unit	
			µA	µA
			Max Limit	3.32
			Min Limit	1.98
Krad(Si)	Serial #	PRE	POST	Delta
0	295	2.812	2.811	-0.001
0	297	2.893	2.893	0.000
0	298	2.874	2.874	0.000
3	192	2.956	2.939	-0.017
3	193	2.879	2.867	-0.012
3	194	2.785	2.784	-0.001
3	196	2.952	2.943	-0.009
3	197	2.880	2.878	-0.002
5	200	2.943	2.958	0.015
5	202	2.924	2.920	-0.004
5	203	2.936	2.940	0.004
5	204	2.790	2.795	0.005
5	206	2.964	2.958	-0.006
10	207	2.938	2.958	0.020
10	209	2.900	2.896	-0.004
10	210	2.946	2.976	0.030
10	211	2.794	2.755	-0.039
10	212	2.951	2.940	-0.011
15	214	2.737	2.751	0.014
15	217	2.933	2.932	-0.001
15	218	2.731	2.736	0.005
15	219	2.914	2.930	0.016
15	222	2.804	2.811	0.007
30	224	2.875	2.944	0.069
30	225	2.870	2.926	0.056
30	226	2.698	2.680	-0.018
30	227	2.974	2.965	-0.009
30	229	2.871	2.864	-0.007
35	230	2.883	2.852	-0.031
35	231	2.897	2.892	-0.005
35	232	2.813	2.780	-0.033
35	234	2.687	2.753	0.066
35	237	2.852	2.824	-0.028
50	272	2.891	2.934	0.043
50	275	2.904	2.867	-0.037
50	276	2.895	2.933	0.038
50	277	2.895	2.857	-0.038
50	278	2.892	2.958	0.066
55	279	2.710	2.753	0.043
55	280	2.936	2.936	0.000
55	281	2.921	2.902	-0.019
55	282	2.844	2.805	-0.039
55	283	2.861	2.800	-0.061
100	317	2.915	2.912	-0.003
100	318	2.826	2.800	-0.026
100	319	2.854	2.896	0.042
100	320	2.850	2.877	0.027
100	321	2.788	2.824	0.036
100	322	2.874	2.879	0.005
100	323	2.686	2.698	0.012
100	324	2.970	2.979	0.009
100	326	2.900	3.019	0.119
100	327	2.844	2.881	0.037
100	328	2.916	2.913	-0.003
100	329	2.900	3.000	0.100
100	332	2.826	2.904	0.078
100	333	2.779	2.765	-0.014
100	334	2.930	2.909	-0.021
100	335	2.952	2.954	0.002
100	336	2.978	3.027	0.049
100	337	2.911	2.926	0.015
100	338	2.902	2.857	-0.045
100	339	2.914	3.001	0.087
100	340	2.793	2.757	-0.036
100	341	2.760	2.769	0.009
100	342	2.811	2.898	0.087
100	343	2.842	2.817	-0.025
105	344	2.947	2.899	-0.048
105	346	2.793	2.750	-0.043
105	349	2.884	2.803	-0.081
105	350	2.897	2.854	-0.043
105	351	2.690	2.683	-0.007
105	352	2.856	2.801	-0.055
	Max	2.978	3.027	0.119
	Average	2.866	2.870	0.005
	Min	2.686	2.680	-0.081
	Std Dev	0.075	0.083	0.039

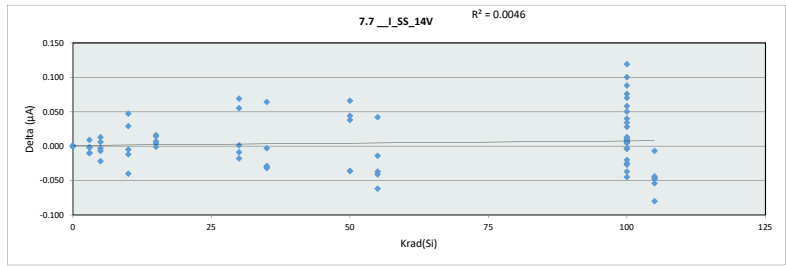


7.5_I_SS_12V															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL			3.32	1.98	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min					2.811	2.784	2.795	2.755	2.736	2.680	2.753	2.857	2.753	2.698	2.683
Average					2.859	2.882	2.914	2.905	2.832	2.876	2.820	2.910	2.839	2.886	2.798
Max					2.893	2.943	2.958	2.976	2.932	2.965	2.892	2.958	2.936	3.027	2.899
UL					3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

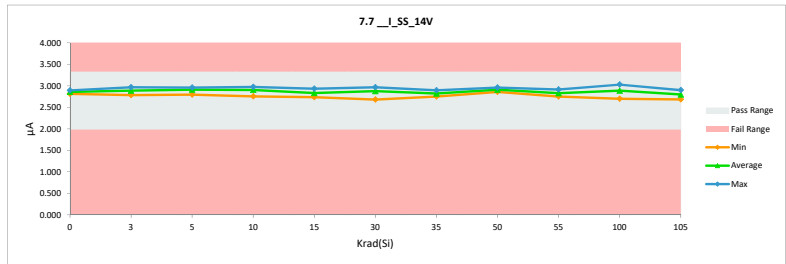


HDR TID Report  
TPS7H5004-SP

7.7_I_SS_14V				
Test Site	Tester	Test Number	Unit	
			µA	µA
			Max Limit	3.32
			Min Limit	1.98
Krad(Si)	Serial #	PRE	POST	Delta
0	295	2.811	2.812	0.001
0	297	2.894	2.893	-0.001
0	298	2.874	2.874	0.000
3	192	2.956	2.965	0.009
3	193	2.878	2.868	-0.010
3	194	2.786	2.784	-0.002
3	196	2.952	2.942	-0.010
3	197	2.880	2.878	-0.002
5	200	2.944	2.957	0.013
5	202	2.924	2.921	-0.003
5	203	2.936	2.914	-0.022
5	204	2.789	2.795	0.006
5	206	2.964	2.957	-0.007
10	207	2.913	2.960	0.047
10	209	2.900	2.895	-0.005
10	210	2.946	2.975	0.029
10	211	2.794	2.754	-0.040
10	212	2.952	2.940	-0.012
15	214	2.736	2.750	0.014
15	217	2.934	2.933	-0.001
15	218	2.732	2.736	0.004
15	219	2.914	2.930	0.016
15	222	2.804	2.811	0.007
30	224	2.875	2.944	0.069
30	225	2.870	2.925	0.055
30	226	2.698	2.680	-0.018
30	227	2.965	2.966	0.001
30	229	2.872	2.863	-0.009
35	230	2.883	2.852	-0.031
35	231	2.897	2.894	-0.003
35	232	2.813	2.781	-0.032
35	234	2.688	2.752	0.064
35	237	2.853	2.824	-0.029
50	272	2.891	2.935	0.044
50	275	2.904	2.868	-0.036
50	276	2.895	2.933	0.038
50	277	2.894	2.858	-0.036
50	278	2.893	2.959	0.066
55	279	2.710	2.752	0.042
55	280	2.927	2.913	-0.014
55	281	2.921	2.880	-0.041
55	282	2.843	2.806	-0.037
55	283	2.861	2.799	-0.062
100	317	2.915	2.911	-0.004
100	318	2.826	2.799	-0.027
100	319	2.854	2.894	0.040
100	320	2.849	2.877	0.028
100	321	2.788	2.822	0.034
100	322	2.874	2.880	0.006
100	323	2.686	2.698	0.012
100	324	2.970	2.979	0.009
100	326	2.901	3.020	0.119
100	327	2.824	2.882	0.058
100	328	2.916	2.913	-0.003
100	329	2.899	2.999	0.100
100	332	2.827	2.903	0.076
100	333	2.780	2.786	0.006
100	334	2.930	2.910	-0.020
100	335	2.951	2.955	0.004
100	336	2.978	3.028	0.050
100	337	2.912	2.925	0.013
100	338	2.902	2.857	-0.045
100	339	2.913	3.001	0.088
100	340	2.793	2.756	-0.037
100	341	2.760	2.769	0.009
100	342	2.828	2.898	0.070
100	343	2.842	2.817	-0.025
105	344	2.947	2.899	-0.048
105	346	2.793	2.749	-0.044
105	349	2.883	2.803	-0.080
105	350	2.898	2.852	-0.046
105	351	2.690	2.683	-0.007
105	352	2.856	2.802	-0.054
	Max	2.978	3.028	0.119
	Average	2.865	2.870	0.005
	Min	2.686	2.680	-0.080
	Std Dev	0.074	0.083	0.040

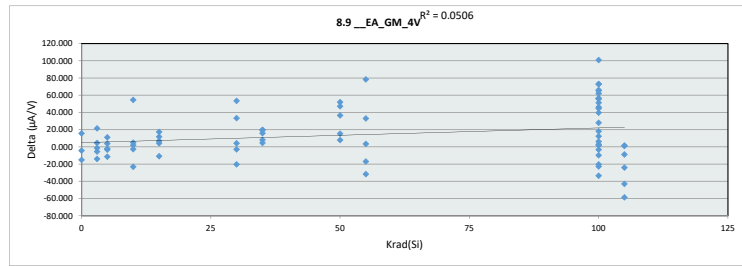


7.7_I_SS_14V															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL			3.32	1.98	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min					2.812	2.784	2.795	2.754	2.736	2.680	2.752	2.858	2.752	2.698	2.683
Average					2.860	2.887	2.909	2.905	2.832	2.876	2.821	2.911	2.830	2.887	2.798
Max					2.893	2.965	2.957	2.975	2.933	2.966	2.894	2.959	2.913	3.028	2.899
UL					3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320

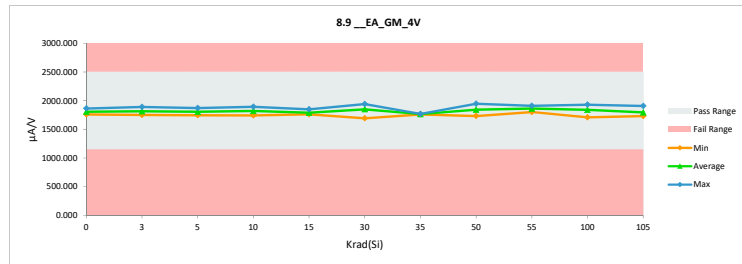


HDR TID Report  
TPS7H5004-SP

8.9_EA_GM_4V				
Test Site	Tester	Test Number	Unit	
			µA/V	µA/V
			Max Limit	2500
			Min Limit	1150
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1745.300	1760.935	15.635
0	297	1869.776	1865.171	-4.605
0	298	1813.822	1798.587	-15.235
3	192	1870.192	1891.509	21.317
3	193	1764.917	1750.782	-14.135
3	194	1767.325	1765.846	-1.479
3	196	1848.505	1852.986	4.481
3	197	1820.879	1815.195	-5.684
5	200	1867.185	1870.839	3.654
5	202	1853.723	1842.142	-11.581
5	203	1780.917	1791.748	10.831
5	204	1749.212	1746.917	-2.295
5	206	1789.770	1786.311	-3.459
10	207	1888.773	1885.875	-2.898
10	209	1770.225	1775.076	4.851
10	210	1840.414	1894.896	54.482
10	211	1766.106	1742.870	-23.236
10	212	1802.703	1804.555	1.852
15	214	1788.787	1806.021	17.234
15	217	1781.238	1770.225	-11.013
15	218	1764.444	1768.461	4.017
15	219	1837.954	1849.667	11.713
15	222	1757.117	1763.832	6.715
30	224	1837.220	1890.497	53.277
30	225	1820.929	1825.029	4.100
30	226	1714.077	1693.488	-20.589
30	227	1908.454	1941.661	33.207
30	229	1907.716	1904.640	-3.076
35	230	1749.732	1765.447	15.715
35	231	1749.212	1768.816	19.604
35	232	1764.927	1769.263	4.336
35	234	1759.642	1767.576	7.934
35	237	1742.300	1761.582	19.282
50	272	1718.625	1733.775	15.150
50	275	1781.456	1817.870	36.414
50	276	1894.767	1946.561	51.794
50	277	1806.089	1813.802	7.713
50	278	1863.307	1910.192	46.885
55	279	1793.108	1871.382	78.274
55	280	1899.734	1903.096	3.362
55	281	1877.339	1910.192	32.853
55	282	1850.381	1833.235	-17.146
55	283	1834.498	1802.703	-31.795
100	317	1818.018	1918.690	100.672
100	318	1783.492	1762.942	-20.550
100	319	1805.759	1845.232	39.473
100	320	1852.311	1829.307	-23.004
100	321	1745.590	1801.338	55.748
100	322	1811.188	1801.338	-9.850
100	323	1758.904	1761.554	2.650
100	324	1894.008	1906.264	12.256
100	326	1818.018	1864.042	46.024
100	327	1801.338	1856.996	55.658
100	328	1821.383	1877.917	56.534
100	329	1824.672	1890.689	66.017
100	332	1815.470	1877.234	61.764
100	333	1780.170	1852.894	72.724
100	334	1875.922	1881.909	5.987
100	335	1875.912	1903.733	27.821
100	336	1801.329	1852.311	50.982
100	337	1867.964	1912.457	44.493
100	338	1761.656	1779.576	17.920
100	339	1858.745	1931.568	72.823
100	340	1779.566	1780.983	1.417
100	341	1756.556	1753.087	-3.469
100	342	1798.972	1864.042	65.070
100	343	1743.576	1709.832	-33.744
105	344	1908.030	1908.834	0.804
105	346	1828.031	1784.877	-43.154
105	349	1790.381	1731.591	-58.790
105	350	1779.576	1755.352	-24.224
105	351	1779.566	1770.654	-8.912
105	352	1822.283	1823.640	1.357
Max		1908.454	1946.561	100.672
Average		1810.564	1824.961	14.397
Min		1714.077	1693.488	-58.790
Std Dev		49.993	62.643	31.464

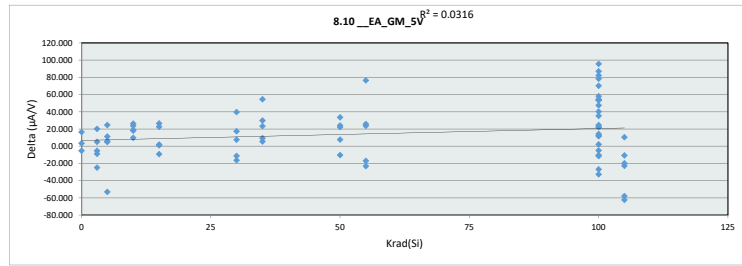


8.9_EA_GM_4V																
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105	
LL			2500	1150	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min					1760.935	1750.782	1746.917	1742.870	1763.832	1693.488	1761.582	1733.775	1802.703	1709.832	1731.591	
Average					1808.231	1815.264	1807.591	1820.654	1791.641	1851.063	1766.537	1844.440	1864.122	1842.331	1795.825	
Max					1865.171	1891.509	1870.839	1894.896	1849.667	1941.661	1769.263	1946.561	1910.192	1931.568	1908.834	
UL					2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	

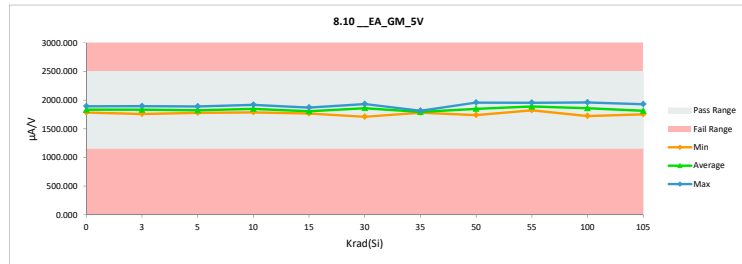


HDR TID Report  
TPS7H5004-SP

8.10 __EA_GM_5V					
Test Site	Serial #	PRE	POST	Delta	
0	295	1767.111	1783.179	16.068	
0	297	1888.794	1891.968	3.174	
0	298	1834.187	1828.490	-5.697	
3	192	1901.015	1895.452	-5.563	
3	193	1784.772	1759.660	-25.112	
3	194	1781.645	1786.330	4.685	
3	196	1865.991	1885.907	19.916	
3	197	1851.790	1842.709	-9.081	
5	200	1885.070	1889.667	4.597	
5	202	1859.569	1870.391	10.822	
5	203	1836.405	1782.932	-53.473	
5	204	1754.425	1778.783	24.358	
5	206	1791.739	1797.209	5.470	
10	207	1887.700	1906.166	18.466	
10	209	1786.311	1804.089	17.778	
10	210	1892.064	1918.065	26.001	
10	211	1764.917	1788.273	23.356	
10	212	1813.802	1823.145	9.343	
15	214	1804.031	1830.195	26.164	
15	217	1791.748	1782.270	-9.478	
15	218	1767.278	1768.023	0.745	
15	219	1849.075	1871.382	22.307	
15	222	1782.800	1784.374	1.574	
30	224	1873.492	1912.882	39.390	
30	225	1830.694	1847.670	16.976	
30	226	1726.158	1709.519	-16.639	
30	227	1925.406	1932.558	7.152	
30	229	1910.768	1899.239	-11.529	
35	230	1776.053	1785.105	9.052	
35	231	1772.600	1802.025	29.425	
35	232	1759.660	1813.802	54.142	
35	234	1764.927	1787.949	23.022	
35	237	1775.555	1780.614	5.059	
50	272	1731.520	1738.896	7.376	
50	275	1801.329	1825.019	23.690	
50	276	1934.843	1956.652	21.809	
50	277	1818.836	1808.231	-10.605	
50	278	1886.872	1920.150	33.278	
55	279	1805.234	1881.339	76.105	
55	280	1931.757	1955.181	23.424	
55	281	1910.018	1935.434	25.416	
55	282	1869.026	1851.596	-17.430	
55	283	1846.674	1823.145	-23.529	
100	317	1849.402	1931.268	81.866	
100	318	1790.391	1779.774	-10.617	
100	319	1796.189	1850.381	54.192	
100	320	1864.042	1852.321	-11.721	
100	321	1763.582	1821.393	57.811	
100	322	1844.136	1845.892	1.756	
100	323	1783.492	1778.161	-5.331	
100	324	1918.690	1930.002	11.312	
100	326	1818.018	1887.944	69.926	
100	327	1829.307	1851.259	21.952	
100	328	1852.321	1887.275	34.954	
100	329	1849.075	1902.157	53.082	
100	332	1818.659	1905.322	86.663	
100	333	1802.267	1849.412	47.145	
100	334	1875.912	1887.944	12.032	
100	335	1890.540	1912.457	21.917	
100	336	1814.665	1867.985	53.320	
100	337	1885.027	1924.964	39.937	
100	338	1783.767	1797.671	13.904	
100	339	1864.042	1959.326	95.284	
100	340	1834.367	1801.338	-33.029	
100	341	1781.361	1805.633	24.272	
100	342	1799.957	1877.906	77.949	
100	343	1749.613	1722.330	-27.283	
105	344	1953.042	1929.991	-23.051	
105	346	1844.177	1785.950	-58.227	
105	349	1815.029	1752.336	-62.693	
105	350	1806.858	1786.692	-20.166	
105	351	1806.858	1795.843	-11.015	
105	352	1830.545	1840.737	10.192	
Max		1953.042	1959.326	95.284	
Average		1828.890	1843.251	14.361	
Min		1726.158	1709.519	-62.693	
Std Dev		52.983	61.433	32.062	

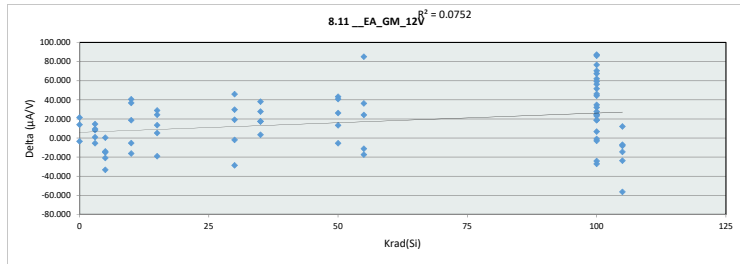


8.10 __EA_GM_5V											
Test Site	Serial #	PRE	POST	Delta							
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1783.179	1759.660	1778.783	1788.273	1768.023	1709.519	1780.614	1738.896	1823.145	1722.330	1752.336
Average	1834.546	1834.012	1823.796	1847.948	1807.249	1860.374	1793.899	1849.790	1889.339	1859.588	1815.258
Max	1891.968	1895.452	1889.667	1918.065	1871.382	1932.558	1813.802	1956.652	1955.181	1959.326	1929.991
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

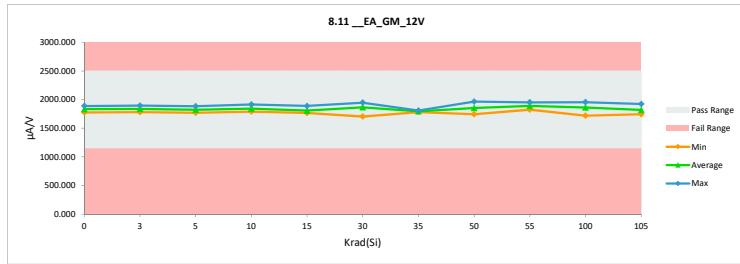


HDR TID Report  
TPS7H5004-SP

8.11_EA_GM_12V				
Test Site				
Tester				
Test Number				
Unit	µA/V	µA/V		
Max Limit	2500	2500		
Min Limit	1150	1150		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1753.087	1774.212	21.125
0	297	1874.277	1887.934	13.657
0	298	1839.294	1835.411	-3.883
3	192	1894.981	1895.452	0.471
3	193	1771.290	1780.435	9.145
3	194	1788.568	1782.724	-5.844
3	196	1861.240	1869.161	7.921
3	197	1835.883	1850.085	14.202
5	200	1898.842	1883.514	-15.328
5	202	1865.451	1865.451	0.000
5	203	1825.744	1804.535	-21.209
5	204	1783.264	1768.900	-14.364
5	206	1819.921	1786.321	-33.600
10	207	1877.591	1913.907	36.316
10	209	1808.231	1791.748	-16.483
10	210	1883.335	1901.564	18.229
10	211	1749.796	1790.114	40.318
10	212	1825.039	1819.398	-5.641
15	214	1808.845	1813.802	4.957
15	217	1802.016	1782.715	-19.301
15	218	1752.336	1765.521	13.185
15	219	1865.462	1889.379	23.917
15	222	1761.757	1790.267	28.510
30	224	1868.401	1914.038	45.637
30	225	1837.824	1856.657	18.833
30	226	1733.085	1704.214	-28.871
30	227	1917.013	1946.301	29.288
30	229	1905.387	1903.107	-2.280
35	230	1764.398	1791.748	27.350
35	231	1768.107	1805.769	37.662
35	232	1791.748	1808.855	17.107
35	234	1784.298	1787.301	3.003
35	237	1764.917	1781.815	16.898
50	272	1703.712	1744.039	40.327
50	275	1797.989	1811.012	13.023
50	276	1939.103	1964.948	25.845
50	277	1826.072	1820.198	-5.874
50	278	1883.462	1926.423	42.961
55	279	1802.703	1887.562	84.859
55	280	1928.161	1951.951	23.790
55	281	1887.350	1923.276	35.926
55	282	1871.204	1859.744	-11.460
55	283	1842.142	1824.404	-17.738
100	317	1844.197	1900.121	55.924
100	318	1813.822	1789.398	-24.424
100	319	1806.401	1830.435	24.034
100	320	1852.321	1858.817	6.496
100	321	1753.087	1829.307	76.220
100	322	1820.830	1839.305	18.475
100	323	1763.591	1786.445	22.854
100	324	1906.264	1930.646	24.382
100	326	1852.311	1870.620	18.309
100	327	1835.009	1869.411	34.402
100	328	1835.009	1894.008	58.999
100	329	1844.938	1911.977	67.039
100	332	1813.214	1900.121	86.907
100	333	1804.661	1866.261	61.600
100	334	1895.356	1894.008	-1.348
100	335	1880.717	1912.457	31.740
100	336	1830.854	1858.158	27.304
100	337	1891.808	1937.625	45.817
100	338	1765.948	1809.908	43.960
100	339	1884.329	1954.339	70.010
100	340	1815.785	1812.430	-3.355
100	341	1758.323	1809.625	51.302
100	342	1823.640	1909.366	85.726
100	343	1745.944	1718.563	-27.381
105	344	1931.268	1922.857	-8.411
105	346	1838.589	1823.650	-14.939
105	349	1800.246	1743.459	-56.787
105	350	1806.858	1782.866	-23.992
105	351	1795.843	1788.559	-7.284
105	352	1837.089	1848.831	11.742
Max		1939.103	1964.948	86.907
Average		1827.556	1844.650	17.095
Min		1703.712	1704.214	-56.787
Std Dev		52.886	61.270	30.312



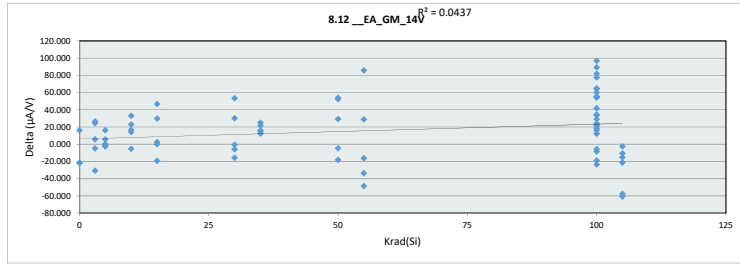
8.11_EA_GM_12V											
Test Site											
Tester											
Test Number											
Max Limit	2500	µA/V									
Min Limit	1150	µA/V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1774.212	1780.435	1768.900	1790.114	1765.521	1704.214	1781.815	1744.039	1824.404	1718.563	1743.459
Average	1832.519	1835.571	1821.744	1843.346	1808.337	1864.863	1795.098	1853.324	1889.387	1862.223	1818.370
Max	1887.934	1895.452	1883.514	1913.907	1889.379	1946.301	1808.855	1964.948	1951.951	1954.339	1922.857
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



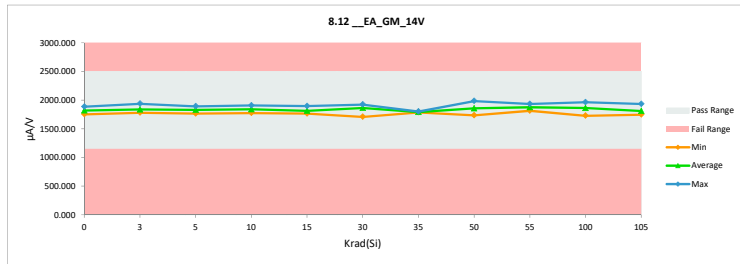


HDR TID Report  
TPS7H5004-SP

8.12 __EA_GM_14V				
Test Site	Tester	Test Number	Unit	
			µA/V	µA/V
			Max Limit	2500
			Min Limit	1150
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1774.203	1752.574	-21.629
0	297	1868.974	1884.922	15.948
0	298	1841.384	1819.142	-22.242
3	192	1912.358	1936.373	24.015
3	193	1755.361	1781.456	26.095
3	194	1785.114	1779.859	-5.255
3	196	1877.339	1846.257	-31.082
3	197	1835.371	1840.697	5.326
5	200	1893.420	1892.064	-1.356
5	202	1871.382	1871.371	-0.011
5	203	1800.333	1797.209	-3.124
5	204	1759.660	1764.917	5.257
5	206	1808.231	1824.225	15.994
10	207	1891.403	1907.716	16.313
10	209	1775.555	1808.240	32.685
10	210	1883.335	1897.274	13.939
10	211	1752.464	1775.066	22.602
10	212	1819.398	1813.792	-5.606
15	214	1804.079	1833.545	29.466
15	217	1789.579	1791.748	2.169
15	218	1764.936	1764.546	-0.390
15	219	1848.963	1895.441	46.478
15	222	1798.442	1778.906	-19.536
30	224	1857.973	1911.160	53.187
30	225	1842.142	1872.050	29.908
30	226	1722.790	1706.832	-15.958
30	227	1923.629	1922.648	-0.981
30	229	1913.918	1907.716	-6.202
35	230	1785.722	1800.642	14.920
35	231	1771.402	1796.112	24.710
35	232	1780.917	1792.897	11.980
35	234	1764.927	1786.311	21.384
35	237	1770.225	1785.703	15.478
50	272	1738.887	1733.792	-5.095
50	275	1797.893	1826.917	29.024
50	276	1929.636	1983.310	53.674
50	277	1830.704	1812.274	-18.430
50	278	1876.006	1927.917	51.911
55	279	1787.092	1872.468	85.376
55	280	1948.438	1931.835	-16.603
55	281	1896.663	1925.295	28.632
55	282	1872.206	1838.045	-34.161
55	283	1862.831	1813.792	-49.039
100	317	1835.973	1924.953	88.980
100	318	1795.853	1772.076	-23.777
100	319	1804.099	1858.168	54.069
100	320	1846.521	1869.953	23.432
100	321	1758.323	1818.008	59.685
100	322	1825.535	1846.521	20.986
100	323	1764.732	1793.424	28.692
100	324	1894.019	1927.895	33.876
100	326	1846.511	1869.963	23.452
100	327	1812.430	1866.717	54.287
100	328	1843.610	1884.932	41.322
100	329	1837.300	1914.530	77.230
100	332	1816.620	1913.242	96.622
100	333	1796.949	1860.837	63.888
100	334	1900.121	1894.019	-6.102
100	335	1881.909	1900.121	18.212
100	336	1834.999	1868.485	33.486
100	337	1882.563	1937.614	55.051
100	338	1789.474	1801.338	11.864
100	339	1881.255	1962.741	81.486
100	340	1804.661	1795.843	-8.818
100	341	1786.502	1802.374	15.872
100	342	1829.307	1894.019	64.712
100	343	1746.335	1727.126	-19.209
105	344	1953.679	1932.080	-21.599
105	346	1842.031	1780.766	-61.265
105	349	1804.351	1746.326	-58.025
105	350	1797.681	1782.270	-15.411
105	351	1801.338	1790.381	-10.957
105	352	1839.345	1836.436	-2.909
Max		1953.679	1983.310	96.622
Average		1827.936	1843.537	15.601
Min		1722.790	1706.832	-61.265
Std Dev		53.376	63.225	33.870

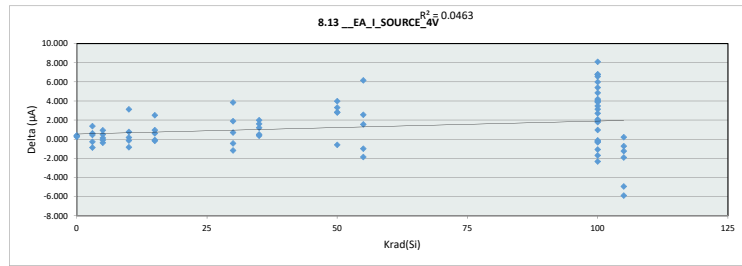


8.12 __EA_GM_14V															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL			2500	1150	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min					1752.574	1779.859	1764.917	1775.066	1764.546	1706.832	1785.703	1733.792	1813.792	1727.126	1746.326
Average					1818.879	1836.928	1829.957	1840.418	1812.837	1864.081	1792.333	1856.842	1876.287	1862.704	1811.377
Max					1884.922	1936.373	1892.064	1907.716	1895.441	1922.648	1800.642	1983.310	1931.835	1962.741	1932.080
UL					2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000

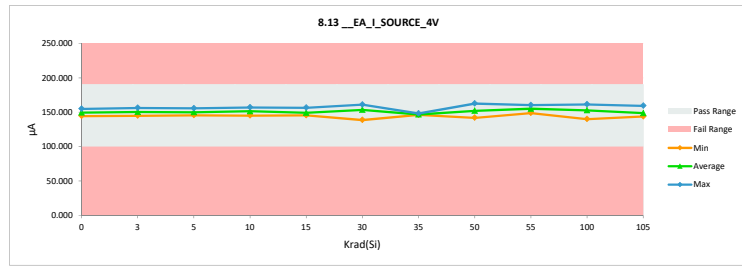


HDR TID Report  
TPS7H5004-SP

8.13 __EA_I_SOURCE_4V				
Test Site	Tester	Test Number	Unit	
			µA	µA
			Max Limit	190
			Min Limit	100
Krad(Si)	Serial #	PRE	POST	Delta
0	295	144.107	144.323	0.216
0	297	154.431	154.791	0.360
0	298	148.927	149.232	0.305
3	192	154.837	156.170	1.333
3	193	145.837	144.936	-0.901
3	194	144.171	144.747	0.576
3	196	153.452	153.869	0.417
3	197	151.717	151.419	-0.298
5	200	154.660	155.555	0.895
5	202	151.729	151.825	0.096
5	203	148.843	148.730	-0.113
5	204	144.941	145.405	0.464
5	206	148.420	148.024	-0.396
10	207	156.114	156.843	0.729
10	209	148.508	147.638	-0.870
10	210	151.651	154.743	3.092
10	211	144.886	145.038	0.152
10	212	152.801	152.625	-0.176
15	214	149.652	150.566	0.914
15	217	145.829	146.410	0.581
15	218	145.533	145.336	-0.197
15	219	153.918	156.399	2.481
15	222	146.654	146.538	-0.116
30	224	153.039	156.852	3.813
30	225	150.217	152.067	1.850
30	226	139.738	138.529	-1.209
30	227	160.276	160.939	0.663
30	229	159.195	158.718	-0.477
35	230	145.568	146.029	0.461
35	231	146.917	148.085	1.168
35	232	145.952	146.290	0.338
35	234	144.484	146.443	1.959
35	237	144.998	146.560	1.562
50	272	138.457	141.739	3.282
50	275	146.756	149.531	2.775
50	276	158.535	162.475	3.940
50	277	149.191	148.562	-0.629
50	278	154.825	157.608	2.783
55	279	148.667	154.780	6.113
55	280	158.691	160.210	1.519
55	281	156.633	159.159	2.526
55	282	153.634	152.605	-1.029
55	283	150.570	148.683	-1.887
100	317	152.743	157.551	4.808
100	318	148.352	145.987	-2.365
100	319	148.351	152.394	4.043
100	320	153.531	153.372	-0.159
100	321	143.678	149.628	5.950
100	322	150.747	150.391	-0.356
100	323	144.438	146.195	1.757
100	324	156.290	158.966	2.676
100	326	150.031	153.910	3.879
100	327	149.069	153.234	4.165
100	328	150.650	152.570	1.920
100	329	150.389	157.147	6.758
100	332	148.961	157.021	8.060
100	333	146.903	152.279	5.376
100	334	154.176	153.908	-0.268
100	335	156.785	157.713	0.928
100	336	149.933	153.045	3.112
100	337	155.507	158.946	3.439
100	338	145.364	149.234	3.870
100	339	154.770	161.296	6.526
100	340	148.155	147.041	-1.114
100	341	144.076	146.119	2.043
100	342	148.513	155.244	6.731
100	343	141.622	139.890	-1.732
105	344	160.526	159.256	-1.270
105	346	150.385	145.419	-4.966
105	349	149.635	143.715	-5.920
105	350	148.550	146.603	-1.947
105	351	147.179	146.418	-0.761
105	352	150.183	150.357	0.174
	Max	160.526	162.475	8.060
	Average	149.965	151.259	1.293
	Min	138.457	138.529	-5.920
	Std Dev	4.772	5.506	2.602

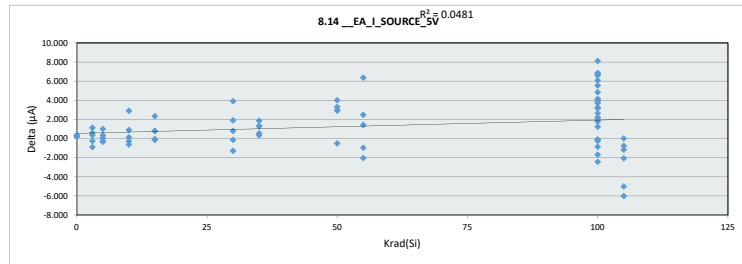


8.13 __EA_I_SOURCE_4V															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL			190	100	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min					144.323	144.747	145.405	145.038	145.336	138.529	146.029	141.739	148.683	139.890	143.715
Average					149.449	150.228	149.908	151.377	149.050	153.421	146.681	151.983	155.087	152.628	148.628
Max					154.791	156.170	155.555	156.843	156.399	160.939	148.085	162.475	160.210	161.296	159.256
UL			190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

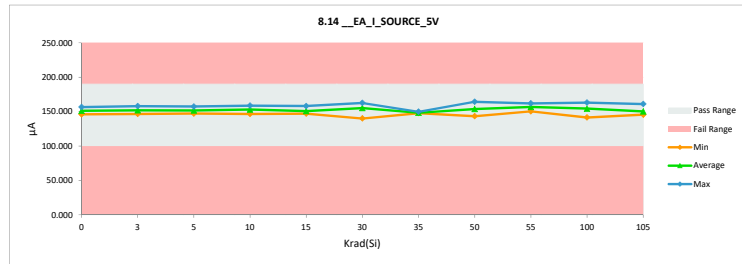


HDR TID Report  
TPS7H5004-SP

8.14 __EA_I_SOURCE_5V				
Test Site				
Tester				
Test Number				
Unit	µA		µA	
Max Limit	190		190	
Min Limit	100		100	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	145.774	145.942	0.168
0	297	156.243	156.542	0.299
0	298	150.535	150.875	0.340
3	192	156.773	157.891	1.118
3	193	147.397	146.483	-0.914
3	194	145.835	146.423	0.588
3	196	155.178	155.506	0.328
3	197	153.262	152.989	-0.273
5	200	156.354	157.345	0.991
5	202	153.650	153.696	0.046
5	203	150.504	150.253	-0.251
5	204	146.714	147.053	0.339
5	206	150.092	149.745	-0.347
10	207	157.725	158.602	0.877
10	209	150.112	149.467	-0.645
10	210	153.414	156.312	2.898
10	211	146.342	146.463	0.121
10	212	154.523	154.227	-0.296
15	214	151.431	152.212	0.781
15	217	147.334	148.113	0.779
15	218	147.164	147.015	-0.149
15	219	155.655	157.980	2.325
15	222	148.158	148.091	-0.067
30	224	154.872	158.765	3.893
30	225	151.977	153.858	1.881
30	226	141.217	139.933	-1.284
30	227	161.811	162.591	0.780
30	229	160.818	160.666	-0.152
35	230	147.088	147.616	0.528
35	231	148.390	149.680	1.290
35	232	147.700	148.037	0.337
35	234	146.090	147.949	1.859
35	237	146.696	148.005	1.309
50	272	140.015	143.304	3.289
50	275	148.412	151.373	2.961
50	276	160.260	164.262	4.002
50	277	150.784	150.271	-0.513
50	278	156.383	159.297	2.914
55	279	150.163	156.512	6.349
55	280	160.467	161.885	1.418
55	281	158.451	160.920	2.469
55	282	155.238	154.261	-0.977
55	283	152.402	150.359	-2.043
100	317	154.527	159.368	4.841
100	318	149.918	147.468	-2.450
100	319	149.983	154.058	4.075
100	320	155.239	155.059	-0.180
100	321	145.430	151.479	6.049
100	322	152.447	152.338	-0.109
100	323	146.150	147.909	1.759
100	324	158.079	160.718	2.639
100	326	151.683	155.566	3.883
100	327	150.815	154.947	4.132
100	328	152.219	154.246	2.027
100	329	152.086	158.941	6.855
100	332	150.463	158.560	8.097
100	333	148.477	154.018	5.541
100	334	155.875	155.620	-0.255
100	335	158.459	159.670	1.211
100	336	151.527	154.766	3.239
100	337	157.090	160.253	3.163
100	338	147.069	150.769	3.700
100	339	156.444	163.010	6.566
100	340	149.671	148.791	-0.880
100	341	145.725	147.922	2.197
100	342	150.088	156.777	6.689
100	343	143.137	141.452	-1.685
105	344	162.080	160.902	-1.178
105	346	152.091	147.070	-5.021
105	349	151.505	145.480	-6.025
105	350	150.126	148.055	-2.071
105	351	148.747	147.976	-0.771
105	352	151.999	152.002	0.003
Max		162.080	164.262	8.097
Average		151.624	152.931	1.307
Min		140.015	139.933	-6.025
Std Dev		4.808	5.552	2.619

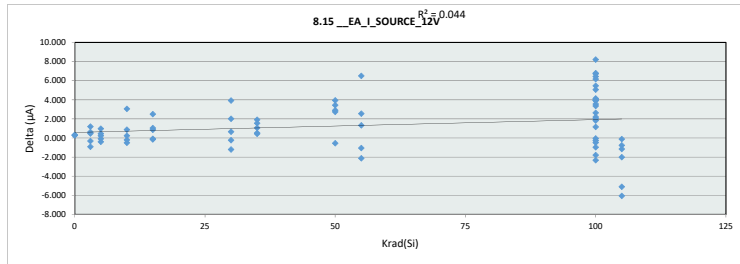


8.14 EA_I_SOURCE											
Test Site											
Tester											
Test Number											
Max Limit	190	µA									
Min Limit	100	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	145.942	146.423	147.053	146.463	147.015	139.933	147.616	143.304	150.359	141.452	145.480
Average	151.120	151.858	151.618	153.014	150.682	155.163	148.257	153.701	156.787	154.321	150.248
Max	156.542	157.891	157.345	158.602	157.980	162.591	149.680	164.262	161.885	163.010	160.902
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

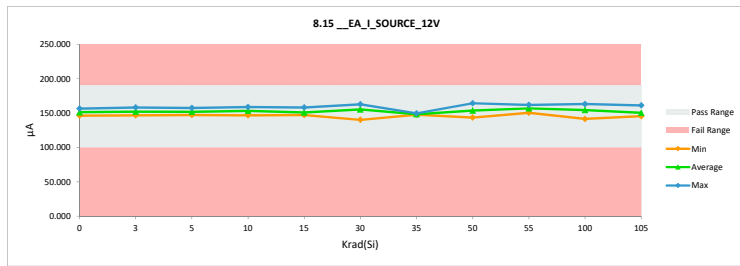


HDR TID Report  
TPS7H5004-SP

8.15 __EA_I_SOURCE_12V				
Test Site	Serial #	PRE	POST	Delta
0	295	145.873	146.128	0.255
0	297	156.197	156.492	0.295
0	298	150.604	150.908	0.304
3	192	156.827	158.002	1.175
3	193	147.557	146.616	-0.941
3	194	145.976	146.576	0.600
3	196	155.129	155.594	0.465
3	197	153.369	153.033	-0.336
5	200	156.492	157.447	0.955
5	202	153.576	153.798	0.222
5	203	150.423	150.326	-0.097
5	204	146.693	147.133	0.440
5	206	150.142	149.726	-0.416
10	207	157.902	158.764	0.862
10	209	150.228	149.714	-0.514
10	210	153.475	156.504	3.029
10	211	146.375	146.599	0.224
10	212	154.562	154.355	-0.207
15	214	151.449	152.473	1.024
15	217	147.435	148.272	0.837
15	218	147.328	147.166	-0.162
15	219	155.660	158.142	2.482
15	222	148.228	148.138	-0.090
30	224	154.978	158.891	3.913
30	225	152.023	154.021	1.998
30	226	141.260	140.047	-1.213
30	227	162.001	162.644	0.643
30	229	160.825	160.581	-0.244
35	230	147.173	147.590	0.417
35	231	148.493	149.542	1.049
35	232	147.634	148.136	0.502
35	234	146.236	148.139	1.903
35	237	146.673	148.200	1.527
50	272	139.926	143.355	3.429
50	275	148.496	151.376	2.880
50	276	160.297	164.217	3.920
50	277	150.823	150.256	-0.567
50	278	156.498	159.210	2.712
55	279	150.278	156.756	6.478
55	280	160.470	161.786	1.316
55	281	158.427	160.959	2.532
55	282	155.249	154.178	-1.071
55	283	152.494	150.348	-2.146
100	317	154.588	159.626	5.038
100	318	149.964	147.618	-2.346
100	319	150.100	154.134	4.034
100	320	155.316	155.253	-0.063
100	321	145.402	151.557	6.155
100	322	152.491	152.212	-0.279
100	323	146.017	147.824	1.807
100	324	158.064	160.680	2.616
100	326	151.733	155.642	3.909
100	327	150.857	154.981	4.124
100	328	152.254	154.432	2.178
100	329	152.252	158.957	6.705
100	332	150.556	158.756	8.200
100	333	148.666	154.110	5.444
100	334	156.042	155.551	-0.491
100	335	158.567	159.708	1.141
100	336	151.546	154.885	3.339
100	337	157.223	160.767	3.544
100	338	147.124	151.028	3.904
100	339	156.634	163.060	6.426
100	340	149.936	148.950	-0.986
100	341	145.757	147.764	2.007
100	342	150.197	156.976	6.779
100	343	143.210	141.410	-1.800
105	344	162.266	161.100	-1.166
105	346	152.143	147.020	-5.123
105	349	151.453	145.378	-6.075
105	350	150.163	148.145	-2.018
105	351	148.784	148.008	-0.776
105	352	152.097	151.970	-0.127
Max		162.266	164.217	8.200
Average		151.687	153.009	1.322
Min		139.926	140.047	-6.075
Std Dev		4.819	5.561	2.641



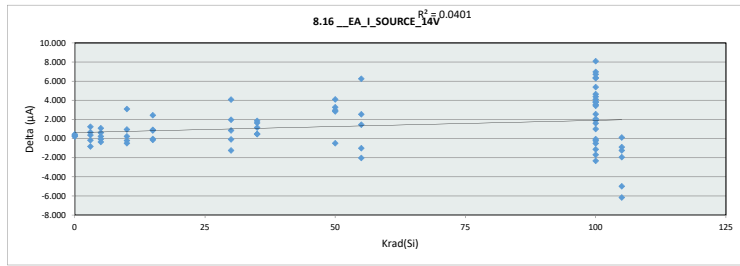
8.15 __EA_I_SOURCE_12V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	146.128	146.576	147.133	146.599	147.166	140.047	147.590	143.355	150.348	141.410	145.378
Average	151.176	151.964	151.686	153.187	150.838	155.237	148.321	153.683	156.805	154.412	150.270
Max	156.492	158.002	157.447	158.764	158.142	162.644	149.542	164.217	161.786	163.060	161.100
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



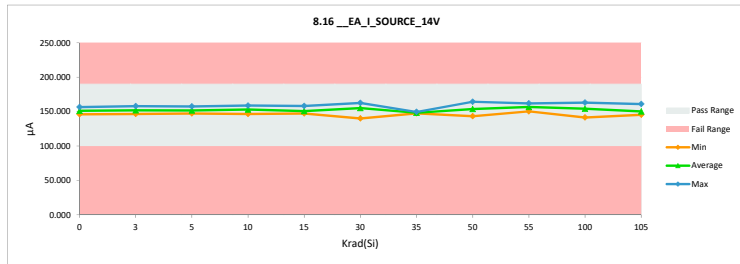
HDR TID Report  
TPS7H5004-SP

8.16 __EA_I_SOURCE_14V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	295	145.832	146.024	0.192
0	297	156.177	156.603	0.426
0	298	150.572	150.869	0.297
3	192	156.715	157.931	1.216
3	193	147.433	146.595	-0.838
3	194	145.888	146.536	0.648
3	196	155.112	155.468	0.356
3	197	153.220	153.028	-0.192
5	200	156.427	157.488	1.061
5	202	153.575	153.795	0.220
5	203	150.380	150.312	-0.068
5	204	146.610	147.202	0.592
5	206	150.049	149.685	-0.364
10	207	157.804	158.732	0.928
10	209	150.132	149.621	-0.511
10	210	153.353	156.435	3.082
10	211	146.317	146.539	0.222
10	212	154.487	154.265	-0.222
15	214	151.422	152.258	0.836
15	217	147.347	148.240	0.893
15	218	147.282	147.134	-0.148
15	219	155.732	158.153	2.421
15	222	148.188	148.136	-0.052
30	224	154.854	158.909	4.055
30	225	151.961	153.922	1.961
30	226	141.210	139.947	-1.263
30	227	161.834	162.675	0.841
30	229	160.763	160.652	-0.111
35	230	147.100	147.563	0.463
35	231	148.343	149.484	1.141
35	232	147.562	148.003	0.441
35	234	146.214	148.051	1.837
35	237	146.517	148.130	1.613
50	272	140.029	143.283	3.254
50	275	148.465	151.406	2.941
50	276	160.211	164.292	4.081
50	277	150.746	150.238	-0.508
50	278	156.419	159.252	2.833
55	279	150.334	156.579	6.245
55	280	160.336	161.775	1.439
55	281	158.416	160.946	2.530
55	282	155.187	154.170	-1.017
55	283	152.410	150.368	-2.042
100	317	154.687	159.319	4.632
100	318	149.893	147.543	-2.350
100	319	150.029	154.115	4.086
100	320	155.176	154.942	-0.234
100	321	145.264	151.567	6.303
100	322	152.407	152.332	-0.075
100	323	146.134	147.716	1.582
100	324	157.956	160.504	2.548
100	326	151.775	155.612	3.837
100	327	150.749	155.116	4.367
100	328	152.097	154.133	2.036
100	329	152.113	159.071	6.958
100	332	150.544	158.629	8.085
100	333	148.551	153.922	5.371
100	334	155.910	155.381	-0.529
100	335	158.419	159.404	0.985
100	336	151.548	154.963	3.415
100	337	157.095	160.617	3.522
100	338	147.060	150.832	3.772
100	339	156.592	162.934	6.342
100	340	149.845	148.698	-1.147
100	341	145.697	147.606	1.909
100	342	150.130	156.836	6.706
100	343	143.117	141.406	-1.711
105	344	162.209	160.954	-1.255
105	346	152.027	147.023	-5.004
105	349	151.508	145.326	-6.182
105	350	150.054	148.100	-1.954
105	351	148.915	148.001	-0.914
105	352	151.915	152.007	0.092
Max		162.209	164.292	8.085
Average		151.622	152.949	1.328
Min		140.029	139.947	-6.182
Std Dev		4.805	5.559	2.626

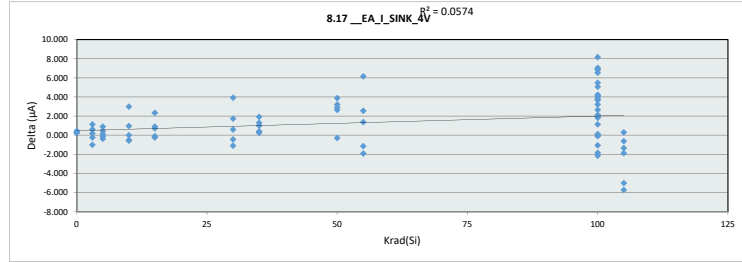


8.16 __EA_I_SOURCE_14V		0	3	5	10	15	30	35	50	55	100	105
LL		100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min		146.024	146.536	147.202	146.539	147.134	139.947	147.563	143.283	150.368	141.406	145.326
Average		151.165	151.912	151.696	153.118	150.784	155.221	148.246	153.694	156.768	154.300	150.235
Max		156.603	157.931	157.488	158.732	158.153	162.675	149.484	164.292	161.775	162.934	160.954
UL		190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

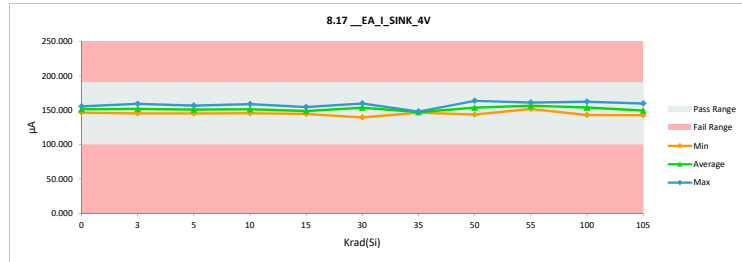


HDR TID Report  
TPS7H5004-SP

8.17 __EA_I_SINK_4V				
Test Site	Tester	Test Number	Unit	
			µA	µA
			Max Limit	190
			Min Limit	100
Krad(Si)	Serial #	PRE	POST	Delta
0	295	146.055	146.332	0.277
0	297	155.111	155.537	0.426
0	298	151.797	152.015	0.218
3	192	158.068	159.191	1.123
3	193	146.353	145.328	-1.025
3	194	148.775	149.368	0.593
3	196	155.252	155.436	0.184
3	197	150.442	150.192	-0.250
5	200	155.917	156.797	0.880
5	202	154.919	155.010	0.091
5	203	148.391	148.231	-0.160
5	204	144.765	145.195	0.430
5	206	149.100	148.709	-0.391
10	207	156.744	157.699	0.955
10	209	147.613	147.041	-0.572
10	210	155.802	158.777	2.975
10	211	145.542	145.530	-0.012
10	212	148.377	147.833	-0.544
15	214	148.774	149.666	0.892
15	217	147.104	147.803	0.699
15	218	144.856	144.601	-0.255
15	219	152.365	154.689	2.324
15	222	147.227	147.115	-0.112
30	224	153.755	157.666	3.911
30	225	151.544	153.257	1.713
30	226	140.511	139.406	-1.105
30	227	159.066	159.647	0.581
30	229	159.177	158.726	-0.451
35	230	147.025	147.268	0.243
35	231	146.029	147.017	0.988
35	232	147.603	147.991	0.388
35	234	145.330	147.236	1.906
35	237	145.147	146.434	1.287
50	272	140.510	143.708	3.198
50	275	148.754	151.641	2.887
50	276	159.691	163.550	3.859
50	277	150.799	150.494	-0.305
50	278	155.951	158.595	2.644
55	279	148.963	155.103	6.140
55	280	159.820	161.178	1.358
55	281	158.647	161.195	2.548
55	282	154.755	153.596	-1.159
55	283	153.717	151.797	-1.920
100	317	154.448	159.505	5.057
100	318	146.806	144.646	-2.160
100	319	147.658	151.875	4.217
100	320	153.336	153.438	0.102
100	321	143.455	149.984	6.529
100	322	152.651	152.528	-0.123
100	323	145.418	147.229	1.811
100	324	158.128	160.767	2.639
100	326	153.247	157.175	3.928
100	327	151.384	155.501	4.117
100	328	153.536	155.641	2.105
100	329	152.052	159.076	7.024
100	332	148.702	156.854	8.152
100	333	147.423	152.892	5.469
100	334	156.925	156.934	0.009
100	335	158.422	159.539	1.117
100	336	150.870	154.073	3.203
100	337	156.184	159.854	3.670
100	338	144.217	147.940	3.723
100	339	155.425	162.254	6.829
100	340	149.372	148.295	-1.077
100	341	147.146	149.079	1.933
100	342	149.104	155.980	6.876
100	343	144.709	142.847	-1.862
105	344	161.146	159.793	-1.353
105	346	153.461	148.437	-5.024
105	349	148.434	142.697	-5.737
105	350	148.314	146.427	-1.887
105	351	148.334	147.698	-0.636
105	352	152.058	152.335	0.277
Max		161.146	163.550	8.152
Average		150.938	152.259	1.320
Min		140.510	139.406	-5.737
Std Dev		4.919	5.674	2.637

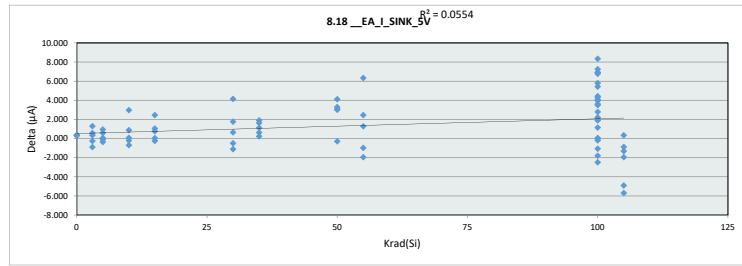


8.17 __EA_I_SINK															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL			190	100	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min					146.332	145.328	145.195	145.530	144.601	139.406	146.434	143.708	151.797	142.847	142.697
Average					151.295	151.903	150.788	151.376	148.775	153.740	147.189	153.598	156.574	153.913	149.565
Max					155.537	159.191	156.797	158.777	154.689	159.647	147.991	163.550	161.195	162.254	159.793
UL			190	100	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

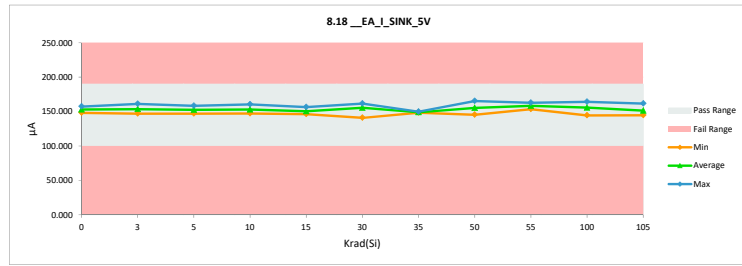


HDR TID Report  
TPS7H5004-SP

8.18 __EA_I_SINK_5V				
Test Site				
Tester				
Test Number				
Unit	µA		µA	
Max Limit	190		190	
Min Limit	100		100	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	147.917	148.177	0.260
0	297	157.019	157.361	0.342
0	298	153.459	153.768	0.309
3	192	159.917	161.181	1.264
3	193	147.882	146.964	-0.918
3	194	150.490	151.046	0.556
3	196	156.980	157.291	0.311
3	197	152.024	151.731	-0.293
5	200	157.580	158.506	0.926
5	202	156.740	156.771	0.031
5	203	149.973	149.861	-0.112
5	204	146.476	147.031	0.555
5	206	150.977	150.597	-0.380
10	207	158.651	159.503	0.852
10	209	149.493	148.763	-0.730
10	210	157.535	160.476	2.941
10	211	147.269	147.325	0.056
10	212	149.899	149.667	-0.232
15	214	150.520	151.538	1.018
15	217	148.784	149.510	0.726
15	218	146.538	146.268	-0.270
15	219	154.088	156.508	2.420
15	222	148.913	148.927	0.014
30	224	155.587	159.706	4.119
30	225	153.238	154.982	1.744
30	226	142.011	140.871	-1.140
30	227	160.980	161.589	0.609
30	229	161.121	160.600	-0.521
35	230	148.809	149.001	0.192
35	231	147.674	148.770	1.096
35	232	149.273	149.877	0.604
35	234	147.170	149.037	1.867
35	237	146.743	148.355	1.612
50	272	142.165	145.430	3.265
50	275	150.425	153.458	3.033
50	276	161.318	165.393	4.075
50	277	152.372	152.059	-0.313
50	278	157.619	160.605	2.986
55	279	150.839	157.150	6.311
55	280	161.584	162.839	1.255
55	281	160.449	162.873	2.424
55	282	156.564	155.555	-1.009
55	283	155.479	153.494	-1.985
100	317	156.287	161.696	5.409
100	318	148.607	146.100	-2.507
100	319	149.293	153.577	4.284
100	320	155.157	155.102	-0.055
100	321	145.292	152.024	6.732
100	322	154.505	154.540	0.035
100	323	147.117	149.047	1.930
100	324	160.029	162.791	2.762
100	326	155.023	159.066	4.043
100	327	153.149	157.540	4.391
100	328	155.315	157.509	2.194
100	329	153.697	160.924	7.227
100	332	150.335	158.641	8.306
100	333	149.079	154.847	5.768
100	334	158.941	158.722	-0.219
100	335	160.395	161.521	1.126
100	336	152.575	156.056	3.481
100	337	157.894	161.473	3.579
100	338	145.805	149.738	3.933
100	339	157.323	164.213	6.890
100	340	151.140	150.058	-1.082
100	341	148.938	150.817	1.879
100	342	150.911	157.790	6.879
100	343	146.201	144.373	-1.828
105	344	163.006	161.664	-1.342
105	346	155.268	150.331	-4.937
105	349	150.396	144.676	-5.720
105	350	150.041	148.071	-1.970
105	351	150.212	149.299	-0.913
105	352	153.845	154.156	0.311
Max		163.006	165.393	8.306
Average		152.689	154.065	1.376
Min		142.011	140.871	-5.720
Std Dev		4.976	5.738	2.700

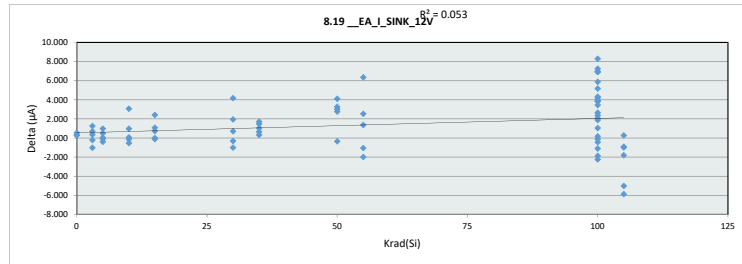


8.18 __EA_I_SINK_5V											
Test Site											
Tester											
Test Number											
Max Limit	190		µA								
Min Limit	100		µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	148.177	146.964	147.031	147.325	146.268	140.871	148.355	145.430	153.494	144.373	144.676
Average	153.102	153.643	152.553	153.147	150.550	155.550	149.008	155.389	158.382	155.757	151.366
Max	157.361	161.181	158.506	160.476	156.508	161.589	149.877	165.393	162.873	164.213	161.664
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

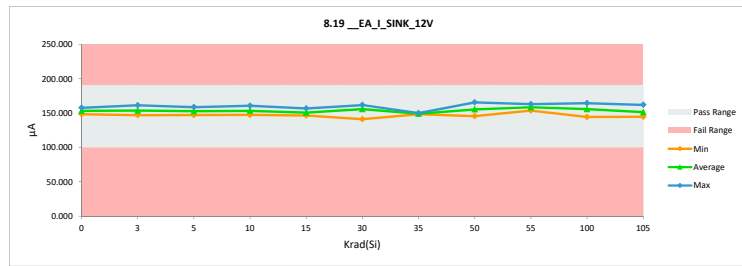


HDR TID Report  
TPS7H5004-SP

8.19 __EA_I_SINK_12V				
Test Site				
Tester				
Test Number				
Unit	µA		µA	
Max Limit	190		190	
Min Limit	100		100	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	148.078	148.347	0.269
0	297	157.112	157.623	0.511
0	298	153.563	153.827	0.264
3	192	160.066	161.295	1.229
3	193	148.099	147.057	-1.042
3	194	150.574	151.252	0.678
3	196	157.046	157.364	0.318
3	197	152.076	151.824	-0.252
5	200	157.760	158.697	0.937
5	202	156.997	156.999	0.002
5	203	150.081	149.953	-0.128
5	204	146.685	147.150	0.465
5	206	151.006	150.578	-0.428
10	207	158.766	159.713	0.947
10	209	149.617	149.057	-0.560
10	210	157.578	160.609	3.031
10	211	147.321	147.356	0.035
10	212	149.972	149.811	-0.161
15	214	150.553	151.602	1.049
15	217	148.953	149.647	0.694
15	218	146.660	146.507	-0.153
15	219	154.335	156.719	2.384
15	222	149.043	149.017	-0.026
30	224	155.734	159.871	4.137
30	225	153.317	155.238	1.921
30	226	142.085	141.044	-1.041
30	227	161.058	161.730	0.672
30	229	161.242	160.897	-0.345
35	230	148.865	149.159	0.294
35	231	147.755	148.755	1.000
35	232	149.354	149.962	0.608
35	234	147.264	148.944	1.680
35	237	147.024	148.466	1.442
50	272	142.261	145.502	3.241
50	275	150.551	153.542	2.991
50	276	161.462	165.538	4.076
50	277	152.564	152.183	-0.381
50	278	157.745	160.475	2.730
55	279	150.982	157.296	6.314
55	280	161.554	162.885	1.331
55	281	160.505	163.011	2.506
55	282	156.597	155.508	-1.089
55	283	155.595	153.579	-2.016
100	317	156.435	161.584	5.149
100	318	148.719	146.453	-2.266
100	319	149.559	153.716	4.157
100	320	155.150	155.292	0.142
100	321	145.257	152.144	6.887
100	322	154.524	154.379	-0.145
100	323	147.125	149.161	2.036
100	324	160.043	162.658	2.615
100	326	155.233	159.118	3.885
100	327	153.180	157.478	4.298
100	328	155.352	157.687	2.335
100	329	153.823	161.044	7.221
100	332	150.455	158.718	8.263
100	333	149.155	155.008	5.853
100	334	159.002	158.546	-0.456
100	335	160.481	161.499	1.018
100	336	152.653	156.077	3.424
100	337	158.103	161.902	3.799
100	338	145.910	149.734	3.824
100	339	157.463	164.331	6.868
100	340	151.279	150.138	-1.141
100	341	148.975	150.828	1.853
100	342	151.037	157.971	6.934
100	343	146.326	144.430	-1.896
105	344	162.994	161.969	-1.025
105	346	155.305	150.251	-5.054
105	349	150.450	144.551	-5.899
105	350	150.132	148.295	-1.837
105	351	150.217	149.267	-0.950
105	352	154.008	154.239	0.231
Max		162.994	165.538	8.263
Average		152.792	154.165	1.373
Min		142.085	141.044	-5.899
Std Dev		4.970	5.743	2.695



8.19 __EA_I_SINK											
Test Site											
Tester											
Test Number											
Max Limit	190		µA								
Min Limit	100		µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	148.347	147.057	147.150	147.356	146.507	141.044	148.466	145.502	153.579	144.430	144.551
Average	153.266	153.758	152.675	153.309	150.698	155.756	149.057	155.448	158.456	155.829	151.429
Max	157.623	161.295	158.697	160.609	156.719	161.730	149.962	165.538	163.011	164.331	161.969
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

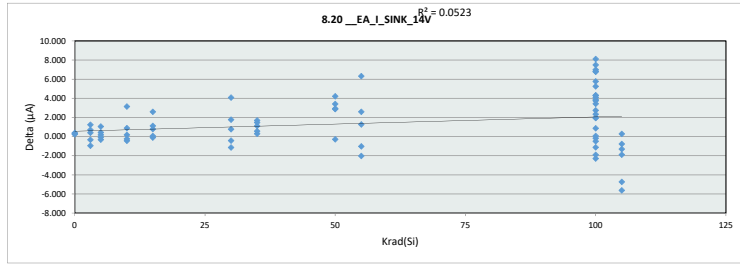




HDR TID Report  
TPS7H5004-SP

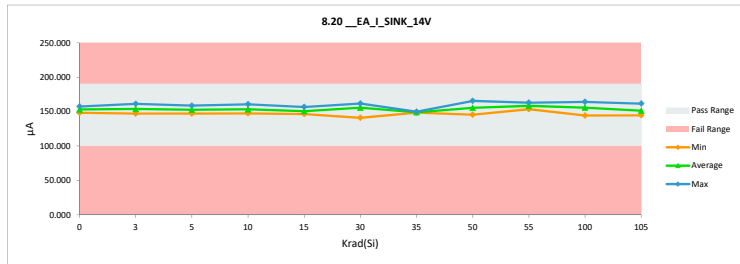
8.20 __EA_I_SINK_14V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	295	147.955	148.276	0.321
0	297	157.047	157.414	0.367
0	298	153.541	153.760	0.219
3	192	159.983	161.208	1.225
3	193	148.050	147.080	-0.970
3	194	150.555	151.233	0.678
3	196	156.976	157.361	0.385
3	197	152.192	151.834	-0.358
5	200	157.661	158.697	1.036
5	202	156.771	156.931	0.160
5	203	149.988	149.895	-0.093
5	204	146.646	147.058	0.412
5	206	150.931	150.586	-0.345
10	207	158.720	159.592	0.872
10	209	149.510	149.050	-0.460
10	210	157.555	160.678	3.123
10	211	147.245	147.402	0.157
10	212	149.939	149.688	-0.251
15	214	150.509	151.607	1.098
15	217	148.853	149.620	0.767
15	218	146.611	146.490	-0.121
15	219	154.127	156.693	2.566
15	222	148.974	149.016	0.042
30	224	155.703	159.763	4.060
30	225	153.347	155.097	1.750
30	226	142.053	140.897	-1.156
30	227	160.983	161.740	0.757
30	229	161.102	160.670	-0.432
35	230	148.831	149.119	0.288
35	231	147.626	148.732	1.106
35	232	149.290	149.836	0.546
35	234	147.244	148.913	1.669
35	237	146.941	148.386	1.445
50	272	142.076	145.474	3.398
50	275	150.572	153.425	2.853
50	276	161.345	165.539	4.194
50	277	152.387	152.088	-0.299
50	278	157.623	160.540	2.917
55	279	150.779	157.083	6.304
55	280	161.490	162.736	1.246
55	281	160.400	162.967	2.567
55	282	156.508	155.471	-1.037
55	283	155.566	153.530	-2.036
100	317	156.410	161.643	5.233
100	318	148.533	146.215	-2.318
100	319	149.342	153.632	4.290
100	320	155.033	155.077	0.044
100	321	145.233	152.040	6.807
100	322	154.436	154.242	-0.194
100	323	147.114	149.073	1.959
100	324	159.986	162.709	2.723
100	326	155.066	159.154	4.088
100	327	153.121	157.396	4.275
100	328	155.277	157.595	2.318
100	329	153.708	161.181	7.473
100	332	150.470	158.558	8.088
100	333	149.069	154.818	5.749
100	334	158.898	158.393	-0.505
100	335	160.417	161.272	0.855
100	336	152.590	155.996	3.406
100	337	157.985	161.721	3.736
100	338	145.821	149.642	3.821
100	339	157.391	164.144	6.753
100	340	151.195	150.059	-1.136
100	341	148.827	150.743	1.916
100	342	150.938	157.912	6.974
100	343	146.248	144.317	-1.931
105	344	162.917	161.588	-1.329
105	346	155.083	150.324	-4.759
105	349	150.255	144.608	-5.647
105	350	150.095	148.178	-1.917
105	351	150.197	149.411	-0.786
105	352	153.951	154.211	0.260
Max		162.917	165.539	8.088
Average		152.710	154.096	1.387
Min		142.053	140.897	-5.647
Std Dev		4.966	5.730	2.685



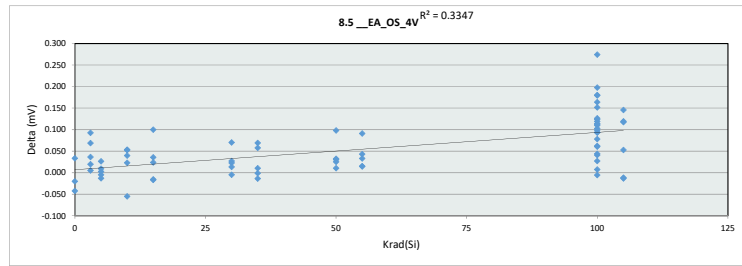
8.20 __EA_I_SINK	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	148.276	147.080	147.058	147.402	146.490	140.897	148.386	145.474	153.530	144.317	144.608
Average	153.150	153.743	152.633	153.282	150.685	155.633	148.997	155.413	158.357	155.731	151.387
Max	157.414	161.208	158.697	160.678	156.693	161.740	149.836	165.539	162.967	164.144	161.588
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

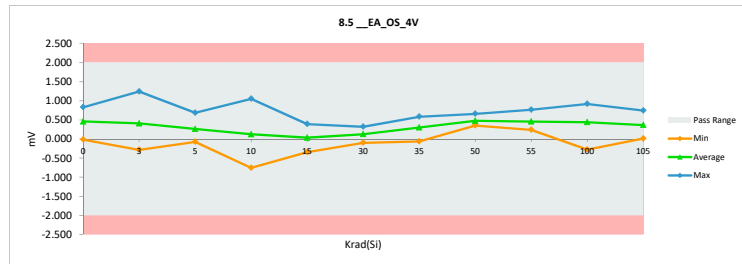


HDR TID Report  
TPS7H5004-SP

		8.5_EA_OS_4V		
Test Site				
Tester				
Test Number				
Unit		mV	mV	
Max Limit		2	2	
Min Limit		-2	-2	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.530	0.563	0.033
0	297	0.026	-0.017	-0.043
0	298	0.850	0.829	-0.021
3	192	0.631	0.650	0.019
3	193	0.105	0.173	0.068
3	194	1.152	1.243	0.092
3	196	0.228	0.264	0.036
3	197	-0.289	-0.285	0.004
5	200	0.431	0.425	-0.005
5	202	0.685	0.687	0.002
5	203	0.040	0.065	0.026
5	204	-0.083	-0.075	0.009
5	206	0.228	0.214	-0.014
10	207	-0.008	0.044	0.052
10	209	-0.171	-0.226	-0.056
10	210	1.029	1.052	0.022
10	211	0.467	0.506	0.039
10	212	-0.809	-0.756	0.053
15	214	-0.093	0.006	0.099
15	217	0.222	0.205	-0.016
15	218	-0.070	-0.087	-0.017
15	219	-0.381	-0.346	0.035
15	222	0.367	0.391	0.023
30	224	0.075	0.144	0.069
30	225	0.298	0.320	0.022
30	226	0.182	0.208	0.026
30	227	-0.095	-0.101	-0.006
30	229	0.044	0.057	0.013
35	230	0.544	0.529	-0.014
35	231	-0.132	-0.064	0.068
35	232	0.526	0.583	0.057
35	234	0.318	0.316	-0.002
35	237	0.139	0.148	0.010
50	272	0.626	0.658	0.032
50	275	0.388	0.398	0.009
50	276	0.254	0.352	0.097
50	277	0.514	0.544	0.030
50	278	0.406	0.431	0.025
55	279	0.224	0.239	0.014
55	280	0.294	0.336	0.042
55	281	0.510	0.600	0.090
55	282	0.328	0.342	0.014
55	283	0.733	0.766	0.032
100	317	0.529	0.726	0.197
100	318	-0.364	-0.246	0.118
100	319	-0.183	-0.070	0.113
100	320	-0.024	0.089	0.112
100	321	0.102	0.376	0.274
100	322	0.573	0.635	0.061
100	323	0.659	0.761	0.102
100	324	0.509	0.607	0.098
100	326	0.808	0.918	0.109
100	327	0.632	0.658	0.026
100	328	0.734	0.774	0.040
100	329	0.384	0.548	0.163
100	332	-0.059	0.034	0.093
100	333	0.172	0.265	0.093
100	334	0.709	0.787	0.077
100	335	0.426	0.469	0.043
100	336	0.161	0.284	0.123
100	337	0.281	0.461	0.180
100	338	-0.429	-0.278	0.151
100	339	0.369	0.548	0.179
100	340	0.118	0.125	0.007
100	341	0.808	0.802	-0.006
100	342	0.352	0.412	0.060
100	343	0.780	0.906	0.126
105	344	0.285	0.404	0.119
105	346	0.623	0.610	-0.012
105	349	-0.134	0.011	0.145
105	350	0.040	0.092	0.052
105	351	0.339	0.324	-0.014
105	352	0.629	0.745	0.117
Max		1.152	1.243	0.274
Average		0.289	0.344	0.055
Min		-0.809	-0.756	-0.056
Std Dev		0.363	0.365	0.062

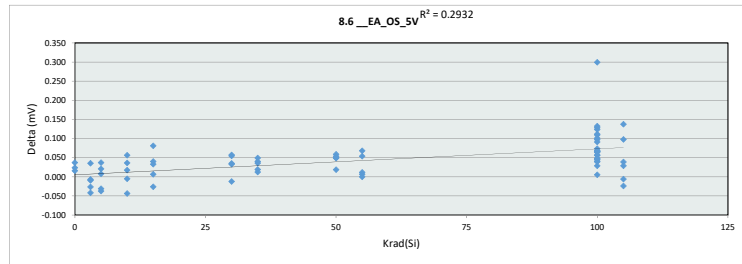


		8.5_EA_OS_4V										
Test Site												
Tester												
Test Number												
Max Limit		2	mV									
Min Limit		-2	mV									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	
Min	-0.017	-0.285	-0.075	-0.756	-0.346	-0.101	-0.064	0.352	0.239	-0.279	0.011	
Average	0.458	0.409	0.263	0.124	0.034	0.126	0.303	0.476	0.456	0.441	0.364	
Max	0.829	1.243	0.687	1.052	0.391	0.320	0.583	0.658	0.766	0.918	0.745	
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	

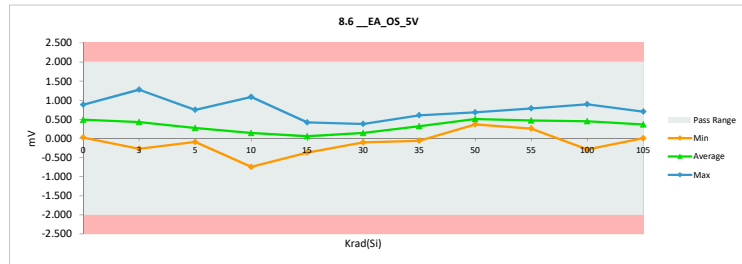


HDR TID Report  
TPS7H5004-SP

		8.6_EA_OS_5V		
Test Site				
Tester				
Test Number				
Unit		mV	mV	
Max Limit		2	2	
Min Limit		-2	-2	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.542	0.566	0.023
0	297	-0.012	0.024	0.037
0	298	0.867	0.882	0.015
3	192	0.727	0.720	-0.007
3	193	0.177	0.167	-0.010
3	194	1.242	1.277	0.035
3	196	0.295	0.253	-0.042
3	197	-0.244	-0.271	-0.027
5	200	0.459	0.496	0.037
5	202	0.727	0.748	0.021
5	203	0.009	0.017	0.007
5	204	-0.054	-0.091	-0.038
5	206	0.235	0.204	-0.031
10	207	0.047	0.004	-0.044
10	209	-0.184	-0.128	0.056
10	210	1.052	1.088	0.036
10	211	0.499	0.494	-0.005
10	212	-0.761	-0.744	0.017
15	214	-0.013	0.027	0.039
15	217	0.234	0.267	0.033
15	218	-0.061	-0.055	0.006
15	219	-0.343	-0.370	-0.026
15	222	0.339	0.420	0.081
30	224	0.096	0.131	0.035
30	225	0.346	0.379	0.033
30	226	0.173	0.227	0.054
30	227	-0.091	-0.104	-0.013
30	229	0.030	0.086	0.057
35	230	0.538	0.557	0.019
35	231	-0.098	-0.059	0.040
35	232	0.571	0.607	0.035
35	234	0.313	0.325	0.012
35	237	0.131	0.180	0.049
50	272	0.635	0.684	0.049
50	275	0.387	0.405	0.018
50	276	0.313	0.367	0.053
50	277	0.564	0.623	0.059
50	278	0.414	0.463	0.049
55	279	0.248	0.259	0.011
55	280	0.310	0.363	0.053
55	281	0.548	0.616	0.068
55	282	0.333	0.332	0.000
55	283	0.780	0.786	0.006
100	317	0.591	0.692	0.101
100	318	-0.361	-0.229	0.132
100	319	-0.102	-0.054	0.048
100	320	0.016	0.144	0.128
100	321	0.122	0.421	0.299
100	322	0.593	0.649	0.056
100	323	0.751	0.796	0.045
100	324	0.569	0.608	0.039
100	326	0.868	0.873	0.005
100	327	0.697	0.743	0.047
100	328	0.757	0.847	0.091
100	329	0.435	0.544	0.109
100	332	-0.085	-0.022	0.064
100	333	0.197	0.238	0.040
100	334	0.706	0.817	0.111
100	335	0.451	0.548	0.097
100	336	0.210	0.238	0.028
100	337	0.372	0.439	0.067
100	338	-0.362	-0.289	0.073
100	339	0.387	0.511	0.123
100	340	0.087	0.152	0.065
100	341	0.788	0.854	0.066
100	342	0.353	0.410	0.057
100	343	0.826	0.894	0.068
105	344	0.317	0.414	0.097
105	346	0.653	0.628	-0.024
105	349	-0.132	0.005	0.137
105	350	0.096	0.090	-0.006
105	351	0.326	0.365	0.039
105	352	0.673	0.702	0.029
Max		1.242	1.277	0.299
Average		0.317	0.360	0.043
Min		-0.761	-0.744	-0.044
Std Dev		0.368	0.372	0.052



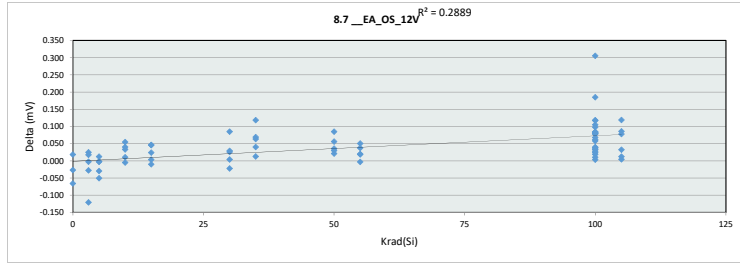
		8.6_EA_OS_5V										
Test Site												
Tester												
Test Number												
Max Limit		2	mV									
Min Limit		-2	mV									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min		0.024	-0.271	-0.091	-0.744	-0.370	-0.104	-0.059	0.367	0.259	-0.289	0.005
Average		0.491	0.429	0.274	0.143	0.058	0.144	0.322	0.508	0.471	0.451	0.367
Max		0.882	1.277	0.748	1.088	0.420	0.379	0.607	0.684	0.786	0.894	0.702
UL		2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



HDR TID Report  
TPS7H5004-SP

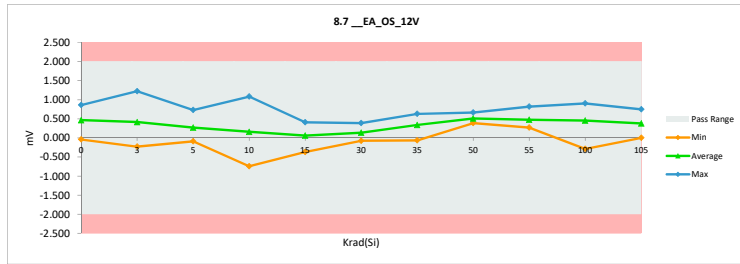
8.7_EA_OS_12V	
Test Site	
Tester	
Test Number	
Unit	mV mV
Max Limit	2 2
Min Limit	-2 -2

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.554	0.572	0.018
0	297	0.024	-0.042	-0.066
0	298	0.888	0.860	-0.027
3	192	0.716	0.688	-0.028
3	193	0.165	0.190	0.024
3	194	1.225	1.222	-0.003
3	196	0.319	0.198	-0.121
3	197	-0.246	-0.229	0.017
5	200	0.490	0.460	-0.030
5	202	0.719	0.731	0.012
5	203	0.063	0.060	-0.003
5	204	-0.088	-0.090	-0.003
5	206	0.232	0.181	-0.051
10	207	0.026	0.066	0.040
10	209	-0.220	-0.165	0.054
10	210	1.087	1.081	-0.005
10	211	0.518	0.550	0.033
10	212	-0.754	-0.744	0.010
15	214	0.001	0.047	0.046
15	217	0.233	0.256	0.024
15	218	-0.043	-0.040	0.003
15	219	-0.360	-0.370	-0.010
15	222	0.361	0.407	0.045
30	224	0.110	0.135	0.025
30	225	0.302	0.387	0.085
30	226	0.176	0.205	0.028
30	227	-0.080	-0.076	0.003
30	229	0.048	0.025	-0.023
35	230	0.519	0.559	0.040
35	231	-0.078	-0.066	0.012
35	232	0.567	0.631	0.063
35	234	0.257	0.375	0.118
35	237	0.122	0.191	0.069
50	272	0.629	0.664	0.035
50	275	0.378	0.408	0.030
50	276	0.302	0.387	0.085
50	277	0.560	0.615	0.055
50	278	0.443	0.464	0.021
55	279	0.249	0.268	0.019
55	280	0.319	0.369	0.050
55	281	0.587	0.606	0.019
55	282	0.318	0.314	-0.003
55	283	0.782	0.818	0.037
100	317	0.576	0.761	0.185
100	318	-0.367	-0.289	0.078
100	319	-0.110	-0.076	0.034
100	320	0.024	0.128	0.104
100	321	0.116	0.421	0.305
100	322	0.599	0.656	0.057
100	323	0.714	0.724	0.010
100	324	0.553	0.586	0.033
100	326	0.871	0.894	0.023
100	327	0.703	0.742	0.039
100	328	0.766	0.845	0.079
100	329	0.461	0.529	0.068
100	332	-0.056	0.061	0.117
100	333	0.228	0.231	0.003
100	334	0.752	0.780	0.028
100	335	0.452	0.535	0.083
100	336	0.221	0.303	0.082
100	337	0.373	0.456	0.083
100	338	-0.371	-0.273	0.097
100	339	0.420	0.537	0.117
100	340	0.132	0.151	0.018
100	341	0.807	0.845	0.038
100	342	0.361	0.422	0.061
100	343	0.822	0.901	0.079
105	344	0.378	0.389	0.012
105	346	0.605	0.637	0.032
105	349	-0.116	0.002	0.118
105	350	0.095	0.099	0.004
105	351	0.301	0.386	0.086
105	352	0.671	0.748	0.078
Max		1.225	1.222	0.305
Average		0.321	0.360	0.040
Min		-0.754	-0.744	-0.121
Std Dev		0.368	0.370	0.057



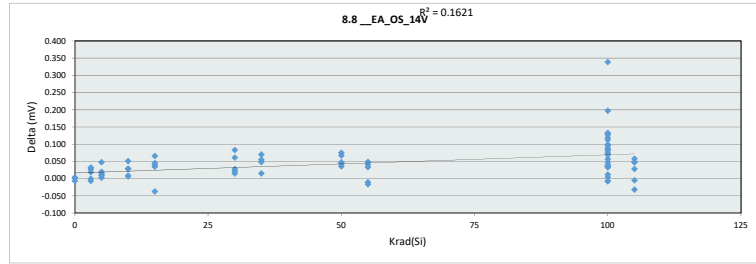
8.7_EA_OS_12V	
Test Site	
Tester	
Test Number	
Max Limit	2 mV
Min Limit	-2 mV

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.042	-0.229	-0.090	-0.744	-0.370	-0.076	-0.066	0.387	0.268	-0.289	0.002
Average	0.464	0.414	0.268	0.158	0.060	0.135	0.338	0.508	0.475	0.453	0.377
Max	0.860	1.222	0.731	1.081	0.407	0.387	0.631	0.664	0.818	0.901	0.748
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

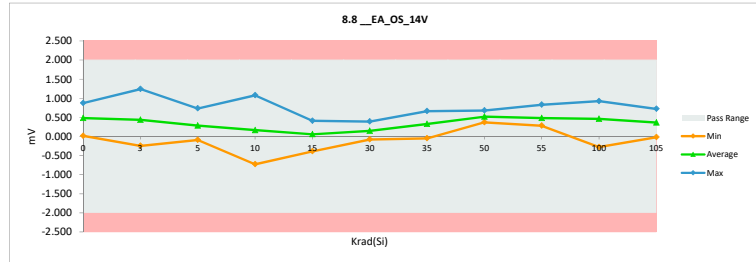


HDR TID Report  
TPS7H5004-SP

8.8_EA_OS_14V				
Test Site				
Tester				
Test Number				
Unit	mV		mV	
Max Limit	2		2	
Min Limit	-2		-2	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.565	0.557	-0.007
0	297	0.013	0.014	0.001
0	298	0.869	0.872	0.003
3	192	0.694	0.686	-0.008
3	193	0.166	0.192	0.027
3	194	1.240	1.239	-0.001
3	196	0.285	0.303	0.018
3	197	-0.281	-0.250	0.031
5	200	0.473	0.475	0.002
5	202	0.723	0.731	0.008
5	203	0.068	0.081	0.013
5	204	-0.111	-0.092	0.019
5	206	0.180	0.227	0.047
10	207	0.051	0.057	0.006
10	209	-0.168	-0.141	0.027
10	210	1.069	1.077	0.008
10	211	0.507	0.558	0.050
10	212	-0.758	-0.730	0.028
15	214	0.007	0.040	0.034
15	217	0.206	0.270	0.065
15	218	-0.090	-0.045	0.045
15	219	-0.352	-0.390	-0.038
15	222	0.368	0.407	0.039
30	224	0.107	0.128	0.021
30	225	0.306	0.389	0.082
30	226	0.182	0.209	0.027
30	227	-0.096	-0.082	0.014
30	229	0.025	0.085	0.060
35	230	0.495	0.549	0.055
35	231	-0.100	-0.052	0.048
35	232	0.591	0.660	0.069
35	234	0.329	0.343	0.014
35	237	0.080	0.128	0.048
50	272	0.642	0.677	0.035
50	275	0.371	0.446	0.074
50	276	0.303	0.369	0.066
50	277	0.573	0.620	0.047
50	278	0.434	0.476	0.042
55	279	0.241	0.281	0.040
55	280	0.322	0.370	0.048
55	281	0.605	0.588	-0.017
55	282	0.347	0.335	-0.011
55	283	0.794	0.827	0.033
100	317	0.644	0.691	0.047
100	318	-0.322	-0.210	0.112
100	319	-0.091	-0.059	0.032
100	320	-0.001	0.131	0.132
100	321	0.099	0.437	0.338
100	322	0.595	0.693	0.098
100	323	0.714	0.753	0.039
100	324	0.558	0.592	0.034
100	326	0.880	0.872	-0.009
100	327	0.684	0.740	0.056
100	328	0.724	0.809	0.085
100	329	0.448	0.528	0.081
100	332	-0.130	0.067	0.197
100	333	0.214	0.225	0.011
100	334	0.741	0.836	0.095
100	335	0.452	0.539	0.087
100	336	0.220	0.293	0.073
100	337	0.346	0.477	0.131
100	338	-0.348	-0.280	0.068
100	339	0.427	0.553	0.126
100	340	0.160	0.152	-0.008
100	341	0.848	0.851	0.003
100	342	0.354	0.389	0.035
100	343	0.804	0.923	0.118
105	344	0.374	0.431	0.057
105	346	0.648	0.615	-0.033
105	349	-0.069	-0.022	0.047
105	350	0.063	0.091	0.027
105	351	0.349	0.343	-0.006
105	352	0.672	0.718	0.047
Max		1.240	1.239	0.338
Average		0.320	0.365	0.046
Min		-0.758	-0.730	-0.038
Std Dev		0.370	0.367	0.054



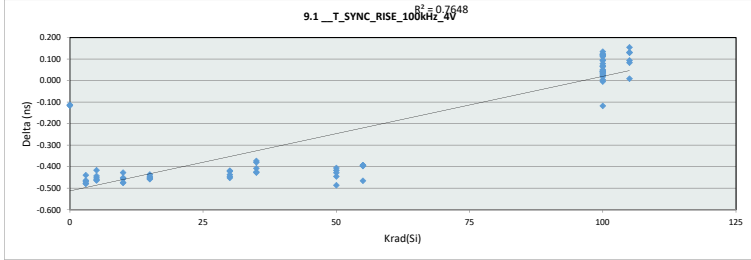
8.8_EA_OS_14V											
Test Site											
Tester											
Test Number											
Max Limit	2										
Min Limit	-2										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	0.014	-0.250	-0.092	-0.730	-0.390	-0.082	-0.052	0.369	0.281	-0.280	-0.022
Average	0.481	0.434	0.284	0.164	0.057	0.146	0.326	0.517	0.480	0.458	0.363
Max	0.872	1.239	0.731	1.077	0.407	0.389	0.660	0.677	0.827	0.923	0.718
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



HDR TID Report  
TPS7H5004-SP

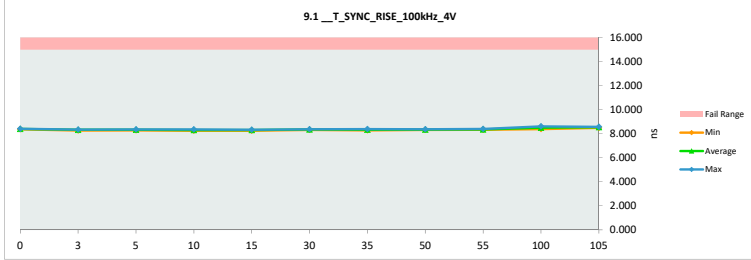
		9.1 _T_SYNC_RISE_100kHz_4V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.527	8.410	-0.117
0	297	8.485	8.369	-0.116
0	298	8.500	8.387	-0.113
3	192	8.823	8.343	-0.480
3	193	8.790	8.325	-0.465
3	194	8.731	8.261	-0.470
3	196	8.783	8.343	-0.440
3	197	8.811	8.333	-0.478
5	200	8.761	8.344	-0.417
5	202	8.715	8.271	-0.444
5	203	8.789	8.324	-0.465
5	204	8.811	8.358	-0.453
5	206	8.783	8.322	-0.461
10	207	8.787	8.359	-0.428
10	209	8.824	8.350	-0.474
10	210	8.728	8.276	-0.452
10	211	8.722	8.246	-0.476
10	212	8.719	8.262	-0.457
15	214	8.728	8.283	-0.445
15	217	8.764	8.313	-0.451
15	218	8.785	8.329	-0.456
15	219	8.781	8.322	-0.459
15	222	8.689	8.252	-0.437
30	224	8.806	8.353	-0.453
30	225	8.776	8.357	-0.419
30	226	8.755	8.317	-0.438
30	227	8.817	8.370	-0.447
30	229	8.782	8.360	-0.422
35	230	8.699	8.271	-0.428
35	231	8.740	8.366	-0.374
35	232	8.722	8.314	-0.408
35	234	8.758	8.377	-0.381
35	237	8.742	8.317	-0.425
50	272	8.797	8.351	-0.446
50	275	8.772	8.354	-0.418
50	276	8.849	8.362	-0.487
50	277	8.747	8.318	-0.429
50	278	8.746	8.339	-0.407
55	279	8.727	8.329	-0.398
55	280	8.814	8.348	-0.466
55	281	8.736	8.342	-0.394
55	282	8.754	8.360	-0.394
55	283	8.785	8.390	-0.395
100	317	8.387	8.382	-0.005
100	318	8.469	8.493	0.024
100	319	8.491	8.536	0.045
100	320	8.459	8.498	0.039
100	321	8.394	8.505	0.111
100	322	8.366	8.396	0.030
100	323	8.319	8.453	0.134
100	324	8.455	8.489	0.034
100	326	8.423	8.490	0.067
100	327	8.403	8.495	0.092
100	328	8.382	8.432	0.050
100	329	8.395	8.517	0.122
100	332	8.419	8.537	0.118
100	333	8.445	8.560	0.115
100	334	8.402	8.444	0.042
100	335	8.550	8.432	-0.118
100	336	8.490	8.554	0.064
100	337	8.471	8.474	0.003
100	338	8.446	8.524	0.078
100	339	8.401	8.468	0.067
100	340	8.509	8.526	0.017
100	341	8.486	8.609	0.123
100	342	8.462	8.557	0.095
100	343	8.440	8.482	0.042
105	344	8.421	8.552	0.131
105	346	8.528	8.536	0.008
105	349	8.440	8.569	0.129
105	350	8.391	8.484	0.093
105	351	8.453	8.536	0.083
105	352	8.403	8.557	0.154
	Max	8.849	8.609	0.154
	Average	8.620	8.402	-0.218
	Min	8.319	8.246	-0.487
	Std Dev	0.168	0.096	0.251



		9.1 _T_SYNC_RIS	
Test Site			
Tester			
Test Number			
Max Limit	15	ns	
Min Limit			

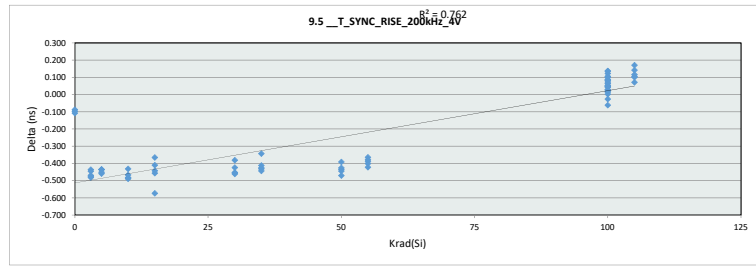
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.369	8.261	8.271	8.246	8.252	8.317	8.271	8.318	8.329	8.382	8.484
Average	8.389	8.321	8.324	8.299	8.300	8.351	8.329	8.345	8.354	8.494	8.539
Max	8.410	8.343	8.358	8.359	8.329	8.370	8.377	8.362	8.390	8.609	8.569
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

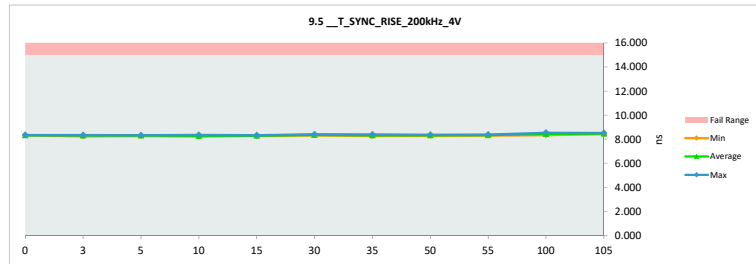
		9.5 _T_SYNC_RISE_200kHz_4V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.435	8.336	-0.099
0	297	8.409	8.320	-0.089
0	298	8.431	8.321	-0.110
3	192	8.795	8.324	-0.471
3	193	8.768	8.321	-0.447
3	194	8.716	8.233	-0.483
3	196	8.809	8.332	-0.477
3	197	8.762	8.324	-0.438
5	200	8.764	8.311	-0.453
5	202	8.689	8.252	-0.437
5	203	8.765	8.327	-0.438
5	204	8.753	8.316	-0.437
5	206	8.772	8.311	-0.461
10	207	8.784	8.351	-0.433
10	209	8.798	8.329	-0.469
10	210	8.734	8.250	-0.484
10	211	8.707	8.216	-0.491
10	212	8.737	8.251	-0.486
15	214	8.705	8.293	-0.412
15	217	8.768	8.310	-0.458
15	218	8.760	8.316	-0.444
15	219	8.861	8.286	-0.575
15	222	8.636	8.269	-0.367
30	224	8.789	8.406	-0.383
30	225	8.799	8.342	-0.457
30	226	8.759	8.304	-0.455
30	227	8.797	8.334	-0.463
30	229	8.781	8.355	-0.426
35	230	8.684	8.270	-0.414
35	231	8.729	8.384	-0.345
35	232	8.729	8.296	-0.433
35	234	8.761	8.333	-0.428
35	237	8.747	8.302	-0.445
50	272	8.770	8.334	-0.436
50	275	8.784	8.338	-0.446
50	276	8.748	8.355	-0.393
50	277	8.742	8.314	-0.428
50	278	8.758	8.286	-0.472
55	279	8.665	8.299	-0.366
55	280	8.728	8.348	-0.380
55	281	8.729	8.339	-0.390
55	282	8.742	8.339	-0.403
55	283	8.797	8.373	-0.424
100	317	8.366	8.338	-0.028
100	318	8.363	8.497	0.134
100	319	8.404	8.468	0.064
100	320	8.394	8.478	0.084
100	321	8.384	8.434	0.050
100	322	8.317	8.382	0.065
100	323	8.301	8.401	0.100
100	324	8.382	8.467	0.085
100	326	8.365	8.391	0.026
100	327	8.399	8.440	0.041
100	328	8.325	8.423	0.098
100	329	8.321	8.398	0.077
100	332	8.414	8.459	0.045
100	333	8.352	8.456	0.104
100	334	8.329	8.381	0.052
100	335	8.395	8.414	0.019
100	336	8.447	8.528	0.081
100	337	8.370	8.506	0.136
100	338	8.392	8.405	0.013
100	339	8.424	8.425	0.001
100	340	8.407	8.528	0.121
100	341	8.519	8.456	-0.063
100	342	8.379	8.424	0.045
100	343	8.387	8.414	0.027
105	344	8.359	8.429	0.070
105	346	8.403	8.506	0.103
105	349	8.389	8.488	0.099
105	350	8.299	8.413	0.114
105	351	8.316	8.485	0.169
105	352	8.365	8.505	0.140
	Max	8.861	8.528	0.169
	Average	8.584	8.368	-0.216
	Min	8.299	8.216	-0.575
	Std Dev	0.192	0.077	0.253



		9.5 _T_SYNC_RIS	
Test Site			
Tester			
Test Number			
Max Limit	15	ns	
Min Limit		ns	

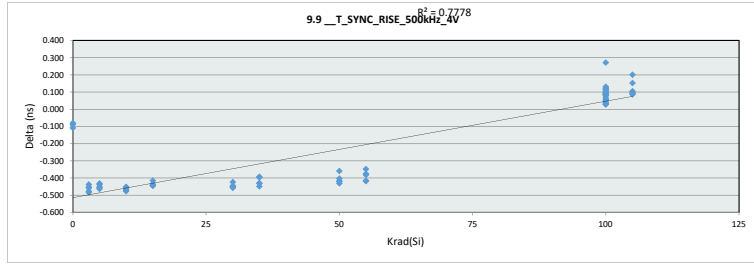
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.320	8.233	8.252	8.216	8.269	8.304	8.270	8.286	8.299	8.338	8.413
Average	8.326	8.307	8.303	8.279	8.295	8.348	8.317	8.325	8.340	8.438	8.471
Max	8.336	8.332	8.327	8.351	8.316	8.406	8.384	8.355	8.373	8.528	8.506
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

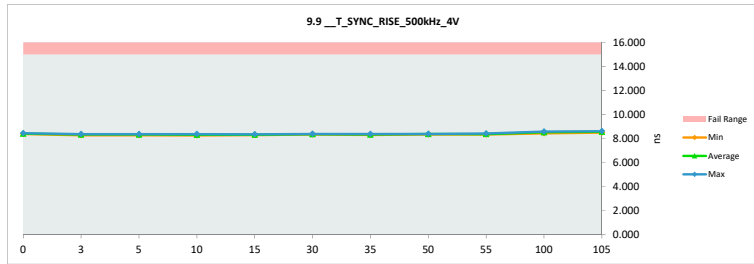
		9.9_T_SYNC_RISE_500kHz_4V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.533	8.444	-0.089
0	297	8.465	8.384	-0.081
0	298	8.529	8.419	-0.110
3	192	8.828	8.375	-0.453
3	193	8.825	8.348	-0.477
3	194	8.733	8.274	-0.459
3	196	8.843	8.358	-0.485
3	197	8.807	8.369	-0.438
5	200	8.824	8.360	-0.464
5	202	8.727	8.273	-0.454
5	203	8.805	8.366	-0.439
5	204	8.803	8.370	-0.433
5	206	8.813	8.358	-0.455
10	207	8.836	8.359	-0.477
10	209	8.827	8.375	-0.452
10	210	8.742	8.286	-0.456
10	211	8.738	8.264	-0.474
10	212	8.759	8.293	-0.466
15	214	8.730	8.284	-0.446
15	217	8.774	8.338	-0.436
15	218	8.764	8.348	-0.416
15	219	8.797	8.352	-0.445
15	222	8.732	8.290	-0.442
30	224	8.827	8.377	-0.450
30	225	8.834	8.388	-0.446
30	226	8.790	8.338	-0.452
30	227	8.833	8.375	-0.458
30	229	8.808	8.384	-0.424
35	230	8.700	8.303	-0.397
35	231	8.778	8.385	-0.393
35	232	8.751	8.319	-0.432
35	234	8.810	8.379	-0.431
35	237	8.773	8.324	-0.449
50	272	8.792	8.388	-0.404
50	275	8.817	8.385	-0.432
50	276	8.796	8.375	-0.421
50	277	8.762	8.344	-0.418
50	278	8.749	8.389	-0.360
55	279	8.696	8.347	-0.349
55	280	8.746	8.370	-0.376
55	281	8.753	8.371	-0.382
55	282	8.786	8.370	-0.416
55	283	8.835	8.416	-0.419
100	317	8.399	8.439	0.040
100	318	8.452	8.545	0.093
100	319	8.494	8.523	0.029
100	320	8.463	8.550	0.087
100	321	8.396	8.526	0.130
100	322	8.384	8.497	0.113
100	323	8.260	8.531	0.271
100	324	8.469	8.546	0.077
100	326	8.402	8.503	0.101
100	327	8.426	8.535	0.109
100	328	8.403	8.498	0.095
100	329	8.425	8.452	0.027
100	332	8.457	8.577	0.120
100	333	8.466	8.553	0.087
100	334	8.420	8.542	0.122
100	335	8.463	8.546	0.083
100	336	8.486	8.575	0.089
100	337	8.498	8.582	0.084
100	338	8.484	8.536	0.052
100	339	8.451	8.508	0.057
100	340	8.490	8.522	0.032
100	341	8.522	8.587	0.065
100	342	8.445	8.539	0.094
100	343	8.458	8.497	0.039
105	344	8.441	8.545	0.104
105	346	8.529	8.615	0.086
105	349	8.488	8.578	0.090
105	350	8.304	8.504	0.200
105	351	8.460	8.612	0.152
105	352	8.476	8.574	0.098
	Max	8.843	8.615	0.271
	Average	8.633	8.429	-0.203
	Min	8.260	8.264	-0.485
	Std Dev	0.174	0.100	0.262



		9.9_T_SYNC_RIS	
Test Site			
Tester			
Test Number			
Max Limit	15	ns	
Min Limit		ns	

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.384	8.274	8.273	8.264	8.284	8.338	8.303	8.344	8.347	8.439	8.504
Average	8.416	8.345	8.345	8.315	8.322	8.372	8.342	8.376	8.375	8.530	8.571
Max	8.444	8.375	8.370	8.375	8.352	8.388	8.385	8.389	8.416	8.587	8.615
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

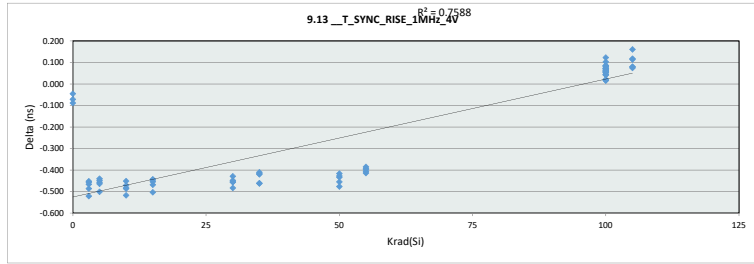




HDR TID Report  
TPS7H5004-SP

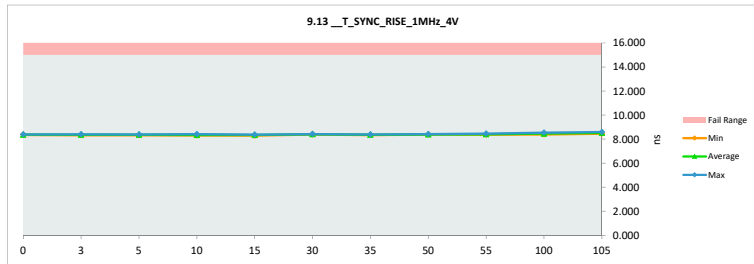
9.13 _T_SYNC_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.482	8.393	-0.089
0	297	8.401	8.355	-0.046
0	298	8.464	8.392	-0.072
3	192	8.866	8.399	-0.467
3	193	8.841	8.388	-0.453
3	194	8.781	8.324	-0.457
3	196	8.868	8.381	-0.487
3	197	8.870	8.348	-0.522
5	200	8.838	8.390	-0.448
5	202	8.775	8.318	-0.457
5	203	8.866	8.364	-0.502
5	204	8.856	8.391	-0.465
5	206	8.824	8.383	-0.441
10	207	8.870	8.398	-0.472
10	209	8.871	8.419	-0.452
10	210	8.793	8.306	-0.487
10	211	8.796	8.312	-0.484
10	212	8.844	8.326	-0.518
15	214	8.800	8.296	-0.504
15	217	8.822	8.367	-0.455
15	218	8.820	8.374	-0.446
15	219	8.842	8.373	-0.469
15	222	8.738	8.294	-0.444
30	224	8.879	8.422	-0.457
30	225	8.869	8.420	-0.449
30	226	8.816	8.386	-0.430
30	227	8.887	8.403	-0.484
30	229	8.864	8.408	-0.456
35	230	8.758	8.341	-0.417
35	231	8.803	8.392	-0.411
35	232	8.794	8.373	-0.421
35	234	8.851	8.389	-0.462
35	237	8.820	8.357	-0.463
50	272	8.855	8.399	-0.456
50	275	8.871	8.394	-0.477
50	276	8.847	8.413	-0.434
50	277	8.812	8.384	-0.428
50	278	8.811	8.394	-0.417
55	279	8.761	8.375	-0.386
55	280	8.819	8.411	-0.408
55	281	8.806	8.391	-0.415
55	282	8.799	8.406	-0.393
55	283	8.854	8.455	-0.399
100	317	8.341	8.393	0.052
100	318	8.397	8.453	0.056
100	319	8.463	8.479	0.016
100	320	8.434	8.486	0.052
100	321	8.395	8.466	0.071
100	322	8.347	8.406	0.059
100	323	8.318	8.402	0.084
100	324	8.415	8.517	0.102
100	326	8.391	8.450	0.059
100	327	8.391	8.461	0.070
100	328	8.357	8.400	0.043
100	329	8.386	8.428	0.042
100	332	8.398	8.477	0.079
100	333	8.407	8.529	0.122
100	334	8.386	8.455	0.069
100	335	8.433	8.449	0.016
100	336	8.440	8.520	0.080
100	337	8.403	8.487	0.084
100	338	8.441	8.462	0.021
100	339	8.385	8.449	0.064
100	340	8.451	8.493	0.042
100	341	8.452	8.536	0.084
100	342	8.441	8.513	0.072
100	343	8.430	8.492	0.062
105	344	8.382	8.456	0.074
105	346	8.431	8.591	0.160
105	349	8.404	8.521	0.117
105	350	8.396	8.474	0.078
105	351	8.433	8.513	0.080
105	352	8.440	8.553	0.113
	Max	8.887	8.591	0.160
	Average	8.640	8.418	-0.222
	Min	8.318	8.294	-0.522
	Std Dev	0.213	0.064	0.259



9.13 _T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns

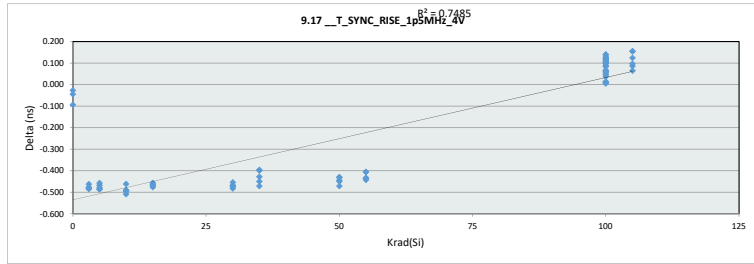
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.355	8.324	8.318	8.306	8.294	8.386	8.341	8.384	8.375	8.393	8.456
Average	8.380	8.368	8.369	8.352	8.341	8.408	8.370	8.397	8.408	8.467	8.518
Max	8.393	8.399	8.391	8.419	8.374	8.422	8.392	8.413	8.455	8.536	8.591
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

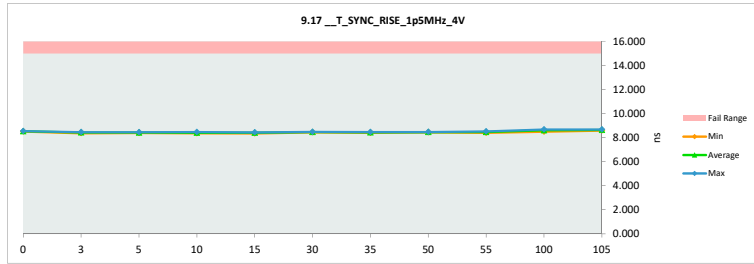
9.17 __T_SYNC_RISE_1p5MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.642	8.547	-0.095
0	297	8.548	8.502	-0.046
0	298	8.567	8.539	-0.028
3	192	8.941	8.455	-0.486
3	193	8.910	8.446	-0.464
3	194	8.843	8.365	-0.478
3	196	8.923	8.442	-0.481
3	197	8.924	8.441	-0.483
5	200	8.912	8.425	-0.487
5	202	8.852	8.383	-0.469
5	203	8.927	8.446	-0.481
5	204	8.909	8.450	-0.459
5	206	8.910	8.422	-0.488
10	207	8.926	8.463	-0.463
10	209	8.955	8.457	-0.498
10	210	8.860	8.367	-0.493
10	211	8.866	8.355	-0.511
10	212	8.882	8.390	-0.492
15	214	8.848	8.378	-0.470
15	217	8.902	8.438	-0.464
15	218	8.892	8.434	-0.458
15	219	8.905	8.435	-0.470
15	222	8.825	8.347	-0.478
30	224	8.936	8.467	-0.469
30	225	8.945	8.461	-0.484
30	226	8.889	8.433	-0.456
30	227	8.949	8.477	-0.472
30	229	8.937	8.461	-0.476
35	230	8.800	8.399	-0.401
35	231	8.857	8.459	-0.398
35	232	8.863	8.433	-0.430
35	234	8.920	8.447	-0.473
35	237	8.887	8.436	-0.451
50	272	8.911	8.460	-0.451
50	275	8.931	8.458	-0.473
50	276	8.915	8.470	-0.445
50	277	8.865	8.433	-0.432
50	278	8.886	8.454	-0.432
55	279	8.829	8.394	-0.435
55	280	8.892	8.448	-0.444
55	281	8.865	8.458	-0.407
55	282	8.885	8.477	-0.408
55	283	8.954	8.518	-0.436
100	317	8.471	8.507	0.036
100	318	8.605	8.615	0.010
100	319	8.590	8.637	0.047
100	320	8.542	8.587	0.045
100	321	8.512	8.609	0.097
100	322	8.436	8.556	0.120
100	323	8.481	8.489	0.008
100	324	8.517	8.640	0.123
100	326	8.466	8.603	0.137
100	327	8.540	8.645	0.105
100	328	8.478	8.593	0.115
100	329	8.519	8.616	0.097
100	332	8.556	8.666	0.110
100	333	8.524	8.651	0.127
100	334	8.520	8.659	0.139
100	335	8.578	8.582	0.004
100	336	8.610	8.674	0.064
100	337	8.558	8.661	0.103
100	338	8.562	8.575	0.013
100	339	8.508	8.564	0.056
100	340	8.556	8.616	0.060
100	341	8.578	8.643	0.065
100	342	8.558	8.644	0.086
100	343	8.557	8.639	0.082
105	344	8.528	8.623	0.095
105	346	8.524	8.678	0.154
105	349	8.576	8.639	0.063
105	350	8.473	8.596	0.123
105	351	8.512	8.665	0.153
105	352	8.552	8.637	0.085
Max		8.955	8.678	0.154
Average		8.734	8.513	-0.220
Min		8.436	8.347	-0.511
Std Dev		0.184	0.098	0.270



9.17 __T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns

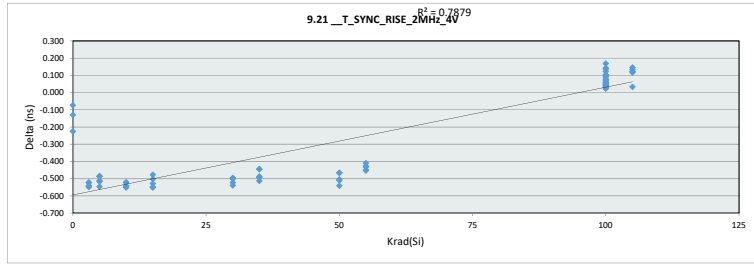
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.502	8.365	8.383	8.355	8.347	8.433	8.399	8.433	8.394	8.489	8.596
Average	8.529	8.430	8.425	8.406	8.406	8.460	8.435	8.455	8.459	8.611	8.640
Max	8.547	8.455	8.450	8.463	8.438	8.477	8.459	8.470	8.518	8.674	8.678
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

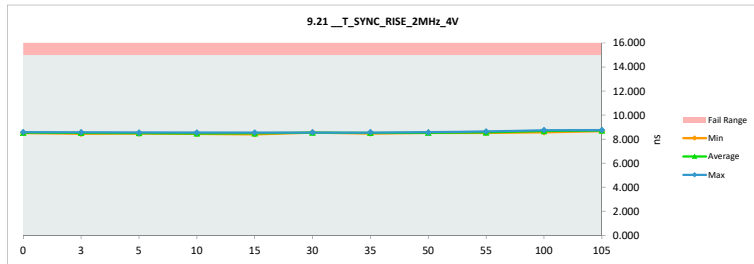
9.21 __T_SYNC_RISE_2MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.651	8.577	-0.074
0	297	8.669	8.539	-0.130
0	298	8.728	8.502	-0.226
3	192	9.038	8.515	-0.523
3	193	9.078	8.529	-0.549
3	194	8.966	8.443	-0.523
3	196	9.070	8.527	-0.543
3	197	9.105	8.562	-0.543
5	200	9.019	8.533	-0.486
5	202	8.941	8.454	-0.487
5	203	9.038	8.490	-0.548
5	204	9.053	8.540	-0.513
5	206	9.030	8.512	-0.518
10	207	9.052	8.530	-0.522
10	209	9.088	8.536	-0.552
10	210	8.976	8.452	-0.524
10	211	8.976	8.432	-0.544
10	212	8.979	8.436	-0.543
15	214	9.003	8.451	-0.552
15	217	9.052	8.523	-0.529
15	218	9.013	8.535	-0.478
15	219	9.053	8.504	-0.549
15	222	8.905	8.402	-0.503
30	224	9.083	8.543	-0.540
30	225	9.078	8.555	-0.523
30	226	9.045	8.545	-0.500
30	227	9.042	8.541	-0.501
30	229	9.041	8.545	-0.496
35	230	8.911	8.466	-0.445
35	231	8.997	8.552	-0.445
35	232	8.993	8.498	-0.495
35	234	9.056	8.568	-0.488
35	237	9.003	8.489	-0.514
50	272	9.061	8.550	-0.511
50	275	9.072	8.530	-0.542
50	276	9.039	8.534	-0.505
50	277	8.983	8.515	-0.468
50	278	8.987	8.520	-0.467
55	279	8.935	8.524	-0.411
55	280	9.011	8.579	-0.432
55	281	8.967	8.513	-0.454
55	282	9.002	8.573	-0.429
55	283	9.071	8.621	-0.450
100	317	8.533	8.590	0.057
100	318	8.609	8.680	0.071
100	319	8.683	8.711	0.028
100	320	8.637	8.691	0.054
100	321	8.555	8.655	0.100
100	322	8.470	8.560	0.090
100	323	8.505	8.605	0.100
100	324	8.615	8.710	0.095
100	326	8.507	8.629	0.122
100	327	8.565	8.701	0.136
100	328	8.577	8.626	0.049
100	329	8.597	8.641	0.044
100	332	8.602	8.744	0.142
100	333	8.632	8.709	0.077
100	334	8.586	8.608	0.022
100	335	8.526	8.694	0.168
100	336	8.665	8.726	0.061
100	337	8.660	8.705	0.045
100	338	8.633	8.665	0.032
100	339	8.583	8.655	0.072
100	340	8.650	8.679	0.029
100	341	8.622	8.720	0.098
100	342	8.641	8.681	0.040
100	343	8.590	8.690	0.100
105	344	8.630	8.662	0.032
105	346	8.608	8.731	0.123
105	349	8.605	8.750	0.145
105	350	8.548	8.671	0.123
105	351	8.606	8.741	0.135
105	352	8.617	8.732	0.115
Max		9.105	8.750	0.168
Average		8.832	8.584	-0.248
Min		8.470	8.402	-0.552
Std Dev		0.215	0.092	0.290



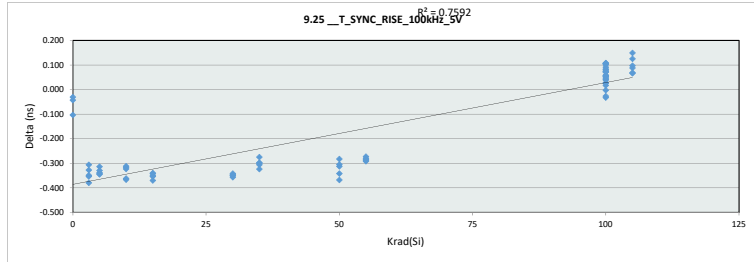
9.21 __T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.502	8.443	8.454	8.432	8.402	8.541	8.466	8.515	8.513	8.560	8.662
Average	8.539	8.515	8.506	8.477	8.483	8.546	8.515	8.530	8.562	8.670	8.715
Max	8.577	8.562	8.540	8.536	8.535	8.555	8.568	8.550	8.621	8.744	8.750
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

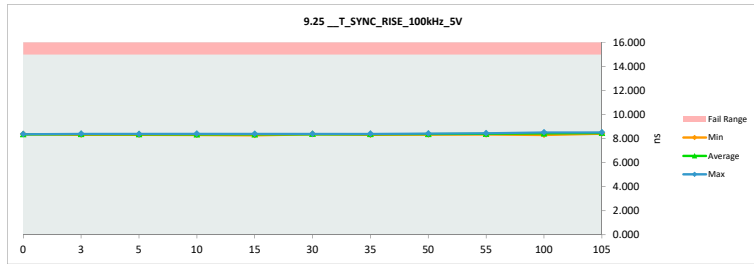


HDR TID Report  
TPS7H5004-SP

9.25 __T_SYNC_RISE_100kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns	ns		
Max Limit	15	15		
Min Limit				
Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.467	8.362	-0.105
0	297	8.396	8.352	-0.044
0	298	8.380	8.348	-0.032
3	192	8.717	8.388	-0.329
3	193	8.728	8.378	-0.350
3	194	8.668	8.313	-0.355
3	196	8.697	8.390	-0.307
3	197	8.748	8.367	-0.381
5	200	8.696	8.352	-0.344
5	202	8.627	8.312	-0.315
5	203	8.712	8.372	-0.340
5	204	8.713	8.367	-0.346
5	206	8.694	8.363	-0.331
10	207	8.696	8.383	-0.313
10	209	8.722	8.398	-0.324
10	210	8.655	8.337	-0.318
10	211	8.658	8.295	-0.363
10	212	8.673	8.305	-0.368
15	214	8.652	8.311	-0.341
15	217	8.726	8.355	-0.371
15	218	8.710	8.366	-0.344
15	219	8.727	8.375	-0.352
15	222	8.632	8.277	-0.355
30	224	8.730	8.381	-0.349
30	225	8.741	8.383	-0.358
30	226	8.705	8.353	-0.352
30	227	8.736	8.387	-0.349
30	229	8.719	8.376	-0.343
35	230	8.608	8.307	-0.301
35	231	8.672	8.396	-0.276
35	232	8.656	8.357	-0.299
35	234	8.704	8.397	-0.307
35	237	8.679	8.354	-0.325
50	272	8.703	8.397	-0.306
50	275	8.746	8.377	-0.369
50	276	8.685	8.401	-0.284
50	277	8.671	8.328	-0.343
50	278	8.668	8.354	-0.314
55	279	8.620	8.334	-0.286
55	280	8.670	8.389	-0.281
55	281	8.660	8.367	-0.293
55	282	8.661	8.387	-0.274
55	283	8.720	8.431	-0.289
100	317	8.327	8.299	-0.028
100	318	8.403	8.399	-0.004
100	319	8.387	8.491	0.104
100	320	8.406	8.480	0.074
100	321	8.497	8.463	-0.034
100	322	8.253	8.310	0.057
100	323	8.302	8.381	0.079
100	324	8.340	8.447	0.107
100	326	8.343	8.384	0.041
100	327	8.361	8.407	0.046
100	328	8.329	8.380	0.051
100	329	8.344	8.434	0.090
100	332	8.371	8.428	0.057
100	333	8.391	8.461	0.070
100	334	8.351	8.433	0.082
100	335	8.378	8.403	0.025
100	336	8.367	8.472	0.105
100	337	8.342	8.447	0.105
100	338	8.363	8.401	0.038
100	339	8.347	8.418	0.071
100	340	8.402	8.502	0.100
100	341	8.431	8.457	0.026
100	342	8.424	8.440	0.016
100	343	8.381	8.433	0.052
105	344	8.339	8.406	0.067
105	346	8.442	8.507	0.065
105	349	8.327	8.475	0.148
105	350	8.322	8.409	0.087
105	351	8.351	8.475	0.124
105	352	8.371	8.467	0.096
	Max	8.748	8.507	0.148
	Average	8.546	8.389	-0.156
	Min	8.253	8.277	-0.381
	Std Dev	0.165	0.053	0.196



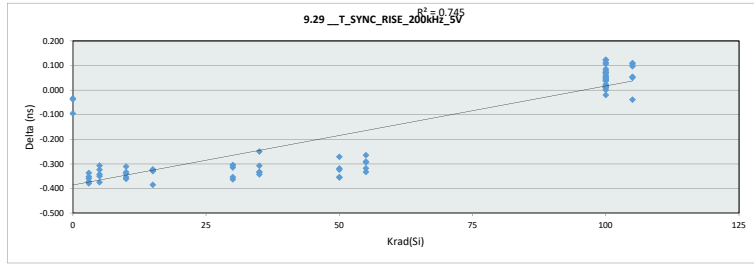
9.25 __T_SYNC_RI											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.348	8.313	8.312	8.295	8.277	8.353	8.307	8.328	8.334	8.299	8.406
Average	8.354	8.367	8.353	8.344	8.337	8.376	8.362	8.371	8.382	8.424	8.457
Max	8.362	8.390	8.372	8.398	8.375	8.387	8.397	8.401	8.431	8.502	8.507
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

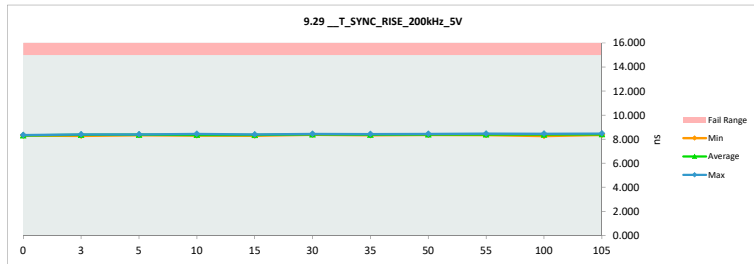
9.29 __T_SYNC_RISE_200kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.414	8.318	-0.096
0	297	8.370	8.331	-0.039
0	298	8.320	8.285	-0.035
3	192	8.723	8.385	-0.338
3	193	8.726	8.373	-0.353
3	194	8.630	8.269	-0.361
3	196	8.747	8.367	-0.380
3	197	8.730	8.357	-0.373
5	200	8.692	8.367	-0.325
5	202	8.628	8.320	-0.308
5	203	8.760	8.384	-0.376
5	204	8.737	8.386	-0.351
5	206	8.706	8.363	-0.343
10	207	8.736	8.381	-0.355
10	209	8.733	8.421	-0.312
10	210	8.647	8.312	-0.335
10	211	8.647	8.306	-0.341
10	212	8.654	8.292	-0.362
15	214	8.685	8.299	-0.386
15	217	8.693	8.364	-0.329
15	218	8.687	8.363	-0.324
15	219	8.708	8.379	-0.329
15	222	8.611	8.281	-0.330
30	224	8.736	8.381	-0.355
30	225	8.744	8.428	-0.316
30	226	8.670	8.363	-0.307
30	227	8.750	8.386	-0.364
30	229	8.702	8.396	-0.306
35	230	8.656	8.312	-0.344
35	231	8.664	8.413	-0.251
35	232	8.671	8.336	-0.335
35	234	8.710	8.401	-0.309
35	237	8.690	8.356	-0.334
50	272	8.746	8.391	-0.355
50	275	8.738	8.382	-0.356
50	276	8.699	8.379	-0.320
50	277	8.670	8.345	-0.325
50	278	8.693	8.421	-0.272
55	279	8.624	8.329	-0.295
55	280	8.659	8.393	-0.266
55	281	8.682	8.363	-0.319
55	282	8.717	8.383	-0.334
55	283	8.736	8.444	-0.292
100	317	8.271	8.250	-0.021
100	318	8.345	8.381	0.036
100	319	8.370	8.426	0.056
100	320	8.413	8.413	0.000
100	321	8.321	8.398	0.077
100	322	8.254	8.299	0.045
100	323	8.265	8.318	0.053
100	324	8.349	8.419	0.070
100	326	8.280	8.351	0.071
100	327	8.289	8.412	0.123
100	328	8.247	8.368	0.121
100	329	8.332	8.349	0.017
100	332	8.350	8.361	0.011
100	333	8.337	8.422	0.085
100	334	8.357	8.366	0.009
100	335	8.334	8.356	0.022
100	336	8.386	8.437	0.051
100	337	8.267	8.372	0.105
100	338	8.354	8.419	0.065
100	339	8.376	8.385	0.009
100	340	8.377	8.416	0.039
100	341	8.375	8.387	0.012
100	342	8.350	8.392	0.042
100	343	8.303	8.413	0.110
105	344	8.309	8.358	0.049
105	346	8.396	8.449	0.053
105	349	8.386	8.346	-0.040
105	350	8.269	8.365	0.096
105	351	8.336	8.445	0.109
105	352	8.353	8.458	0.105
	Max	8.760	8.458	0.123
	Average	8.533	8.370	-0.162
	Min	8.247	8.250	-0.386
	Std Dev	0.186	0.045	0.192



9.29 __T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15
Min Limit	ns

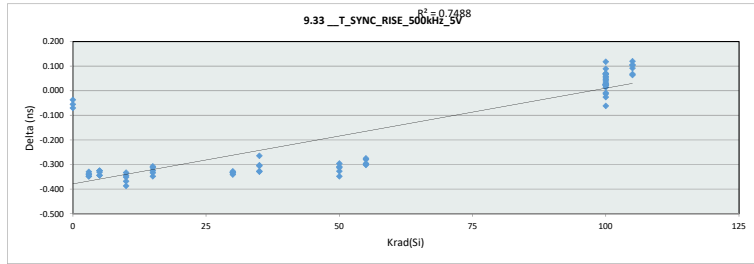
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.285	8.269	8.320	8.292	8.281	8.363	8.312	8.345	8.329	8.250	8.346
Average	8.311	8.350	8.364	8.342	8.337	8.391	8.364	8.384	8.382	8.380	8.404
Max	8.331	8.385	8.386	8.421	8.379	8.428	8.413	8.421	8.444	8.437	8.458
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

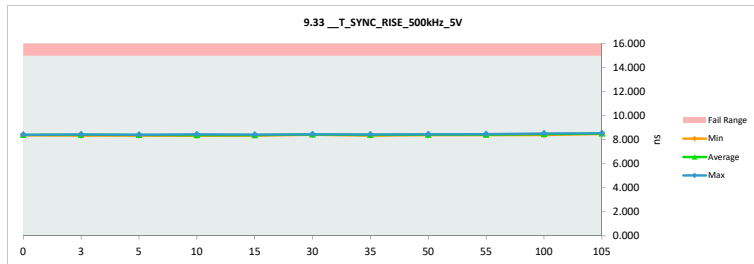
9.33 __T_SYNC_RISE_500kHz_5V				
Test Site				
Tester				
Test Number				
Unit	ns		ns	
Max Limit	15		15	
Min Limit				

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.445	8.407	-0.038
0	297	8.454	8.398	-0.056
0	298	8.437	8.366	-0.071
3	192	8.779	8.430	-0.349
3	193	8.749	8.408	-0.341
3	194	8.663	8.332	-0.331
3	196	8.768	8.419	-0.349
3	197	8.756	8.417	-0.339
5	200	8.718	8.386	-0.332
5	202	8.688	8.343	-0.345
5	203	8.754	8.409	-0.345
5	204	8.737	8.411	-0.326
5	206	8.734	8.408	-0.326
10	207	8.769	8.417	-0.352
10	209	8.774	8.440	-0.334
10	210	8.705	8.336	-0.369
10	211	8.716	8.328	-0.388
10	212	8.686	8.340	-0.346
15	214	8.687	8.338	-0.349
15	217	8.700	8.386	-0.314
15	218	8.739	8.413	-0.326
15	219	8.727	8.392	-0.335
15	222	8.648	8.339	-0.309
30	224	8.769	8.436	-0.333
30	225	8.776	8.441	-0.335
30	226	8.741	8.399	-0.342
30	227	8.781	8.447	-0.334
30	229	8.757	8.428	-0.329
35	230	8.650	8.343	-0.307
35	231	8.698	8.432	-0.266
35	232	8.699	8.395	-0.304
35	234	8.756	8.428	-0.328
35	237	8.713	8.383	-0.330
50	272	8.756	8.446	-0.310
50	275	8.771	8.422	-0.349
50	276	8.732	8.418	-0.314
50	277	8.701	8.373	-0.328
50	278	8.704	8.407	-0.297
55	279	8.661	8.380	-0.281
55	280	8.710	8.413	-0.297
55	281	8.713	8.413	-0.300
55	282	8.703	8.427	-0.276
55	283	8.763	8.460	-0.303
100	317	8.297	8.414	0.117
100	318	8.411	8.433	0.022
100	319	8.443	8.512	0.069
100	320	8.457	8.482	0.025
100	321	8.370	8.458	0.088
100	322	8.455	8.392	-0.063
100	323	8.348	8.387	0.039
100	324	8.423	8.480	0.057
100	326	8.384	8.407	0.023
100	327	8.412	8.432	0.020
100	328	8.387	8.417	0.030
100	329	8.402	8.447	0.045
100	332	8.419	8.488	0.069
100	333	8.429	8.496	0.067
100	334	8.405	8.456	0.051
100	335	8.420	8.443	0.023
100	336	8.473	8.486	0.013
100	337	8.496	8.486	-0.010
100	338	8.411	8.437	0.026
100	339	8.447	8.468	0.021
100	340	8.471	8.457	-0.014
100	341	8.522	8.495	-0.027
100	342	8.477	8.502	0.025
100	343	8.394	8.459	0.065
105	344	8.374	8.493	0.119
105	346	8.460	8.527	0.067
105	349	8.427	8.518	0.091
105	350	8.406	8.469	0.063
105	351	8.431	8.534	0.103
105	352	8.423	8.527	0.104
Max		8.781	8.534	0.119
Average		8.590	8.427	-0.163
Min		8.297	8.328	-0.388
Std Dev		0.156	0.050	0.185



9.33 __T_SYNC_RI				
Test Site				
Tester				
Test Number				
Max Limit	15		ns	
Min Limit			ns	

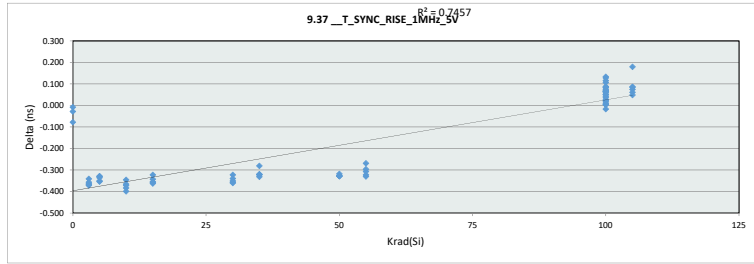
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.366	8.332	8.343	8.328	8.338	8.399	8.343	8.373	8.380	8.387	8.469
Average	8.390	8.401	8.391	8.372	8.374	8.430	8.396	8.413	8.419	8.456	8.511
Max	8.407	8.430	8.411	8.440	8.413	8.447	8.432	8.446	8.460	8.512	8.534
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

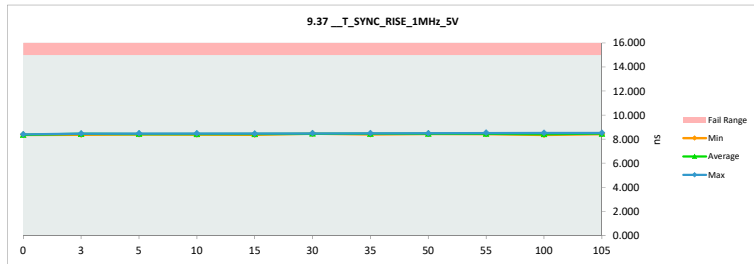
9.37_T_SYNC_RISE_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.402	8.393	-0.009
0	297	8.420	8.341	-0.079
0	298	8.383	8.354	-0.029
3	192	8.812	8.446	-0.366
3	193	8.824	8.451	-0.373
3	194	8.724	8.366	-0.358
3	196	8.799	8.457	-0.342
3	197	8.812	8.445	-0.367
5	200	8.782	8.451	-0.331
5	202	8.713	8.383	-0.330
5	203	8.790	8.438	-0.352
5	204	8.818	8.463	-0.355
5	206	8.778	8.441	-0.337
10	207	8.828	8.461	-0.367
10	209	8.819	8.472	-0.347
10	210	8.761	8.388	-0.373
10	211	8.772	8.372	-0.400
10	212	8.755	8.370	-0.385
15	214	8.740	8.376	-0.364
15	217	8.799	8.442	-0.357
15	218	8.768	8.444	-0.324
15	219	8.766	8.422	-0.344
15	222	8.719	8.362	-0.357
30	224	8.786	8.462	-0.324
30	225	8.837	8.479	-0.358
30	226	8.787	8.446	-0.341
30	227	8.821	8.459	-0.362
30	229	8.815	8.463	-0.352
35	230	8.709	8.387	-0.322
35	231	8.752	8.470	-0.282
35	232	8.765	8.432	-0.333
35	234	8.802	8.481	-0.321
35	237	8.759	8.435	-0.324
50	272	8.783	8.464	-0.319
50	275	8.798	8.468	-0.330
50	276	8.788	8.460	-0.328
50	277	8.756	8.427	-0.329
50	278	8.771	8.441	-0.330
55	279	8.697	8.427	-0.270
55	280	8.777	8.453	-0.324
55	281	8.750	8.419	-0.331
55	282	8.761	8.453	-0.308
55	283	8.820	8.522	-0.298
100	317	8.295	8.364	0.069
100	318	8.378	8.399	0.021
100	319	8.377	8.441	0.064
100	320	8.359	8.433	0.074
100	321	8.284	8.398	0.114
100	322	8.243	8.370	0.127
100	323	8.278	8.365	0.087
100	324	8.403	8.440	0.037
100	326	8.283	8.388	0.105
100	327	8.347	8.389	0.042
100	328	8.284	8.348	0.064
100	329	8.408	8.458	0.050
100	332	8.369	8.501	0.132
100	333	8.371	8.436	0.065
100	334	8.378	8.405	0.027
100	335	8.361	8.376	0.015
100	336	8.391	8.475	0.084
100	337	8.426	8.429	0.003
100	338	8.359	8.341	-0.018
100	339	8.356	8.358	0.002
100	340	8.384	8.434	0.050
100	341	8.406	8.488	0.082
100	342	8.372	8.432	0.060
100	343	8.397	8.404	0.007
105	344	8.373	8.433	0.060
105	346	8.411	8.484	0.073
105	349	8.326	8.504	0.178
105	350	8.326	8.411	0.085
105	351	8.373	8.457	0.084
105	352	8.420	8.467	0.047
Max		8.837	8.522	0.178
Average		8.590	8.428	-0.162
Min		8.243	8.341	-0.400
Std Dev		0.212	0.043	0.201



9.37_T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns

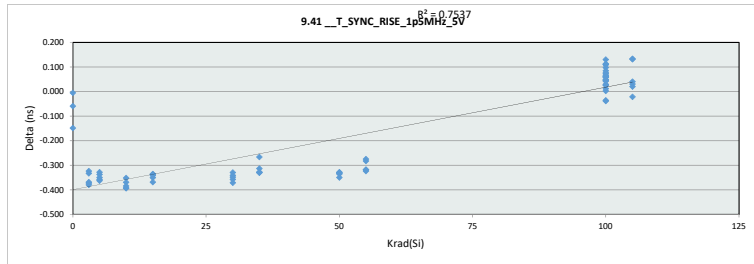
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.341	8.366	8.383	8.370	8.362	8.446	8.387	8.427	8.419	8.341	8.411
Average	8.363	8.433	8.435	8.413	8.409	8.462	8.441	8.452	8.455	8.411	8.459
Max	8.393	8.457	8.463	8.472	8.444	8.479	8.481	8.468	8.522	8.501	8.504
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

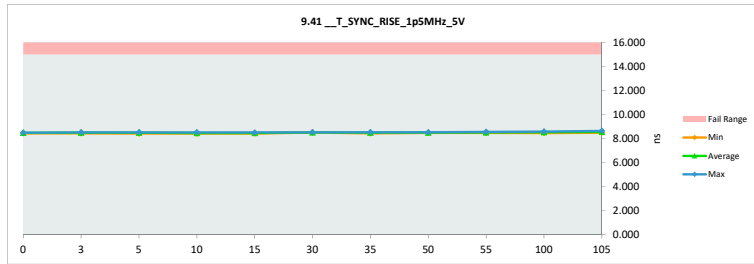
9.41 __T_SYNC_RISE_1p5MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.639	8.490	-0.149
0	297	8.490	8.484	-0.006
0	298	8.491	8.431	-0.060
3	192	8.875	8.506	-0.369
3	193	8.873	8.500	-0.373
3	194	8.756	8.432	-0.324
3	196	8.880	8.500	-0.380
3	197	8.849	8.516	-0.333
5	200	8.832	8.494	-0.338
5	202	8.776	8.413	-0.363
5	203	8.853	8.495	-0.358
5	204	8.847	8.518	-0.329
5	206	8.848	8.498	-0.350
10	207	8.878	8.508	-0.370
10	209	8.898	8.504	-0.394
10	210	8.793	8.441	-0.352
10	211	8.817	8.432	-0.385
10	212	8.806	8.413	-0.393
15	214	8.791	8.422	-0.369
15	217	8.825	8.483	-0.342
15	218	8.827	8.491	-0.336
15	219	8.835	8.484	-0.351
15	222	8.754	8.416	-0.338
30	224	8.877	8.505	-0.372
30	225	8.873	8.524	-0.349
30	226	8.852	8.494	-0.358
30	227	8.869	8.527	-0.342
30	229	8.841	8.511	-0.330
35	230	8.762	8.432	-0.330
35	231	8.785	8.518	-0.267
35	232	8.798	8.467	-0.331
35	234	8.837	8.523	-0.314
35	237	8.811	8.483	-0.328
50	272	8.850	8.514	-0.336
50	275	8.868	8.518	-0.350
50	276	8.838	8.509	-0.329
50	277	8.803	8.472	-0.331
50	278	8.821	8.488	-0.333
55	279	8.760	8.477	-0.283
55	280	8.799	8.524	-0.275
55	281	8.806	8.487	-0.319
55	282	8.816	8.492	-0.324
55	283	8.877	8.559	-0.318
100	317	8.430	8.475	0.045
100	318	8.508	8.519	0.011
100	319	8.486	8.562	0.076
100	320	8.515	8.562	0.047
100	321	8.459	8.461	0.002
100	322	8.391	8.521	0.130
100	323	8.427	8.524	0.097
100	324	8.457	8.541	0.084
100	326	8.465	8.527	0.062
100	327	8.488	8.548	0.060
100	328	8.399	8.471	0.072
100	329	8.491	8.516	0.025
100	332	8.504	8.551	0.047
100	333	8.516	8.575	0.059
100	334	8.440	8.549	0.109
100	335	8.469	8.579	0.110
100	336	8.475	8.540	0.065
100	337	8.408	8.520	0.112
100	338	8.578	8.539	-0.039
100	339	8.515	8.479	-0.036
100	340	8.524	8.553	0.029
100	341	8.528	8.585	0.057
100	342	8.518	8.544	0.026
100	343	8.485	8.527	0.042
105	344	8.461	8.491	0.030
105	346	8.565	8.585	0.020
105	349	8.502	8.633	0.131
105	350	8.464	8.504	0.040
105	351	8.450	8.583	0.133
105	352	8.550	8.528	-0.022
Max		8.898	8.633	0.133
Average		8.675	8.507	-0.168
Min		8.391	8.413	-0.394
Std Dev		0.177	0.044	0.198



9.41 __T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.431	8.432	8.413	8.413	8.416	8.494	8.432	8.472	8.477	8.461	8.491
Average	8.468	8.491	8.484	8.460	8.459	8.512	8.485	8.500	8.508	8.532	8.554
Max	8.490	8.516	8.518	8.508	8.491	8.527	8.523	8.518	8.559	8.585	8.633
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

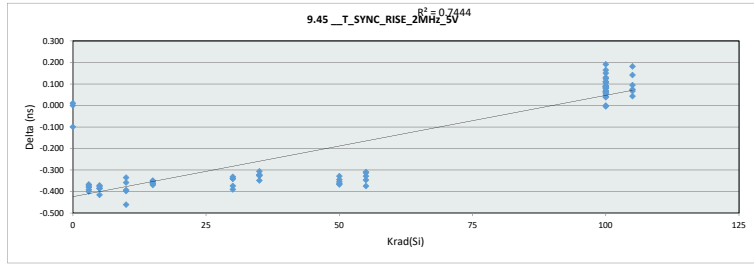




HDR TID Report  
TPS7H5004-SP

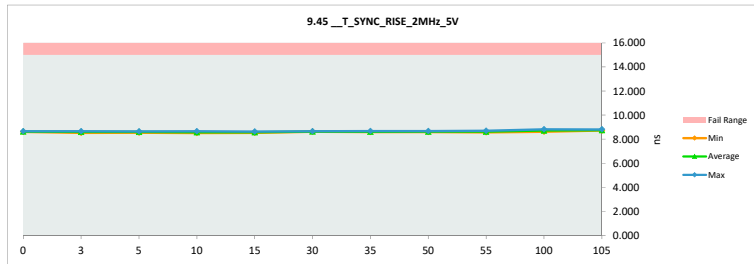
9.45 __T_SYNC_RISE_2MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.705	8.605	-0.100
0	297	8.646	8.656	0.010
0	298	8.641	8.641	0.000
3	192	9.045	8.653	-0.392
3	193	8.974	8.593	-0.381
3	194	8.918	8.518	-0.400
3	196	8.995	8.627	-0.368
3	197	8.987	8.612	-0.375
5	200	8.968	8.590	-0.378
5	202	8.948	8.532	-0.416
5	203	9.023	8.637	-0.386
5	204	8.979	8.593	-0.386
5	206	9.017	8.645	-0.372
10	207	8.988	8.652	-0.336
10	209	9.019	8.621	-0.398
10	210	8.930	8.571	-0.359
10	211	8.922	8.528	-0.394
10	212	8.964	8.503	-0.461
15	214	8.907	8.545	-0.362
15	217	8.996	8.626	-0.370
15	218	8.945	8.581	-0.364
15	219	8.976	8.624	-0.352
15	222	8.871	8.521	-0.350
30	224	8.963	8.631	-0.332
30	225	8.994	8.653	-0.341
30	226	8.961	8.620	-0.341
30	227	9.043	8.652	-0.391
30	229	8.993	8.619	-0.374
35	230	8.939	8.590	-0.349
35	231	8.970	8.663	-0.307
35	232	8.909	8.587	-0.322
35	234	8.962	8.635	-0.327
35	237	8.950	8.624	-0.326
50	272	8.985	8.617	-0.368
50	275	9.018	8.662	-0.356
50	276	9.027	8.663	-0.364
50	277	8.954	8.625	-0.329
50	278	8.936	8.591	-0.345
55	279	8.876	8.563	-0.313
55	280	8.973	8.644	-0.329
55	281	8.965	8.590	-0.375
55	282	8.999	8.652	-0.347
55	283	9.006	8.696	-0.310
100	317	8.624	8.619	-0.005
100	318	8.607	8.757	0.150
100	319	8.663	8.723	0.060
100	320	8.675	8.785	0.110
100	321	8.592	8.722	0.130
100	322	8.640	8.693	0.053
100	323	8.624	8.665	0.041
100	324	8.669	8.760	0.091
100	326	8.604	8.690	0.086
100	327	8.608	8.696	0.088
100	328	8.614	8.652	0.038
100	329	8.644	8.643	-0.001
100	332	8.621	8.812	0.191
100	333	8.650	8.728	0.078
100	334	8.605	8.686	0.081
100	335	8.670	8.737	0.067
100	336	8.686	8.752	0.066
100	337	8.680	8.768	0.088
100	338	8.630	8.740	0.110
100	339	8.634	8.758	0.124
100	340	8.692	8.775	0.083
100	341	8.663	8.767	0.104
100	342	8.578	8.742	0.164
100	343	8.622	8.687	0.065
105	344	8.645	8.719	0.074
105	346	8.646	8.713	0.067
105	349	8.667	8.808	0.141
105	350	8.639	8.733	0.094
105	351	8.567	8.748	0.181
105	352	8.726	8.769	0.043
Max		9.045	8.812	0.191
Average		8.822	8.659	-0.163
Min		8.567	8.503	-0.461
Std Dev		0.169	0.074	0.225



9.45 __T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15
Min Limit	ns

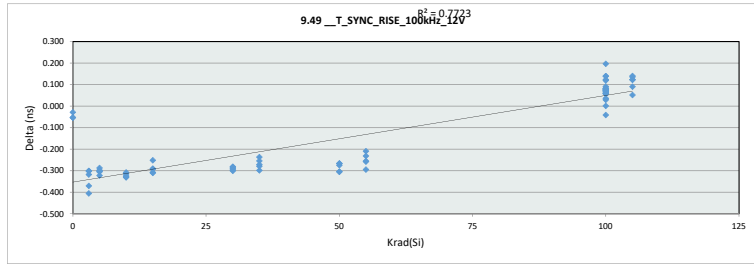
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.605	8.518	8.532	8.503	8.521	8.619	8.587	8.591	8.563	8.619	8.713
Average	8.634	8.601	8.599	8.575	8.579	8.635	8.620	8.632	8.629	8.723	8.748
Max	8.656	8.653	8.645	8.652	8.626	8.653	8.663	8.663	8.696	8.812	8.808
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

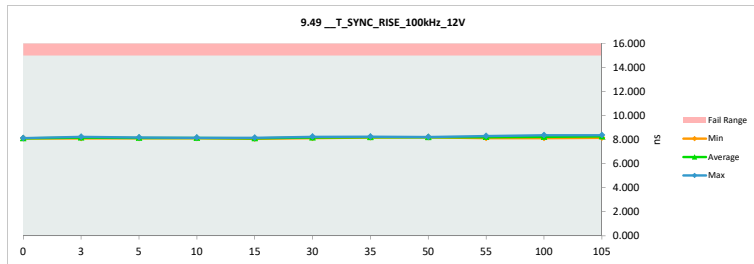
		9.49 __T_SYNC_RISE_100kHz_12V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.183	8.128	-0.055
0	297	8.187	8.133	-0.054
0	298	8.130	8.100	-0.030
3	192	8.519	8.200	-0.319
3	193	8.446	8.144	-0.302
3	194	8.482	8.110	-0.372
3	196	8.639	8.232	-0.407
3	197	8.497	8.193	-0.304
5	200	8.437	8.133	-0.304
5	202	8.406	8.117	-0.289
5	203	8.483	8.184	-0.299
5	204	8.489	8.165	-0.324
5	206	8.456	8.149	-0.307
10	207	8.479	8.163	-0.316
10	209	8.478	8.145	-0.333
10	210	8.449	8.119	-0.330
10	211	8.467	8.157	-0.310
10	212	8.453	8.127	-0.326
15	214	8.424	8.132	-0.292
15	217	8.421	8.111	-0.310
15	218	8.463	8.151	-0.312
15	219	8.366	8.113	-0.253
15	222	8.366	8.072	-0.294
30	224	8.435	8.133	-0.302
30	225	8.476	8.178	-0.298
30	226	8.534	8.243	-0.291
30	227	8.432	8.147	-0.285
30	229	8.498	8.215	-0.283
35	230	8.468	8.195	-0.273
35	231	8.484	8.246	-0.238
35	232	8.480	8.224	-0.256
35	234	8.485	8.185	-0.300
35	237	8.459	8.178	-0.281
50	272	8.479	8.211	-0.268
50	275	8.534	8.227	-0.307
50	276	8.513	8.207	-0.306
50	277	8.469	8.193	-0.276
50	278	8.465	8.197	-0.268
55	279	8.361	8.128	-0.233
55	280	8.603	8.307	-0.296
55	281	8.447	8.190	-0.257
55	282	8.452	8.241	-0.211
55	283	8.468	8.209	-0.259
100	317	8.098	8.133	0.035
100	318	8.152	8.219	0.067
100	319	8.290	8.247	-0.043
100	320	8.228	8.366	0.138
100	321	8.204	8.342	0.138
100	322	8.102	8.224	0.122
100	323	8.209	8.288	0.079
100	324	8.195	8.251	0.056
100	326	8.075	8.193	0.118
100	327	8.209	8.270	0.061
100	328	8.118	8.190	0.072
100	329	8.130	8.186	0.056
100	332	8.174	8.232	0.058
100	333	8.159	8.241	0.082
100	334	8.161	8.240	0.079
100	335	8.082	8.155	0.073
100	336	8.206	8.296	0.090
100	337	8.123	8.151	0.028
100	338	8.129	8.205	0.076
100	339	8.075	8.143	0.068
100	340	8.148	8.343	0.195
100	341	8.208	8.286	0.078
100	342	8.195	8.267	0.072
100	343	8.189	8.189	0.000
105	344	8.160	8.249	0.089
105	346	8.202	8.324	0.122
105	349	8.173	8.312	0.139
105	350	8.127	8.248	0.121
105	351	8.233	8.367	0.134
105	352	8.111	8.161	0.050
Max		8.639	8.367	0.195
Average		8.331	8.201	-0.130
Min		8.075	8.072	-0.407
Std Dev		0.162	0.067	0.189



		9.49 __T_SYNC_RI	
Test Site			
Tester			
Test Number			
Max Limit	15	ns	
Min Limit		ns	

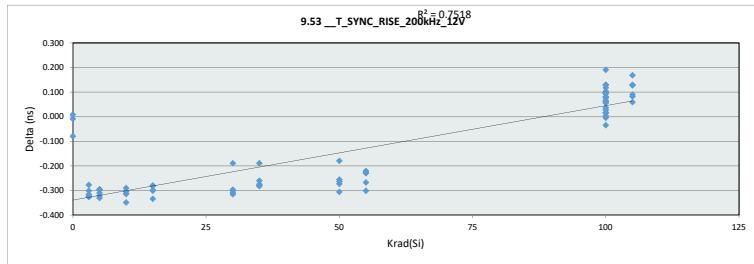
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.100	8.110	8.117	8.119	8.072	8.133	8.178	8.193	8.128	8.133	8.161
Average	8.120	8.176	8.150	8.142	8.116	8.183	8.206	8.207	8.215	8.236	8.277
Max	8.133	8.232	8.184	8.163	8.151	8.243	8.246	8.227	8.307	8.366	8.367
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

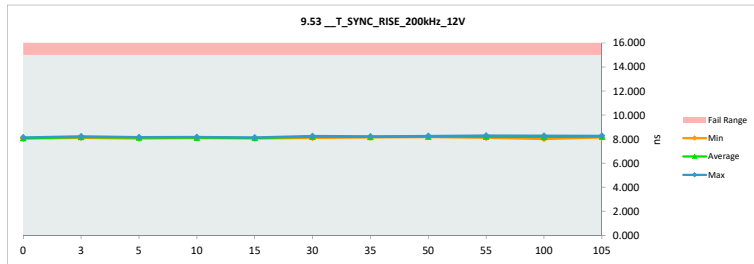
		9.53 __T_SYNC_RISE_200kHz_12V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.135	8.143	0.008
0	297	8.127	8.047	-0.080
0	298	8.055	8.046	-0.009
3	192	8.484	8.167	-0.317
3	193	8.424	8.146	-0.278
3	194	8.426	8.102	-0.324
3	196	8.556	8.229	-0.327
3	197	8.504	8.202	-0.302
5	200	8.424	8.114	-0.310
5	202	8.376	8.045	-0.331
5	203	8.481	8.159	-0.322
5	204	8.460	8.165	-0.295
5	206	8.455	8.158	-0.297
10	207	8.465	8.174	-0.291
10	209	8.418	8.104	-0.314
10	210	8.429	8.126	-0.303
10	211	8.447	8.134	-0.313
10	212	8.440	8.091	-0.349
15	214	8.424	8.123	-0.301
15	217	8.384	8.104	-0.280
15	218	8.433	8.134	-0.299
15	219	8.388	8.106	-0.282
15	222	8.374	8.040	-0.334
30	224	8.397	8.100	-0.297
30	225	8.445	8.255	-0.190
30	226	8.540	8.230	-0.310
30	227	8.449	8.133	-0.316
30	229	8.517	8.215	-0.302
35	230	8.398	8.137	-0.261
35	231	8.421	8.231	-0.190
35	232	8.483	8.200	-0.283
35	234	8.460	8.185	-0.275
35	237	8.424	8.145	-0.279
50	272	8.445	8.181	-0.264
50	275	8.529	8.223	-0.306
50	276	8.452	8.178	-0.274
50	277	8.432	8.176	-0.256
50	278	8.441	8.261	-0.180
55	279	8.336	8.106	-0.230
55	280	8.519	8.290	-0.229
55	281	8.444	8.142	-0.302
55	282	8.476	8.255	-0.221
55	283	8.456	8.188	-0.268
100	317	8.050	8.015	-0.035
100	318	8.121	8.214	0.093
100	319	8.123	8.252	0.129
100	320	8.157	8.222	0.065
100	321	8.136	8.232	0.096
100	322	8.093	8.188	0.095
100	323	8.094	8.284	0.190
100	324	8.066	8.165	0.099
100	326	8.013	8.116	0.103
100	327	8.132	8.212	0.080
100	328	8.094	8.109	0.015
100	329	8.065	8.145	0.080
100	332	8.126	8.187	0.061
100	333	8.185	8.212	0.027
100	334	8.234	8.229	-0.005
100	335	8.061	8.076	0.015
100	336	8.146	8.206	0.060
100	337	8.065	8.120	0.055
100	338	8.123	8.159	0.036
100	339	8.034	8.110	0.076
100	340	8.075	8.204	0.129
100	341	8.199	8.202	0.003
100	342	8.126	8.243	0.117
100	343	8.142	8.157	0.015
105	344	8.116	8.175	0.059
105	346	8.166	8.248	0.082
105	349	8.124	8.253	0.129
105	350	8.033	8.201	0.168
105	351	8.148	8.275	0.127
105	352	8.051	8.140	0.089
	Max	8.556	8.290	0.190
	Average	8.294	8.168	-0.126
	Min	8.013	8.015	-0.349
	Std Dev	0.176	0.063	0.183



		9.53 __T_SYNC_RI	
Test Site			
Tester			
Test Number			
Max Limit	15	ns	
Min Limit		ns	

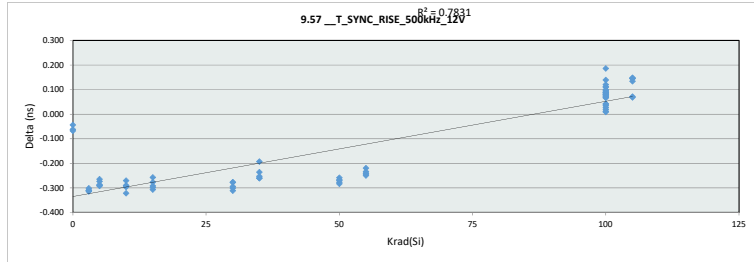
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.046	8.102	8.045	8.091	8.040	8.100	8.137	8.176	8.106	8.015	8.140
Average	8.079	8.169	8.128	8.126	8.101	8.187	8.180	8.204	8.196	8.177	8.215
Max	8.143	8.229	8.165	8.174	8.134	8.255	8.231	8.261	8.290	8.284	8.275
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

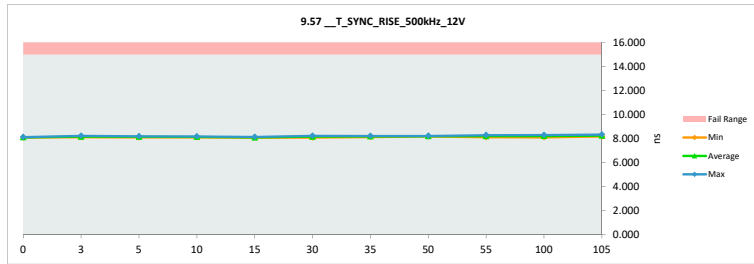
9.57 __T_SYNC_RISE_500kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.160	8.115	-0.045
0	297	8.139	8.076	-0.063
0	298	8.162	8.093	-0.069
3	192	8.493	8.181	-0.312
3	193	8.422	8.111	-0.311
3	194	8.424	8.111	-0.313
3	196	8.537	8.221	-0.316
3	197	8.473	8.170	-0.303
5	200	8.416	8.123	-0.293
5	202	8.360	8.094	-0.266
5	203	8.456	8.164	-0.292
5	204	8.469	8.181	-0.288
5	206	8.426	8.151	-0.275
10	207	8.440	8.168	-0.272
10	209	8.452	8.160	-0.292
10	210	8.405	8.113	-0.292
10	211	8.452	8.154	-0.298
10	212	8.418	8.095	-0.323
15	214	8.414	8.106	-0.308
15	217	8.369	8.091	-0.278
15	218	8.421	8.128	-0.293
15	219	8.370	8.071	-0.299
15	222	8.327	8.068	-0.259
30	224	8.394	8.081	-0.313
30	225	8.451	8.155	-0.296
30	226	8.523	8.221	-0.302
30	227	8.414	8.136	-0.278
30	229	8.486	8.207	-0.279
35	230	8.364	8.127	-0.237
35	231	8.406	8.212	-0.194
35	232	8.468	8.214	-0.254
35	234	8.445	8.183	-0.262
35	237	8.404	8.143	-0.261
50	272	8.480	8.195	-0.285
50	275	8.487	8.217	-0.270
50	276	8.456	8.196	-0.260
50	277	8.442	8.173	-0.269
50	278	8.461	8.183	-0.278
55	279	8.325	8.104	-0.221
55	280	8.532	8.287	-0.245
55	281	8.423	8.172	-0.251
55	282	8.453	8.218	-0.235
55	283	8.432	8.190	-0.242
100	317	8.098	8.108	0.010
100	318	8.109	8.191	0.082
100	319	8.190	8.224	0.034
100	320	8.208	8.273	0.065
100	321	8.199	8.288	0.089
100	322	8.109	8.197	0.088
100	323	8.127	8.213	0.086
100	324	8.124	8.201	0.077
100	326	8.072	8.257	0.185
100	327	8.139	8.248	0.109
100	328	8.088	8.207	0.119
100	329	8.111	8.221	0.110
100	332	8.145	8.223	0.078
100	333	8.188	8.257	0.069
100	334	8.137	8.206	0.069
100	335	8.125	8.133	0.008
100	336	8.134	8.232	0.098
100	337	8.058	8.152	0.094
100	338	8.121	8.198	0.077
100	339	8.057	8.097	0.040
100	340	8.044	8.182	0.138
100	341	8.208	8.234	0.026
100	342	8.191	8.209	0.018
100	343	8.153	8.193	0.040
105	344	8.150	8.220	0.070
105	346	8.179	8.323	0.144
105	349	8.193	8.260	0.067
105	350	8.026	8.173	0.147
105	351	8.189	8.322	0.133
105	352	8.067	8.211	0.144
Max		8.537	8.323	0.185
Average		8.298	8.178	-0.120
Min		8.026	8.068	-0.323
Std Dev		0.159	0.060	0.180



9.57 __T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns

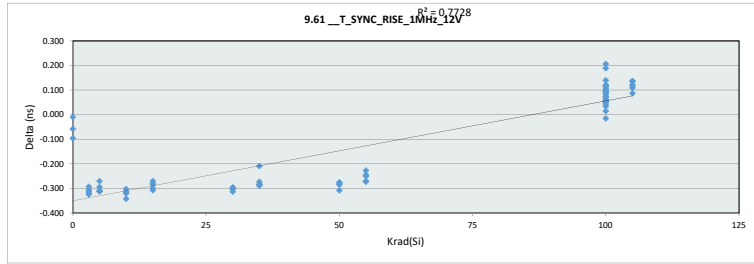
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.076	8.111	8.094	8.095	8.068	8.081	8.127	8.173	8.104	8.097	8.173
Average	8.095	8.159	8.143	8.138	8.093	8.160	8.176	8.193	8.194	8.206	8.252
Max	8.115	8.221	8.181	8.168	8.128	8.221	8.214	8.217	8.287	8.288	8.323
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

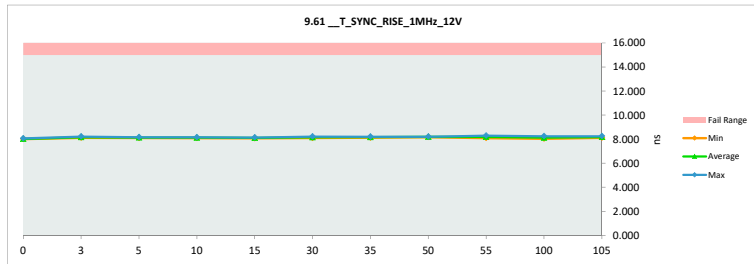
9.61 __ T_SYNC_RISE_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.110	8.051	-0.059
0	297	8.093	7.996	-0.097
0	298	8.009	7.998	-0.011
3	192	8.490	8.184	-0.306
3	193	8.414	8.120	-0.294
3	194	8.428	8.103	-0.325
3	196	8.531	8.211	-0.320
3	197	8.489	8.177	-0.312
5	200	8.405	8.134	-0.271
5	202	8.381	8.086	-0.295
5	203	8.471	8.159	-0.312
5	204	8.465	8.155	-0.310
5	206	8.432	8.119	-0.313
10	207	8.457	8.153	-0.304
10	209	8.450	8.135	-0.315
10	210	8.427	8.110	-0.317
10	211	8.456	8.135	-0.321
10	212	8.424	8.081	-0.343
15	214	8.412	8.104	-0.308
15	217	8.372	8.101	-0.271
15	218	8.430	8.130	-0.300
15	219	8.378	8.094	-0.284
15	222	8.339	8.058	-0.281
30	224	8.397	8.083	-0.314
30	225	8.458	8.154	-0.304
30	226	8.510	8.212	-0.298
30	227	8.406	8.109	-0.297
30	229	8.499	8.200	-0.299
35	230	8.385	8.111	-0.274
35	231	8.409	8.199	-0.210
35	232	8.478	8.194	-0.284
35	234	8.457	8.173	-0.284
35	237	8.430	8.140	-0.290
50	272	8.472	8.195	-0.277
50	275	8.509	8.200	-0.309
50	276	8.472	8.193	-0.279
50	277	8.434	8.157	-0.277
50	278	8.466	8.180	-0.286
55	279	8.331	8.080	-0.251
55	280	8.513	8.285	-0.228
55	281	8.440	8.169	-0.271
55	282	8.447	8.201	-0.246
55	283	8.448	8.174	-0.274
100	317	7.933	8.024	0.091
100	318	8.038	8.092	0.054
100	319	8.109	8.142	0.033
100	320	8.130	8.114	-0.016
100	321	8.095	8.207	0.112
100	322	8.054	8.145	0.091
100	323	8.015	8.154	0.139
100	324	8.005	8.124	0.119
100	326	7.945	8.028	0.083
100	327	8.082	8.155	0.073
100	328	8.059	8.101	0.042
100	329	7.941	8.129	0.188
100	332	8.077	8.138	0.061
100	333	8.062	8.160	0.098
100	334	8.049	8.154	0.105
100	335	7.980	8.035	0.055
100	336	8.031	8.236	0.205
100	337	7.964	8.083	0.119
100	338	8.053	8.101	0.048
100	339	7.989	8.079	0.090
100	340	8.045	8.115	0.070
100	341	8.122	8.136	0.014
100	342	8.094	8.146	0.052
100	343	8.066	8.139	0.073
105	344	8.057	8.177	0.120
105	346	8.103	8.237	0.134
105	349	8.097	8.183	0.086
105	350	7.979	8.115	0.136
105	351	8.100	8.217	0.117
105	352	8.000	8.108	0.108
	Max	8.531	8.285	0.205
	Average	8.262	8.137	-0.125
	Min	7.933	7.996	-0.343
	Std Dev	0.204	0.056	0.190



9.61 __ T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15
Min Limit	ns

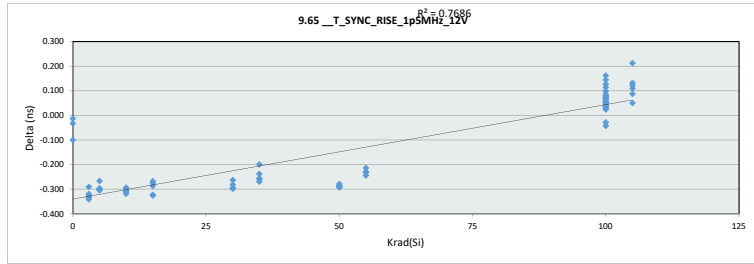
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.996	8.103	8.086	8.081	8.058	8.083	8.111	8.157	8.080	8.024	8.108
Average	8.015	8.159	8.131	8.123	8.097	8.152	8.163	8.185	8.182	8.122	8.173
Max	8.051	8.211	8.159	8.153	8.130	8.212	8.199	8.200	8.285	8.236	8.237
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

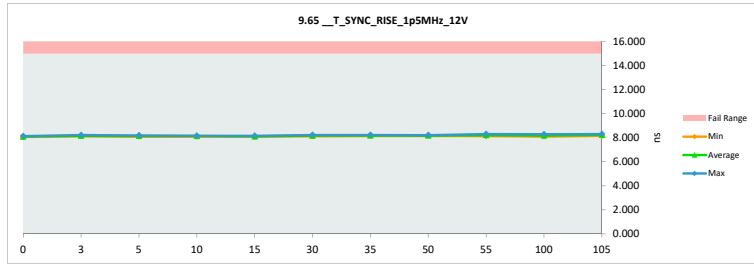
		9.65 __T_SYNC_RISE_1p5MHz_12V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.171	8.070	-0.101
0	297	8.145	8.131	-0.014
0	298	8.088	8.054	-0.034
3	192	8.500	8.165	-0.335
3	193	8.409	8.117	-0.292
3	194	8.431	8.110	-0.321
3	196	8.555	8.212	-0.343
3	197	8.496	8.165	-0.331
5	200	8.425	8.121	-0.304
5	202	8.394	8.088	-0.306
5	203	8.450	8.182	-0.268
5	204	8.472	8.172	-0.300
5	206	8.436	8.138	-0.298
10	207	8.448	8.153	-0.295
10	209	8.464	8.152	-0.312
10	210	8.417	8.120	-0.297
10	211	8.451	8.143	-0.308
10	212	8.409	8.089	-0.320
15	214	8.422	8.095	-0.327
15	217	8.368	8.099	-0.269
15	218	8.426	8.149	-0.277
15	219	8.395	8.071	-0.324
15	222	8.352	8.064	-0.288
30	224	8.399	8.116	-0.283
30	225	8.464	8.168	-0.296
30	226	8.518	8.220	-0.298
30	227	8.436	8.137	-0.299
30	229	8.480	8.216	-0.264
35	230	8.391	8.130	-0.261
35	231	8.416	8.215	-0.201
35	232	8.471	8.214	-0.257
35	234	8.448	8.177	-0.271
35	237	8.414	8.175	-0.239
50	272	8.477	8.191	-0.286
50	275	8.499	8.208	-0.291
50	276	8.468	8.188	-0.280
50	277	8.457	8.163	-0.294
50	278	8.459	8.166	-0.293
55	279	8.341	8.109	-0.232
55	280	8.533	8.299	-0.234
55	281	8.440	8.194	-0.246
55	282	8.440	8.225	-0.215
55	283	8.431	8.200	-0.231
100	317	8.032	8.088	0.056
100	318	8.122	8.172	0.050
100	319	8.169	8.192	0.023
100	320	8.158	8.255	0.097
100	321	8.166	8.279	0.113
100	322	8.162	8.118	-0.044
100	323	8.198	8.168	-0.030
100	324	8.096	8.173	0.077
100	326	8.085	8.138	0.053
100	327	8.180	8.221	0.041
100	328	8.035	8.160	0.125
100	329	8.041	8.184	0.143
100	332	8.072	8.233	0.161
100	333	8.187	8.220	0.033
100	334	8.097	8.151	0.054
100	335	8.004	8.088	0.084
100	336	8.157	8.225	0.068
100	337	8.043	8.121	0.078
100	338	8.114	8.178	0.064
100	339	8.080	8.147	0.067
100	340	8.079	8.150	0.071
100	341	8.155	8.191	0.036
100	342	8.154	8.182	0.028
100	343	8.152	8.201	0.049
105	344	8.069	8.200	0.131
105	346	8.146	8.267	0.121
105	349	8.144	8.253	0.109
105	350	8.050	8.261	0.211
105	351	8.215	8.301	0.086
105	352	8.097	8.146	0.049
	Max	8.555	8.301	0.211
	Average	8.295	8.168	-0.128
	Min	8.004	8.054	-0.343
	Std Dev	0.171	0.056	0.180



		9.65 __T_SYNC_RI	
Test Site			
Tester			
Test Number			
Max Limit	15	ns	
Min Limit		ns	

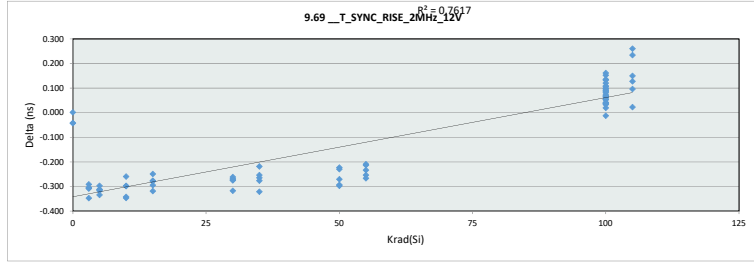
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.054	8.110	8.088	8.089	8.064	8.116	8.130	8.163	8.109	8.088	8.146
Average	8.085	8.154	8.140	8.131	8.096	8.171	8.182	8.183	8.205	8.176	8.238
Max	8.131	8.212	8.182	8.153	8.149	8.220	8.215	8.208	8.299	8.279	8.301
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

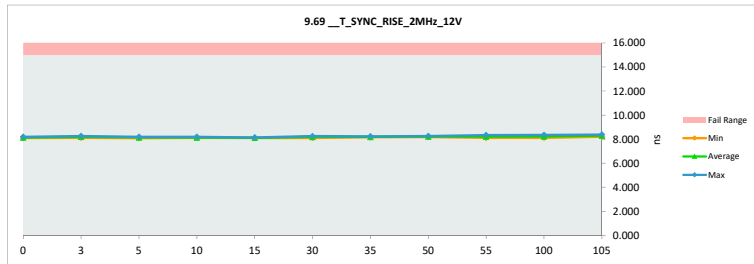
9.69 __T_SYNC_RISE_2MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.161	8.118	-0.043
0	297	8.181	8.182	0.001
0	298	8.133	8.090	-0.043
3	192	8.528	8.219	-0.309
3	193	8.411	8.104	-0.307
3	194	8.434	8.129	-0.305
3	196	8.596	8.248	-0.348
3	197	8.500	8.208	-0.292
5	200	8.420	8.106	-0.314
5	202	8.410	8.075	-0.335
5	203	8.491	8.179	-0.312
5	204	8.467	8.149	-0.318
5	206	8.486	8.188	-0.298
10	207	8.448	8.188	-0.260
10	209	8.444	8.145	-0.299
10	210	8.416	8.118	-0.298
10	211	8.467	8.120	-0.347
10	212	8.443	8.100	-0.343
15	214	8.410	8.131	-0.279
15	217	8.411	8.116	-0.295
15	218	8.434	8.115	-0.319
15	219	8.379	8.102	-0.277
15	222	8.337	8.087	-0.250
30	224	8.374	8.098	-0.276
30	225	8.455	8.186	-0.269
30	226	8.519	8.245	-0.274
30	227	8.449	8.131	-0.318
30	229	8.503	8.241	-0.262
35	230	8.435	8.170	-0.265
35	231	8.443	8.224	-0.219
35	232	8.476	8.199	-0.277
35	234	8.481	8.159	-0.322
35	237	8.406	8.152	-0.254
50	272	8.473	8.175	-0.298
50	275	8.503	8.232	-0.271
50	276	8.478	8.254	-0.224
50	277	8.445	8.215	-0.230
50	278	8.460	8.166	-0.294
55	279	8.314	8.100	-0.214
55	280	8.542	8.332	-0.210
55	281	8.443	8.176	-0.267
55	282	8.486	8.232	-0.254
55	283	8.435	8.201	-0.234
100	317	8.072	8.105	0.033
100	318	8.146	8.244	0.098
100	319	8.181	8.246	0.065
100	320	8.214	8.252	0.038
100	321	8.194	8.346	0.152
100	322	8.139	8.211	0.072
100	323	8.212	8.308	0.096
100	324	8.124	8.229	0.105
100	326	8.110	8.245	0.135
100	327	8.181	8.249	0.068
100	328	8.130	8.217	0.087
100	329	8.120	8.209	0.089
100	332	8.132	8.240	0.108
100	333	8.201	8.220	0.019
100	334	8.142	8.262	0.120
100	335	8.104	8.141	0.037
100	336	8.235	8.289	0.054
100	337	8.090	8.150	0.060
100	338	8.137	8.189	0.052
100	339	8.090	8.251	0.161
100	340	8.074	8.206	0.132
100	341	8.180	8.261	0.081
100	342	8.194	8.235	0.041
100	343	8.179	8.166	-0.013
105	344	8.204	8.226	0.022
105	346	8.116	8.376	0.260
105	349	8.222	8.349	0.127
105	350	8.120	8.269	0.149
105	351	8.066	8.300	0.234
105	352	8.102	8.198	0.096
Max		8.596	8.376	0.260
Average		8.314	8.196	-0.118
Min		8.066	8.075	-0.348
Std Dev		0.160	0.069	0.190



9.69 __T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns

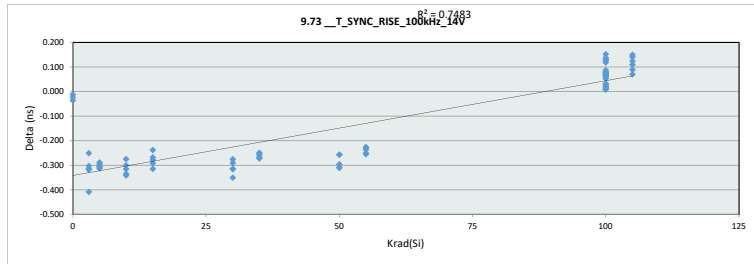
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.090	8.104	8.075	8.100	8.087	8.098	8.152	8.166	8.100	8.105	8.198
Average	8.130	8.182	8.139	8.134	8.110	8.180	8.181	8.208	8.208	8.228	8.286
Max	8.182	8.248	8.188	8.188	8.131	8.245	8.224	8.254	8.332	8.346	8.376
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

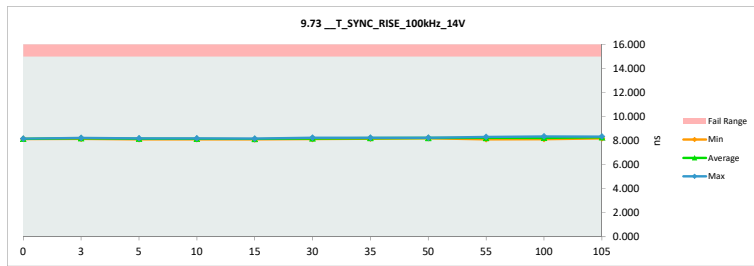
9.73 __T_SYNC_RISE_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.178	8.142	-0.036
0	297	8.183	8.159	-0.024
0	298	8.137	8.125	-0.012
3	192	8.505	8.187	-0.318
3	193	8.459	8.145	-0.314
3	194	8.451	8.200	-0.251
3	196	8.635	8.226	-0.409
3	197	8.499	8.196	-0.303
5	200	8.452	8.163	-0.289
5	202	8.408	8.102	-0.306
5	203	8.487	8.179	-0.308
5	204	8.493	8.180	-0.313
5	206	8.467	8.171	-0.296
10	207	8.455	8.180	-0.275
10	209	8.472	8.156	-0.316
10	210	8.453	8.118	-0.335
10	211	8.474	8.173	-0.301
10	212	8.446	8.104	-0.342
15	214	8.433	8.118	-0.315
15	217	8.389	8.121	-0.268
15	218	8.439	8.161	-0.278
15	219	8.407	8.115	-0.292
15	222	8.330	8.092	-0.238
30	224	8.472	8.121	-0.351
30	225	8.456	8.180	-0.276
30	226	8.554	8.238	-0.316
30	227	8.467	8.152	-0.315
30	229	8.506	8.215	-0.291
35	230	8.411	8.158	-0.253
35	231	8.479	8.229	-0.250
35	232	8.495	8.235	-0.260
35	234	8.476	8.207	-0.269
35	237	8.464	8.191	-0.273
50	272	8.514	8.203	-0.311
50	275	8.541	8.232	-0.309
50	276	8.494	8.236	-0.258
50	277	8.486	8.190	-0.296
50	278	8.466	8.209	-0.257
55	279	8.349	8.098	-0.251
55	280	8.531	8.302	-0.229
55	281	8.431	8.204	-0.227
55	282	8.468	8.231	-0.237
55	283	8.468	8.213	-0.255
100	317	8.098	8.108	0.010
100	318	8.210	8.219	0.009
100	319	8.234	8.287	0.053
100	320	8.232	8.298	0.066
100	321	8.238	8.316	0.078
100	322	8.112	8.177	0.065
100	323	8.201	8.270	0.069
100	324	8.168	8.200	0.032
100	326	8.083	8.170	0.087
100	327	8.174	8.228	0.054
100	328	8.108	8.226	0.118
100	329	8.122	8.198	0.076
100	332	8.150	8.302	0.152
100	333	8.169	8.300	0.131
100	334	8.158	8.233	0.075
100	335	8.093	8.120	0.027
100	336	8.203	8.340	0.137
100	337	8.078	8.204	0.126
100	338	8.141	8.203	0.062
100	339	8.095	8.110	0.015
100	340	8.131	8.152	0.021
100	341	8.196	8.275	0.079
100	342	8.195	8.203	0.008
100	343	8.169	8.233	0.064
105	344	8.166	8.306	0.140
105	346	8.199	8.322	0.123
105	349	8.182	8.271	0.089
105	350	8.106	8.255	0.149
105	351	8.209	8.318	0.109
105	352	8.113	8.183	0.070
	Max	8.635	8.340	0.152
	Average	8.328	8.200	-0.128
	Min	8.078	8.092	-0.409
	Std Dev	0.162	0.062	0.184



9.73 __T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.125	8.145	8.102	8.104	8.092	8.121	8.158	8.190	8.098	8.108	8.183
Average	8.142	8.191	8.159	8.146	8.121	8.181	8.204	8.214	8.210	8.224	8.276
Max	8.159	8.226	8.180	8.180	8.161	8.238	8.235	8.236	8.302	8.340	8.322
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

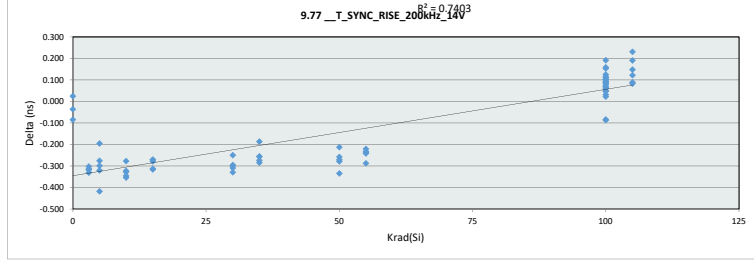




HDR TID Report  
TPS7H5004-SP

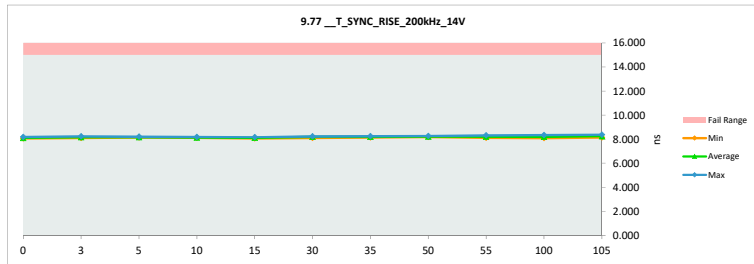
9.77 __T_SYNC_RISE_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.150	8.174	0.024
0	297	8.137	8.052	-0.085
0	298	8.110	8.073	-0.037
3	192	8.478	8.175	-0.303
3	193	8.440	8.124	-0.316
3	194	8.413	8.098	-0.315
3	196	8.553	8.222	-0.331
3	197	8.506	8.190	-0.316
5	200	8.416	8.140	-0.276
5	202	8.384	8.188	-0.196
5	203	8.613	8.195	-0.418
5	204	8.466	8.167	-0.299
5	206	8.465	8.144	-0.321
10	207	8.458	8.180	-0.278
10	209	8.474	8.120	-0.354
10	210	8.468	8.122	-0.346
10	211	8.470	8.142	-0.328
10	212	8.433	8.110	-0.323
15	214	8.437	8.121	-0.316
15	217	8.385	8.113	-0.272
15	218	8.434	8.155	-0.279
15	219	8.396	8.125	-0.271
15	222	8.371	8.058	-0.313
30	224	8.397	8.101	-0.296
30	225	8.477	8.227	-0.250
30	226	8.536	8.226	-0.310
30	227	8.464	8.134	-0.330
30	229	8.528	8.229	-0.299
35	230	8.392	8.135	-0.257
35	231	8.425	8.238	-0.187
35	232	8.484	8.210	-0.274
35	234	8.468	8.183	-0.285
35	237	8.432	8.176	-0.256
50	272	8.536	8.201	-0.335
50	275	8.502	8.223	-0.279
50	276	8.479	8.221	-0.258
50	277	8.448	8.176	-0.272
50	278	8.472	8.259	-0.213
55	279	8.347	8.114	-0.233
55	280	8.535	8.314	-0.221
55	281	8.439	8.151	-0.288
55	282	8.483	8.244	-0.239
55	283	8.449	8.207	-0.242
100	317	8.036	8.083	0.047
100	318	8.097	8.176	0.079
100	319	8.147	8.178	0.031
100	320	8.205	8.227	0.022
100	321	8.197	8.288	0.091
100	322	8.062	8.215	0.153
100	323	8.132	8.200	0.068
100	324	8.098	8.210	0.112
100	326	8.024	8.149	0.125
100	327	8.108	8.210	0.102
100	328	8.234	8.147	-0.087
100	329	8.110	8.157	0.047
100	332	8.132	8.192	0.060
100	333	8.148	8.262	0.114
100	334	8.225	8.141	-0.084
100	335	8.034	8.102	0.068
100	336	8.157	8.251	0.094
100	337	8.026	8.112	0.086
100	338	8.074	8.177	0.103
100	339	8.046	8.115	0.069
100	340	8.081	8.239	0.158
100	341	8.133	8.220	0.087
100	342	8.143	8.334	0.191
100	343	8.132	8.181	0.049
105	344	8.128	8.210	0.082
105	346	8.115	8.263	0.148
105	349	8.154	8.276	0.122
105	350	8.037	8.227	0.190
105	351	8.137	8.367	0.230
105	352	8.052	8.140	0.088
Max		8.613	8.367	0.230
Average		8.303	8.181	-0.123
Min		8.024	8.052	-0.418
Std Dev		0.181	0.063	0.192



9.77 __T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns

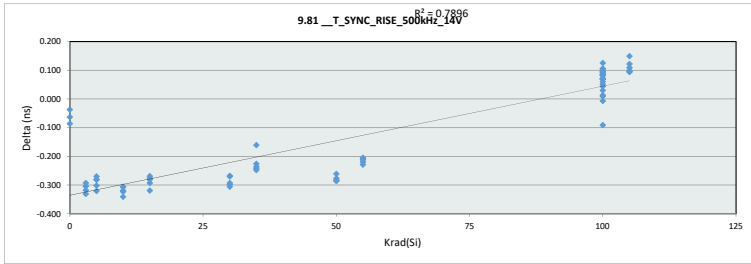
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.052	8.098	8.140	8.110	8.058	8.101	8.135	8.176	8.114	8.083	8.140
Average	8.100	8.162	8.167	8.135	8.114	8.183	8.188	8.216	8.206	8.190	8.247
Max	8.174	8.222	8.195	8.180	8.155	8.229	8.238	8.259	8.314	8.334	8.367
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



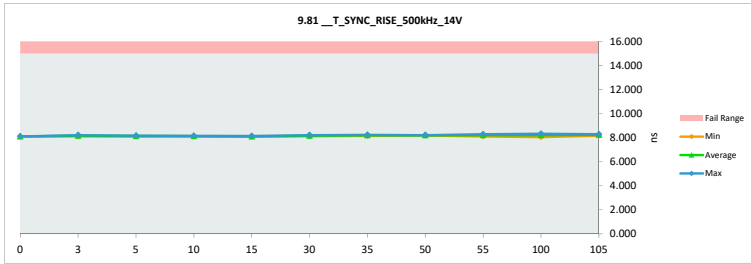
HDR TID Report  
TPS7H5004-SP

		9.81 __ T_SYNC_RISE_500kHz_14V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.169	8.105	-0.064
0	297	8.175	8.088	-0.087
0	298	8.145	8.107	-0.038
3	192	8.487	8.193	-0.294
3	193	8.432	8.113	-0.319
3	194	8.414	8.109	-0.305
3	196	8.548	8.216	-0.332
3	197	8.489	8.184	-0.305
5	200	8.417	8.134	-0.283
5	202	8.374	8.092	-0.282
5	203	8.469	8.166	-0.303
5	204	8.480	8.158	-0.322
5	206	8.422	8.151	-0.271
10	207	8.459	8.151	-0.308
10	209	8.458	8.138	-0.320
10	210	8.416	8.109	-0.307
10	211	8.468	8.144	-0.324
10	212	8.427	8.085	-0.342
15	214	8.406	8.112	-0.294
15	217	8.382	8.100	-0.282
15	218	8.411	8.141	-0.270
15	219	8.422	8.102	-0.320
15	222	8.348	8.072	-0.276
30	224	8.398	8.127	-0.271
30	225	8.445	8.176	-0.269
30	226	8.523	8.216	-0.307
30	227	8.420	8.121	-0.299
30	229	8.500	8.206	-0.294
35	230	8.387	8.160	-0.227
35	231	8.393	8.231	-0.162
35	232	8.459	8.222	-0.237
35	234	8.440	8.197	-0.243
35	237	8.412	8.163	-0.249
50	272	8.479	8.193	-0.286
50	275	8.503	8.216	-0.287
50	276	8.465	8.181	-0.284
50	277	8.446	8.184	-0.262
50	278	8.466	8.188	-0.278
55	279	8.327	8.106	-0.221
55	280	8.518	8.288	-0.230
55	281	8.415	8.206	-0.209
55	282	8.441	8.228	-0.213
55	283	8.421	8.215	-0.206
100	317	8.144	8.052	-0.092
100	318	8.173	8.232	0.059
100	319	8.199	8.242	0.043
100	320	8.187	8.270	0.083
100	321	8.228	8.325	0.097
100	322	8.091	8.141	0.050
100	323	8.134	8.203	0.069
100	324	8.137	8.222	0.085
100	326	8.053	8.123	0.070
100	327	8.178	8.271	0.093
100	328	8.116	8.206	0.090
100	329	8.086	8.190	0.104
100	332	8.147	8.242	0.095
100	333	8.188	8.256	0.068
100	334	8.144	8.248	0.104
100	335	8.077	8.106	0.029
100	336	8.172	8.240	0.068
100	337	8.110	8.118	0.008
100	338	8.134	8.214	0.080
100	339	8.038	8.124	0.086
100	340	8.073	8.197	0.124
100	341	8.195	8.277	0.082
100	342	8.189	8.181	-0.008
100	343	8.188	8.200	0.012
105	344	8.158	8.252	0.094
105	346	8.170	8.263	0.093
105	349	8.176	8.284	0.108
105	350	8.019	8.167	0.148
105	351	8.164	8.262	0.098
105	352	8.077	8.198	0.121
	Max	8.548	8.325	0.148
	Average	8.304	8.179	-0.125
	Min	8.019	8.052	-0.342
	Std Dev	0.158	0.061	0.176



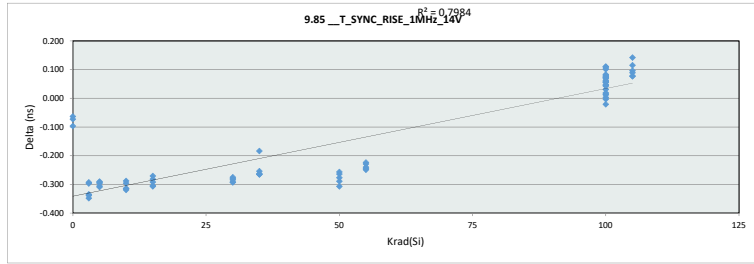
		9.81 __ T_SYNC_RI												
Test Site														
Tester														
Test Number														
Max Limit	15	ns												
Min Limit		ns												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	8.088	8.109	8.092	8.085	8.072	8.121	8.160	8.181	8.106	8.052	8.167			
Average	8.100	8.163	8.140	8.125	8.105	8.169	8.195	8.192	8.209	8.203	8.238			
Max	8.107	8.216	8.166	8.151	8.141	8.216	8.231	8.216	8.288	8.325	8.284			
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000			



HDR TID Report  
TPS7H5004-SP

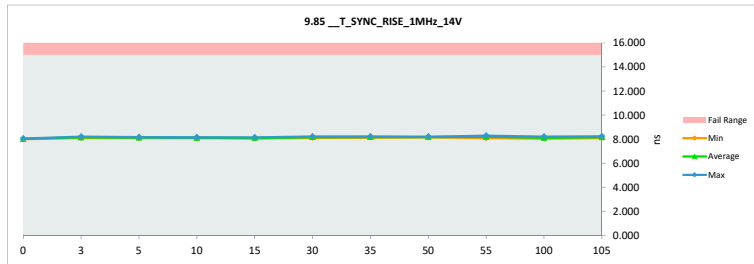
		9.85_T_SYNC_RISE_1MHz_14V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.126	8.028	-0.098
0	297	8.113	8.039	-0.074
0	298	8.059	7.995	-0.064
3	192	8.522	8.173	-0.349
3	193	8.420	8.122	-0.298
3	194	8.428	8.092	-0.336
3	196	8.538	8.198	-0.340
3	197	8.478	8.184	-0.294
5	200	8.427	8.130	-0.297
5	202	8.380	8.089	-0.291
5	203	8.463	8.156	-0.307
5	204	8.461	8.167	-0.294
5	206	8.446	8.136	-0.310
10	207	8.461	8.145	-0.316
10	209	8.452	8.137	-0.315
10	210	8.408	8.111	-0.297
10	211	8.443	8.154	-0.289
10	212	8.407	8.087	-0.320
15	214	8.416	8.110	-0.306
15	217	8.377	8.084	-0.293
15	218	8.419	8.136	-0.283
15	219	8.386	8.114	-0.272
15	222	8.372	8.064	-0.308
30	224	8.402	8.109	-0.293
30	225	8.450	8.174	-0.276
30	226	8.513	8.219	-0.294
30	227	8.419	8.134	-0.285
30	229	8.491	8.210	-0.281
35	230	8.384	8.129	-0.255
35	231	8.390	8.205	-0.185
35	232	8.483	8.217	-0.266
35	234	8.445	8.181	-0.264
35	237	8.419	8.153	-0.266
50	272	8.457	8.192	-0.265
50	275	8.500	8.192	-0.308
50	276	8.455	8.197	-0.258
50	277	8.439	8.149	-0.290
50	278	8.461	8.183	-0.278
55	279	8.326	8.096	-0.230
55	280	8.512	8.287	-0.225
55	281	8.437	8.187	-0.250
55	282	8.461	8.220	-0.241
55	283	8.448	8.202	-0.246
100	317	7.978	8.057	0.079
100	318	8.042	8.110	0.068
100	319	8.132	8.134	0.002
100	320	8.098	8.207	0.109
100	321	8.078	8.187	0.109
100	322	8.075	8.089	0.014
100	323	8.079	8.153	0.074
100	324	8.064	8.119	0.055
100	326	8.006	8.111	0.105
100	327	8.099	8.171	0.072
100	328	8.100	8.078	-0.022
100	329	8.062	8.092	0.030
100	332	8.106	8.188	0.082
100	333	8.099	8.146	0.047
100	334	8.043	8.116	0.073
100	335	7.995	8.052	0.057
100	336	8.147	8.143	-0.004
100	337	8.044	8.086	0.042
100	338	8.030	8.073	0.043
100	339	7.941	8.041	0.100
100	340	8.055	8.066	0.011
100	341	8.092	8.153	0.061
100	342	8.096	8.151	0.055
100	343	8.115	8.133	0.018
105	344	8.070	8.158	0.088
105	346	8.087	8.201	0.114
105	349	8.092	8.168	0.076
105	350	7.990	8.131	0.141
105	351	8.132	8.227	0.095
105	352	8.025	8.102	0.077
Max		8.538	8.287	0.141
Average		8.272	8.139	-0.133
Min		7.941	7.995	-0.349
Std Dev		0.192	0.055	0.173



		9.85_T_SYNC_RI	
Test Site			
Tester			
Test Number			
Max Limit	15	ns	
Min Limit		ns	

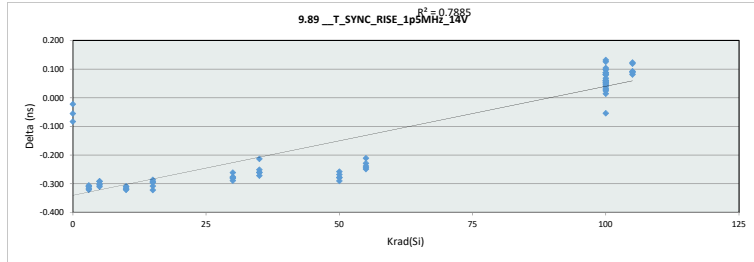
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.995	8.092	8.089	8.087	8.064	8.109	8.129	8.149	8.096	8.041	8.102
Average	8.021	8.154	8.136	8.127	8.102	8.169	8.177	8.183	8.198	8.119	8.165
Max	8.039	8.198	8.167	8.154	8.136	8.219	8.217	8.197	8.287	8.207	8.227
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

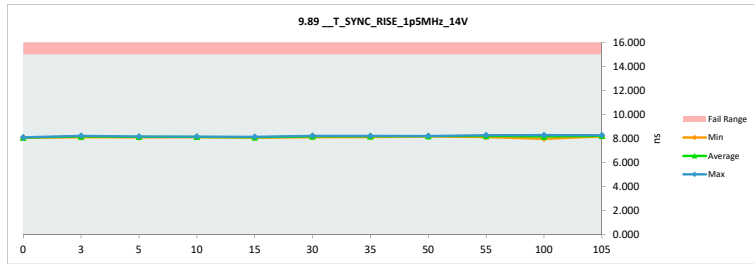
9.89 __T_SYNC_RISE_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.141	8.057	-0.084
0	297	8.110	8.087	-0.023
0	298	8.155	8.099	-0.056
3	192	8.510	8.188	-0.322
3	193	8.437	8.130	-0.307
3	194	8.422	8.104	-0.318
3	196	8.543	8.232	-0.311
3	197	8.488	8.178	-0.310
5	200	8.438	8.146	-0.292
5	202	8.381	8.088	-0.293
5	203	8.472	8.167	-0.305
5	204	8.484	8.173	-0.311
5	206	8.436	8.134	-0.302
10	207	8.474	8.163	-0.311
10	209	8.470	8.148	-0.322
10	210	8.434	8.117	-0.317
10	211	8.452	8.142	-0.310
10	212	8.426	8.105	-0.321
15	214	8.433	8.110	-0.323
15	217	8.385	8.098	-0.287
15	218	8.437	8.143	-0.294
15	219	8.394	8.097	-0.297
15	222	8.371	8.062	-0.309
30	224	8.392	8.110	-0.282
30	225	8.455	8.178	-0.277
30	226	8.509	8.230	-0.279
30	227	8.435	8.145	-0.290
30	229	8.482	8.220	-0.262
35	230	8.379	8.117	-0.262
35	231	8.431	8.217	-0.214
35	232	8.488	8.227	-0.261
35	234	8.461	8.188	-0.273
35	237	8.416	8.164	-0.252
50	272	8.485	8.206	-0.279
50	275	8.510	8.219	-0.291
50	276	8.460	8.201	-0.259
50	277	8.449	8.180	-0.269
50	278	8.465	8.185	-0.280
55	279	8.355	8.125	-0.230
55	280	8.521	8.282	-0.239
55	281	8.433	8.183	-0.250
55	282	8.452	8.240	-0.212
55	283	8.450	8.205	-0.245
100	317	8.029	8.080	0.051
100	318	8.115	8.162	0.047
100	319	8.128	8.215	0.087
100	320	8.164	8.208	0.044
100	321	8.208	8.287	0.079
100	322	8.078	8.116	0.038
100	323	8.149	8.235	0.086
100	324	8.127	8.150	0.023
100	326	8.053	8.184	0.131
100	327	8.153	8.256	0.103
100	328	8.118	8.185	0.067
100	329	8.109	8.190	0.081
100	332	8.114	8.211	0.097
100	333	8.117	8.242	0.125
100	334	8.116	8.169	0.053
100	335	8.033	7.978	-0.055
100	336	8.148	8.205	0.057
100	337	8.065	8.078	0.013
100	338	8.076	8.135	0.059
100	339	8.054	8.152	0.098
100	340	8.089	8.149	0.060
100	341	8.107	8.187	0.080
100	342	8.139	8.166	0.027
100	343	8.162	8.194	0.032
105	344	8.067	8.185	0.118
105	346	8.162	8.252	0.090
105	349	8.175	8.261	0.086
105	350	8.117	8.197	0.080
105	351	8.159	8.281	0.122
105	352	8.078	8.169	0.091
Max		8.543	8.287	0.131
Average		8.298	8.168	-0.130
Min		8.029	7.978	-0.323
Std Dev		0.172	0.059	0.176



9.89 __T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15
Min Limit	ns

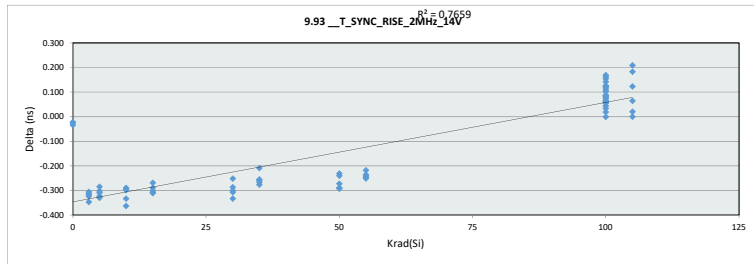
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.057	8.104	8.088	8.105	8.062	8.110	8.117	8.180	8.125	7.978	8.169
Average	8.081	8.166	8.142	8.135	8.102	8.177	8.183	8.198	8.207	8.172	8.224
Max	8.099	8.232	8.173	8.163	8.143	8.230	8.227	8.219	8.282	8.287	8.281
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

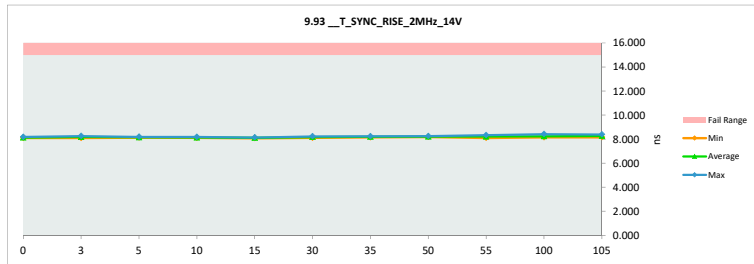
9.93 __T_SYNC_RISE_2MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.160	8.125	-0.035
0	297	8.204	8.181	-0.023
0	298	8.119	8.093	-0.026
3	192	8.529	8.222	-0.307
3	193	8.410	8.085	-0.325
3	194	8.445	8.127	-0.318
3	196	8.601	8.253	-0.348
3	197	8.524	8.210	-0.314
5	200	8.422	8.119	-0.303
5	202	8.425	8.115	-0.310
5	203	8.503	8.179	-0.324
5	204	8.493	8.162	-0.331
5	206	8.471	8.185	-0.286
10	207	8.470	8.176	-0.294
10	209	8.459	8.159	-0.300
10	210	8.422	8.131	-0.291
10	211	8.474	8.140	-0.334
10	212	8.458	8.094	-0.364
15	214	8.424	8.122	-0.302
15	217	8.432	8.123	-0.309
15	218	8.425	8.113	-0.312
15	219	8.398	8.128	-0.270
15	222	8.353	8.063	-0.290
30	224	8.402	8.100	-0.302
30	225	8.460	8.207	-0.253
30	226	8.514	8.226	-0.288
30	227	8.450	8.116	-0.334
30	229	8.514	8.206	-0.308
35	230	8.442	8.181	-0.261
35	231	8.443	8.233	-0.210
35	232	8.487	8.231	-0.256
35	234	8.468	8.200	-0.268
35	237	8.419	8.141	-0.278
50	272	8.477	8.187	-0.290
50	275	8.510	8.236	-0.274
50	276	8.477	8.245	-0.232
50	277	8.447	8.206	-0.241
50	278	8.462	8.168	-0.294
55	279	8.340	8.093	-0.247
55	280	8.544	8.325	-0.219
55	281	8.451	8.199	-0.252
55	282	8.506	8.269	-0.237
55	283	8.446	8.204	-0.242
100	317	8.105	8.138	0.033
100	318	8.124	8.211	0.087
100	319	8.177	8.256	0.079
100	320	8.187	8.243	0.056
100	321	8.241	8.409	0.168
100	322	8.134	8.235	0.101
100	323	8.189	8.314	0.125
100	324	8.174	8.297	0.123
100	326	8.103	8.218	0.115
100	327	8.162	8.281	0.119
100	328	8.172	8.215	0.043
100	329	8.117	8.278	0.161
100	332	8.145	8.219	0.074
100	333	8.184	8.202	0.018
100	334	8.137	8.278	0.141
100	335	8.125	8.185	0.060
100	336	8.277	8.275	-0.002
100	337	8.091	8.148	0.057
100	338	8.167	8.251	0.084
100	339	8.118	8.225	0.107
100	340	8.053	8.206	0.153
100	341	8.185	8.268	0.083
100	342	8.074	8.140	0.066
100	343	8.134	8.188	0.054
105	344	8.186	8.185	-0.001
105	346	8.168	8.376	0.208
105	349	8.175	8.297	0.122
105	350	8.095	8.277	0.182
105	351	8.289	8.308	0.019
105	352	8.080	8.143	0.063
Max		8.601	8.409	0.208
Average		8.321	8.199	-0.122
Min		8.053	8.063	-0.364
Std Dev		0.162	0.071	0.190



9.93 __T_SYNC_RI	
Test Site	
Tester	
Test Number	
Max Limit	15
Min Limit	ns

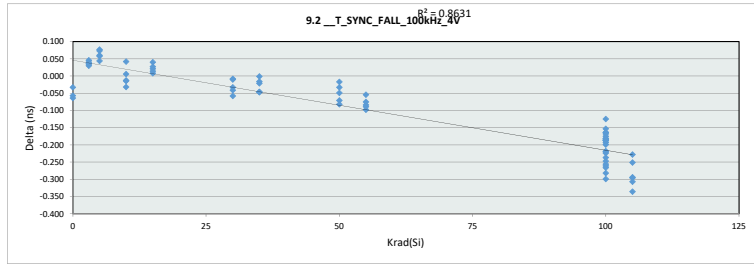
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.093	8.085	8.115	8.094	8.063	8.100	8.141	8.168	8.093	8.138	8.143
Average	8.133	8.179	8.152	8.140	8.110	8.171	8.197	8.208	8.218	8.237	8.264
Max	8.181	8.253	8.185	8.176	8.128	8.226	8.233	8.245	8.325	8.409	8.376
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



HDR TID Report  
TPS7H5004-SP

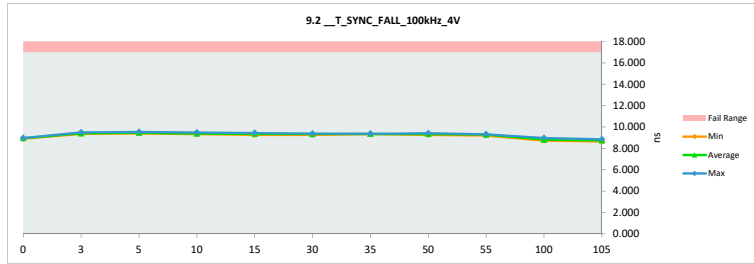
9.2 _T_SYNC_FALL_100kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	9.005	8.971	-0.034
0	297	9.017	8.952	-0.065
0	298	8.961	8.903	-0.058
3	192	9.450	9.495	0.045
3	193	9.389	9.423	0.034
3	194	9.332	9.371	0.039
3	196	9.314	9.349	0.035
3	197	9.349	9.378	0.029
5	200	9.395	9.452	0.057
5	202	9.325	9.401	0.076
5	203	9.487	9.530	0.043
5	204	9.383	9.442	0.059
5	206	9.418	9.490	0.072
10	207	9.438	9.479	0.041
10	209	9.397	9.382	-0.015
10	210	9.325	9.330	0.005
10	211	9.369	9.355	-0.014
10	212	9.412	9.379	-0.033
15	214	9.366	9.373	0.007
15	217	9.315	9.328	0.013
15	218	9.324	9.344	0.020
15	219	9.405	9.444	0.039
15	222	9.233	9.259	0.026
30	224	9.353	9.311	-0.042
30	225	9.350	9.339	-0.011
30	226	9.292	9.258	-0.034
30	227	9.392	9.333	-0.059
30	229	9.384	9.375	-0.009
35	230	9.371	9.354	-0.017
35	231	9.355	9.353	-0.002
35	232	9.378	9.331	-0.047
35	234	9.389	9.367	-0.022
35	237	9.422	9.374	-0.048
50	272	9.331	9.259	-0.072
50	275	9.398	9.315	-0.083
50	276	9.452	9.418	-0.034
50	277	9.385	9.335	-0.050
50	278	9.352	9.334	-0.018
55	279	9.280	9.204	-0.076
55	280	9.316	9.261	-0.055
55	281	9.406	9.321	-0.085
55	282	9.412	9.313	-0.099
55	283	9.390	9.300	-0.090
100	317	9.123	8.823	-0.300
100	318	8.974	8.717	-0.257
100	319	9.032	8.807	-0.225
100	320	8.944	8.725	-0.219
100	321	9.011	8.811	-0.200
100	322	9.041	8.865	-0.176
100	323	8.946	8.781	-0.165
100	324	9.030	8.848	-0.182
100	326	8.929	8.741	-0.188
100	327	9.022	8.857	-0.165
100	328	9.086	8.838	-0.248
100	329	9.052	8.864	-0.188
100	332	9.015	8.827	-0.188
100	333	8.971	8.817	-0.154
100	334	9.018	8.824	-0.194
100	335	9.064	8.781	-0.283
100	336	9.154	8.970	-0.184
100	337	9.088	8.919	-0.169
100	338	9.003	8.741	-0.262
100	339	9.017	8.796	-0.221
100	340	9.077	8.893	-0.184
100	341	9.058	8.791	-0.267
100	342	9.011	8.885	-0.126
100	343	8.997	8.759	-0.238
105	344	9.103	8.806	-0.297
105	346	8.926	8.674	-0.252
105	349	9.116	8.779	-0.337
105	350	8.961	8.653	-0.308
105	351	8.999	8.704	-0.295
105	352	9.071	8.842	-0.229
	Max	9.487	9.530	0.076
	Average	9.214	9.115	-0.099
	Min	8.926	8.653	-0.337
	Std Dev	0.181	0.283	0.116



9.2 _T_SYNC_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

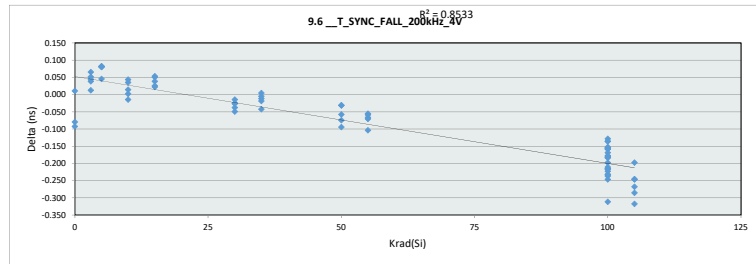
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.903	9.349	9.401	9.330	9.259	9.258	9.331	9.259	9.204	8.717	8.653
Average	8.942	9.403	9.463	9.385	9.350	9.323	9.356	9.332	9.280	8.820	8.743
Max	8.971	9.495	9.530	9.479	9.444	9.375	9.374	9.418	9.321	8.970	8.842
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

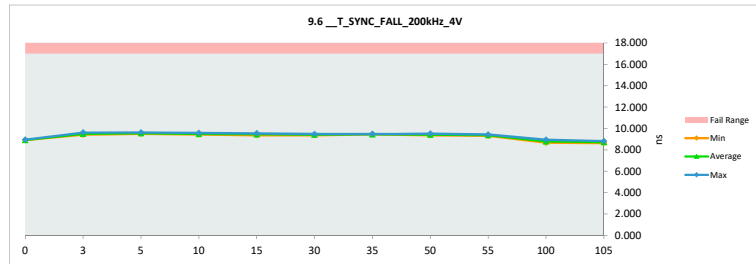
9.6 __T_SYNC_FALL_200kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.988	8.908	-0.080
0	297	8.996	8.903	-0.093
0	298	8.936	8.946	0.010
3	192	9.550	9.615	0.065
3	193	9.470	9.514	0.044
3	194	9.423	9.461	0.038
3	196	9.401	9.413	0.012
3	197	9.430	9.481	0.051
5	200	9.455	9.536	0.081
5	202	9.414	9.494	0.080
5	203	9.577	9.622	0.045
5	204	9.458	9.540	0.082
5	206	9.504	9.584	0.080
10	207	9.530	9.573	0.043
10	209	9.474	9.488	0.014
10	210	9.398	9.433	0.035
10	211	9.445	9.447	0.002
10	212	9.493	9.478	-0.015
15	214	9.465	9.487	0.022
15	217	9.390	9.428	0.038
15	218	9.431	9.456	0.025
15	219	9.493	9.543	0.050
15	222	9.304	9.357	0.053
30	224	9.436	9.409	-0.027
30	225	9.446	9.421	-0.025
30	226	9.396	9.358	-0.038
30	227	9.490	9.440	-0.050
30	229	9.480	9.465	-0.015
35	230	9.448	9.452	0.004
35	231	9.459	9.447	-0.012
35	232	9.464	9.445	-0.019
35	234	9.462	9.457	-0.005
35	237	9.518	9.475	-0.043
50	272	9.415	9.357	-0.058
50	275	9.486	9.391	-0.095
50	276	9.540	9.509	-0.031
50	277	9.498	9.423	-0.075
50	278	9.448	9.416	-0.032
55	279	9.378	9.307	-0.071
55	280	9.410	9.351	-0.059
55	281	9.481	9.425	-0.056
55	282	9.488	9.421	-0.067
55	283	9.484	9.380	-0.104
100	317	9.026	8.794	-0.232
100	318	8.913	8.755	-0.158
100	319	8.994	8.757	-0.237
100	320	8.880	8.663	-0.217
100	321	8.948	8.770	-0.178
100	322	9.053	8.830	-0.223
100	323	8.950	8.814	-0.136
100	324	9.036	8.838	-0.198
100	326	8.895	8.741	-0.154
100	327	8.992	8.812	-0.180
100	328	8.969	8.816	-0.153
100	329	9.012	8.796	-0.216
100	332	8.994	8.810	-0.184
100	333	9.009	8.793	-0.216
100	334	8.904	8.768	-0.136
100	335	9.031	8.719	-0.312
100	336	9.096	8.937	-0.159
100	337	9.085	8.838	-0.247
100	338	9.009	8.775	-0.234
100	339	8.952	8.795	-0.157
100	340	9.003	8.874	-0.129
100	341	8.994	8.783	-0.211
100	342	8.984	8.801	-0.183
100	343	8.931	8.762	-0.169
105	344	9.069	8.751	-0.318
105	346	8.907	8.621	-0.286
105	349	9.061	8.814	-0.247
105	350	8.867	8.669	-0.198
105	351	8.949	8.703	-0.246
105	352	9.036	8.768	-0.268
	Max	9.577	9.622	0.082
	Average	9.244	9.156	-0.087
	Min	8.867	8.621	-0.318
	Std Dev	0.244	0.341	0.112



9.6 __T_SYNC_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

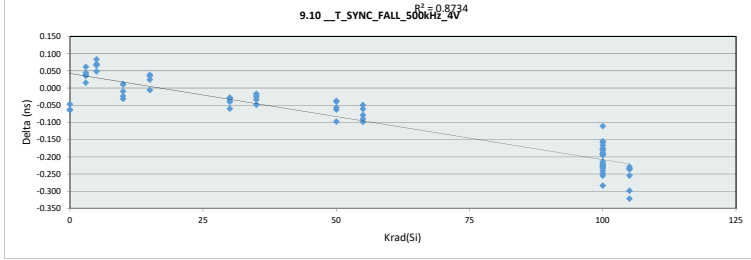
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.903	9.413	9.494	9.433	9.357	9.358	9.445	9.357	9.307	8.663	8.621
Average	8.919	9.497	9.555	9.484	9.454	9.419	9.455	9.419	9.377	8.793	8.721
Max	8.946	9.615	9.622	9.573	9.543	9.465	9.475	9.509	9.425	8.937	8.814
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



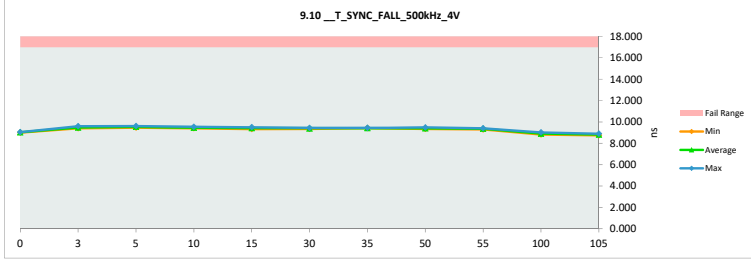
HDR TID Report  
TPS7H5004-SP

		9.10 __T_SYNC_FALL_500kHz_4V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	9.077	9.012	-0.065
0	297	9.089	9.042	-0.047
0	298	9.060	8.997	-0.063
3	192	9.531	9.592	0.061
3	193	9.461	9.499	0.038
3	194	9.394	9.429	0.035
3	196	9.386	9.401	0.015
3	197	9.405	9.449	0.044
5	200	9.446	9.515	0.069
5	202	9.387	9.453	0.066
5	203	9.561	9.609	0.048
5	204	9.448	9.516	0.068
5	206	9.489	9.572	0.083
10	207	9.524	9.535	0.011
10	209	9.460	9.450	-0.010
10	210	9.386	9.396	0.010
10	211	9.435	9.411	-0.024
10	212	9.486	9.454	-0.032
15	214	9.449	9.443	-0.006
15	217	9.371	9.406	0.035
15	218	9.392	9.427	0.035
15	219	9.477	9.501	0.024
15	222	9.284	9.322	0.038
30	224	9.412	9.373	-0.039
30	225	9.434	9.394	-0.040
30	226	9.364	9.336	-0.028
30	227	9.460	9.400	-0.060
30	229	9.454	9.423	-0.031
35	230	9.434	9.417	-0.017
35	231	9.439	9.416	-0.023
35	232	9.435	9.409	-0.026
35	234	9.451	9.417	-0.034
35	237	9.491	9.442	-0.049
50	272	9.394	9.337	-0.057
50	275	9.465	9.367	-0.098
50	276	9.519	9.481	-0.038
50	277	9.464	9.401	-0.063
50	278	9.431	9.391	-0.040
55	279	9.336	9.286	-0.050
55	280	9.404	9.325	-0.079
55	281	9.461	9.400	-0.061
55	282	9.485	9.386	-0.099
55	283	9.459	9.369	-0.090
100	317	9.155	8.871	-0.284
100	318	8.995	8.813	-0.182
100	319	9.109	8.879	-0.230
100	320	9.015	8.837	-0.178
100	321	9.065	8.886	-0.179
100	322	9.152	8.962	-0.190
100	323	8.962	8.851	-0.111
100	324	9.137	8.961	-0.176
100	326	9.043	8.852	-0.191
100	327	9.134	8.908	-0.226
100	328	9.085	8.918	-0.167
100	329	9.150	8.909	-0.241
100	332	9.047	8.888	-0.159
100	333	9.096	8.882	-0.214
100	334	9.061	8.906	-0.155
100	335	9.099	8.865	-0.234
100	336	9.253	9.004	-0.249
100	337	9.186	8.992	-0.194
100	338	9.118	8.863	-0.255
100	339	9.069	8.873	-0.196
100	340	9.136	8.915	-0.221
100	341	9.107	8.916	-0.191
100	342	9.087	8.865	-0.222
100	343	9.067	8.838	-0.229
105	344	9.170	8.848	-0.322
105	346	9.031	8.732	-0.299
105	349	9.120	8.886	-0.234
105	350	8.967	8.738	-0.229
105	351	9.037	8.800	-0.237
105	352	9.138	8.883	-0.255
Max		9.561	9.609	0.083
Average		9.282	9.186	-0.096
Min		8.962	8.732	-0.322
Std Dev		0.184	0.279	0.110



		9.10 __T_SYNC_FALL_500kHz_4V										
Test Site												
Tester												
Test Number												
Max Limit	17	ns										
Min Limit		ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	8.997	9.401	9.453	9.396	9.322	9.336	9.409	9.337	9.286	8.813	8.732	
Average	9.017	9.474	9.533	9.449	9.420	9.385	9.420	9.395	9.353	8.894	8.815	
Max	9.042	9.592	9.609	9.535	9.501	9.423	9.442	9.481	9.400	9.004	8.886	
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	

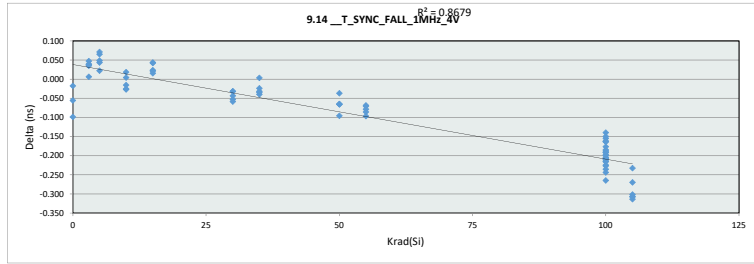




HDR TID Report  
TPS7H5004-SP

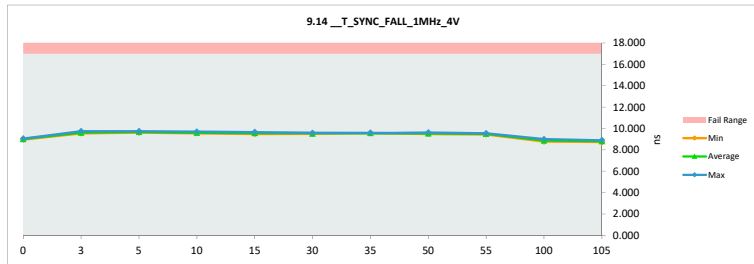
		9.14 _T_SYNC_FALL_1MHz_4V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	9.075	9.019	-0.056
0	297	9.068	9.050	-0.018
0	298	9.057	8.958	-0.099
3	192	9.684	9.731	0.047
3	193	9.606	9.644	0.038
3	194	9.533	9.572	0.039
3	196	9.540	9.546	0.006
3	197	9.552	9.587	0.035
5	200	9.607	9.655	0.048
5	202	9.542	9.607	0.065
5	203	9.709	9.731	0.022
5	204	9.600	9.643	0.043
5	206	9.633	9.704	0.071
10	207	9.675	9.693	0.018
10	209	9.609	9.593	-0.016
10	210	9.539	9.543	0.004
10	211	9.578	9.551	-0.027
10	212	9.620	9.594	-0.026
15	214	9.580	9.595	0.015
15	217	9.525	9.548	0.023
15	218	9.547	9.589	0.042
15	219	9.632	9.653	0.021
15	222	9.431	9.474	0.043
30	224	9.554	9.501	-0.053
30	225	9.588	9.544	-0.044
30	226	9.523	9.491	-0.032
30	227	9.610	9.551	-0.059
30	229	9.603	9.571	-0.032
35	230	9.587	9.555	-0.032
35	231	9.566	9.569	0.003
35	232	9.591	9.551	-0.040
35	234	9.598	9.563	-0.035
35	237	9.614	9.590	-0.024
50	272	9.551	9.485	-0.066
50	275	9.612	9.516	-0.096
50	276	9.675	9.609	-0.066
50	277	9.622	9.557	-0.065
50	278	9.573	9.536	-0.037
55	279	9.499	9.430	-0.069
55	280	9.546	9.475	-0.071
55	281	9.620	9.541	-0.079
55	282	9.626	9.540	-0.086
55	283	9.606	9.509	-0.097
100	317	9.139	8.934	-0.205
100	318	9.008	8.800	-0.208
100	319	9.075	8.864	-0.211
100	320	9.012	8.768	-0.244
100	321	8.983	8.843	-0.140
100	322	9.140	8.942	-0.198
100	323	9.017	8.855	-0.162
100	324	9.127	8.912	-0.215
100	326	9.018	8.868	-0.150
100	327	9.097	8.888	-0.209
100	328	9.099	8.899	-0.200
100	329	9.158	8.922	-0.236
100	332	9.059	8.895	-0.164
100	333	9.063	8.886	-0.177
100	334	9.041	8.849	-0.192
100	335	9.088	8.861	-0.227
100	336	9.210	8.994	-0.216
100	337	9.148	8.960	-0.188
100	338	9.094	8.869	-0.225
100	339	9.046	8.891	-0.155
100	340	9.087	8.923	-0.164
100	341	9.088	8.903	-0.185
100	342	9.052	8.860	-0.192
100	343	9.047	8.782	-0.265
105	344	9.161	8.891	-0.270
105	346	9.030	8.722	-0.308
105	349	9.157	8.855	-0.302
105	350	8.980	8.747	-0.233
105	351	9.071	8.764	-0.307
105	352	9.157	8.843	-0.314
Max		9.709	9.731	0.071
Average		9.358	9.260	-0.099
Min		8.980	8.722	-0.314
Std Dev		0.260	0.355	0.109



		9.14 _T_SYNC_FA	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	

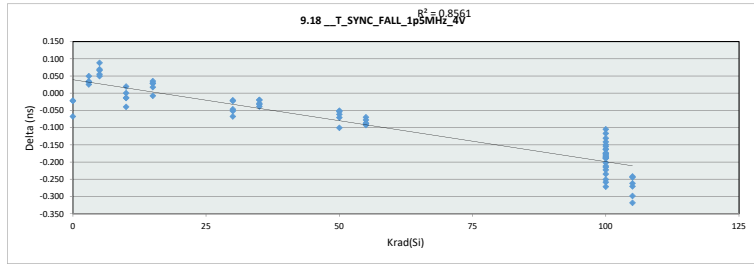
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.958	9.546	9.607	9.543	9.474	9.491	9.551	9.485	9.430	8.768	8.722
Average	9.009	9.616	9.668	9.595	9.572	9.532	9.566	9.541	9.499	8.882	8.804
Max	9.050	9.731	9.731	9.693	9.653	9.571	9.590	9.609	9.541	8.994	8.891
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

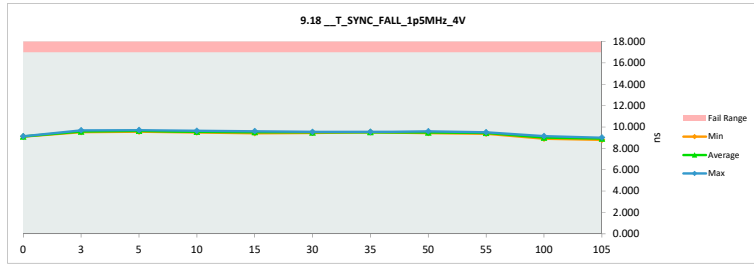
		9.18 __T_SYNC_FALL_1p5MHz_4V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	9.126	9.104	-0.022
0	297	9.151	9.128	-0.023
0	298	9.187	9.119	-0.068
3	192	9.637	9.686	0.049
3	193	9.562	9.595	0.033
3	194	9.498	9.523	0.025
3	196	9.488	9.520	0.032
3	197	9.521	9.555	0.034
5	200	9.564	9.619	0.055
5	202	9.490	9.556	0.066
5	203	9.657	9.706	0.049
5	204	9.554	9.623	0.069
5	206	9.577	9.665	0.088
10	207	9.623	9.642	0.019
10	209	9.564	9.550	-0.014
10	210	9.495	9.495	0.000
10	211	9.525	9.511	-0.014
10	212	9.594	9.554	-0.040
15	214	9.556	9.548	-0.008
15	217	9.465	9.500	0.035
15	218	9.511	9.539	0.028
15	219	9.587	9.604	0.017
15	222	9.385	9.414	0.029
30	224	9.528	9.476	-0.052
30	225	9.538	9.491	-0.047
30	226	9.463	9.440	-0.023
30	227	9.570	9.502	-0.068
30	229	9.548	9.527	-0.021
35	230	9.544	9.505	-0.039
35	231	9.551	9.531	-0.020
35	232	9.549	9.519	-0.030
35	234	9.537	9.517	-0.020
35	237	9.586	9.553	-0.033
50	272	9.500	9.429	-0.071
50	275	9.574	9.473	-0.101
50	276	9.640	9.586	-0.054
50	277	9.559	9.497	-0.062
50	278	9.545	9.494	-0.051
55	279	9.444	9.374	-0.070
55	280	9.503	9.415	-0.088
55	281	9.577	9.484	-0.093
55	282	9.578	9.500	-0.078
55	283	9.549	9.461	-0.088
100	317	9.244	8.985	-0.259
100	318	9.180	8.908	-0.272
100	319	9.225	8.974	-0.251
100	320	9.125	8.902	-0.223
100	321	9.185	9.022	-0.163
100	322	9.169	9.052	-0.117
100	323	9.101	8.959	-0.142
100	324	9.255	9.066	-0.189
100	326	9.095	8.964	-0.131
100	327	9.203	9.026	-0.177
100	328	9.238	9.036	-0.202
100	329	9.206	9.030	-0.176
100	332	9.155	9.005	-0.150
100	333	9.185	9.003	-0.182
100	334	9.107	9.002	-0.105
100	335	9.139	8.954	-0.185
100	336	9.303	9.148	-0.155
100	337	9.247	9.084	-0.163
100	338	9.195	8.960	-0.235
100	339	9.143	8.969	-0.174
100	340	9.237	9.025	-0.212
100	341	9.191	9.002	-0.189
100	342	9.226	9.011	-0.215
100	343	9.143	8.955	-0.188
105	344	9.235	8.936	-0.299
105	346	9.172	8.853	-0.319
105	349	9.274	9.003	-0.271
105	350	9.062	8.800	-0.262
105	351	9.168	8.926	-0.242
105	352	9.233	8.988	-0.245
	Max	9.657	9.706	0.088
	Average	9.381	9.289	-0.093
	Min	9.062	8.800	-0.319
	Std Dev	0.188	0.277	0.106



		9.18 __T_SYNC_FA	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	

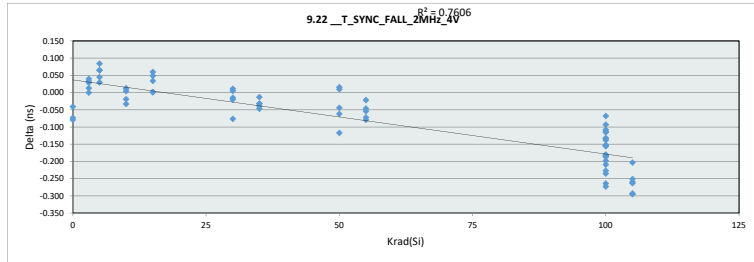
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.104	9.520	9.556	9.495	9.414	9.440	9.505	9.429	9.374	8.902	8.800
Average	9.117	9.576	9.634	9.550	9.521	9.487	9.525	9.496	9.447	9.002	8.918
Max	9.128	9.686	9.706	9.642	9.604	9.527	9.553	9.586	9.500	9.148	9.003
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

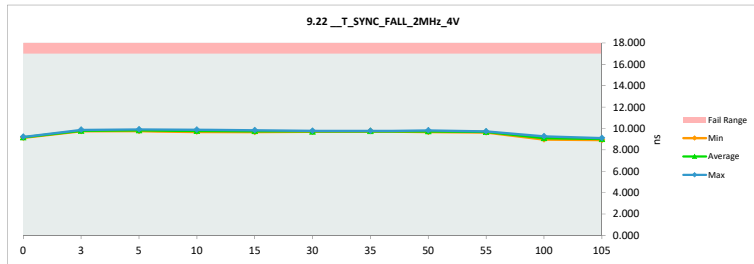
9.22 __T_SYNC_FALL_2MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	9.247	9.205	-0.042
0	297	9.266	9.192	-0.074
0	298	9.197	9.117	-0.080
3	192	9.851	9.863	0.012
3	193	9.732	9.771	0.039
3	194	9.728	9.756	0.028
3	196	9.735	9.734	-0.001
3	197	9.731	9.763	0.032
5	200	9.773	9.837	0.064
5	202	9.684	9.748	0.064
5	203	9.876	9.905	0.029
5	204	9.701	9.784	0.083
5	206	9.818	9.862	0.044
10	207	9.866	9.877	0.011
10	209	9.769	9.772	0.003
10	210	9.710	9.721	0.011
10	211	9.721	9.687	-0.034
10	212	9.775	9.755	-0.020
15	214	9.780	9.781	0.001
15	217	9.696	9.729	0.033
15	218	9.714	9.762	0.048
15	219	9.828	9.828	0.000
15	222	9.588	9.647	0.059
30	224	9.713	9.697	-0.016
30	225	9.688	9.692	0.004
30	226	9.701	9.680	-0.021
30	227	9.805	9.728	-0.077
30	229	9.755	9.765	0.010
35	230	9.770	9.730	-0.040
35	231	9.776	9.728	-0.048
35	232	9.780	9.747	-0.033
35	234	9.769	9.755	-0.014
35	237	9.802	9.769	-0.033
50	272	9.648	9.663	0.015
50	275	9.817	9.699	-0.118
50	276	9.851	9.806	-0.045
50	277	9.806	9.743	-0.063
50	278	9.720	9.729	0.009
55	279	9.644	9.621	-0.023
55	280	9.723	9.643	-0.080
55	281	9.779	9.724	-0.055
55	282	9.799	9.726	-0.073
55	283	9.745	9.698	-0.047
100	317	9.341	9.105	-0.236
100	318	9.220	8.955	-0.265
100	319	9.234	9.035	-0.199
100	320	9.187	9.031	-0.156
100	321	9.200	9.106	-0.094
100	322	9.310	9.100	-0.210
100	323	9.193	9.037	-0.156
100	324	9.356	9.175	-0.181
100	326	9.126	9.057	-0.069
100	327	9.256	9.117	-0.139
100	328	9.300	9.146	-0.154
100	329	9.332	9.104	-0.228
100	332	9.217	9.108	-0.109
100	333	9.215	9.080	-0.135
100	334	9.214	9.098	-0.116
100	335	9.278	9.095	-0.183
100	336	9.394	9.261	-0.133
100	337	9.371	9.184	-0.187
100	338	9.260	9.077	-0.183
100	339	9.243	9.129	-0.114
100	340	9.274	9.120	-0.154
100	341	9.288	9.101	-0.187
100	342	9.263	9.108	-0.155
100	343	9.291	9.017	-0.274
105	344	9.292	9.088	-0.204
105	346	9.180	8.916	-0.264
105	349	9.381	9.087	-0.294
105	350	9.220	8.924	-0.296
105	351	9.202	8.950	-0.252
105	352	9.333	9.072	-0.261
Max		9.876	9.905	0.083
Average		9.532	9.450	-0.083
Min		9.126	8.916	-0.296
Std Dev		0.254	0.338	0.102



9.22 __T_SYNC_FALL_2MHz_4V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	17 ns

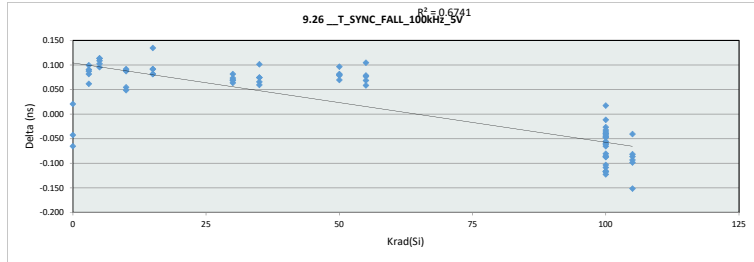
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.117	9.734	9.748	9.687	9.647	9.680	9.728	9.663	9.621	8.955	8.916
Average	9.171	9.777	9.827	9.762	9.749	9.712	9.746	9.728	9.682	9.098	9.006
Max	9.205	9.863	9.905	9.877	9.828	9.765	9.769	9.806	9.726	9.261	9.088
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



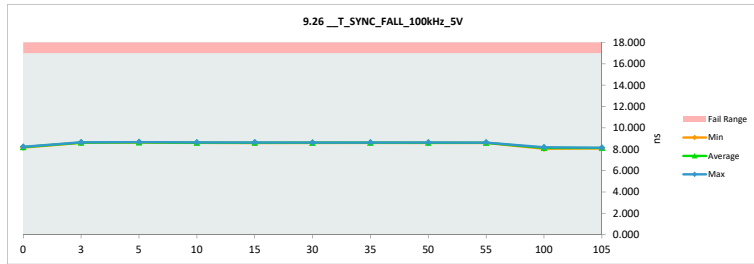
HDR TID Report  
TPS7H5004-SP

		9.26 __T_SYNC_FALL_100kHz_5V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.215	8.235	0.020
0	297	8.252	8.209	-0.043
0	298	8.225	8.159	-0.066
3	192	8.559	8.649	0.090
3	193	8.552	8.651	0.099
3	194	8.556	8.617	0.061
3	196	8.531	8.612	0.081
3	197	8.528	8.615	0.087
5	200	8.547	8.649	0.102
5	202	8.526	8.634	0.108
5	203	8.577	8.672	0.095
5	204	8.521	8.634	0.113
5	206	8.534	8.647	0.113
10	207	8.546	8.633	0.087
10	209	8.535	8.624	0.089
10	210	8.505	8.596	0.091
10	211	8.545	8.593	0.048
10	212	8.572	8.626	0.054
15	214	8.547	8.628	0.081
15	217	8.504	8.638	0.134
15	218	8.534	8.615	0.081
15	219	8.556	8.647	0.091
15	222	8.504	8.595	0.091
30	224	8.529	8.597	0.068
30	225	8.533	8.614	0.081
30	226	8.528	8.601	0.073
30	227	8.527	8.596	0.069
30	229	8.533	8.596	0.063
35	230	8.537	8.596	0.059
35	231	8.539	8.640	0.101
35	232	8.538	8.612	0.074
35	234	8.555	8.620	0.065
35	237	8.545	8.619	0.074
50	272	8.513	8.591	0.078
50	275	8.527	8.608	0.081
50	276	8.568	8.637	0.069
50	277	8.526	8.622	0.096
50	278	8.528	8.608	0.080
55	279	8.536	8.594	0.058
55	280	8.524	8.628	0.104
55	281	8.540	8.608	0.068
55	282	8.540	8.618	0.078
55	283	8.524	8.600	0.076
100	317	8.235	8.126	-0.109
100	318	8.171	8.048	-0.123
100	319	8.172	8.091	-0.081
100	320	8.166	8.129	-0.037
100	321	8.162	8.129	-0.033
100	322	8.226	8.166	-0.060
100	323	8.184	8.118	-0.066
100	324	8.174	8.126	-0.048
100	326	8.190	8.102	-0.088
100	327	8.157	8.117	-0.040
100	328	8.235	8.117	-0.118
100	329	8.222	8.135	-0.087
100	332	8.181	8.125	-0.056
100	333	8.149	8.122	-0.027
100	334	8.145	8.133	-0.012
100	335	8.216	8.112	-0.104
100	336	8.214	8.173	-0.041
100	337	8.200	8.155	-0.045
100	338	8.233	8.116	-0.117
100	339	8.178	8.131	-0.047
100	340	8.233	8.116	-0.117
100	341	8.170	8.187	0.017
100	342	8.188	8.126	-0.062
100	343	8.191	8.105	-0.086
105	344	8.256	8.104	-0.152
105	346	8.177	8.090	-0.087
105	349	8.228	8.146	-0.082
105	350	8.174	8.133	-0.041
105	351	8.203	8.109	-0.094
105	352	8.226	8.127	-0.099
Max		8.577	8.672	0.134
Average		8.384	8.399	0.015
Min		8.145	8.048	-0.152
Std Dev		0.171	0.246	0.081



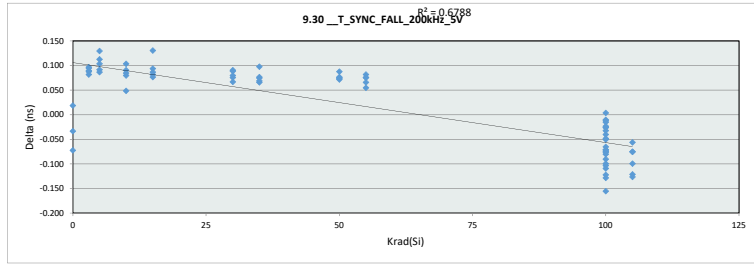
		9.26 __T_SYNC_FALL_100kHz_5V											
Test Site													
Tester													
Test Number													
Max Limit	17	ns											
Min Limit		ns											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL													
Min	8.159	8.612	8.634	8.593	8.595	8.596	8.596	8.591	8.594	8.048	8.090		
Average	8.201	8.629	8.647	8.614	8.625	8.601	8.617	8.613	8.610	8.125	8.118		
Max	8.235	8.651	8.672	8.633	8.647	8.614	8.640	8.637	8.628	8.187	8.146		
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000		



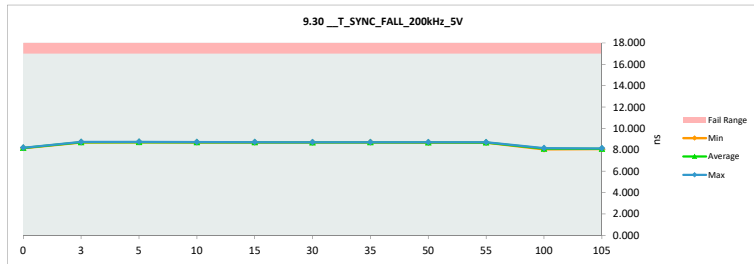
HDR TID Report  
TPS7H5004-SP

		9.30 __T_SYNC_FALL_200kHz_5V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.218	8.184	-0.034
0	297	8.170	8.188	0.018
0	298	8.215	8.142	-0.073
3	192	8.648	8.737	0.089
3	193	8.629	8.716	0.087
3	194	8.605	8.699	0.094
3	196	8.600	8.681	0.081
3	197	8.607	8.703	0.096
5	200	8.607	8.719	0.112
5	202	8.586	8.715	0.129
5	203	8.637	8.740	0.103
5	204	8.620	8.706	0.086
5	206	8.634	8.725	0.091
10	207	8.645	8.724	0.079
10	209	8.611	8.714	0.103
10	210	8.597	8.681	0.084
10	211	8.593	8.683	0.090
10	212	8.636	8.684	0.048
15	214	8.618	8.704	0.086
15	217	8.576	8.706	0.130
15	218	8.623	8.704	0.081
15	219	8.641	8.717	0.076
15	222	8.579	8.672	0.093
30	224	8.606	8.685	0.079
30	225	8.594	8.684	0.090
30	226	8.611	8.699	0.088
30	227	8.612	8.678	0.066
30	229	8.622	8.697	0.075
35	230	8.606	8.703	0.097
35	231	8.627	8.703	0.076
35	232	8.623	8.691	0.068
35	234	8.608	8.682	0.074
35	237	8.642	8.707	0.065
50	272	8.585	8.656	0.071
50	275	8.618	8.692	0.074
50	276	8.619	8.706	0.087
50	277	8.622	8.698	0.076
50	278	8.603	8.677	0.074
55	279	8.590	8.671	0.081
55	280	8.603	8.678	0.075
55	281	8.636	8.701	0.065
55	282	8.623	8.698	0.075
55	283	8.609	8.663	0.054
100	317	8.203	8.131	-0.072
100	318	8.190	8.080	-0.110
100	319	8.171	8.096	-0.075
100	320	8.143	8.062	-0.081
100	321	8.219	8.096	-0.123
100	322	8.155	8.131	-0.024
100	323	8.150	8.084	-0.066
100	324	8.162	8.033	-0.129
100	326	8.180	8.076	-0.104
100	327	8.134	8.108	-0.026
100	328	8.125	8.128	0.003
100	329	8.199	8.099	-0.100
100	332	8.207	8.051	-0.156
100	333	8.147	8.114	-0.033
100	334	8.117	8.106	-0.011
100	335	8.174	8.147	-0.027
100	336	8.193	8.116	-0.077
100	337	8.189	8.117	-0.072
100	338	8.159	8.110	-0.049
100	339	8.149	8.133	-0.016
100	340	8.194	8.153	-0.041
100	341	8.203	8.112	-0.091
100	342	8.145	8.132	-0.013
100	343	8.125	8.074	-0.051
105	344	8.204	8.104	-0.100
105	346	8.186	8.064	-0.122
105	349	8.233	8.106	-0.127
105	350	8.157	8.100	-0.057
105	351	8.141	8.065	-0.076
105	352	8.202	8.126	-0.076
	Max	8.648	8.740	0.130
	Average	8.415	8.431	0.016
	Min	8.117	8.033	-0.156
	Std Dev	0.222	0.297	0.081



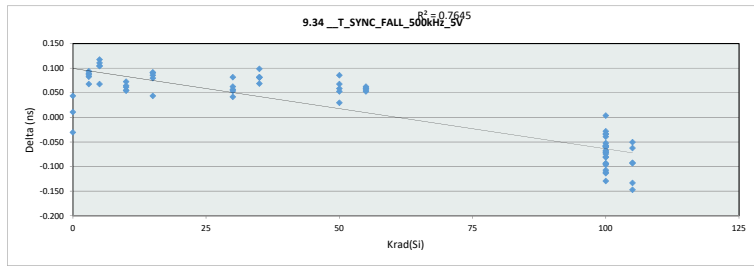
		9.30 __T_SYNC_FALL_200kHz_5V										
Test Site												
Tester												
Test Number												
Max Limit	17	ns										
Min Limit		ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	8.142	8.681	8.706	8.681	8.672	8.678	8.682	8.656	8.663	8.033	8.064	
Average	8.171	8.707	8.721	8.697	8.701	8.689	8.697	8.686	8.682	8.104	8.094	
Max	8.188	8.737	8.740	8.724	8.717	8.699	8.707	8.706	8.701	8.153	8.126	
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	



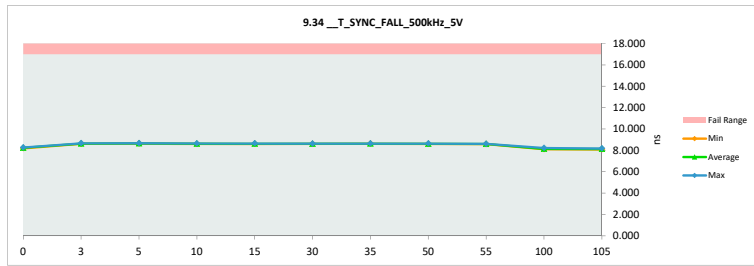
HDR TID Report  
TPS7H5004-SP

		9.34 __T_SYNC_FALL_500kHz_5V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.234	8.244	0.010
0	297	8.220	8.263	0.043
0	298	8.215	8.184	-0.031
3	192	8.568	8.657	0.089
3	193	8.550	8.632	0.082
3	194	8.545	8.612	0.067
3	196	8.525	8.611	0.086
3	197	8.531	8.624	0.093
5	200	8.542	8.646	0.104
5	202	8.525	8.635	0.110
5	203	8.594	8.661	0.067
5	204	8.530	8.647	0.117
5	206	8.552	8.656	0.104
10	207	8.565	8.637	0.072
10	209	8.560	8.614	0.054
10	210	8.534	8.595	0.061
10	211	8.552	8.606	0.054
10	212	8.557	8.621	0.064
15	214	8.566	8.609	0.043
15	217	8.527	8.617	0.090
15	218	8.537	8.628	0.091
15	219	8.557	8.642	0.085
15	222	8.522	8.601	0.079
30	224	8.548	8.601	0.053
30	225	8.539	8.595	0.056
30	226	8.521	8.602	0.081
30	227	8.552	8.593	0.041
30	229	8.551	8.613	0.062
35	230	8.534	8.615	0.081
35	231	8.548	8.628	0.080
35	232	8.542	8.640	0.098
35	234	8.554	8.635	0.081
35	237	8.566	8.634	0.068
50	272	8.536	8.621	0.085
50	275	8.561	8.590	0.029
50	276	8.575	8.627	0.052
50	277	8.544	8.611	0.067
50	278	8.544	8.602	0.058
55	279	8.520	8.578	0.058
55	280	8.531	8.593	0.062
55	281	8.545	8.597	0.052
55	282	8.550	8.605	0.055
55	283	8.548	8.607	0.059
100	317	8.223	8.129	-0.094
100	318	8.224	8.129	-0.095
100	319	8.181	8.123	-0.058
100	320	8.167	8.127	-0.040
100	321	8.203	8.131	-0.072
100	322	8.226	8.144	-0.082
100	323	8.171	8.137	-0.034
100	324	8.238	8.167	-0.071
100	326	8.191	8.130	-0.061
100	327	8.176	8.141	-0.035
100	328	8.176	8.147	-0.029
100	329	8.217	8.159	-0.058
100	332	8.214	8.146	-0.068
100	333	8.168	8.115	-0.053
100	334	8.232	8.120	-0.112
100	335	8.252	8.144	-0.108
100	336	8.215	8.218	0.003
100	337	8.196	8.136	-0.060
100	338	8.267	8.137	-0.130
100	339	8.212	8.098	-0.114
100	340	8.202	8.142	-0.060
100	341	8.224	8.127	-0.097
100	342	8.224	8.143	-0.081
100	343	8.203	8.128	-0.075
105	344	8.226	8.092	-0.134
105	346	8.226	8.078	-0.148
105	349	8.263	8.170	-0.093
105	350	8.203	8.109	-0.094
105	351	8.189	8.126	-0.063
105	352	8.207	8.156	-0.051
	Max	8.594	8.661	0.117
	Average	8.395	8.404	0.009
	Min	8.167	8.078	-0.148
	Std Dev	0.169	0.240	0.077



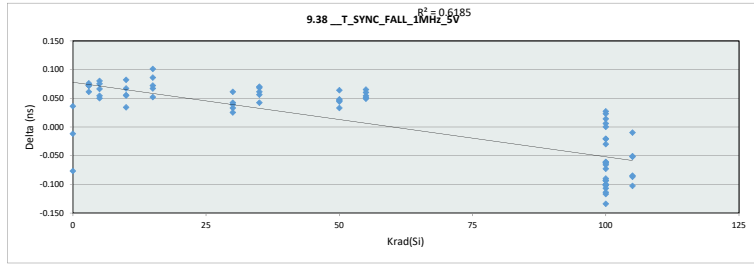
		9.34 __T_SYNC_FALL_500kHz_5V										
Test Site												
Tester												
Test Number												
Max Limit	17	ns										
Min Limit		ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	8.184	8.611	8.635	8.595	8.601	8.593	8.615	8.590	8.578	8.098	8.078	
Average	8.230	8.627	8.649	8.615	8.619	8.601	8.630	8.610	8.596	8.138	8.122	
Max	8.263	8.657	8.661	8.637	8.642	8.613	8.640	8.627	8.607	8.218	8.170	
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	



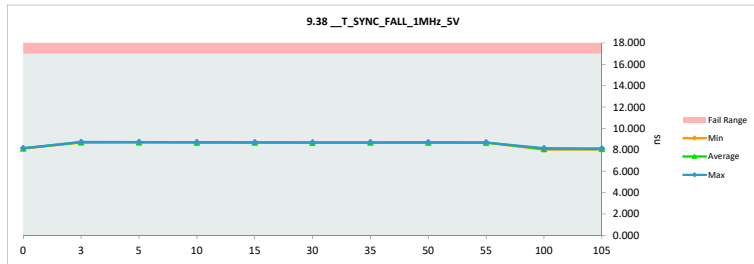
HDR TID Report  
TPS7H5004-SP

		9.38 __T_SYNC_FALL_1MHz_5V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.106	8.142	0.036
0	297	8.189	8.112	-0.077
0	298	8.165	8.153	-0.012
3	192	8.668	8.729	0.061
3	193	8.645	8.717	0.072
3	194	8.620	8.692	0.072
3	196	8.612	8.684	0.072
3	197	8.617	8.693	0.076
5	200	8.635	8.701	0.066
5	202	8.614	8.694	0.080
5	203	8.677	8.727	0.050
5	204	8.641	8.716	0.075
5	206	8.647	8.701	0.054
10	207	8.655	8.722	0.067
10	209	8.629	8.711	0.082
10	210	8.607	8.662	0.055
10	211	8.627	8.661	0.034
10	212	8.641	8.696	0.055
15	214	8.630	8.702	0.072
15	217	8.609	8.695	0.086
15	218	8.618	8.685	0.067
15	219	8.644	8.696	0.052
15	222	8.570	8.671	0.101
30	224	8.600	8.661	0.061
30	225	8.620	8.645	0.025
30	226	8.627	8.666	0.039
30	227	8.638	8.671	0.033
30	229	8.644	8.686	0.042
35	230	8.635	8.696	0.061
35	231	8.627	8.695	0.068
35	232	8.626	8.682	0.056
35	234	8.626	8.696	0.070
35	237	8.655	8.697	0.042
50	272	8.611	8.675	0.064
50	275	8.632	8.665	0.033
50	276	8.658	8.704	0.046
50	277	8.642	8.690	0.048
50	278	8.630	8.674	0.044
55	279	8.606	8.666	0.060
55	280	8.607	8.672	0.065
55	281	8.628	8.682	0.054
55	282	8.634	8.683	0.049
55	283	8.628	8.679	0.051
100	317	8.158	8.092	-0.066
100	318	8.139	8.022	-0.117
100	319	8.073	8.073	0.000
100	320	8.121	8.020	-0.101
100	321	8.091	8.028	-0.063
100	322	8.111	8.125	0.014
100	323	8.152	8.058	-0.094
100	324	8.100	8.123	0.023
100	326	8.101	8.107	0.006
100	327	8.110	8.080	-0.030
100	328	8.117	8.096	-0.021
100	329	8.211	8.097	-0.114
100	332	8.152	8.050	-0.102
100	333	8.110	8.037	-0.073
100	334	8.138	8.077	-0.061
100	335	8.181	8.088	-0.093
100	336	8.196	8.134	-0.062
100	337	8.167	8.077	-0.090
100	338	8.187	8.053	-0.134
100	339	8.127	8.154	0.027
100	340	8.172	8.072	-0.100
100	341	8.122	8.101	-0.021
100	342	8.185	8.084	-0.101
100	343	8.154	8.047	-0.107
105	344	8.179	8.076	-0.103
105	346	8.115	8.063	-0.052
105	349	8.128	8.118	-0.010
105	350	8.124	8.037	-0.087
105	351	8.124	8.073	-0.051
105	352	8.137	8.052	-0.085
	Max	8.677	8.729	0.101
	Average	8.409	8.415	0.006
	Min	8.073	8.020	-0.134
	Std Dev	0.246	0.305	0.068



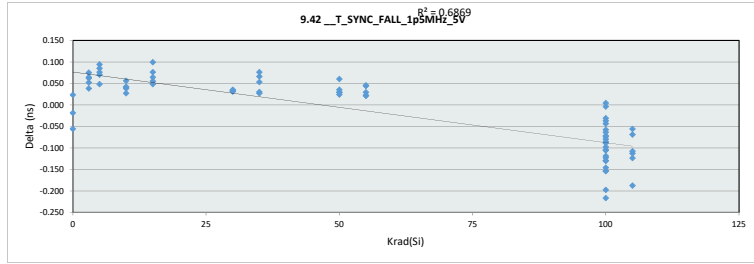
		9.38 __T_SYNC_FALL_1MHz_5V												
Test Site														
Tester														
Test Number														
Max Limit	17	ns												
Min Limit		ns												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	8.112	8.684	8.694	8.661	8.671	8.645	8.682	8.665	8.666	8.020	8.037			
Average	8.136	8.703	8.708	8.690	8.690	8.666	8.693	8.682	8.676	8.079	8.070			
Max	8.153	8.729	8.727	8.722	8.702	8.686	8.697	8.704	8.683	8.154	8.118			
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000			



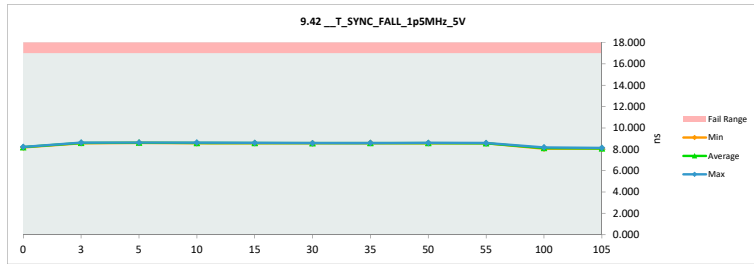
HDR TID Report  
TPS7H5004-SP

		9.42 __T_SYNC_FALL_1p5MHz_5V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.218	8.199	-0.019
0	297	8.271	8.215	-0.056
0	298	8.138	8.161	0.023
3	192	8.578	8.616	0.038
3	193	8.533	8.597	0.064
3	194	8.518	8.580	0.062
3	196	8.509	8.561	0.052
3	197	8.518	8.593	0.075
5	200	8.526	8.611	0.085
5	202	8.514	8.590	0.076
5	203	8.578	8.626	0.048
5	204	8.524	8.618	0.094
5	206	8.555	8.625	0.070
10	207	8.557	8.613	0.056
10	209	8.546	8.586	0.040
10	210	8.507	8.549	0.042
10	211	8.538	8.565	0.027
10	212	8.547	8.585	0.038
15	214	8.526	8.574	0.048
15	217	8.505	8.569	0.064
15	218	8.507	8.583	0.076
15	219	8.539	8.594	0.055
15	222	8.466	8.565	0.099
30	224	8.515	8.550	0.035
30	225	8.522	8.555	0.033
30	226	8.515	8.547	0.032
30	227	8.534	8.565	0.031
30	229	8.513	8.545	0.032
35	230	8.524	8.590	0.066
35	231	8.512	8.588	0.076
35	232	8.516	8.569	0.053
35	234	8.542	8.572	0.030
35	237	8.547	8.574	0.027
50	272	8.507	8.567	0.060
50	275	8.534	8.563	0.029
50	276	8.564	8.593	0.029
50	277	8.537	8.561	0.024
50	278	8.524	8.559	0.035
55	279	8.503	8.548	0.045
55	280	8.509	8.553	0.044
55	281	8.531	8.552	0.021
55	282	8.558	8.580	0.022
55	283	8.528	8.557	0.029
100	317	8.209	8.172	-0.037
100	318	8.171	8.091	-0.080
100	319	8.228	8.074	-0.154
100	320	8.141	8.067	-0.074
100	321	8.164	8.168	0.004
100	322	8.208	8.110	-0.098
100	323	8.135	8.131	-0.004
100	324	8.221	8.115	-0.106
100	326	8.162	8.131	-0.031
100	327	8.205	8.133	-0.072
100	328	8.213	8.067	-0.146
100	329	8.315	8.098	-0.217
100	332	8.152	8.094	-0.058
100	333	8.170	8.084	-0.086
100	334	8.200	8.046	-0.154
100	335	8.216	8.097	-0.119
100	336	8.293	8.170	-0.123
100	337	8.308	8.110	-0.198
100	338	8.194	8.064	-0.130
100	339	8.202	8.072	-0.130
100	340	8.163	8.119	-0.044
100	341	8.210	8.146	-0.064
100	342	8.211	8.106	-0.105
100	343	8.152	8.063	-0.089
105	344	8.315	8.127	-0.188
105	346	8.163	8.055	-0.108
105	349	8.184	8.060	-0.124
105	350	8.151	8.095	-0.056
105	351	8.170	8.057	-0.113
105	352	8.174	8.105	-0.069
	Max	8.578	8.626	0.099
	Average	8.380	8.366	-0.015
	Min	8.135	8.046	-0.217
	Std Dev	0.168	0.237	0.082



		9.42 __T_SYNC_FALL_1p5MHz_5V											
Test Site													
Tester													
Test Number													
Max Limit	17	ns											
Min Limit		ns											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL													
Min	8.161	8.561	8.590	8.549	8.565	8.545	8.569	8.559	8.548	8.046	8.055		
Average	8.192	8.589	8.614	8.580	8.577	8.552	8.579	8.569	8.558	8.105	8.083		
Max	8.215	8.616	8.626	8.613	8.594	8.565	8.590	8.593	8.580	8.172	8.127		
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000		

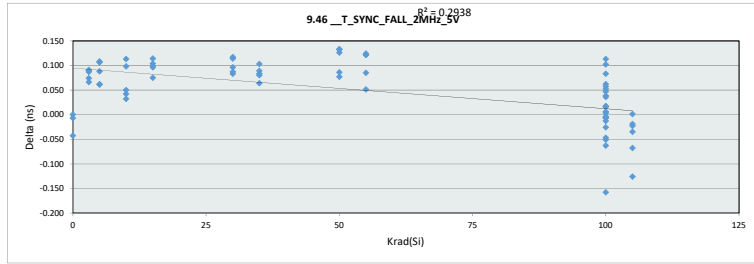




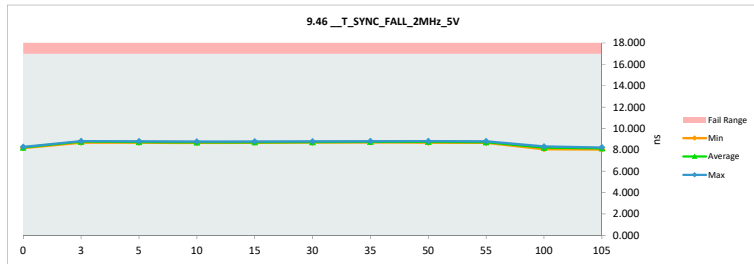
HDR TID Report  
TPS7H5004-SP

		9.46 __T_SYNC_FALL_2MHz_5V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.256	8.213	-0.043
0	297	8.267	8.267	0.000
0	298	8.174	8.167	-0.007
3	192	8.714	8.803	0.089
3	193	8.604	8.670	0.066
3	194	8.681	8.772	0.091
3	196	8.698	8.785	0.087
3	197	8.738	8.812	0.074
5	200	8.617	8.723	0.106
5	202	8.583	8.671	0.088
5	203	8.723	8.784	0.061
5	204	8.617	8.679	0.062
5	206	8.688	8.796	0.108
10	207	8.670	8.768	0.098
10	209	8.660	8.710	0.050
10	210	8.629	8.742	0.113
10	211	8.600	8.642	0.042
10	212	8.616	8.648	0.032
15	214	8.624	8.738	0.114
15	217	8.675	8.771	0.096
15	218	8.594	8.669	0.075
15	219	8.650	8.749	0.099
15	222	8.574	8.678	0.104
30	224	8.594	8.690	0.096
30	225	8.588	8.702	0.114
30	226	8.593	8.676	0.083
30	227	8.663	8.780	0.117
30	229	8.615	8.702	0.087
35	230	8.740	8.804	0.064
35	231	8.716	8.799	0.083
35	232	8.610	8.699	0.089
35	234	8.618	8.698	0.080
35	237	8.612	8.715	0.103
50	272	8.589	8.666	0.077
50	275	8.662	8.788	0.126
50	276	8.680	8.812	0.132
50	277	8.665	8.798	0.133
50	278	8.600	8.686	0.086
55	279	8.597	8.648	0.051
55	280	8.638	8.760	0.122
55	281	8.608	8.732	0.124
55	282	8.669	8.790	0.121
55	283	8.589	8.674	0.085
100	317	8.247	8.221	-0.026
100	318	8.172	8.219	0.047
100	319	8.157	8.219	0.062
100	320	8.172	8.166	-0.006
100	321	8.180	8.232	0.052
100	322	8.289	8.238	-0.051
100	323	8.218	8.155	-0.063
100	324	8.224	8.218	-0.006
100	326	8.153	8.156	0.003
100	327	8.157	8.214	0.057
100	328	8.206	8.212	0.006
100	329	8.218	8.060	-0.158
100	332	8.185	8.202	0.017
100	333	8.163	8.201	0.038
100	334	8.196	8.213	0.017
100	335	8.150	8.233	0.083
100	336	8.203	8.305	0.102
100	337	8.170	8.283	0.113
100	338	8.206	8.222	0.016
100	339	8.200	8.187	-0.013
100	340	8.181	8.177	-0.004
100	341	8.216	8.209	-0.007
100	342	8.117	8.153	0.036
100	343	8.178	8.131	-0.047
105	344	8.212	8.144	-0.068
105	346	8.153	8.027	-0.126
105	349	8.239	8.216	-0.023
105	350	8.174	8.175	0.001
105	351	8.198	8.163	-0.035
105	352	8.209	8.190	-0.019
	Max	8.740	8.812	0.133
	Average	8.439	8.488	0.049
	Min	8.117	8.027	-0.158
	Std Dev	0.227	0.275	0.063



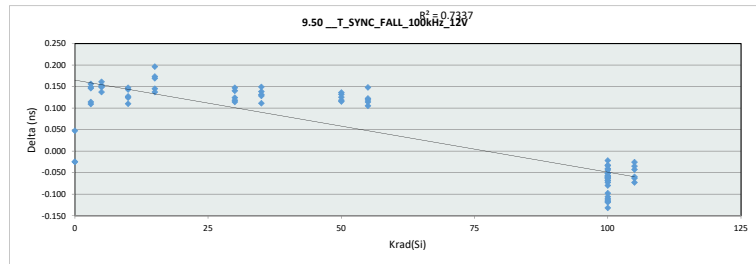
		9.46 __T_SYNC_FALL_2MHz_5V												
Test Site														
Tester														
Test Number														
Max Limit	17	ns												
Min Limit		ns												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	8.167	8.670	8.671	8.642	8.669	8.676	8.698	8.666	8.648	8.060	8.027			
Average	8.216	8.768	8.731	8.702	8.721	8.710	8.743	8.750	8.721	8.201	8.153			
Max	8.267	8.812	8.796	8.768	8.771	8.780	8.804	8.812	8.790	8.305	8.216			
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000			



HDR TID Report  
TPS7H5004-SP

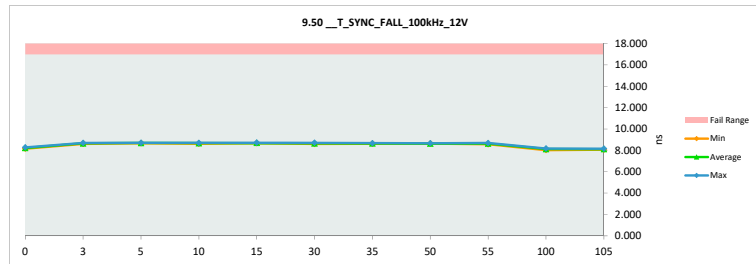
		9.50 __T_SYNC_FALL_100kHz_12V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.223	8.270	0.047
0	297	8.174	8.149	-0.025
0	298	8.232	8.207	-0.025
3	192	8.548	8.662	0.114
3	193	8.542	8.698	0.156
3	194	8.523	8.669	0.146
3	196	8.498	8.607	0.109
3	197	8.496	8.645	0.149
5	200	8.555	8.704	0.149
5	202	8.524	8.661	0.137
5	203	8.553	8.702	0.149
5	204	8.524	8.677	0.153
5	206	8.526	8.687	0.161
10	207	8.549	8.696	0.147
10	209	8.547	8.690	0.143
10	210	8.518	8.642	0.124
10	211	8.513	8.623	0.110
10	212	8.542	8.669	0.127
15	214	8.519	8.664	0.145
15	217	8.560	8.697	0.137
15	218	8.517	8.690	0.173
15	219	8.549	8.718	0.169
15	222	8.489	8.685	0.196
30	224	8.564	8.704	0.140
30	225	8.529	8.643	0.114
30	226	8.472	8.596	0.124
30	227	8.555	8.702	0.147
30	229	8.512	8.630	0.118
35	230	8.511	8.649	0.138
35	231	8.511	8.660	0.149
35	232	8.486	8.617	0.131
35	234	8.543	8.671	0.128
35	237	8.560	8.671	0.111
50	272	8.499	8.635	0.136
50	275	8.512	8.627	0.115
50	276	8.536	8.661	0.125
50	277	8.513	8.631	0.118
50	278	8.510	8.642	0.132
55	279	8.538	8.686	0.148
55	280	8.461	8.566	0.105
55	281	8.520	8.642	0.122
55	282	8.522	8.636	0.114
55	283	8.539	8.658	0.119
100	317	8.178	8.156	-0.022
100	318	8.173	8.132	-0.041
100	319	8.174	8.141	-0.033
100	320	8.141	8.084	-0.057
100	321	8.131	8.014	-0.117
100	322	8.215	8.164	-0.051
100	323	8.123	8.089	-0.034
100	324	8.220	8.088	-0.132
100	326	8.181	8.118	-0.063
100	327	8.159	8.086	-0.073
100	328	8.211	8.113	-0.098
100	329	8.195	8.134	-0.061
100	332	8.197	8.078	-0.119
100	333	8.154	8.110	-0.044
100	334	8.167	8.087	-0.080
100	335	8.240	8.129	-0.111
100	336	8.227	8.164	-0.063
100	337	8.272	8.166	-0.106
100	338	8.203	8.134	-0.069
100	339	8.241	8.127	-0.114
100	340	8.222	8.164	-0.058
100	341	8.165	8.113	-0.052
100	342	8.188	8.128	-0.060
100	343	8.156	8.088	-0.068
105	344	8.142	8.069	-0.073
105	346	8.158	8.132	-0.026
105	349	8.196	8.133	-0.063
105	350	8.156	8.121	-0.035
105	351	8.109	8.066	-0.043
105	352	8.212	8.152	-0.060
	Max	8.564	8.718	0.196
	Average	8.372	8.418	0.047
	Min	8.109	8.014	-0.132
	Std Dev	0.173	0.271	0.103



		9.50 __T_SYNC_FA	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	

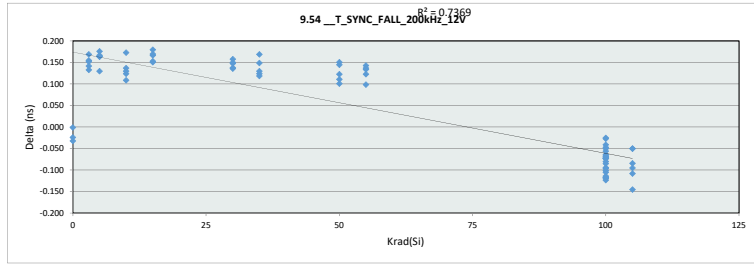
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.149	8.607	8.661	8.623	8.664	8.596	8.617	8.627	8.566	8.014	8.066
Average	8.209	8.656	8.686	8.664	8.691	8.655	8.654	8.639	8.638	8.117	8.112
Max	8.270	8.698	8.704	8.696	8.718	8.704	8.671	8.661	8.686	8.166	8.152
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



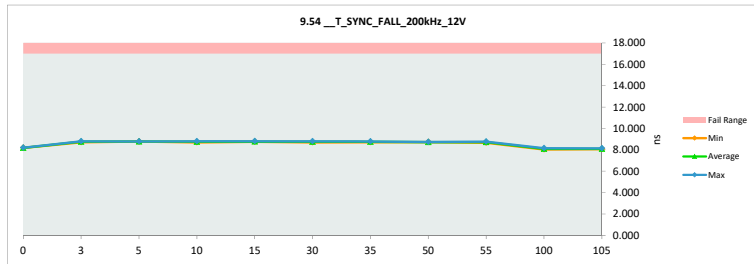
HDR TID Report  
TPS7H5004-SP

		9.54 __T_SYNC_FALL_200kHz_12V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.187	8.185	-0.002
0	297	8.217	8.192	-0.025
0	298	8.208	8.175	-0.033
3	192	8.633	8.787	0.154
3	193	8.623	8.791	0.168
3	194	8.615	8.747	0.132
3	196	8.565	8.706	0.141
3	197	8.596	8.747	0.151
5	200	8.618	8.784	0.166
5	202	8.604	8.767	0.163
5	203	8.641	8.770	0.129
5	204	8.605	8.768	0.163
5	206	8.596	8.771	0.175
10	207	8.634	8.770	0.136
10	209	8.624	8.796	0.172
10	210	8.601	8.730	0.129
10	211	8.582	8.690	0.108
10	212	8.628	8.751	0.123
15	214	8.591	8.741	0.150
15	217	8.623	8.789	0.166
15	218	8.623	8.775	0.152
15	219	8.627	8.806	0.179
15	222	8.594	8.763	0.169
30	224	8.651	8.788	0.137
30	225	8.595	8.742	0.147
30	226	8.550	8.685	0.135
30	227	8.622	8.779	0.157
30	229	8.573	8.722	0.149
35	230	8.578	8.746	0.168
35	231	8.614	8.732	0.118
35	232	8.574	8.697	0.123
35	234	8.624	8.753	0.129
35	237	8.625	8.773	0.148
50	272	8.586	8.736	0.150
50	275	8.604	8.714	0.110
50	276	8.633	8.733	0.100
50	277	8.595	8.717	0.122
50	278	8.575	8.719	0.144
55	279	8.624	8.766	0.142
55	280	8.535	8.657	0.122
55	281	8.595	8.728	0.133
55	282	8.608	8.706	0.098
55	283	8.622	8.758	0.136
100	317	8.206	8.132	-0.074
100	318	8.187	8.117	-0.070
100	319	8.141	8.115	-0.026
100	320	8.119	8.046	-0.073
100	321	8.107	8.026	-0.081
100	322	8.204	8.084	-0.120
100	323	8.130	8.065	-0.065
100	324	8.212	8.126	-0.086
100	326	8.168	8.050	-0.118
100	327	8.139	8.037	-0.102
100	328	8.129	8.078	-0.051
100	329	8.183	8.068	-0.115
100	332	8.168	8.073	-0.095
100	333	8.194	8.088	-0.106
100	334	8.109	8.081	-0.028
100	335	8.220	8.096	-0.124
100	336	8.158	8.101	-0.057
100	337	8.217	8.144	-0.073
100	338	8.158	8.116	-0.042
100	339	8.205	8.137	-0.068
100	340	8.209	8.135	-0.074
100	341	8.190	8.072	-0.118
100	342	8.157	8.109	-0.048
100	343	8.173	8.076	-0.097
105	344	8.189	8.080	-0.109
105	346	8.139	8.088	-0.051
105	349	8.247	8.101	-0.146
105	350	8.151	8.100	-0.051
105	351	8.139	8.054	-0.085
105	352	8.225	8.129	-0.096
	Max	8.651	8.806	0.179
	Average	8.411	8.454	0.044
	Min	8.107	8.026	-0.146
	Std Dev	0.218	0.327	0.113



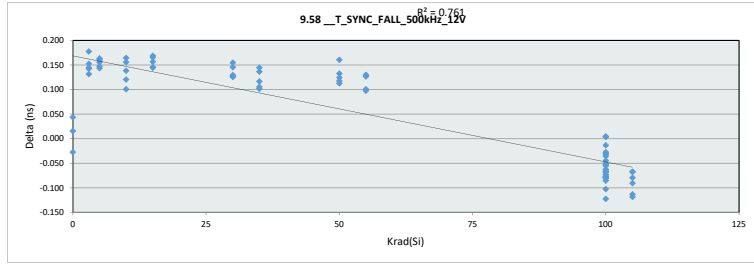
		9.54 __T_SYNC_FALL_200kHz_12V												
Test Site														
Tester														
Test Number														
Max Limit	17	ns												
Min Limit		ns												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	8.175	8.706	8.767	8.690	8.741	8.685	8.697	8.714	8.657	8.026	8.054			
Average	8.184	8.756	8.772	8.747	8.775	8.743	8.740	8.724	8.723	8.091	8.092			
Max	8.192	8.791	8.784	8.796	8.806	8.788	8.773	8.736	8.766	8.144	8.129			
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000			



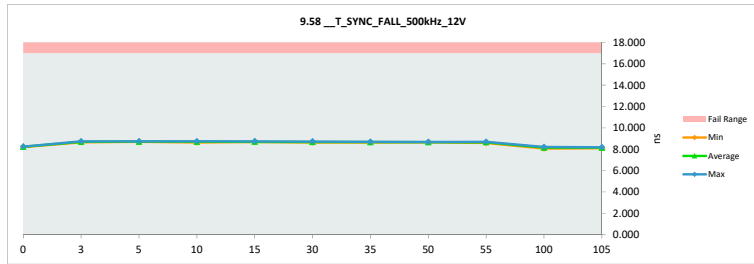
HDR TID Report  
TPS7H5004-SP

		9.58 __T_SYNC_FALL_500kHz_12V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.234	8.249	0.015
0	297	8.237	8.209	-0.028
0	298	8.192	8.235	0.043
3	192	8.561	8.705	0.144
3	193	8.561	8.738	0.177
3	194	8.551	8.693	0.142
3	196	8.519	8.650	0.131
3	197	8.519	8.671	0.152
5	200	8.574	8.734	0.160
5	202	8.558	8.713	0.155
5	203	8.575	8.718	0.143
5	204	8.549	8.696	0.147
5	206	8.546	8.709	0.163
10	207	8.568	8.732	0.164
10	209	8.565	8.720	0.155
10	210	8.531	8.669	0.138
10	211	8.534	8.634	0.100
10	212	8.566	8.686	0.120
15	214	8.538	8.683	0.145
15	217	8.565	8.733	0.168
15	218	8.551	8.707	0.156
15	219	8.589	8.754	0.165
15	222	8.540	8.684	0.144
30	224	8.579	8.733	0.154
30	225	8.541	8.686	0.145
30	226	8.504	8.630	0.126
30	227	8.576	8.705	0.129
30	229	8.538	8.663	0.125
35	230	8.544	8.688	0.144
35	231	8.545	8.681	0.136
35	232	8.518	8.634	0.116
35	234	8.570	8.675	0.105
35	237	8.578	8.679	0.101
50	272	8.516	8.676	0.160
50	275	8.525	8.657	0.132
50	276	8.557	8.674	0.117
50	277	8.536	8.648	0.112
50	278	8.527	8.651	0.124
55	279	8.559	8.688	0.129
55	280	8.492	8.589	0.097
55	281	8.543	8.669	0.126
55	282	8.554	8.654	0.100
55	283	8.561	8.690	0.129
100	317	8.251	8.128	-0.123
100	318	8.245	8.182	-0.063
100	319	8.208	8.157	-0.051
100	320	8.179	8.093	-0.086
100	321	8.134	8.065	-0.069
100	322	8.196	8.182	-0.014
100	323	8.113	8.085	-0.028
100	324	8.199	8.163	-0.036
100	326	8.219	8.142	-0.077
100	327	8.193	8.125	-0.068
100	328	8.241	8.168	-0.073
100	329	8.261	8.158	-0.103
100	332	8.235	8.154	-0.081
100	333	8.174	8.094	-0.080
100	334	8.179	8.126	-0.053
100	335	8.203	8.207	0.004
100	336	8.232	8.166	-0.066
100	337	8.250	8.194	-0.056
100	338	8.195	8.150	-0.045
100	339	8.229	8.150	-0.079
100	340	8.288	8.211	-0.077
100	341	8.196	8.166	-0.030
100	342	8.164	8.167	0.003
100	343	8.187	8.155	-0.032
105	344	8.253	8.162	-0.091
105	346	8.216	8.102	-0.114
105	349	8.260	8.141	-0.119
105	350	8.225	8.157	-0.068
105	351	8.196	8.128	-0.068
105	352	8.268	8.188	-0.080
	Max	8.589	8.754	0.177
	Average	8.397	8.446	0.049
	Min	8.113	8.065	-0.123
	Std Dev	0.170	0.268	0.102



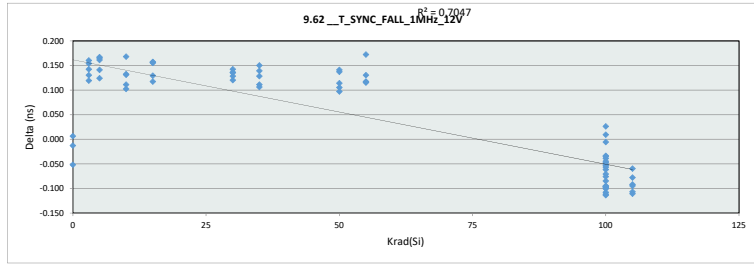
		9.58 __T_SYNC_FALL_500kHz_12V												
Test Site														
Tester														
Test Number														
Max Limit	17	ns												
Min Limit		ns												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	8.209	8.650	8.696	8.634	8.683	8.630	8.634	8.648	8.589	8.065	8.102			
Average	8.231	8.691	8.714	8.688	8.712	8.683	8.671	8.661	8.658	8.150	8.146			
Max	8.249	8.738	8.734	8.732	8.754	8.733	8.688	8.676	8.690	8.211	8.188			
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000			



HDR TID Report  
TPS7H5004-SP

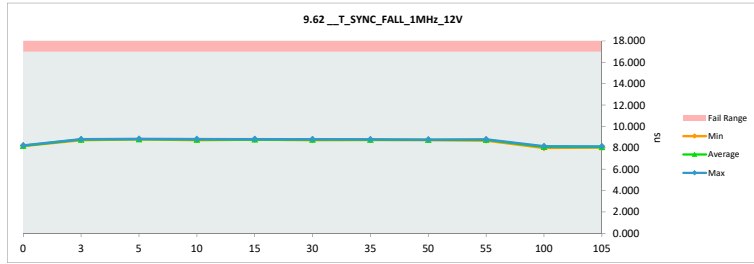
		9.62 __T_SYNC_FALL_1MHz_12V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.224	8.230	0.006
0	297	8.206	8.193	-0.013
0	298	8.212	8.160	-0.052
3	192	8.644	8.804	0.160
3	193	8.655	8.809	0.154
3	194	8.644	8.774	0.130
3	196	8.608	8.727	0.119
3	197	8.621	8.763	0.142
5	200	8.668	8.835	0.167
5	202	8.632	8.797	0.165
5	203	8.677	8.801	0.124
5	204	8.649	8.790	0.141
5	206	8.629	8.790	0.161
10	207	8.675	8.807	0.132
10	209	8.651	8.819	0.168
10	210	8.626	8.757	0.131
10	211	8.616	8.718	0.102
10	212	8.650	8.761	0.111
15	214	8.625	8.780	0.155
15	217	8.659	8.815	0.156
15	218	8.654	8.771	0.117
15	219	8.680	8.809	0.129
15	222	8.623	8.780	0.157
30	224	8.678	8.813	0.135
30	225	8.648	8.768	0.120
30	226	8.599	8.727	0.128
30	227	8.666	8.808	0.142
30	229	8.611	8.747	0.136
35	230	8.623	8.773	0.150
35	231	8.645	8.773	0.128
35	232	8.630	8.736	0.106
35	234	8.655	8.794	0.139
35	237	8.657	8.768	0.111
50	272	8.628	8.769	0.141
50	275	8.622	8.727	0.105
50	276	8.658	8.772	0.114
50	277	8.637	8.734	0.097
50	278	8.612	8.749	0.137
55	279	8.653	8.783	0.130
55	280	8.566	8.682	0.116
55	281	8.626	8.741	0.115
55	282	8.638	8.755	0.117
55	283	8.625	8.797	0.172
100	317	8.199	8.091	-0.108
100	318	8.177	8.120	-0.057
100	319	8.141	8.107	-0.034
100	320	8.145	8.074	-0.071
100	321	8.088	7.974	-0.114
100	322	8.116	8.110	-0.006
100	323	8.072	8.037	-0.035
100	324	8.160	8.115	-0.045
100	326	8.137	8.042	-0.095
100	327	8.134	8.082	-0.052
100	328	8.178	8.081	-0.097
100	329	8.120	8.146	0.026
100	332	8.183	8.084	-0.099
100	333	8.101	8.047	-0.054
100	334	8.171	8.075	-0.096
100	335	8.180	8.079	-0.101
100	336	8.122	8.131	0.009
100	337	8.238	8.126	-0.112
100	338	8.133	8.085	-0.048
100	339	8.172	8.075	-0.097
100	340	8.206	8.130	-0.076
100	341	8.194	8.109	-0.085
100	342	8.160	8.098	-0.062
100	343	8.121	8.082	-0.039
105	344	8.140	8.080	-0.060
105	346	8.144	8.033	-0.111
105	349	8.176	8.084	-0.092
105	350	8.148	8.070	-0.078
105	351	8.165	8.058	-0.107
105	352	8.225	8.130	-0.095
	Max	8.680	8.835	0.172
	Average	8.423	8.467	0.044
	Min	8.072	7.974	-0.114
	Std Dev	0.242	0.342	0.104



		9.62 __T_SYNC_FALL_1MHz_12V	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	

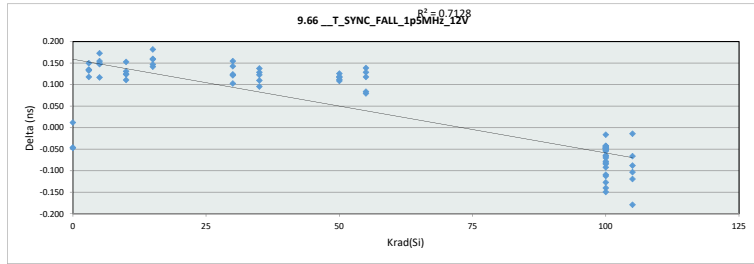
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.160	8.727	8.790	8.718	8.771	8.727	8.736	8.727	8.682	7.974	8.033
Average	8.194	8.775	8.803	8.772	8.791	8.773	8.769	8.750	8.752	8.088	8.076
Max	8.230	8.809	8.835	8.819	8.815	8.813	8.794	8.772	8.797	8.146	8.130
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

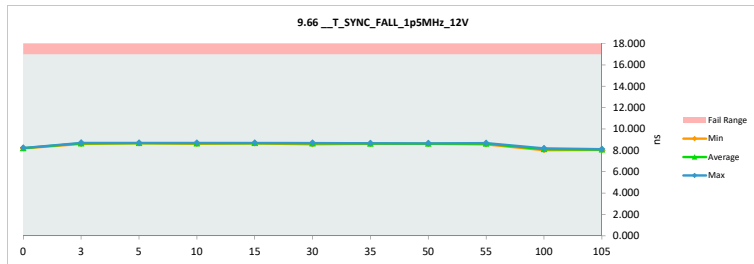
		9.66 __T_SYNC_FALL_1p5MHz_12V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.214	8.225	0.011
0	297	8.257	8.209	-0.048
0	298	8.220	8.173	-0.047
3	192	8.539	8.671	0.132
3	193	8.553	8.702	0.149
3	194	8.510	8.644	0.134
3	196	8.479	8.596	0.117
3	197	8.502	8.636	0.134
5	200	8.548	8.697	0.149
5	202	8.517	8.664	0.147
5	203	8.533	8.649	0.116
5	204	8.513	8.685	0.172
5	206	8.525	8.679	0.154
10	207	8.552	8.676	0.124
10	209	8.536	8.688	0.152
10	210	8.495	8.625	0.130
10	211	8.499	8.609	0.110
10	212	8.536	8.659	0.123
15	214	8.509	8.650	0.141
15	217	8.539	8.685	0.146
15	218	8.518	8.677	0.159
15	219	8.543	8.701	0.158
15	222	8.487	8.668	0.181
30	224	8.559	8.701	0.142
30	225	8.531	8.654	0.123
30	226	8.464	8.566	0.102
30	227	8.542	8.696	0.154
30	229	8.498	8.619	0.121
35	230	8.512	8.640	0.128
35	231	8.511	8.648	0.137
35	232	8.508	8.603	0.095
35	234	8.538	8.660	0.122
35	237	8.545	8.654	0.109
50	272	8.511	8.636	0.125
50	275	8.503	8.611	0.108
50	276	8.543	8.661	0.118
50	277	8.511	8.627	0.116
50	278	8.501	8.612	0.111
55	279	8.543	8.681	0.138
55	280	8.473	8.556	0.083
55	281	8.500	8.628	0.128
55	282	8.534	8.613	0.079
55	283	8.546	8.663	0.117
100	317	8.173	8.130	-0.043
100	318	8.179	8.126	-0.053
100	319	8.150	8.100	-0.050
100	320	8.133	8.054	-0.079
100	321	8.155	8.027	-0.128
100	322	8.124	8.076	-0.048
100	323	8.166	8.016	-0.150
100	324	8.147	8.098	-0.049
100	326	8.234	8.093	-0.141
100	327	8.136	8.093	-0.043
100	328	8.152	8.107	-0.045
100	329	8.180	8.070	-0.110
100	332	8.144	8.089	-0.055
100	333	8.156	8.110	-0.046
100	334	8.165	8.080	-0.085
100	335	8.248	8.183	-0.065
100	336	8.206	8.139	-0.067
100	337	8.213	8.130	-0.083
100	338	8.232	8.119	-0.113
100	339	8.205	8.188	-0.017
100	340	8.234	8.163	-0.071
100	341	8.170	8.077	-0.093
100	342	8.159	8.109	-0.050
100	343	8.153	8.105	-0.048
105	344	8.216	8.036	-0.180
105	346	8.194	8.074	-0.120
105	349	8.174	8.107	-0.067
105	350	8.098	8.083	-0.015
105	351	8.161	8.072	-0.089
105	352	8.218	8.114	-0.104
	Max	8.559	8.702	0.181
	Average	8.367	8.405	0.038
	Min	8.098	8.016	-0.180
	Std Dev	0.173	0.275	0.106



		9.66 __T_SYNC_FA	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	

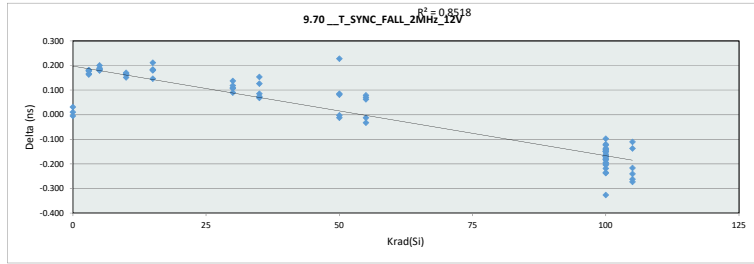
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.173	8.596	8.649	8.609	8.650	8.566	8.603	8.611	8.556	8.016	8.036
Average	8.202	8.650	8.675	8.651	8.676	8.647	8.641	8.629	8.628	8.103	8.081
Max	8.225	8.702	8.697	8.688	8.701	8.701	8.660	8.661	8.681	8.188	8.114
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

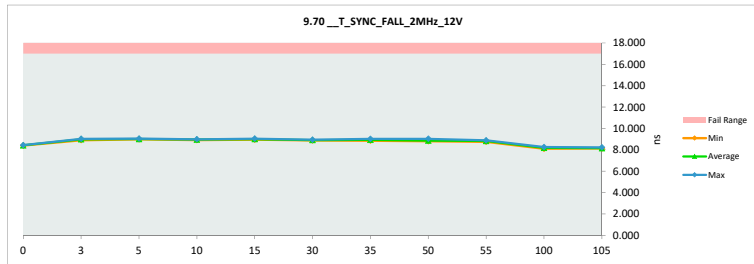
		9.70 __T_SYNC_FALL_2MHz_12V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.398	8.429	0.031
0	297	8.444	8.438	-0.006
0	298	8.379	8.389	0.010
3	192	8.862	9.028	0.166
3	193	8.820	8.999	0.179
3	194	8.802	8.979	0.177
3	196	8.730	8.893	0.163
3	197	8.828	9.009	0.181
5	200	8.826	9.005	0.179
5	202	8.779	8.979	0.200
5	203	8.853	9.042	0.189
5	204	8.809	8.994	0.185
5	206	8.835	9.019	0.184
10	207	8.821	8.985	0.164
10	209	8.819	8.983	0.164
10	210	8.809	8.960	0.151
10	211	8.739	8.902	0.163
10	212	8.802	8.972	0.170
15	214	8.799	8.945	0.146
15	217	8.859	9.041	0.182
15	218	8.805	8.985	0.180
15	219	8.856	9.039	0.183
15	222	8.755	8.966	0.211
30	224	8.821	8.927	0.106
30	225	8.787	8.924	0.137
30	226	8.780	8.869	0.089
30	227	8.826	8.944	0.118
30	229	8.774	8.883	0.109
35	230	8.863	9.016	0.153
35	231	8.852	8.978	0.126
35	232	8.778	8.846	0.068
35	234	8.824	8.898	0.074
35	237	8.812	8.897	0.085
50	272	8.768	8.853	0.085
50	275	8.808	8.795	-0.013
50	276	8.800	9.027	0.227
50	277	8.803	8.800	-0.003
50	278	8.766	8.847	0.081
55	279	8.819	8.889	0.070
55	280	8.757	8.744	-0.013
55	281	8.767	8.829	0.062
55	282	8.829	8.796	-0.033
55	283	8.777	8.855	0.078
100	317	8.401	8.218	-0.183
100	318	8.330	8.179	-0.151
100	319	8.309	8.168	-0.141
100	320	8.284	8.133	-0.151
100	321	8.247	8.149	-0.098
100	322	8.404	8.185	-0.219
100	323	8.346	8.202	-0.144
100	324	8.416	8.178	-0.238
100	326	8.357	8.188	-0.169
100	327	8.318	8.113	-0.205
100	328	8.387	8.215	-0.172
100	329	8.411	8.231	-0.180
100	332	8.345	8.184	-0.161
100	333	8.279	8.158	-0.121
100	334	8.353	8.152	-0.201
100	335	8.407	8.238	-0.169
100	336	8.371	8.234	-0.137
100	337	8.407	8.212	-0.195
100	338	8.376	8.207	-0.169
100	339	8.377	8.253	-0.124
100	340	8.365	8.181	-0.184
100	341	8.355	8.205	-0.150
100	342	8.420	8.093	-0.327
100	343	8.351	8.115	-0.236
105	344	8.407	8.133	-0.274
105	346	8.333	8.222	-0.111
105	349	8.384	8.167	-0.217
105	350	8.364	8.123	-0.241
105	351	8.270	8.132	-0.138
105	352	8.416	8.153	-0.263
	Max	8.863	9.042	0.227
	Average	8.606	8.602	-0.004
	Min	8.247	8.093	-0.327
	Std Dev	0.225	0.377	0.162



		9.70 __T_SYNC_FA	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	

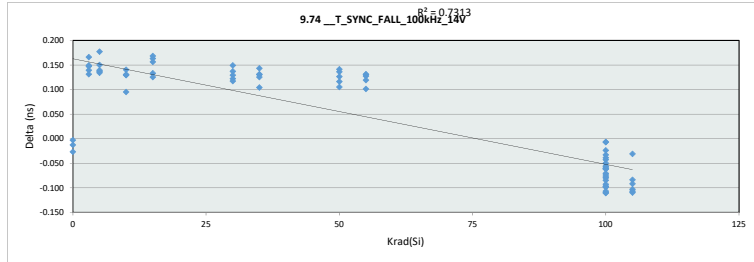
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.389	8.893	8.979	8.902	8.945	8.869	8.846	8.795	8.744	8.093	8.123
Average	8.419	8.982	9.008	8.960	8.995	8.909	8.927	8.864	8.823	8.183	8.155
Max	8.438	9.028	9.042	8.985	9.041	8.944	9.016	9.027	8.889	8.253	8.222
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

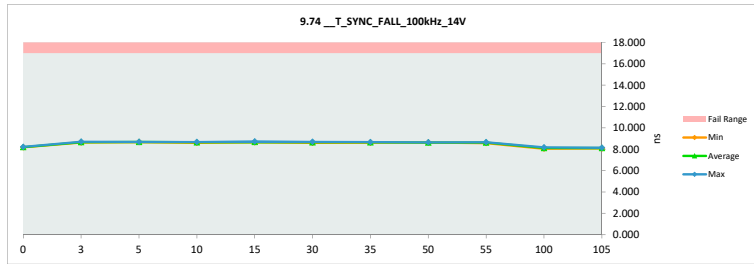
		9.74 __T_SYNC_FALL_100kHz_14V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.220	8.217	-0.003
0	297	8.214	8.201	-0.013
0	298	8.201	8.174	-0.027
3	192	8.527	8.666	0.139
3	193	8.539	8.705	0.166
3	194	8.511	8.660	0.149
3	196	8.490	8.621	0.131
3	197	8.492	8.639	0.147
5	200	8.546	8.683	0.137
5	202	8.517	8.667	0.150
5	203	8.551	8.685	0.134
5	204	8.512	8.689	0.177
5	206	8.536	8.675	0.139
10	207	8.544	8.673	0.129
10	209	8.539	8.669	0.130
10	210	8.490	8.630	0.140
10	211	8.505	8.600	0.095
10	212	8.534	8.664	0.130
15	214	8.513	8.638	0.125
15	217	8.541	8.697	0.156
15	218	8.533	8.666	0.133
15	219	8.556	8.719	0.163
15	222	8.499	8.667	0.168
30	224	8.566	8.695	0.129
30	225	8.535	8.652	0.117
30	226	8.473	8.595	0.122
30	227	8.542	8.691	0.149
30	229	8.494	8.631	0.137
35	230	8.508	8.651	0.143
35	231	8.507	8.637	0.130
35	232	8.507	8.611	0.104
35	234	8.526	8.657	0.131
35	237	8.541	8.666	0.125
50	272	8.499	8.635	0.136
50	275	8.509	8.625	0.116
50	276	8.543	8.648	0.105
50	277	8.500	8.626	0.126
50	278	8.496	8.637	0.141
55	279	8.535	8.662	0.127
55	280	8.453	8.572	0.119
55	281	8.498	8.629	0.131
55	282	8.522	8.623	0.101
55	283	8.532	8.662	0.130
100	317	8.222	8.129	-0.093
100	318	8.214	8.103	-0.111
100	319	8.177	8.121	-0.056
100	320	8.110	8.067	-0.043
100	321	8.097	8.047	-0.050
100	322	8.219	8.134	-0.085
100	323	8.134	8.056	-0.078
100	324	8.181	8.083	-0.098
100	326	8.185	8.078	-0.107
100	327	8.148	8.141	-0.007
100	328	8.181	8.110	-0.071
100	329	8.215	8.154	-0.061
100	332	8.163	8.130	-0.033
100	333	8.179	8.069	-0.110
100	334	8.149	8.088	-0.061
100	335	8.213	8.116	-0.097
100	336	8.183	8.125	-0.058
100	337	8.212	8.173	-0.039
100	338	8.215	8.153	-0.062
100	339	8.200	8.120	-0.080
100	340	8.222	8.123	-0.099
100	341	8.138	8.114	-0.024
100	342	8.173	8.166	-0.007
100	343	8.159	8.085	-0.074
105	344	8.172	8.141	-0.031
105	346	8.173	8.066	-0.107
105	349	8.198	8.095	-0.103
105	350	8.187	8.095	-0.092
105	351	8.212	8.102	-0.110
105	352	8.225	8.141	-0.084
	Max	8.566	8.719	0.177
	Average	8.368	8.411	0.044
	Min	8.097	8.047	-0.111
	Std Dev	0.170	0.270	0.104



		9.74 __T_SYNC_FALL_100kHz_14V	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.174	8.621	8.667	8.600	8.638	8.595	8.611	8.625	8.572	8.047	8.066
Average	8.197	8.658	8.680	8.647	8.677	8.653	8.644	8.634	8.630	8.112	8.107
Max	8.217	8.705	8.689	8.673	8.719	8.695	8.666	8.648	8.662	8.173	8.141
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

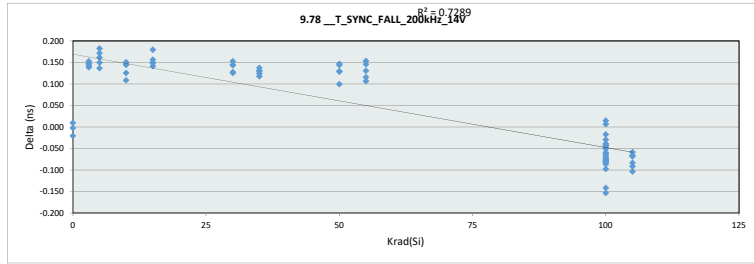




HDR TID Report  
TPS7H5004-SP

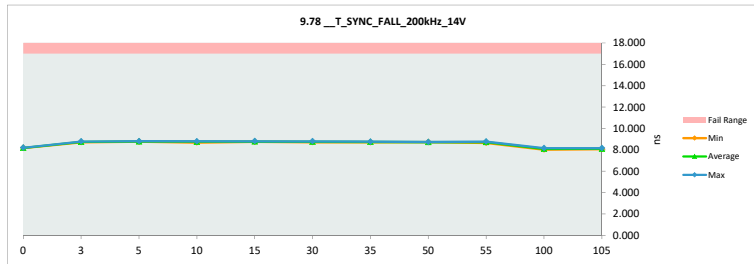
9.78 __T_SYNC_FALL_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.179	8.188	0.009
0	297	8.180	8.177	-0.003
0	298	8.184	8.163	-0.021
3	192	8.604	8.756	0.152
3	193	8.633	8.774	0.141
3	194	8.604	8.742	0.138
3	196	8.553	8.699	0.146
3	197	8.577	8.723	0.146
5	200	8.616	8.798	0.182
5	202	8.604	8.753	0.149
5	203	8.646	8.782	0.136
5	204	8.599	8.760	0.161
5	206	8.599	8.770	0.171
10	207	8.620	8.764	0.144
10	209	8.623	8.773	0.150
10	210	8.578	8.723	0.145
10	211	8.564	8.672	0.108
10	212	8.620	8.745	0.125
15	214	8.615	8.756	0.141
15	217	8.612	8.791	0.179
15	218	8.611	8.759	0.148
15	219	8.650	8.806	0.156
15	222	8.587	8.736	0.149
30	224	8.647	8.790	0.143
30	225	8.592	8.744	0.152
30	226	8.569	8.696	0.127
30	227	8.641	8.766	0.125
30	229	8.579	8.722	0.143
35	230	8.604	8.728	0.124
35	231	8.602	8.732	0.130
35	232	8.575	8.692	0.117
35	234	8.613	8.742	0.129
35	237	8.611	8.748	0.137
50	272	8.568	8.714	0.146
50	275	8.586	8.714	0.128
50	276	8.623	8.722	0.099
50	277	8.592	8.721	0.129
50	278	8.565	8.708	0.143
55	279	8.615	8.760	0.145
55	280	8.534	8.640	0.106
55	281	8.593	8.723	0.130
55	282	8.605	8.720	0.115
55	283	8.594	8.747	0.153
100	317	8.185	8.105	-0.080
100	318	8.137	8.050	-0.087
100	319	8.152	8.102	-0.050
100	320	8.123	8.048	-0.075
100	321	8.111	8.013	-0.098
100	322	8.107	8.089	-0.018
100	323	8.106	8.055	-0.051
100	324	8.177	8.094	-0.083
100	326	8.170	8.028	-0.142
100	327	8.138	8.074	-0.064
100	328	8.179	8.097	-0.082
100	329	8.189	8.112	-0.077
100	332	8.112	8.072	-0.040
100	333	8.086	8.100	0.014
100	334	8.136	8.096	-0.040
100	335	8.223	8.144	-0.079
100	336	8.187	8.033	-0.154
100	337	8.224	8.151	-0.073
100	338	8.172	8.112	-0.060
100	339	8.167	8.124	-0.043
100	340	8.199	8.154	-0.045
100	341	8.156	8.088	-0.068
100	342	8.124	8.094	-0.030
100	343	8.127	8.133	0.006
105	344	8.164	8.105	-0.059
105	346	8.177	8.073	-0.104
105	349	8.187	8.095	-0.092
105	350	8.122	8.053	-0.069
105	351	8.157	8.073	-0.084
105	352	8.218	8.152	-0.066
	Max	8.650	8.806	0.182
	Average	8.401	8.450	0.049
	Min	8.086	8.013	-0.154
	Std Dev	0.223	0.324	0.105



9.78 __T_SYNC_FA	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

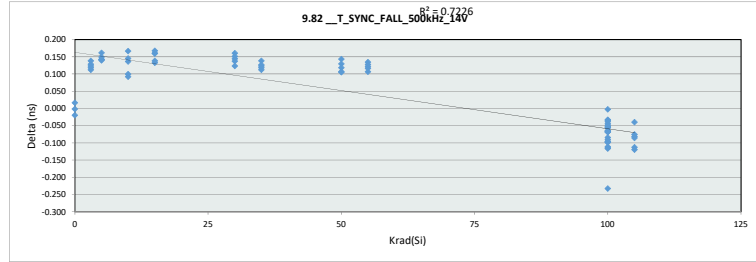
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.163	8.699	8.753	8.672	8.736	8.696	8.692	8.708	8.640	8.013	8.053
Average	8.176	8.739	8.773	8.735	8.770	8.744	8.728	8.716	8.718	8.090	8.092
Max	8.188	8.774	8.798	8.773	8.806	8.790	8.748	8.722	8.760	8.154	8.152
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

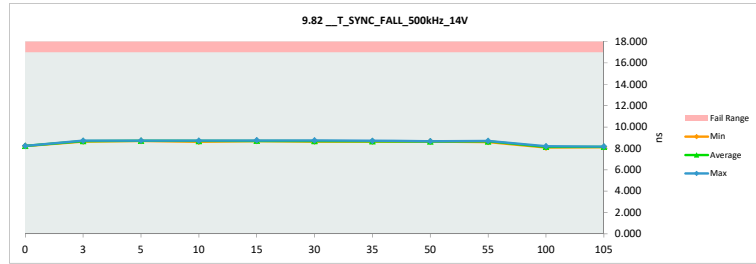
		9.82 __T_SYNC_FALL_500kHz_14V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.248	8.228	-0.020
0	297	8.234	8.232	-0.002
0	298	8.201	8.217	0.016
3	192	8.563	8.691	0.128
3	193	8.591	8.714	0.123
3	194	8.542	8.660	0.118
3	196	8.517	8.629	0.112
3	197	8.530	8.668	0.138
5	200	8.577	8.719	0.142
5	202	8.550	8.699	0.149
5	203	8.569	8.711	0.142
5	204	8.538	8.699	0.161
5	206	8.546	8.685	0.139
10	207	8.570	8.706	0.136
10	209	8.546	8.712	0.166
10	210	8.543	8.642	0.099
10	211	8.524	8.616	0.092
10	212	8.556	8.701	0.145
15	214	8.546	8.678	0.132
15	217	8.563	8.722	0.159
15	218	8.551	8.689	0.138
15	219	8.585	8.752	0.167
15	222	8.535	8.695	0.160
30	224	8.573	8.733	0.160
30	225	8.537	8.674	0.137
30	226	8.484	8.628	0.144
30	227	8.544	8.695	0.151
30	229	8.527	8.650	0.123
35	230	8.546	8.669	0.123
35	231	8.534	8.660	0.126
35	232	8.521	8.633	0.112
35	234	8.561	8.699	0.138
35	237	8.555	8.673	0.118
50	272	8.520	8.663	0.143
50	275	8.510	8.628	0.118
50	276	8.556	8.662	0.106
50	277	8.540	8.645	0.105
50	278	8.520	8.649	0.129
55	279	8.566	8.687	0.121
55	280	8.485	8.591	0.106
55	281	8.528	8.655	0.127
55	282	8.542	8.658	0.116
55	283	8.549	8.683	0.134
100	317	8.178	8.175	-0.003
100	318	8.210	8.144	-0.066
100	319	8.199	8.114	-0.085
100	320	8.159	8.094	-0.065
100	321	8.148	8.079	-0.069
100	322	8.185	8.150	-0.035
100	323	8.159	8.120	-0.039
100	324	8.194	8.149	-0.045
100	326	8.318	8.085	-0.233
100	327	8.174	8.121	-0.053
100	328	8.192	8.123	-0.069
100	329	8.239	8.148	-0.091
100	332	8.221	8.122	-0.099
100	333	8.187	8.070	-0.117
100	334	8.193	8.125	-0.068
100	335	8.229	8.117	-0.112
100	336	8.184	8.135	-0.049
100	337	8.266	8.168	-0.098
100	338	8.270	8.172	-0.098
100	339	8.245	8.190	-0.055
100	340	8.279	8.164	-0.115
100	341	8.213	8.156	-0.057
100	342	8.214	8.181	-0.033
100	343	8.204	8.108	-0.096
105	344	8.240	8.127	-0.113
105	346	8.191	8.105	-0.086
105	349	8.225	8.149	-0.076
105	350	8.204	8.164	-0.040
105	351	8.193	8.111	-0.082
105	352	8.274	8.154	-0.120
	Max	8.591	8.752	0.167
	Average	8.395	8.435	0.040
	Min	8.148	8.070	-0.233
	Std Dev	0.168	0.270	0.108



		9.82 __T_SYNC_FALL_500kHz_14V	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	

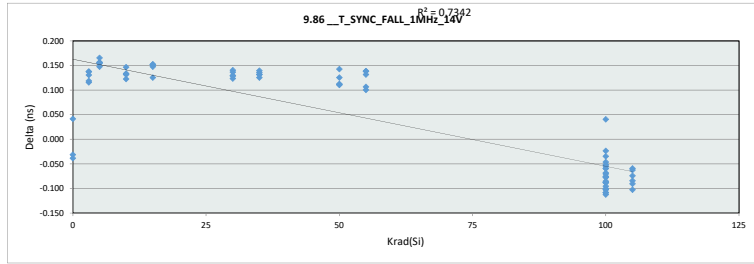
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.217	8.629	8.685	8.616	8.678	8.628	8.633	8.628	8.591	8.070	8.105
Average	8.226	8.672	8.703	8.675	8.707	8.676	8.667	8.649	8.655	8.134	8.135
Max	8.232	8.714	8.719	8.712	8.752	8.733	8.699	8.663	8.687	8.190	8.164
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

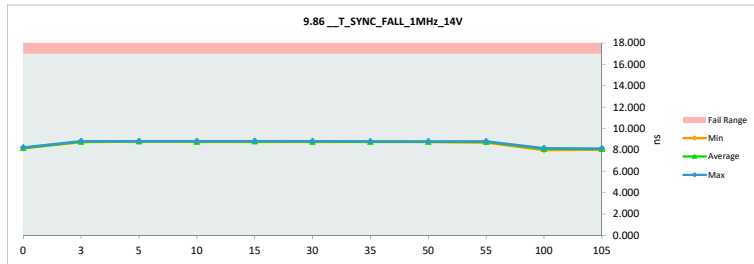
		9.86 __T_SYNC_FALL_1MHz_14V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.173	8.214	0.041
0	297	8.183	8.144	-0.039
0	298	8.177	8.145	-0.032
3	192	8.661	8.776	0.115
3	193	8.666	8.803	0.137
3	194	8.633	8.763	0.130
3	196	8.602	8.720	0.118
3	197	8.615	8.752	0.137
5	200	8.651	8.806	0.155
5	202	8.621	8.777	0.156
5	203	8.647	8.801	0.154
5	204	8.640	8.805	0.165
5	206	8.622	8.769	0.147
10	207	8.655	8.787	0.132
10	209	8.660	8.806	0.146
10	210	8.622	8.755	0.133
10	211	8.597	8.728	0.131
10	212	8.638	8.760	0.122
15	214	8.636	8.761	0.125
15	217	8.654	8.805	0.151
15	218	8.637	8.789	0.152
15	219	8.667	8.818	0.151
15	222	8.615	8.762	0.147
30	224	8.672	8.808	0.136
30	225	8.632	8.772	0.140
30	226	8.596	8.719	0.123
30	227	8.663	8.792	0.129
30	229	8.606	8.734	0.128
35	230	8.628	8.767	0.139
35	231	8.617	8.748	0.131
35	232	8.608	8.743	0.135
35	234	8.652	8.777	0.125
35	237	8.639	8.770	0.131
50	272	8.605	8.747	0.142
50	275	8.622	8.733	0.111
50	276	8.646	8.771	0.125
50	277	8.626	8.736	0.110
50	278	8.620	8.732	0.112
55	279	8.652	8.790	0.138
55	280	8.570	8.676	0.106
55	281	8.611	8.749	0.138
55	282	8.631	8.731	0.100
55	283	8.649	8.780	0.131
100	317	8.169	8.114	-0.055
100	318	8.160	8.083	-0.077
100	319	8.154	8.102	-0.052
100	320	8.100	8.040	-0.060
100	321	8.075	7.962	-0.113
100	322	8.181	8.121	-0.060
100	323	8.085	8.050	-0.035
100	324	8.188	8.110	-0.078
100	326	8.211	8.122	-0.089
100	327	8.132	8.063	-0.069
100	328	8.217	8.114	-0.103
100	329	8.217	8.108	-0.109
100	332	8.142	8.054	-0.088
100	333	8.145	8.056	-0.089
100	334	8.157	8.055	-0.102
100	335	8.164	8.117	-0.047
100	336	8.093	8.133	0.040
100	337	8.214	8.118	-0.096
100	338	8.175	8.066	-0.109
100	339	8.167	8.143	-0.024
100	340	8.188	8.087	-0.101
100	341	8.175	8.099	-0.076
100	342	8.153	8.067	-0.086
100	343	8.117	8.046	-0.071
105	344	8.165	8.074	-0.091
105	346	8.116	8.013	-0.103
105	349	8.156	8.071	-0.085
105	350	8.129	8.069	-0.060
105	351	8.127	8.052	-0.075
105	352	8.185	8.122	-0.063
	Max	8.672	8.818	0.165
	Average	8.417	8.460	0.042
	Min	8.075	7.962	-0.113
	Std Dev	0.240	0.341	0.105



		9.86 __T_SYNC_FA	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	

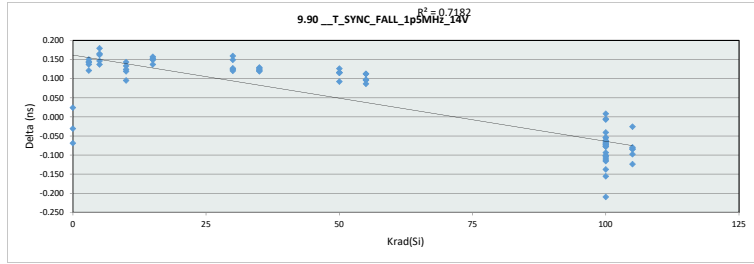
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.144	8.720	8.769	8.728	8.761	8.719	8.743	8.732	8.676	7.962	8.013
Average	8.168	8.763	8.792	8.767	8.787	8.765	8.761	8.744	8.745	8.085	8.067
Max	8.214	8.803	8.806	8.806	8.818	8.808	8.777	8.771	8.790	8.143	8.122
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

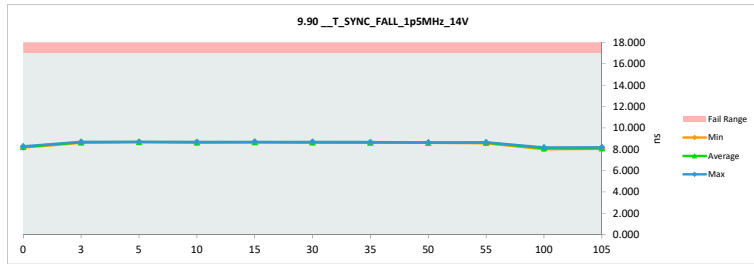
9.90 __T_SYNC_FALL_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.250	8.219	-0.031
0	297	8.251	8.275	0.024
0	298	8.231	8.162	-0.069
3	192	8.529	8.673	0.144
3	193	8.550	8.700	0.150
3	194	8.507	8.644	0.137
3	196	8.480	8.601	0.121
3	197	8.491	8.633	0.142
5	200	8.541	8.687	0.146
5	202	8.525	8.662	0.137
5	203	8.537	8.699	0.162
5	204	8.502	8.681	0.179
5	206	8.515	8.680	0.165
10	207	8.533	8.676	0.143
10	209	8.545	8.678	0.133
10	210	8.504	8.628	0.124
10	211	8.502	8.597	0.095
10	212	8.534	8.653	0.119
15	214	8.515	8.652	0.137
15	217	8.538	8.688	0.150
15	218	8.513	8.662	0.149
15	219	8.549	8.702	0.153
15	222	8.488	8.645	0.157
30	224	8.530	8.689	0.159
30	225	8.517	8.644	0.127
30	226	8.458	8.607	0.149
30	227	8.546	8.670	0.124
30	229	8.489	8.609	0.120
35	230	8.509	8.638	0.129
35	231	8.513	8.634	0.121
35	232	8.483	8.606	0.123
35	234	8.530	8.657	0.127
35	237	8.532	8.651	0.119
50	272	8.511	8.637	0.126
50	275	8.500	8.615	0.115
50	276	8.519	8.635	0.116
50	277	8.513	8.605	0.092
50	278	8.493	8.608	0.115
55	279	8.534	8.646	0.112
55	280	8.473	8.559	0.086
55	281	8.516	8.612	0.096
55	282	8.521	8.618	0.097
55	283	8.532	8.644	0.112
100	317	8.258	8.156	-0.102
100	318	8.163	8.089	-0.074
100	319	8.182	8.110	-0.072
100	320	8.177	8.061	-0.116
100	321	8.089	8.082	-0.007
100	322	8.185	8.047	-0.138
100	323	8.157	8.001	-0.156
100	324	8.201	8.088	-0.113
100	326	8.166	8.174	0.008
100	327	8.153	8.089	-0.064
100	328	8.142	8.136	-0.006
100	329	8.173	8.071	-0.102
100	332	8.175	8.121	-0.054
100	333	8.138	8.097	-0.041
100	334	8.126	8.071	-0.055
100	335	8.291	8.081	-0.210
100	336	8.187	8.109	-0.078
100	337	8.199	8.128	-0.071
100	338	8.194	8.086	-0.108
100	339	8.182	8.104	-0.078
100	340	8.219	8.116	-0.103
100	341	8.156	8.088	-0.068
100	342	8.213	8.119	-0.094
100	343	8.170	8.108	-0.062
105	344	8.187	8.089	-0.098
105	346	8.180	8.100	-0.080
105	349	8.150	8.065	-0.085
105	350	8.184	8.100	-0.084
105	351	8.164	8.040	-0.124
105	352	8.209	8.183	-0.026
Max		8.550	8.702	0.179
Average		8.366	8.403	0.037
Min		8.089	8.001	-0.210
Std Dev		0.169	0.273	0.110



9.90 __T_SYNC_FA	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

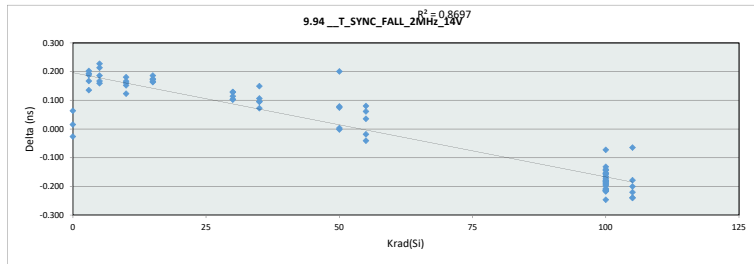
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.162	8.601	8.662	8.597	8.645	8.607	8.606	8.605	8.559	8.001	8.040
Average	8.219	8.650	8.682	8.646	8.670	8.644	8.637	8.620	8.616	8.097	8.096
Max	8.275	8.700	8.699	8.678	8.702	8.689	8.657	8.637	8.646	8.174	8.183
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

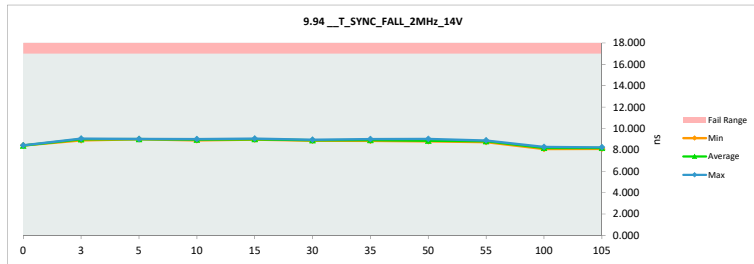
		9.94 __T_SYNC_FALL_2MHz_14V	
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.424	8.397	-0.027
0	297	8.407	8.422	0.015
0	298	8.342	8.405	0.063
3	192	8.849	9.042	0.193
3	193	8.808	9.010	0.202
3	194	8.793	8.960	0.167
3	196	8.754	8.889	0.135
3	197	8.822	9.010	0.188
5	200	8.824	9.010	0.186
5	202	8.771	8.984	0.213
5	203	8.854	9.021	0.167
5	204	8.771	8.998	0.227
5	206	8.848	9.007	0.159
10	207	8.825	8.991	0.166
10	209	8.823	9.003	0.180
10	210	8.793	8.953	0.160
10	211	8.764	8.887	0.123
10	212	8.796	8.948	0.152
15	214	8.797	8.960	0.163
15	217	8.860	9.046	0.186
15	218	8.815	8.980	0.165
15	219	8.852	9.026	0.174
15	222	8.789	8.961	0.172
30	224	8.822	8.925	0.103
30	225	8.790	8.918	0.128
30	226	8.723	8.852	0.129
30	227	8.826	8.940	0.114
30	229	8.778	8.880	0.102
35	230	8.852	9.001	0.149
35	231	8.864	8.970	0.106
35	232	8.747	8.842	0.095
35	234	8.812	8.884	0.072
35	237	8.796	8.891	0.095
50	272	8.764	8.842	0.078
50	275	8.788	8.791	0.003
50	276	8.811	9.011	0.200
50	277	8.793	8.791	-0.002
50	278	8.768	8.842	0.074
55	279	8.814	8.875	0.061
55	280	8.757	8.716	-0.041
55	281	8.776	8.811	0.035
55	282	8.827	8.808	-0.019
55	283	8.777	8.857	0.080
100	317	8.390	8.176	-0.214
100	318	8.356	8.198	-0.158
100	319	8.308	8.154	-0.154
100	320	8.300	8.088	-0.212
100	321	8.252	8.179	-0.073
100	322	8.420	8.202	-0.218
100	323	8.319	8.126	-0.193
100	324	8.398	8.216	-0.182
100	326	8.352	8.186	-0.166
100	327	8.319	8.128	-0.191
100	328	8.357	8.171	-0.186
100	329	8.418	8.220	-0.198
100	332	8.356	8.148	-0.208
100	333	8.292	8.077	-0.215
100	334	8.349	8.175	-0.174
100	335	8.440	8.274	-0.166
100	336	8.360	8.215	-0.145
100	337	8.394	8.239	-0.155
100	338	8.358	8.179	-0.179
100	339	8.409	8.226	-0.183
100	340	8.363	8.147	-0.216
100	341	8.318	8.186	-0.132
100	342	8.310	8.169	-0.141
100	343	8.360	8.113	-0.247
105	344	8.373	8.133	-0.240
105	346	8.297	8.232	-0.065
105	349	8.406	8.205	-0.201
105	350	8.429	8.188	-0.241
105	351	8.318	8.097	-0.221
105	352	8.372	8.193	-0.179
	Max	8.864	9.046	0.227
	Average	8.602	8.597	-0.005
	Min	8.252	8.077	-0.247
	Std Dev	0.225	0.376	0.160



		9.94 __T_SYNC_FA	
Test Site			
Tester			
Test Number			
Max Limit	17	ns	
Min Limit		ns	

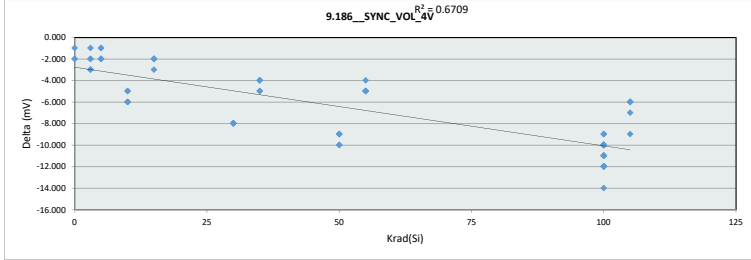
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.397	8.889	8.984	8.887	8.960	8.852	8.842	8.791	8.716	8.077	8.097
Average	8.408	8.982	9.004	8.956	8.995	8.903	8.918	8.855	8.813	8.175	8.175
Max	8.422	9.042	9.021	9.003	9.046	8.940	9.001	9.011	8.875	8.274	8.232
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



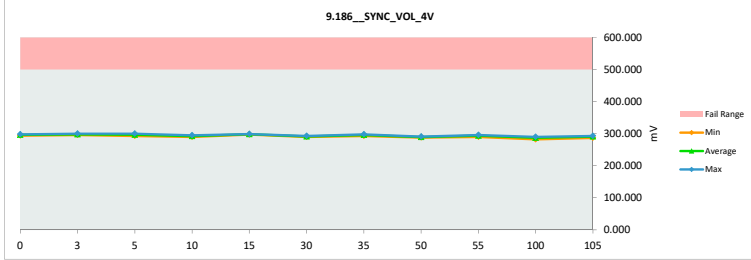
HDR TID Report  
TPS7H5004-SP

9.186_SYNC_VOL_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	295	300.000	298.000	-2.000
0	297	298.000	297.000	-1.000
0	298	296.000	294.000	-2.000
3	192	297.000	296.000	-1.000
3	193	299.000	297.000	-2.000
3	194	302.000	299.000	-3.000
3	196	300.000	297.000	-3.000
3	197	302.000	300.000	-2.000
5	200	298.000	297.000	-1.000
5	202	295.000	293.000	-2.000
5	203	298.000	297.000	-1.000
5	204	302.000	300.000	-2.000
5	206	300.000	298.000	-2.000
10	207	300.000	294.000	-6.000
10	209	301.000	295.000	-6.000
10	210	295.000	290.000	-5.000
10	211	295.000	290.000	-5.000
10	212	299.000	293.000	-6.000
15	214	302.000	299.000	-3.000
15	217	301.000	299.000	-2.000
15	218	301.000	299.000	-2.000
15	219	299.000	297.000	-2.000
15	222	300.000	298.000	-2.000
30	224	300.000	292.000	-8.000
30	225	298.000	290.000	-8.000
30	226	299.000	291.000	-8.000
30	227	301.000	293.000	-8.000
30	229	299.000	291.000	-8.000
35	230	301.000	297.000	-4.000
35	231	302.000	298.000	-4.000
35	232	301.000	296.000	-5.000
35	234	300.000	295.000	-5.000
35	237	297.000	293.000	-4.000
50	272	298.000	288.000	-10.000
50	275	300.000	290.000	-10.000
50	276	298.000	289.000	-9.000
50	277	300.000	291.000	-9.000
50	278	298.000	289.000	-9.000
55	279	301.000	296.000	-5.000
55	280	300.000	295.000	-5.000
55	281	294.000	290.000	-4.000
55	282	298.000	293.000	-5.000
55	283	300.000	295.000	-5.000
100	317	300.000	286.000	-14.000
100	318	300.000	288.000	-12.000
100	319	299.000	287.000	-12.000
100	320	299.000	287.000	-12.000
100	321	298.000	286.000	-12.000
100	322	300.000	290.000	-10.000
100	323	296.000	287.000	-9.000
100	324	297.000	287.000	-10.000
100	326	292.000	282.000	-10.000
100	327	297.000	286.000	-11.000
100	328	295.000	285.000	-10.000
100	329	300.000	288.000	-12.000
100	332	298.000	287.000	-11.000
100	333	298.000	287.000	-11.000
100	334	295.000	286.000	-9.000
100	335	300.000	288.000	-12.000
100	336	297.000	287.000	-10.000
100	337	298.000	288.000	-10.000
100	338	298.000	286.000	-12.000
100	339	296.000	286.000	-10.000
100	340	298.000	288.000	-10.000
100	341	300.000	289.000	-11.000
100	342	298.000	288.000	-10.000
100	343	300.000	289.000	-11.000
105	344	296.000	287.000	-9.000
105	346	299.000	292.000	-7.000
105	349	297.000	291.000	-6.000
105	350	299.000	293.000	-6.000
105	351	299.000	293.000	-6.000
105	352	297.000	291.000	-6.000
Max		302.000	300.000	-1.000
Average		298.644	291.836	-6.808
Min		292.000	282.000	-14.000
Std Dev		2.091	4.537	3.673



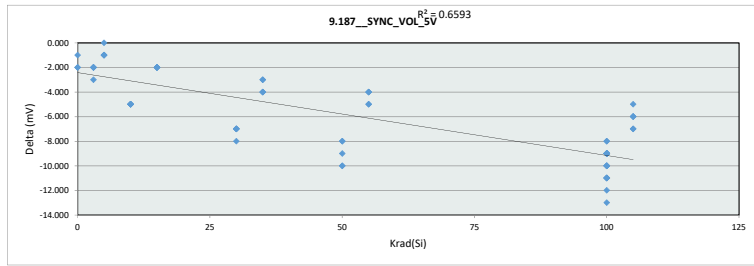
9.186_SYNC_VOL		0	3	5	10	15	30	35	50	55	100	105
LL		294.000	296.000	293.000	290.000	297.000	290.000	293.000	288.000	290.000	282.000	287.000
Min		296.333	297.800	297.000	292.400	298.400	291.400	295.800	289.400	293.800	287.000	291.167
Average		298.000	300.000	300.000	295.000	299.000	293.000	298.000	291.000	296.000	290.000	293.000
Max		500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000
UL		500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



HDR TID Report  
TPS7H5004-SP

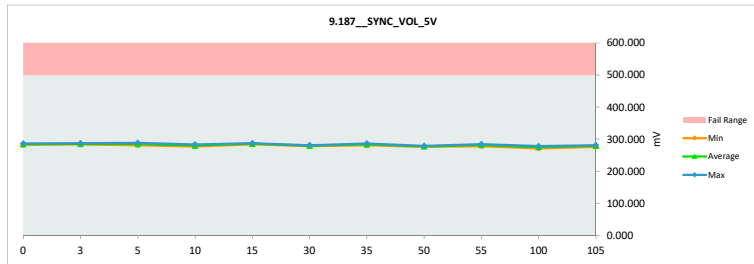
9.187__SYNC_VOL_5V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	295	287.000	286.000	-1.000
0	297	286.000	284.000	-2.000
0	298	284.000	282.000	-2.000
3	192	285.000	283.000	-2.000
3	193	286.000	284.000	-2.000
3	194	289.000	287.000	-2.000
3	196	287.000	284.000	-3.000
3	197	289.000	287.000	-2.000
5	200	285.000	285.000	0.000
5	202	282.000	281.000	-1.000
5	203	285.000	284.000	-1.000
5	204	289.000	288.000	-1.000
5	206	287.000	286.000	-1.000
10	207	287.000	282.000	-5.000
10	209	288.000	283.000	-5.000
10	210	283.000	278.000	-5.000
10	211	282.000	277.000	-5.000
10	212	286.000	281.000	-5.000
15	214	289.000	287.000	-2.000
15	217	288.000	286.000	-2.000
15	218	288.000	286.000	-2.000
15	219	286.000	284.000	-2.000
15	222	288.000	286.000	-2.000
30	224	287.000	279.000	-8.000
30	225	285.000	278.000	-7.000
30	226	286.000	279.000	-7.000
30	227	288.000	281.000	-7.000
30	229	286.000	279.000	-7.000
35	230	288.000	285.000	-3.000
35	231	289.000	286.000	-3.000
35	232	288.000	284.000	-4.000
35	234	287.000	283.000	-4.000
35	237	285.000	281.000	-4.000
50	272	286.000	276.000	-10.000
50	275	288.000	278.000	-10.000
50	276	285.000	277.000	-8.000
50	277	288.000	279.000	-9.000
50	278	285.000	277.000	-8.000
55	279	289.000	284.000	-5.000
55	280	287.000	283.000	-4.000
55	281	282.000	278.000	-4.000
55	282	285.000	281.000	-4.000
55	283	287.000	282.000	-5.000
100	317	287.000	274.000	-13.000
100	318	287.000	276.000	-11.000
100	319	286.000	275.000	-11.000
100	320	287.000	275.000	-12.000
100	321	285.000	275.000	-10.000
100	322	287.000	278.000	-9.000
100	323	283.000	275.000	-8.000
100	324	284.000	275.000	-9.000
100	326	279.000	271.000	-8.000
100	327	284.000	275.000	-9.000
100	328	282.000	273.000	-9.000
100	329	287.000	276.000	-11.000
100	332	285.000	276.000	-9.000
100	333	285.000	276.000	-9.000
100	334	283.000	274.000	-9.000
100	335	287.000	276.000	-11.000
100	336	285.000	275.000	-10.000
100	337	285.000	276.000	-9.000
100	338	285.000	274.000	-11.000
100	339	284.000	274.000	-10.000
100	340	285.000	276.000	-9.000
100	341	287.000	277.000	-10.000
100	342	285.000	276.000	-9.000
100	343	287.000	277.000	-10.000
105	344	283.000	276.000	-7.000
105	346	287.000	280.000	-7.000
105	349	284.000	279.000	-5.000
105	350	287.000	281.000	-6.000
105	351	287.000	281.000	-6.000
105	352	285.000	279.000	-6.000
Max		289.000	288.000	0.000
Average		285.904	279.753	-6.151
Min		279.000	271.000	-13.000
Std Dev		2.022	4.281	3.418



9.187__SYNC_VOL	
Test Site	
Tester	
Test Number	
Max Limit	500 mV
Min Limit	mV

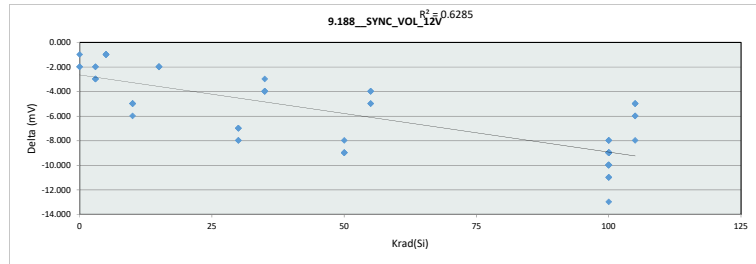
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	282.000	283.000	281.000	277.000	284.000	278.000	281.000	276.000	278.000	271.000	276.000
Average	284.000	285.000	284.800	280.200	285.800	279.200	283.800	281.600	275.208	279.333	279.333
Max	286.000	287.000	288.000	283.000	287.000	281.000	286.000	279.000	284.000	278.000	281.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



HDR TID Report  
TPS7H5004-SP

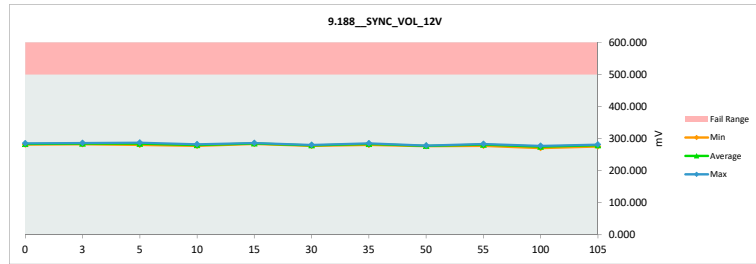
9.188_SYNC_VOL_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	295	286.000	285.000	-1.000
0	297	285.000	283.000	-2.000
0	298	283.000	281.000	-2.000
3	192	284.000	282.000	-2.000
3	193	286.000	283.000	-3.000
3	194	288.000	286.000	-2.000
3	196	287.000	284.000	-3.000
3	197	289.000	286.000	-3.000
5	200	285.000	284.000	-1.000
5	202	281.000	280.000	-1.000
5	203	285.000	284.000	-1.000
5	204	288.000	287.000	-1.000
5	206	286.000	285.000	-1.000
10	207	286.000	281.000	-5.000
10	209	287.000	282.000	-5.000
10	210	282.000	277.000	-5.000
10	211	282.000	277.000	-5.000
10	212	286.000	280.000	-6.000
15	214	288.000	286.000	-2.000
15	217	287.000	285.000	-2.000
15	218	287.000	285.000	-2.000
15	219	285.000	283.000	-2.000
15	222	287.000	285.000	-2.000
30	224	286.000	279.000	-7.000
30	225	284.000	277.000	-7.000
30	226	286.000	278.000	-8.000
30	227	288.000	280.000	-8.000
30	229	286.000	279.000	-7.000
35	230	288.000	284.000	-4.000
35	231	289.000	285.000	-4.000
35	232	287.000	284.000	-3.000
35	234	286.000	282.000	-4.000
35	237	284.000	280.000	-4.000
50	272	285.000	276.000	-9.000
50	275	287.000	278.000	-9.000
50	276	284.000	276.000	-8.000
50	277	287.000	278.000	-9.000
50	278	285.000	276.000	-9.000
55	279	288.000	283.000	-5.000
55	280	287.000	283.000	-4.000
55	281	281.000	277.000	-4.000
55	282	285.000	280.000	-5.000
55	283	286.000	282.000	-4.000
100	317	287.000	274.000	-13.000
100	318	286.000	276.000	-10.000
100	319	285.000	275.000	-10.000
100	320	286.000	275.000	-11.000
100	321	285.000	275.000	-10.000
100	322	286.000	277.000	-9.000
100	323	283.000	275.000	-8.000
100	324	283.000	274.000	-9.000
100	326	279.000	270.000	-9.000
100	327	283.000	274.000	-9.000
100	328	282.000	273.000	-9.000
100	329	286.000	276.000	-10.000
100	332	285.000	275.000	-10.000
100	333	285.000	275.000	-10.000
100	334	282.000	274.000	-8.000
100	335	287.000	276.000	-11.000
100	336	284.000	275.000	-9.000
100	337	284.000	276.000	-8.000
100	338	285.000	274.000	-11.000
100	339	283.000	274.000	-9.000
100	340	285.000	275.000	-10.000
100	341	286.000	276.000	-10.000
100	342	284.000	275.000	-9.000
100	343	286.000	277.000	-9.000
105	344	283.000	275.000	-8.000
105	346	286.000	280.000	-6.000
105	349	284.000	279.000	-5.000
105	350	286.000	281.000	-5.000
105	351	286.000	281.000	-5.000
105	352	284.000	278.000	-6.000
Max		289.000	287.000	-1.000
Average		285.274	279.151	-6.123
Min		279.000	270.000	-13.000
Std Dev		1.974	4.108	3.261



9.188_SYNC_VOL	
Test Site	
Tester	
Test Number	
Max Limit	500 mV
Min Limit	500 mV

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	281.000	282.000	280.000	277.000	283.000	277.000	280.000	276.000	277.000	270.000	275.000
Average	283.000	284.200	284.000	279.400	284.800	278.600	283.000	276.800	281.000	274.833	279.000
Max	285.000	286.000	287.000	282.000	286.000	280.000	285.000	278.000	283.000	277.000	281.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000

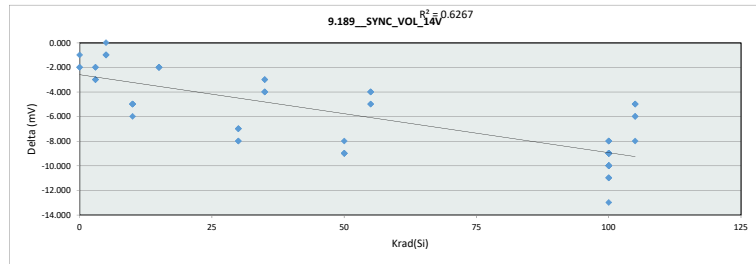




HDR TID Report  
TPS7H5004-SP

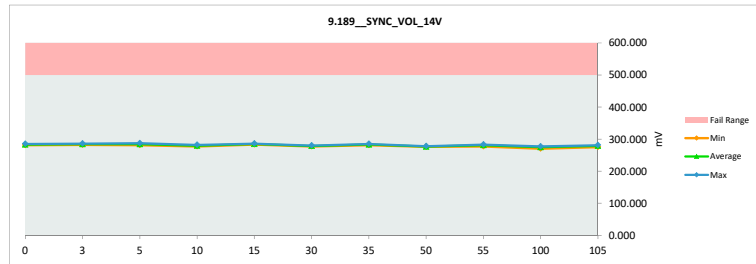
9.189_SYNC_VOL_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	295	286.000	285.000	-1.000
0	297	285.000	283.000	-2.000
0	298	283.000	281.000	-2.000
3	192	284.000	282.000	-2.000
3	193	286.000	283.000	-3.000
3	194	288.000	286.000	-2.000
3	196	287.000	284.000	-3.000
3	197	289.000	286.000	-3.000
5	200	285.000	284.000	-1.000
5	202	281.000	281.000	0.000
5	203	285.000	284.000	-1.000
5	204	288.000	287.000	-1.000
5	206	286.000	285.000	-1.000
10	207	286.000	281.000	-5.000
10	209	287.000	282.000	-5.000
10	210	282.000	277.000	-5.000
10	211	282.000	277.000	-5.000
10	212	286.000	280.000	-6.000
15	214	288.000	286.000	-2.000
15	217	287.000	285.000	-2.000
15	218	287.000	285.000	-2.000
15	219	285.000	283.000	-2.000
15	222	287.000	285.000	-2.000
30	224	286.000	279.000	-7.000
30	225	284.000	277.000	-7.000
30	226	286.000	278.000	-8.000
30	227	288.000	280.000	-8.000
30	229	286.000	279.000	-7.000
35	230	288.000	284.000	-4.000
35	231	289.000	285.000	-4.000
35	232	287.000	284.000	-3.000
35	234	286.000	282.000	-4.000
35	237	284.000	281.000	-3.000
50	272	285.000	276.000	-9.000
50	275	287.000	278.000	-9.000
50	276	284.000	276.000	-8.000
50	277	287.000	278.000	-9.000
50	278	285.000	276.000	-9.000
55	279	288.000	283.000	-5.000
55	280	287.000	283.000	-4.000
55	281	281.000	277.000	-4.000
55	282	285.000	280.000	-5.000
55	283	286.000	282.000	-4.000
100	317	287.000	274.000	-13.000
100	318	286.000	276.000	-10.000
100	319	285.000	275.000	-10.000
100	320	286.000	275.000	-11.000
100	321	285.000	275.000	-10.000
100	322	286.000	277.000	-9.000
100	323	283.000	275.000	-8.000
100	324	283.000	274.000	-9.000
100	326	279.000	270.000	-9.000
100	327	283.000	274.000	-9.000
100	328	282.000	273.000	-9.000
100	329	286.000	276.000	-10.000
100	332	285.000	275.000	-10.000
100	333	285.000	275.000	-10.000
100	334	282.000	274.000	-8.000
100	335	287.000	276.000	-11.000
100	336	284.000	275.000	-9.000
100	337	284.000	276.000	-8.000
100	338	285.000	274.000	-11.000
100	339	283.000	274.000	-9.000
100	340	285.000	275.000	-10.000
100	341	286.000	276.000	-10.000
100	342	284.000	275.000	-9.000
100	343	286.000	277.000	-9.000
105	344	283.000	275.000	-8.000
105	346	286.000	280.000	-6.000
105	349	284.000	279.000	-5.000
105	350	286.000	281.000	-5.000
105	351	286.000	281.000	-5.000
105	352	284.000	278.000	-6.000
Max		289.000	287.000	0.000
Average		285.274	279.178	-6.096
Min		279.000	270.000	-13.000
Std Dev		1.974	4.118	3.296



9.189_SYNC_VOL	
Test Site	
Tester	
Test Number	
Max Limit	500 mV
Min Limit	500 mV

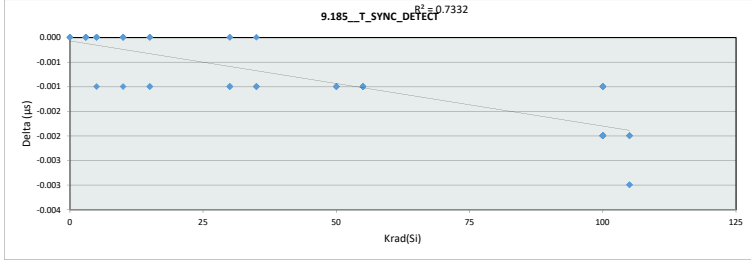
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	281.000	282.000	281.000	277.000	283.000	277.000	281.000	276.000	277.000	270.000	275.000
Min	283.000	284.200	284.200	279.400	284.800	278.600	283.200	276.800	281.000	274.833	279.000
Average	285.000	286.000	287.000	282.000	286.000	280.000	285.000	278.000	283.000	277.000	281.000
Max	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



HDR TID Report  
TPS7H5004-SP

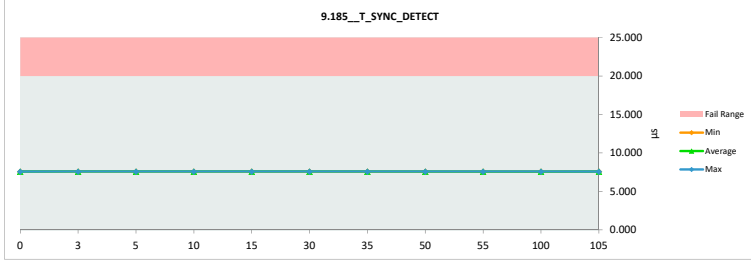
9.185_T_SYNC_DETECT				
Test Site				
Tester				
Test Number				
Unit	µs		µs	
Max Limit	20		20	
Min Limit				

Krad(Si)	Serial #	PRE	POST	Delta
0	295	7.585	7.585	0.000
0	297	7.586	7.586	0.000
0	298	7.585	7.585	0.000
3	192	7.585	7.585	0.000
3	193	7.585	7.585	0.000
3	194	7.584	7.584	0.000
3	196	7.584	7.584	0.000
3	197	7.585	7.585	0.000
5	200	7.585	7.585	0.000
5	202	7.585	7.584	-0.001
5	203	7.585	7.585	0.000
5	204	7.585	7.585	0.000
5	206	7.585	7.585	0.000
10	207	7.585	7.585	0.000
10	209	7.585	7.585	0.000
10	210	7.584	7.584	0.000
10	211	7.585	7.585	0.000
10	212	7.585	7.584	-0.001
15	214	7.584	7.583	-0.001
15	217	7.585	7.584	-0.001
15	218	7.584	7.584	0.000
15	219	7.584	7.584	0.000
15	222	7.583	7.583	0.000
30	224	7.585	7.584	-0.001
30	225	7.585	7.585	0.000
30	226	7.584	7.584	0.000
30	227	7.585	7.584	-0.001
30	229	7.585	7.584	-0.001
35	230	7.584	7.583	-0.001
35	231	7.583	7.583	0.000
35	232	7.584	7.583	-0.001
35	234	7.585	7.584	-0.001
35	237	7.584	7.583	-0.001
50	272	7.585	7.584	-0.001
50	275	7.585	7.584	-0.001
50	276	7.585	7.584	-0.001
50	277	7.584	7.583	-0.001
50	278	7.585	7.584	-0.001
55	279	7.584	7.583	-0.001
55	280	7.585	7.584	-0.001
55	281	7.584	7.583	-0.001
55	282	7.585	7.584	-0.001
55	283	7.585	7.584	-0.001
100	317	7.584	7.582	-0.002
100	318	7.585	7.583	-0.002
100	319	7.586	7.584	-0.002
100	320	7.585	7.583	-0.002
100	321	7.586	7.584	-0.002
100	322	7.584	7.582	-0.002
100	323	7.583	7.582	-0.001
100	324	7.584	7.583	-0.001
100	326	7.585	7.584	-0.001
100	327	7.585	7.584	-0.001
100	328	7.585	7.583	-0.002
100	329	7.585	7.583	-0.002
100	332	7.584	7.583	-0.001
100	333	7.586	7.584	-0.002
100	334	7.585	7.583	-0.002
100	335	7.585	7.583	-0.002
100	336	7.586	7.584	-0.002
100	337	7.585	7.584	-0.001
100	338	7.586	7.584	-0.002
100	339	7.584	7.583	-0.001
100	340	7.585	7.583	-0.002
100	341	7.585	7.584	-0.001
100	342	7.586	7.584	-0.002
100	343	7.585	7.583	-0.002
105	344	7.585	7.583	-0.002
105	346	7.585	7.582	-0.003
105	349	7.585	7.582	-0.003
105	350	7.583	7.581	-0.002
105	351	7.585	7.583	-0.002
105	352	7.584	7.582	-0.002
Max		7.586	7.586	0.000
Average		7.585	7.584	-0.001
Min		7.583	7.581	-0.003
Std Dev		0.001	0.001	0.001



9.185_T_SYNC_D				
Test Site				
Tester				
Test Number				
Max Limit	20	µs		
Min Limit		µs		

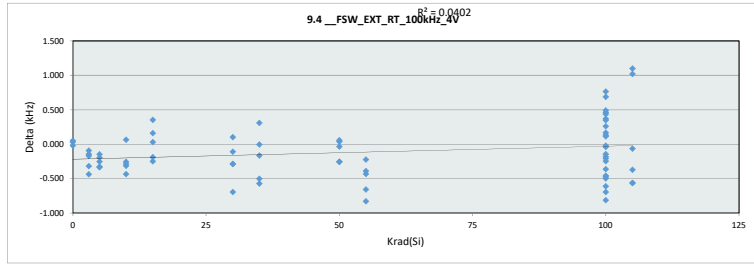
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.585	7.584	7.584	7.584	7.583	7.584	7.583	7.583	7.583	7.582	7.581
Average	7.585	7.585	7.585	7.585	7.584	7.584	7.583	7.584	7.584	7.583	7.582
Max	7.586	7.585	7.585	7.585	7.584	7.585	7.584	7.584	7.584	7.584	7.583
UL	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000



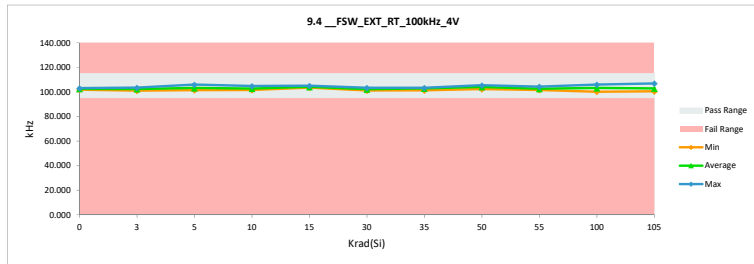
HDR TID Report  
TPS7H5004-SP

9.4_FSW_EXT_RT_100kHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(S)	Serial #	PRE	POST	Delta
0	295	103.082	103.117	0.035
0	297	102.593	102.576	-0.017
0	298	101.804	101.859	0.055
3	192	102.982	102.894	-0.088
3	193	103.902	103.470	-0.432
3	194	102.759	102.595	-0.164
3	196	102.040	101.900	-0.140
3	197	101.356	101.043	-0.313
5	200	103.045	102.722	-0.323
5	202	102.708	102.518	-0.190
5	203	103.661	103.413	-0.248
5	204	101.622	101.480	-0.142
5	206	106.269	105.938	-0.331
10	207	102.495	102.243	-0.252
10	209	102.295	102.365	0.070
10	210	101.892	101.611	-0.281
10	211	102.787	102.477	-0.310
10	212	105.202	104.772	-0.430
15	214	103.563	103.728	0.165
15	217	103.787	103.544	-0.243
15	218	103.449	103.483	0.034
15	219	104.660	105.018	0.358
15	222	105.268	105.084	-0.184
30	224	103.669	103.388	-0.281
30	225	102.502	101.812	-0.690
30	226	101.083	101.190	0.107
30	227	101.521	101.416	-0.105
30	229	103.334	103.050	-0.284
35	230	103.934	103.367	-0.567
35	231	103.090	102.929	-0.161
35	232	102.972	103.286	0.314
35	234	101.308	101.307	-0.001
35	237	103.747	103.250	-0.497
50	272	105.393	105.434	0.041
50	275	104.084	103.836	-0.248
50	276	103.509	103.479	-0.030
50	277	105.316	105.379	0.063
50	278	102.560	102.307	-0.253
55	279	102.256	101.604	-0.652
55	280	102.584	102.366	-0.218
55	281	102.272	101.886	-0.386
55	282	103.287	102.859	-0.428
55	283	105.172	104.348	-0.824
100	317	105.154	104.796	-0.358
100	318	101.820	102.285	0.465
100	319	102.039	101.862	-0.177
100	320	102.939	103.708	0.769
100	321	102.422	102.686	0.264
100	322	104.581	104.719	0.138
100	323	106.087	106.053	-0.034
100	324	100.984	100.517	-0.467
100	326	105.325	105.083	-0.242
100	327	102.506	101.899	-0.607
100	328	102.576	102.929	0.353
100	329	102.541	102.049	-0.492
100	332	103.855	104.351	0.496
100	333	103.781	103.091	-0.690
100	334	102.081	101.273	-0.808
100	335	105.238	105.098	-0.140
100	336	104.263	104.956	0.693
100	337	103.944	103.492	-0.452
100	338	104.142	104.314	0.172
100	339	104.271	104.645	0.374
100	340	102.774	102.573	-0.201
100	341	103.008	103.449	0.441
100	342	101.048	101.167	0.119
100	343	100.187	100.171	-0.016
105	344	104.021	103.463	-0.558
105	346	101.547	101.179	-0.368
105	349	100.815	101.919	1.104
105	350	105.914	106.938	1.024
105	351	102.973	102.913	-0.060
105	352	101.053	100.494	-0.559
Max		106.269	106.938	1.104
Average		103.160	103.047	-0.114
Min		100.187	100.171	-0.824
Std Dev		1.379	1.440	0.390

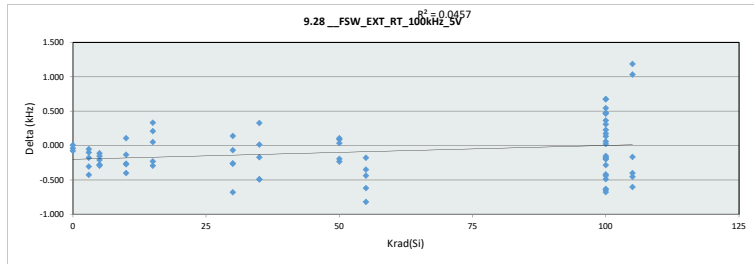


9.4_FSW_EXT_RT		0	3	5	10	15	30	35	50	55	100	105
LL		95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min		101.859	101.043	101.480	101.611	103.483	101.190	101.307	102.307	101.604	100.171	100.494
Average		102.517	102.380	103.214	102.694	104.171	102.171	102.828	104.087	102.613	103.215	102.818
Max		103.117	103.470	105.938	104.772	105.084	103.388	103.367	105.434	104.348	106.053	106.938
UL		115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

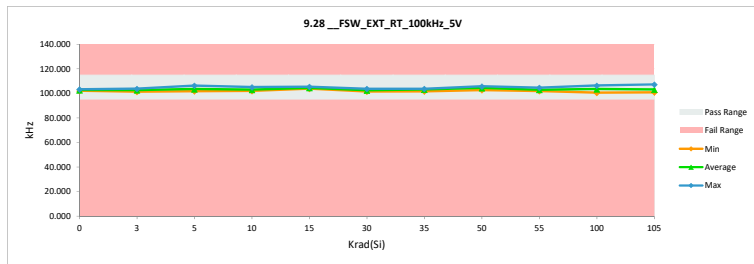


HDR TID Report  
TPS7H5004-SP

9.28 __FSW_EXT_RT_100kHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz		kHz	
Max Limit	115		115	
Min Limit	95		95	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	103.481	103.411	-0.070
0	297	102.975	102.988	0.013
0	298	102.172	102.138	-0.034
3	192	103.334	103.288	-0.046
3	193	104.282	103.863	-0.419
3	194	103.157	102.987	-0.170
3	196	102.366	102.271	-0.095
3	197	101.709	101.410	-0.299
5	200	103.435	103.163	-0.272
5	202	103.019	102.871	-0.148
5	203	103.992	103.788	-0.204
5	204	101.964	101.857	-0.107
5	206	106.670	106.382	-0.288
10	207	102.752	102.625	-0.127
10	209	102.652	102.765	0.113
10	210	102.266	102.008	-0.258
10	211	103.155	102.889	-0.266
10	212	105.576	105.182	-0.394
15	214	103.951	104.165	0.214
15	217	104.152	103.927	-0.225
15	218	103.821	103.879	0.058
15	219	105.085	105.424	0.339
15	222	105.797	105.509	-0.288
30	224	104.052	103.795	-0.257
30	225	102.865	102.191	-0.674
30	226	101.452	101.597	0.145
30	227	101.884	101.822	-0.062
30	229	103.684	103.429	-0.255
35	230	104.265	103.780	-0.485
35	231	103.530	103.365	-0.165
35	232	103.349	103.683	0.334
35	234	101.695	101.715	0.020
35	237	104.104	103.618	-0.486
50	272	105.749	105.842	0.093
50	275	104.481	104.255	-0.226
50	276	103.829	103.870	0.041
50	277	105.694	105.807	0.113
50	278	102.934	102.746	-0.188
55	279	102.592	101.978	-0.614
55	280	102.933	102.763	-0.170
55	281	102.587	102.244	-0.343
55	282	103.652	103.221	-0.431
55	283	105.556	104.742	-0.814
100	317	105.493	105.216	-0.277
100	318	102.186	102.656	0.470
100	319	102.472	102.287	-0.185
100	320	103.357	104.036	0.679
100	321	102.771	103.086	0.315
100	322	104.935	105.168	0.233
100	323	106.429	106.457	0.028
100	324	101.310	100.877	-0.433
100	326	105.631	105.440	-0.191
100	327	102.863	102.238	-0.625
100	328	102.839	103.319	0.480
100	329	102.869	102.457	-0.412
100	332	104.237	104.788	0.551
100	333	104.095	103.457	-0.638
100	334	102.400	101.730	-0.670
100	335	105.654	105.509	-0.145
100	336	104.618	105.300	0.682
100	337	104.342	103.859	-0.483
100	338	104.550	104.729	0.179
100	339	104.597	104.969	0.372
100	340	103.104	102.942	-0.162
100	341	103.399	103.874	0.475
100	342	101.393	101.534	0.141
100	343	100.546	100.613	0.067
105	344	104.434	103.837	-0.597
105	346	101.962	101.513	-0.449
105	349	101.097	102.288	1.191
105	350	106.320	107.359	1.039
105	351	103.419	103.260	-0.159
105	352	101.345	100.950	-0.395
Max		106.670	107.359	1.191
Average		103.525	103.438	-0.087
Min		100.546	100.613	-0.814
Std Dev		1.393	1.447	0.388



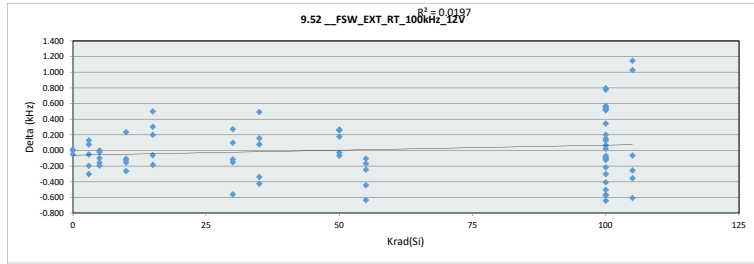
9.28 __FSW_EXT_RT_100kHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	115										
Min Limit	95										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.138	101.410	101.857	102.008	103.879	101.597	101.715	102.746	101.978	100.613	100.950
Average	102.846	102.764	103.612	103.094	104.581	102.567	103.232	104.504	102.990	103.606	103.201
Max	103.411	103.863	106.382	105.182	105.509	103.795	103.780	105.842	104.742	106.457	107.359
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000



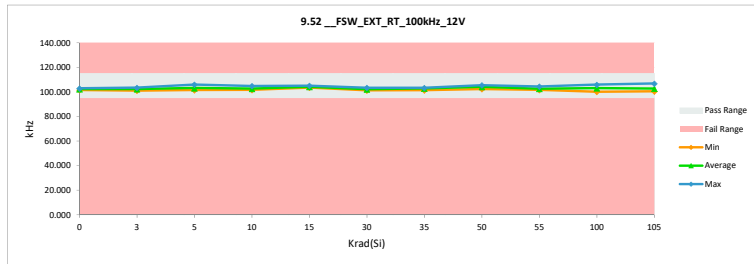
HDR TID Report  
TPS7H5004-SP

		9.52 __FSW_EXT_RT_100kHz_12V	
Test Site			
Tester			
Test Number			
Unit	kHz	kHz	
Max Limit	115	115	
Min Limit	95	95	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	102.989	102.993	0.004
0	297	102.526	102.542	0.016
0	298	101.681	101.635	-0.046
3	192	102.874	102.955	0.081
3	193	103.832	103.533	-0.299
3	194	102.673	102.627	-0.046
3	196	101.813	101.946	0.133
3	197	101.279	101.087	-0.192
5	200	102.997	102.845	-0.152
5	202	102.519	102.521	0.002
5	203	103.508	103.415	-0.093
5	204	101.555	101.539	-0.016
5	206	106.209	106.020	-0.189
10	207	102.393	102.273	-0.120
10	209	102.185	102.422	0.237
10	210	101.800	101.698	-0.102
10	211	102.682	102.531	-0.151
10	212	105.104	104.844	-0.260
15	214	103.481	103.789	0.308
15	217	103.679	103.623	-0.056
15	218	103.363	103.565	0.202
15	219	104.559	105.062	0.503
15	222	105.312	105.132	-0.180
30	224	103.590	103.480	-0.110
30	225	102.436	101.879	-0.557
30	226	101.017	101.294	0.277
30	227	101.428	101.531	0.103
30	229	103.248	103.102	-0.146
35	230	103.795	103.375	-0.420
35	231	102.986	103.068	0.082
35	232	102.896	103.391	0.495
35	234	101.274	101.434	0.160
35	237	103.643	103.309	-0.334
50	272	105.263	105.523	0.260
50	275	104.035	103.972	-0.063
50	276	103.371	103.551	0.180
50	277	105.227	105.499	0.272
50	278	102.465	102.441	-0.024
55	279	102.116	101.677	-0.439
55	280	102.547	102.448	-0.099
55	281	102.083	101.920	-0.163
55	282	103.169	102.929	-0.240
55	283	105.108	104.478	-0.630
100	317	104.934	104.722	-0.212
100	318	101.703	102.274	0.571
100	319	101.930	101.840	-0.090
100	320	102.927	103.708	0.781
100	321	102.279	102.627	0.348
100	322	104.475	104.679	0.204
100	323	105.939	106.011	0.072
100	324	100.895	100.493	-0.402
100	326	105.120	105.054	-0.066
100	327	102.421	101.858	-0.563
100	328	102.373	102.890	0.517
100	329	102.354	102.055	-0.299
100	332	103.782	104.330	0.548
100	333	103.647	103.079	-0.568
100	334	101.949	101.312	-0.637
100	335	105.193	105.089	-0.104
100	336	104.136	104.935	0.799
100	337	103.959	103.461	-0.498
100	338	104.080	104.232	0.152
100	339	104.047	104.607	0.560
100	340	102.666	102.549	-0.117
100	341	102.935	103.463	0.528
100	342	101.019	101.150	0.131
100	343	100.155	100.181	0.026
105	344	103.973	103.371	-0.602
105	346	101.367	101.115	-0.252
105	349	100.677	101.829	1.152
105	350	105.889	106.921	1.032
105	351	102.924	102.863	-0.061
105	352	100.879	100.528	-0.351
Max		106.209	106.921	1.152
Average		103.059	103.070	0.011
Min		100.155	100.181	-0.637
Std Dev		1.383	1.440	0.376

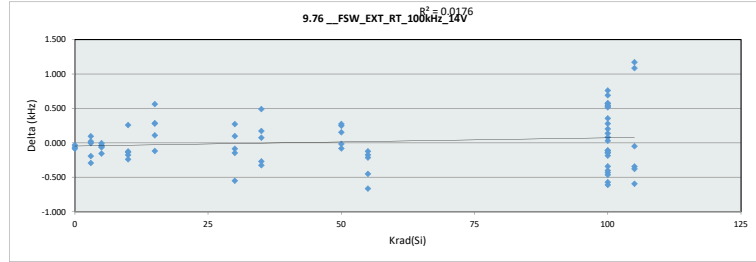


		9.52 __FSW_EXT_RT_100kHz_12V												
Test Site														
Tester														
Test Number														
Max Limit	115	kHz												
Min Limit	95	kHz												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000			
Min	101.635	101.087	101.539	101.698	103.565	101.294	101.434	102.441	101.677	100.181	100.528			
Average	102.390	102.430	103.268	102.754	104.234	102.257	102.915	104.197	102.690	103.192	102.771			
Max	102.993	103.533	106.020	104.844	105.132	103.480	103.391	105.523	104.478	106.011	106.921			
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000			

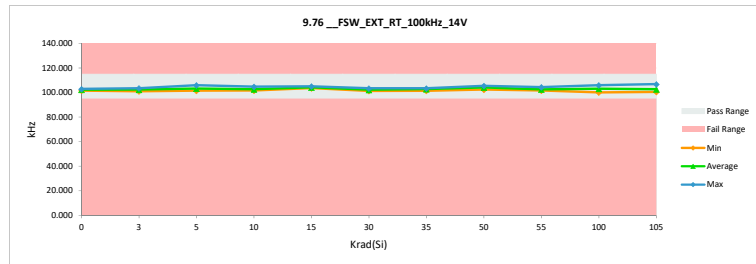


HDR TID Report  
TPS7H5004-SP

9.76 __FSW_EXT_RT_100kHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	115	115		
Min Limit	95	95		
Krad(S)	Serial #	PRE	POST	Delta
0	295	103.045	102.963	-0.082
0	297	102.524	102.464	-0.060
0	298	101.653	101.626	-0.027
3	192	102.854	102.954	0.100
3	193	103.802	103.514	-0.288
3	194	102.640	102.640	0.000
3	196	101.918	101.944	0.026
3	197	101.277	101.089	-0.188
5	200	102.938	102.893	-0.045
5	202	102.515	102.486	-0.029
5	203	103.497	103.437	-0.060
5	204	101.533	101.532	-0.001
5	206	106.185	106.035	-0.150
10	207	102.439	102.268	-0.171
10	209	102.172	102.434	0.262
10	210	101.803	101.670	-0.133
10	211	102.658	102.537	-0.121
10	212	105.090	104.856	-0.234
15	214	103.496	103.784	0.288
15	217	103.725	103.611	-0.114
15	218	103.284	103.569	0.285
15	219	104.495	105.061	0.566
15	222	105.009	105.122	0.113
30	224	103.560	103.478	-0.082
30	225	102.424	101.877	-0.547
30	226	101.026	101.302	0.276
30	227	101.419	101.521	0.102
30	229	103.243	103.101	-0.142
35	230	103.744	103.480	-0.264
35	231	102.992	103.071	0.079
35	232	102.896	103.392	0.496
35	234	101.249	101.425	0.176
35	237	103.639	103.319	-0.320
50	272	105.268	105.521	0.253
50	275	104.040	103.963	-0.077
50	276	103.394	103.551	0.157
50	277	105.226	105.504	0.278
50	278	102.441	102.429	-0.012
55	279	102.109	101.664	-0.445
55	280	102.548	102.429	-0.119
55	281	102.088	101.919	-0.169
55	282	103.133	102.923	-0.210
55	283	105.108	104.449	-0.659
100	317	104.943	104.761	-0.182
100	318	101.732	102.281	0.549
100	319	101.991	101.849	-0.142
100	320	102.960	103.654	0.694
100	321	102.288	102.649	0.361
100	322	104.478	104.684	0.206
100	323	105.956	106.043	0.087
100	324	100.879	100.482	-0.397
100	326	105.097	104.991	-0.106
100	327	102.336	101.875	-0.461
100	328	102.330	102.911	0.581
100	329	102.391	102.055	-0.336
100	332	103.798	104.371	0.573
100	333	103.678	103.071	-0.607
100	334	101.868	101.302	-0.566
100	335	105.166	105.034	-0.132
100	336	104.131	104.895	0.764
100	337	103.900	103.472	-0.428
100	338	104.088	104.231	0.143
100	339	104.045	104.565	0.520
100	340	102.660	102.554	-0.106
100	341	102.894	103.428	0.534
100	342	100.879	101.163	0.284
100	343	100.126	100.162	0.036
105	344	103.936	103.346	-0.590
105	346	101.489	101.114	-0.375
105	349	100.689	101.866	1.177
105	350	105.790	106.880	1.090
105	351	102.932	102.888	-0.044
105	352	100.888	100.550	-0.338
Max		106.185	106.880	1.177
Average		103.047	103.068	0.021
Min		100.126	100.162	-0.659
Std Dev		1.373	1.438	0.374

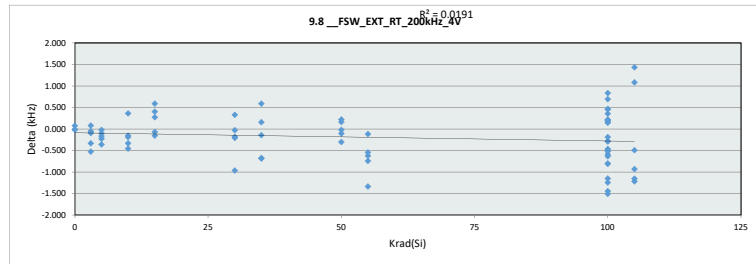


9.76 __FSW_EXT_RT_100kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	115	kHz									
Min Limit	95	kHz									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	101.626	101.089	101.532	101.670	103.569	101.302	101.425	102.429	101.664	100.162	100.550
Average	102.351	102.428	103.277	102.753	104.229	102.256	102.937	104.194	102.677	103.187	102.774
Max	102.963	103.514	106.035	104.856	105.122	103.478	103.480	105.521	104.449	106.043	106.880
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

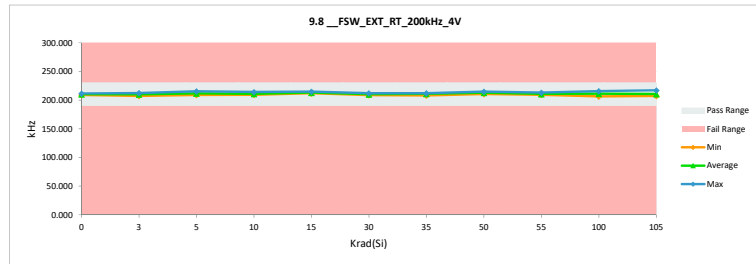


HDR TID Report  
TPS7H5004-SP

9.8_FSW_EXT_RT_200kHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	211.649	211.732	0.083
0	297	210.842	210.834	-0.008
0	298	209.110	209.108	-0.002
3	192	211.139	211.227	0.088
3	193	212.988	212.468	-0.520
3	194	210.515	210.429	-0.086
3	196	209.431	209.388	-0.043
3	197	207.900	207.574	-0.326
5	200	211.070	210.850	-0.220
5	202	211.636	211.544	-0.092
5	203	212.373	212.214	-0.159
5	204	209.106	209.092	-0.014
5	206	215.990	215.638	-0.352
10	207	210.423	210.277	-0.146
10	209	210.253	210.623	0.370
10	210	209.585	209.406	-0.179
10	211	211.260	210.935	-0.325
10	212	214.769	214.322	-0.447
15	214	211.953	212.364	0.411
15	217	212.280	212.133	-0.147
15	218	211.704	211.985	0.281
15	219	213.819	214.416	0.597
15	222	215.025	214.962	-0.063
30	224	212.313	212.114	-0.199
30	225	210.819	209.863	-0.956
30	226	208.479	208.815	0.336
30	227	209.218	209.196	-0.022
30	229	212.171	211.998	-0.173
35	230	212.781	212.102	-0.679
35	231	211.116	210.982	-0.134
35	232	211.384	211.982	0.598
35	234	208.142	208.307	0.165
35	237	212.412	211.744	-0.668
50	272	214.964	214.952	-0.012
50	275	212.895	212.598	-0.297
50	276	212.119	212.287	0.168
50	277	214.790	215.017	0.227
50	278	210.836	210.737	-0.099
55	279	210.064	209.331	-0.733
55	280	210.793	210.683	-0.110
55	281	210.595	210.057	-0.538
55	282	211.984	211.368	-0.616
55	283	214.678	213.347	-1.331
100	317	214.518	213.934	-0.584
100	318	208.931	209.294	0.363
100	319	209.693	209.179	-0.514
100	320	211.190	212.032	0.842
100	321	211.017	211.243	0.226
100	322	213.461	213.675	0.214
100	323	216.132	215.853	-0.279
100	324	208.194	207.398	-0.796
100	326	215.037	214.409	-0.628
100	327	210.553	209.316	-1.237
100	328	210.644	210.850	0.206
100	329	210.466	209.670	-0.796
100	332	212.389	212.863	0.474
100	333	212.176	210.740	-1.436
100	334	210.017	208.516	-1.501
100	335	215.055	214.594	-0.461
100	336	213.144	213.844	0.700
100	337	212.863	211.718	-1.145
100	338	213.144	213.309	0.165
100	339	213.337	213.491	0.154
100	340	211.013	210.540	-0.473
100	341	211.298	211.762	0.464
100	342	208.232	208.053	-0.179
100	343	206.894	206.618	-0.276
105	344	213.172	211.963	-1.209
105	346	208.829	207.904	-0.925
105	349	207.921	209.360	1.439
105	350	216.166	217.258	1.092
105	351	211.169	210.684	-0.485
105	352	208.694	207.547	-1.147
	Max	216.166	217.258	1.439
	Average	211.544	211.351	-0.193
	Min	206.894	206.618	-1.501
	Std Dev	2.175	2.247	0.574

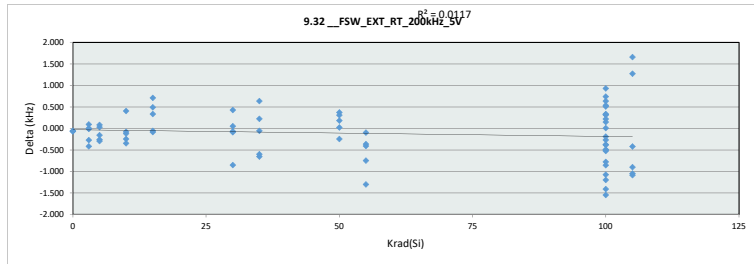


9.8_FSW_EXT_RT											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	209.108	207.574	209.092	209.406	211.985	208.815	208.307	210.737	209.331	206.618	207.547
Average	210.558	210.217	211.868	211.113	213.172	210.397	211.023	213.118	210.957	211.371	210.786
Max	211.732	212.468	215.638	214.322	214.962	212.114	212.102	215.017	213.347	215.853	217.258
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

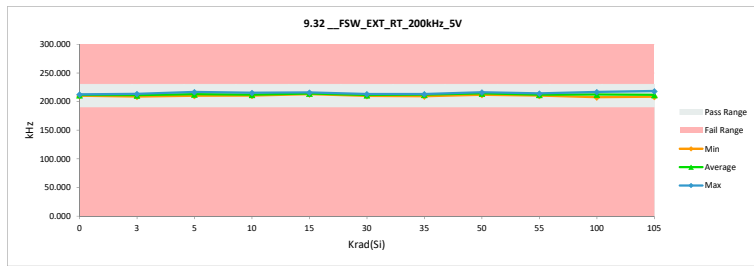


HDR TID Report  
TPS7H5004-SP

		9.32 __FSW_EXT_RT_200kHz_5V		
Test Site				
Tester				
Test Number				
Unit		kHz	kHz	
Max Limit		230	230	
Min Limit		190	190	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	212.734	212.675	-0.059
0	297	211.881	211.833	-0.048
0	298	210.215	210.156	-0.059
3	192	212.266	212.262	-0.004
3	193	213.992	213.583	-0.409
3	194	211.510	211.520	0.010
3	196	210.355	210.454	0.099
3	197	208.933	208.669	-0.264
5	200	212.143	211.993	-0.150
5	202	212.772	212.486	-0.286
5	203	213.194	213.283	0.089
5	204	210.135	210.173	0.038
5	206	217.104	216.851	-0.253
10	207	211.389	211.278	-0.111
10	209	211.435	211.848	0.413
10	210	210.552	210.483	-0.069
10	211	212.278	212.041	-0.237
10	212	215.809	215.470	-0.339
15	214	213.039	213.539	0.500
15	217	213.315	213.236	-0.079
15	218	212.744	213.088	0.344
15	219	214.835	215.552	0.717
15	222	216.214	216.164	-0.050
30	224	213.379	213.321	-0.058
30	225	211.836	210.990	-0.846
30	226	209.500	209.934	0.434
30	227	210.281	210.339	0.058
30	229	213.244	213.161	-0.083
35	230	213.847	213.254	-0.593
35	231	212.213	212.164	-0.049
35	232	212.458	213.100	0.642
35	234	209.215	209.446	0.231
35	237	213.458	212.809	-0.649
50	272	215.926	216.115	0.189
50	275	213.999	213.761	-0.238
50	276	213.136	213.452	0.316
50	277	215.838	216.217	0.379
50	278	211.897	211.926	0.029
55	279	211.075	210.334	-0.741
55	280	211.837	211.746	-0.091
55	281	211.675	211.273	-0.402
55	282	212.950	212.593	-0.357
55	283	215.771	214.476	-1.295
100	317	215.586	215.091	-0.495
100	318	209.908	210.450	0.542
100	319	210.824	210.339	-0.485
100	320	212.289	213.226	0.937
100	321	211.996	212.338	0.342
100	322	214.571	214.895	0.324
100	323	217.142	216.961	-0.181
100	324	209.195	208.347	-0.848
100	326	216.028	215.511	-0.517
100	327	211.566	210.374	-1.192
100	328	211.653	211.982	0.329
100	329	211.587	210.816	-0.771
100	332	213.508	214.025	0.517
100	333	213.195	211.793	-1.402
100	334	211.208	209.666	-1.542
100	335	216.134	215.759	-0.375
100	336	214.084	214.831	0.747
100	337	213.913	212.846	-1.067
100	338	214.164	214.390	0.226
100	339	214.468	214.624	0.156
100	340	211.955	211.586	-0.369
100	341	212.304	212.948	0.644
100	342	209.257	209.273	0.016
100	343	207.959	207.700	-0.259
105	344	214.163	213.120	-1.043
105	346	209.926	209.034	-0.892
105	349	208.876	210.543	1.667
105	350	217.120	218.399	1.279
105	351	212.162	211.749	-0.413
105	352	209.667	208.586	-1.081
	Max	217.142	218.399	1.667
	Average	212.586	212.469	-0.117
	Min	207.959	207.700	-1.542
	Std Dev	2.179	2.266	0.595



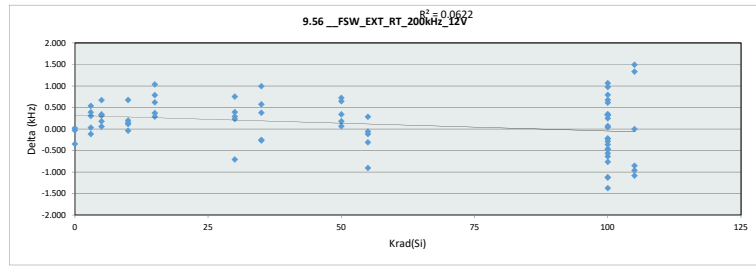
		9.32 __FSW_EXT_RT_200kHz_5V										
Test Site												
Tester												
Test Number												
Max Limit		230	kHz									
Min Limit		190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	
Min	210.156	208.669	210.173	210.483	213.088	209.934	209.446	211.926	210.334	207.700	208.586	
Average	211.555	211.298	212.957	212.224	214.316	211.549	212.155	214.294	212.084	212.490	211.905	
Max	212.675	213.583	216.851	215.470	216.164	213.321	213.254	216.217	214.476	216.961	218.399	
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	



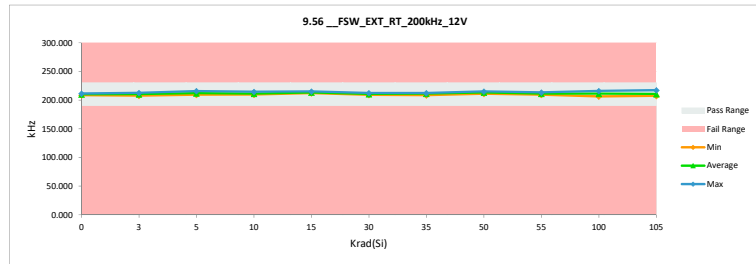


HDR TID Report  
TPS7H5004-SP

9.56 __FSW_EXT_RT_200kHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	211.622	211.598	-0.024
0	297	210.744	210.773	0.029
0	298	209.055	208.714	-0.341
3	192	211.065	211.611	0.546
3	193	212.856	212.748	-0.108
3	194	210.375	210.687	0.312
3	196	209.293	209.690	0.397
3	197	207.843	207.881	0.038
5	200	210.994	211.183	0.189
5	202	211.477	212.157	0.680
5	203	212.064	212.383	0.319
5	204	209.061	209.409	0.348
5	206	215.944	216.010	0.066
10	207	210.332	210.481	0.149
10	209	210.216	210.897	0.681
10	210	209.402	209.608	0.206
10	211	211.059	211.178	0.119
10	212	214.685	214.653	-0.032
15	214	211.929	212.722	0.793
15	217	212.146	212.440	0.294
15	218	211.652	212.279	0.627
15	219	213.680	214.723	1.043
15	222	214.958	215.334	0.376
30	224	212.281	212.580	0.299
30	225	210.756	210.054	-0.702
30	226	208.397	209.156	0.759
30	227	209.222	209.622	0.400
30	229	212.147	212.385	0.238
35	230	212.686	212.429	-0.257
35	231	211.085	211.469	0.384
35	232	211.337	212.339	1.002
35	234	208.136	208.717	0.581
35	237	212.297	212.051	-0.246
50	272	215.106	215.295	0.189
50	275	212.945	213.018	0.073
50	276	212.041	212.693	0.652
50	277	214.673	215.403	0.730
50	278	210.763	211.110	0.347
55	279	209.875	209.571	-0.304
55	280	210.731	211.021	0.290
55	281	210.527	210.419	-0.108
55	282	211.807	211.755	-0.052
55	283	214.663	213.766	-0.897
100	317	214.355	213.885	-0.470
100	318	208.847	209.528	0.681
100	319	209.710	209.356	-0.354
100	320	211.184	212.255	1.071
100	321	210.812	211.148	0.336
100	322	213.417	213.464	0.047
100	323	215.883	216.219	0.336
100	324	208.102	207.470	-0.632
100	326	214.822	214.368	-0.454
100	327	210.477	209.358	-1.119
100	328	210.573	210.925	0.352
100	329	210.367	209.811	-0.556
100	332	212.311	212.929	0.618
100	333	212.039	210.921	-1.118
100	334	209.948	208.580	-1.368
100	335	214.951	214.737	-0.214
100	336	212.907	213.891	0.984
100	337	212.785	212.029	-0.756
100	338	212.999	213.332	0.333
100	339	213.290	213.543	0.253
100	340	210.884	210.665	-0.219
100	341	211.162	211.965	0.803
100	342	208.222	208.299	0.077
100	343	206.898	206.618	-0.280
105	344	213.024	212.071	-0.953
105	346	208.797	207.952	-0.845
105	349	207.775	209.117	1.342
105	350	215.952	217.451	1.499
105	351	211.042	211.048	0.006
105	352	208.644	207.568	-1.076
Max		215.952	217.451	1.499
Average		211.454	211.569	0.115
Min		206.898	206.618	-1.368
Std Dev		2.159	2.282	0.598

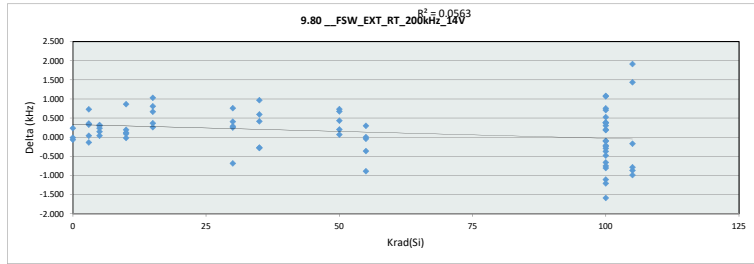


9.56 __FSW_EXT_RT_200kHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	208.714	207.881	209.409	209.608	212.279	209.156	208.717	211.110	209.571	206.618	207.568
Average	210.362	210.523	212.228	211.363	213.500	210.759	211.401	213.504	211.306	211.471	210.868
Max	211.598	212.748	216.010	214.653	215.334	212.580	212.429	215.403	213.766	216.219	217.451
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

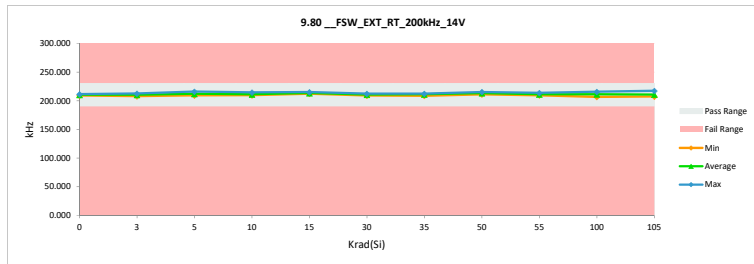


HDR TID Report  
TPS7H5004-SP

9.80 __FSW_EXT_RT_200kHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	211.607	211.605	-0.002
0	297	210.819	210.763	-0.056
0	298	208.833	209.076	0.243
3	192	211.021	211.757	0.736
3	193	212.847	212.720	-0.127
3	194	210.335	210.672	0.337
3	196	209.293	209.658	0.365
3	197	207.838	207.884	0.046
5	200	211.014	211.167	0.153
5	202	211.405	211.710	0.305
5	203	212.140	212.385	0.245
5	204	209.042	209.371	0.329
5	206	215.940	215.987	0.047
10	207	210.315	210.443	0.128
10	209	210.147	211.017	0.870
10	210	209.397	209.600	0.203
10	211	211.069	211.166	0.097
10	212	214.645	214.635	-0.010
15	214	211.892	212.709	0.817
15	217	212.136	212.406	0.270
15	218	211.622	212.295	0.673
15	219	213.666	214.705	1.039
15	222	214.943	215.317	0.374
30	224	212.258	212.550	0.292
30	225	210.745	210.068	-0.677
30	226	208.401	209.166	0.765
30	227	209.156	209.571	0.415
30	229	212.127	212.386	0.259
35	230	212.674	212.413	-0.261
35	231	211.049	211.474	0.425
35	232	211.354	212.329	0.975
35	234	208.105	208.710	0.605
35	237	212.313	212.036	-0.277
50	272	215.017	215.228	0.211
50	275	212.922	212.999	0.077
50	276	211.999	212.681	0.682
50	277	214.651	215.391	0.740
50	278	210.759	211.198	0.439
55	279	209.890	209.536	-0.354
55	280	210.708	211.015	0.307
55	281	210.519	210.487	-0.032
55	282	211.760	211.770	0.010
55	283	214.632	213.752	-0.880
100	317	214.272	213.909	-0.363
100	318	208.803	209.518	0.715
100	319	209.673	209.389	-0.284
100	320	211.196	212.284	1.088
100	321	210.790	211.170	0.380
100	322	213.411	213.802	0.391
100	323	215.941	215.854	-0.087
100	324	208.082	207.432	-0.650
100	326	214.762	214.292	-0.470
100	327	210.487	209.386	-1.101
100	328	210.674	210.882	0.208
100	329	210.489	209.745	-0.744
100	332	212.342	212.873	0.531
100	333	212.096	210.895	-1.201
100	334	210.133	208.554	-1.579
100	335	214.901	214.807	-0.094
100	336	212.853	213.930	1.077
100	337	212.775	211.976	-0.799
100	338	212.950	213.338	0.388
100	339	213.277	213.470	0.193
100	340	210.842	210.613	-0.229
100	341	211.211	211.974	0.763
100	342	208.212	208.524	0.312
100	343	206.929	206.719	-0.210
105	344	212.983	212.122	-0.861
105	346	208.780	208.003	-0.777
105	349	207.699	209.622	1.923
105	350	215.913	217.358	1.445
105	351	210.964	210.803	-0.161
105	352	208.483	207.501	-0.982
	Max	215.941	217.358	1.923
	Average	211.437	211.569	0.132
	Min	206.929	206.719	-1.579
	Std Dev	2.159	2.252	0.625

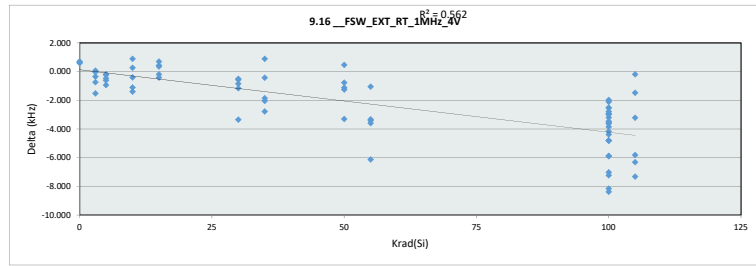


9.80 __FSW_EXT_RT_200kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	209.076	207.884	209.371	209.600	212.295	209.166	208.710	211.198	209.536	206.719	207.501
Average	210.481	210.538	212.124	211.372	213.486	210.748	211.392	213.499	211.312	211.472	210.902
Max	211.605	212.720	215.987	214.635	215.317	212.550	212.413	215.391	213.752	215.854	217.358
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

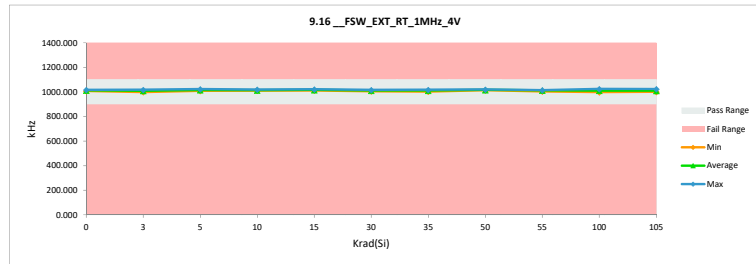


HDR TID Report  
TPS7H5004-SP

9.16 __FSW_EXT_RT_1MHz_4V				
Test Site				
Tester				
Test Number				
Unit	kHz		kHz	
Max Limit	1100		1100	
Min Limit	900		900	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1014.064	1014.725	0.661
0	297	1016.478	1017.211	0.733
0	298	1007.049	1007.668	0.619
3	192	1014.376	1014.469	0.093
3	193	1019.218	1017.727	-1.491
3	194	1008.392	1008.070	-0.322
3	196	1007.594	1007.576	-0.018
3	197	999.952	999.235	-0.717
5	200	1009.552	1009.379	-0.173
5	202	1020.725	1020.128	-0.597
5	203	1018.635	1018.192	-0.443
5	204	1008.658	1008.452	-0.206
5	206	1024.018	1023.105	-0.913
10	207	1011.261	1010.878	-0.383
10	209	1012.469	1013.387	0.918
10	210	1010.584	1010.876	0.292
10	211	1018.126	1016.747	-1.379
10	212	1020.863	1019.782	-1.081
15	214	1013.189	1013.642	0.453
15	217	1014.138	1013.967	-0.171
15	218	1011.754	1012.129	0.375
15	219	1020.119	1020.845	0.726
15	222	1022.421	1022.027	-0.394
30	224	1014.999	1014.176	-0.823
30	225	1012.809	1009.478	-3.331
30	226	1005.915	1005.429	-0.486
30	227	1012.330	1011.779	-0.551
30	229	1017.827	1016.690	-1.137
35	230	1019.793	1017.776	-2.017
35	231	1012.397	1010.570	-1.827
35	232	1014.094	1013.692	-0.402
35	234	1002.095	1003.003	0.908
35	237	1016.959	1014.201	-2.758
50	272	1019.169	1018.074	-1.095
50	275	1017.429	1014.150	-3.279
50	276	1018.122	1018.606	0.484
50	277	1021.387	1020.156	-1.231
50	278	1015.199	1014.446	-0.753
55	279	1007.652	1004.264	-3.388
55	280	1015.087	1014.061	-1.026
55	281	1017.408	1013.838	-3.570
55	282	1018.572	1015.265	-3.307
55	283	1021.453	1015.355	-6.098
100	317	1019.719	1017.227	-2.492
100	318	1005.572	1001.964	-3.608
100	319	1010.537	1005.754	-4.783
100	320	1012.063	1010.109	-1.954
100	321	1017.135	1015.043	-2.092
100	322	1018.440	1015.475	-2.965
100	323	1028.218	1024.079	-4.139
100	324	1010.514	1004.671	-5.843
100	326	1024.305	1019.938	-4.367
100	327	1011.411	1004.204	-7.207
100	328	1015.221	1010.433	-4.788
100	329	1014.543	1011.362	-3.181
100	332	1015.358	1012.604	-2.754
100	333	1011.987	1003.628	-8.359
100	334	1013.567	1006.568	-6.999
100	335	1023.843	1020.305	-3.538
100	336	1017.892	1014.994	-2.898
100	337	1018.801	1010.672	-8.129
100	338	1017.938	1015.014	-2.924
100	339	1019.703	1016.261	-3.442
100	340	1013.237	1008.453	-4.784
100	341	1013.980	1011.467	-2.513
100	342	1005.367	1001.535	-3.832
100	343	1004.020	998.141	-5.879
105	344	1021.577	1015.283	-6.294
105	346	1006.998	1001.205	-5.793
105	349	1008.292	1008.126	-0.166
105	350	1025.947	1024.497	-1.450
105	351	1010.265	1007.074	-3.191
105	352	1013.171	1005.875	-7.296
	Max	1028.218	1024.497	0.918
	Average	1014.712	1012.427	-2.284
	Min	999.952	998.141	-8.359
	Std Dev	5.751	6.060	2.394



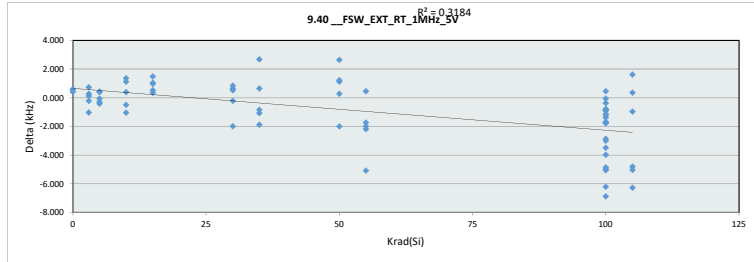
9.16 __FSW_EXT_RT_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	1100										
Min Limit	900										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1007.668	999.235	1008.452	1010.876	1012.129	1005.429	1003.003	1014.150	1004.264	998.141	1001.205
Average	1013.201	1009.415	1015.851	1014.334	1016.522	1011.510	1011.848	1017.086	1012.557	1010.829	1010.343
Max	1017.211	1017.727	1023.105	1019.782	1022.027	1016.690	1017.776	1020.156	1015.355	1024.079	1024.497
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



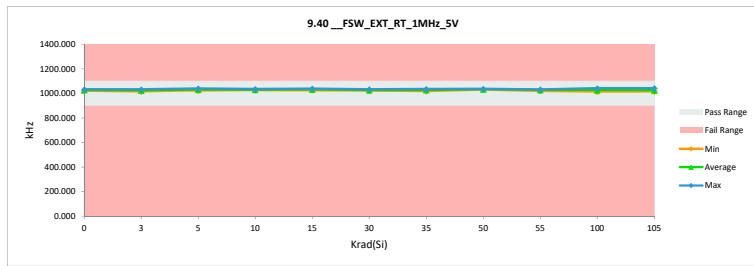
HDR TID Report  
TPS7H5004-SP

9.40 __FSW_EXT_RT_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	1100
Min Limit	900

Krad(Si)	Serial #	PRE	POST	Delta
0	295	1029.977	1030.413	0.436
0	297	1032.796	1033.404	0.608
0	298	1021.620	1022.221	0.601
3	192	1030.342	1031.104	0.762
3	193	1034.055	1033.044	-1.011
3	194	1024.775	1024.913	0.138
3	196	1023.495	1023.794	0.299
3	197	1016.783	1016.596	-0.187
5	200	1024.783	1025.234	0.451
5	202	1036.410	1036.157	-0.253
5	203	1034.731	1034.692	-0.039
5	204	1022.404	1022.828	0.424
5	206	1040.757	1040.359	-0.398
10	207	1026.894	1027.300	0.406
10	209	1028.208	1029.587	1.379
10	210	1026.085	1027.228	1.143
10	211	1032.956	1031.934	-1.022
10	212	1036.298	1035.832	-0.466
15	214	1028.731	1029.787	1.056
15	217	1030.986	1031.532	0.546
15	218	1027.227	1028.214	0.987
15	219	1035.955	1037.455	1.500
15	222	1038.740	1039.105	0.365
30	224	1030.010	1030.880	0.870
30	225	1027.334	1025.368	-1.966
30	226	1020.653	1021.200	0.547
30	227	1028.294	1028.964	0.670
30	229	1034.025	1033.836	-0.189
35	230	1037.140	1036.093	-1.047
35	231	1029.529	1028.715	-0.814
35	232	1029.354	1030.026	0.672
35	234	1016.392	1019.092	2.700
35	237	1032.231	1030.381	-1.850
50	272	1033.835	1035.087	1.252
50	275	1033.458	1031.481	-1.977
50	276	1034.380	1037.043	2.663
50	277	1037.144	1037.443	0.299
50	278	1030.328	1031.474	1.146
55	279	1022.418	1020.713	-1.705
55	280	1031.206	1031.679	0.473
55	281	1032.585	1030.417	-2.168
55	282	1034.578	1032.607	-1.971
55	283	1037.319	1032.256	-5.063
100	317	1035.158	1034.384	-0.774
100	318	1021.395	1019.637	-1.758
100	319	1025.737	1022.763	-2.974
100	320	1028.013	1027.983	-0.030
100	321	1031.873	1032.356	0.483
100	322	1034.955	1034.095	-0.860
100	323	1044.069	1042.378	-1.691
100	324	1025.978	1022.028	-3.950
100	326	1039.017	1037.288	-1.729
100	327	1025.977	1021.030	-4.947
100	328	1030.891	1027.433	-3.458
100	329	1030.278	1029.400	-0.878
100	332	1030.737	1030.392	-0.345
100	333	1027.348	1020.489	-6.859
100	334	1029.240	1024.203	-5.037
100	335	1040.249	1039.147	-1.102
100	336	1033.162	1032.356	-0.806
100	337	1034.504	1028.322	-6.182
100	338	1033.165	1031.802	-1.363
100	339	1035.535	1034.333	-1.202
100	340	1027.552	1024.714	-2.838
100	341	1029.265	1028.478	-0.787
100	342	1019.610	1017.948	-1.662
100	343	1019.931	1015.099	-4.832
105	344	1036.039	1031.020	-5.019
105	346	1021.189	1016.411	-4.778
105	349	1024.010	1025.646	1.636
105	350	1042.508	1042.889	0.381
105	351	1024.764	1023.824	-0.940
105	352	1028.463	1022.206	-6.257
Max		1044.069	1042.889	2.700
Average		1030.244	1029.281	-0.963
Min		1016.392	1015.099	-6.859
Std Dev		5.997	6.352	2.136

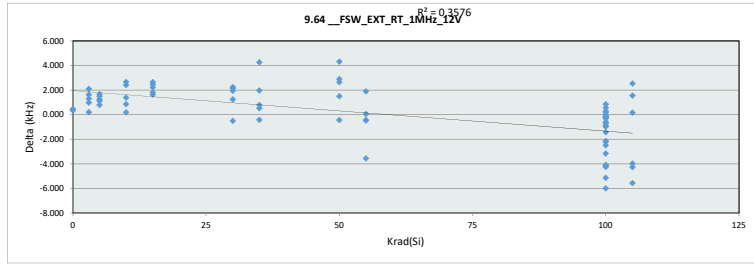


9.40 __FSW_EXT_RT_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	1100 kHz										
Min Limit	900 kHz										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1022.221	1016.596	1022.828	1027.228	1028.214	1021.200	1019.092	1031.474	1020.713	1015.099	1016.411
Average	1028.679	1025.890	1031.854	1030.376	1033.219	1028.050	1028.861	1034.506	1029.534	1028.252	1026.999
Max	1033.404	1033.044	1040.359	1035.832	1039.105	1033.836	1036.093	1037.443	1032.607	1042.378	1042.889
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

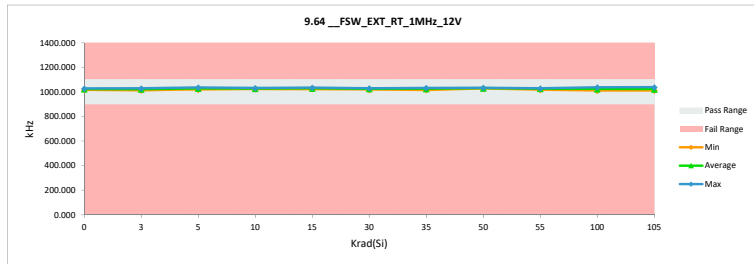


# HDR TID Report TPS7H5004-SP

9.64 __FSW_EXT_RT_1MHz_12V				
Test Site				
Tester				
Test Number				
Unit	kHz		kHz	
Max Limit	1100		1100	
Min Limit	900		900	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1025.751	1026.130	0.379
0	297	1028.338	1028.800	0.462
0	298	1016.939	1017.417	0.478
3	192	1025.792	1027.915	2.123
3	193	1029.384	1029.609	0.225
3	194	1020.196	1021.517	1.321
3	196	1019.024	1020.668	1.644
3	197	1012.550	1013.561	1.011
5	200	1020.293	1021.979	1.686
5	202	1031.415	1032.555	1.140
5	203	1030.206	1031.481	1.275
5	204	1017.877	1019.413	1.536
5	206	1036.271	1037.089	0.818
10	207	1022.621	1024.041	1.420
10	209	1023.815	1026.509	2.694
10	210	1021.336	1023.776	2.440
10	211	1027.905	1028.122	0.217
10	212	1031.587	1032.473	0.886
15	214	1024.198	1026.677	2.479
15	217	1026.727	1028.576	1.849
15	218	1022.772	1025.024	2.252
15	219	1031.449	1034.129	2.680
15	222	1033.984	1035.651	1.667
30	224	1025.736	1028.007	2.271
30	225	1022.723	1022.251	-0.472
30	226	1016.002	1017.970	1.968
30	227	1024.065	1026.241	2.176
30	229	1029.354	1030.619	1.265
35	230	1032.509	1033.066	0.557
35	231	1025.181	1026.000	0.819
35	232	1024.731	1026.734	2.003
35	234	1011.874	1016.169	4.295
35	237	1027.547	1027.147	-0.400
50	272	1029.281	1031.958	2.677
50	275	1028.941	1028.530	-0.411
50	276	1029.724	1034.063	4.339
50	277	1032.530	1034.066	1.536
50	278	1025.470	1028.397	2.927
55	279	1017.762	1017.847	0.085
55	280	1026.701	1028.624	1.923
55	281	1027.690	1027.286	-0.404
55	282	1030.020	1029.573	-0.447
55	283	1032.909	1029.379	-3.530
100	317	1030.347	1030.187	-0.160
100	318	1017.256	1015.859	-1.397
100	319	1021.077	1018.896	-2.181
100	320	1023.574	1024.167	0.593
100	321	1026.979	1027.855	0.876
100	322	1030.282	1030.269	-0.013
100	323	1038.934	1038.039	-0.895
100	324	1021.403	1018.280	-3.123
100	326	1033.744	1033.185	-0.559
100	327	1021.269	1017.213	-4.056
100	328	1025.947	1023.482	-2.465
100	329	1025.713	1025.470	-0.243
100	332	1026.039	1026.364	0.325
100	333	1022.731	1016.765	-5.966
100	334	1024.464	1020.242	-4.222
100	335	1035.680	1035.411	-0.269
100	336	1028.407	1028.612	0.205
100	337	1029.978	1024.867	-5.111
100	338	1028.461	1027.768	-0.693
100	339	1030.860	1030.706	-0.154
100	340	1023.028	1020.918	-2.110
100	341	1024.717	1024.682	-0.035
100	342	1015.133	1014.192	-0.941
100	343	1015.494	1011.378	-4.116
105	344	1031.109	1026.878	-4.231
105	346	1016.398	1012.453	-3.945
105	349	1019.277	1021.839	2.562
105	350	1037.604	1039.193	1.589
105	351	1019.895	1020.088	0.193
105	352	1023.860	1018.328	-5.532
Max		1038.934	1039.193	4.339
Average		1025.628	1025.762	0.134
Min		1011.874	1011.378	-5.966
Std Dev		5.916	6.381	2.263



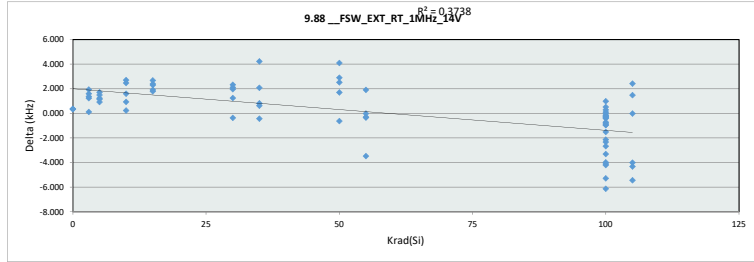
9.64 __FSW_EXT_RT_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	1100										
Min Limit	900										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1017.417	1013.561	1019.413	1023.776	1025.024	1017.970	1016.169	1028.397	1017.847	1011.378	1012.453
Average	1024.116	1022.654	1028.503	1026.984	1030.011	1025.018	1025.823	1031.403	1026.542	1024.367	1023.130
Max	1028.800	1029.609	1037.089	1032.473	1035.651	1030.619	1033.066	1034.066	1029.573	1038.039	1039.193
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



# HDR TID Report TPS7H5004-SP

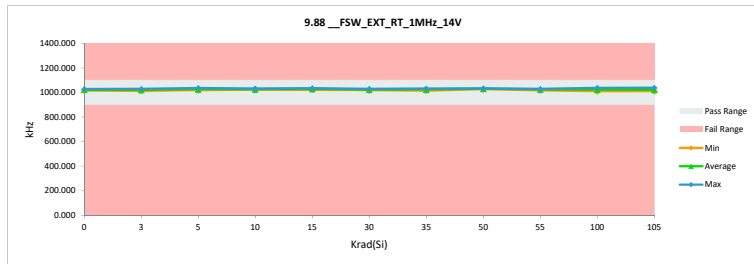
9.88 __FSW_EXT_RT_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	1100
Min Limit	900

Krad(Si)	Serial #	PRE	POST	Delta
0	295	1025.617	1025.997	0.380
0	297	1028.295	1028.649	0.354
0	298	1016.797	1017.179	0.382
3	192	1025.799	1027.756	1.957
3	193	1029.324	1029.456	0.132
3	194	1020.067	1021.457	1.390
3	196	1019.039	1020.667	1.628
3	197	1012.382	1013.635	1.253
5	200	1020.219	1021.960	1.741
5	202	1031.300	1032.477	1.177
5	203	1030.123	1031.378	1.255
5	204	1017.781	1019.297	1.516
5	206	1036.203	1037.145	0.942
10	207	1022.445	1024.062	1.617
10	209	1023.733	1026.458	2.725
10	210	1021.209	1023.708	2.499
10	211	1027.728	1027.985	0.257
10	212	1031.482	1032.441	0.959
15	214	1024.099	1026.520	2.421
15	217	1026.631	1028.567	1.936
15	218	1022.694	1025.014	2.320
15	219	1031.339	1034.045	2.706
15	222	1033.804	1035.623	1.819
30	224	1025.561	1027.910	2.349
30	225	1022.601	1022.252	-0.349
30	226	1015.914	1017.896	1.982
30	227	1024.065	1026.200	2.135
30	229	1029.274	1030.541	1.267
35	230	1032.357	1033.006	0.649
35	231	1025.053	1025.898	0.845
35	232	1024.643	1026.740	2.097
35	234	1011.876	1016.126	4.250
35	237	1027.424	1027.013	-0.411
50	272	1029.247	1031.794	2.547
50	275	1028.937	1028.328	-0.609
50	276	1029.798	1033.919	4.121
50	277	1032.367	1034.088	1.721
50	278	1025.345	1028.267	2.922
55	279	1017.682	1017.694	0.012
55	280	1026.546	1028.479	1.933
55	281	1027.473	1027.155	-0.318
55	282	1029.778	1029.501	-0.277
55	283	1032.758	1029.307	-3.451
100	317	1030.318	1029.931	-0.387
100	318	1017.102	1015.618	-1.484
100	319	1020.985	1018.672	-2.313
100	320	1023.300	1023.843	0.543
100	321	1026.839	1027.847	1.008
100	322	1030.079	1030.204	0.125
100	323	1038.889	1037.966	-0.923
100	324	1021.419	1018.139	-3.280
100	326	1033.714	1032.937	-0.777
100	327	1021.235	1017.268	-3.967
100	328	1025.911	1023.261	-2.650
100	329	1025.507	1023.311	-2.196
100	332	1025.929	1026.240	0.311
100	333	1022.726	1016.614	-6.112
100	334	1024.354	1020.178	-4.176
100	335	1035.699	1035.371	-0.328
100	336	1028.467	1028.306	-0.161
100	337	1029.952	1024.684	-5.268
100	338	1028.353	1027.662	-0.691
100	339	1030.751	1030.480	-0.271
100	340	1022.959	1020.849	-2.110
100	341	1024.640	1024.628	-0.012
100	342	1015.050	1014.137	-0.913
100	343	1015.414	1011.321	-4.093
105	344	1030.873	1026.573	-4.300
105	346	1016.284	1012.303	-3.981
105	349	1019.230	1021.678	2.448
105	350	1037.476	1038.980	1.504
105	351	1019.858	1019.862	0.004
105	352	1023.769	1018.352	-5.417
Max		1038.889	1038.980	4.250
Average		1025.533	1025.655	0.122
Min		1011.876	1011.321	-6.112
Std Dev		5.913	6.378	2.277



9.88 __FSW_EXT_RT_1MHz_14V	
Test Site	
Tester	
Test Number	
Max Limit	1100 kHz
Min Limit	900 kHz

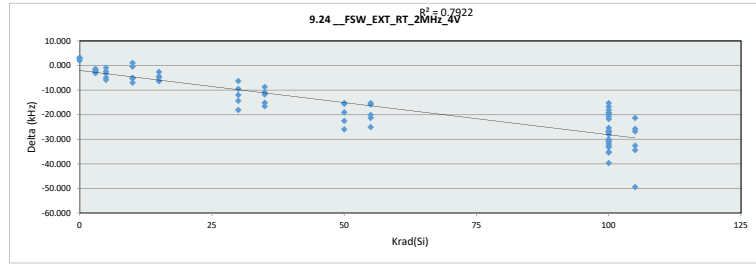
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1017.179	1013.635	1019.297	1023.708	1025.014	1017.896	1016.126	1028.267	1017.694	1011.321	1012.303
Average	1023.942	1022.594	1028.451	1026.931	1029.954	1024.960	1025.757	1031.279	1026.427	1024.228	1022.958
Max	1028.649	1029.456	1037.145	1032.441	1035.623	1030.541	1033.006	1034.088	1029.501	1037.966	1038.980
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



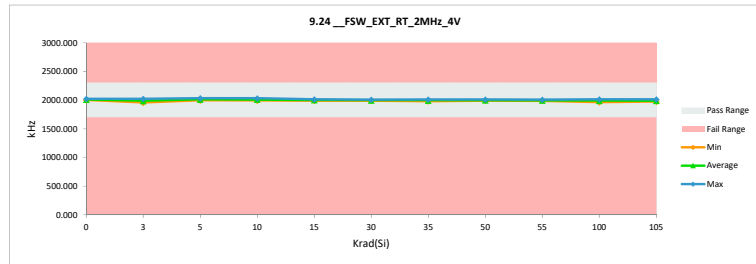
HDR TID Report  
TPS7H5004-SP

9.24 __FSW_EXT_RT_2MHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	2300
Min Limit	1700

Krad(Si)	Serial #	PRE	POST	Delta
0	295	2000.655	2002.843	2.188
0	297	2004.519	2007.503	2.984
0	298	2021.641	2025.031	3.390
3	192	2002.981	2000.672	-2.309
3	193	2027.084	2024.730	-2.354
3	194	1983.625	1981.953	-1.672
3	196	1982.612	1981.400	-1.212
3	197	1960.460	1957.474	-2.986
5	200	2003.342	1997.634	-5.708
5	202	2035.438	2034.643	-0.795
5	203	2007.090	2004.807	-2.283
5	204	2026.552	2021.773	-4.779
5	206	2012.849	2009.729	-3.120
10	207	2001.899	1995.010	-6.889
10	209	2005.528	2006.734	1.206
10	210	2003.492	1998.601	-4.891
10	211	2034.311	2033.990	-0.321
10	212	2036.404	2031.227	-5.177
15	214	2006.214	2003.756	-2.458
15	217	1994.987	1990.373	-4.614
15	218	2014.981	2010.697	-4.284
15	219	2012.812	2006.670	-6.142
15	222	2022.529	2016.357	-6.172
30	224	2011.302	1997.065	-14.237
30	225	2022.780	2004.811	-17.969
30	226	2006.435	1997.052	-9.383
30	227	2000.913	1989.074	-11.839
30	229	2016.888	2010.731	-6.157
35	230	1999.279	1988.568	-10.711
35	231	1990.546	1982.003	-8.543
35	232	2020.765	2009.112	-11.653
35	234	1998.285	1981.827	-16.458
35	237	2028.204	2013.222	-14.982
50	272	2038.673	2012.860	-25.813
50	275	2006.767	1991.606	-15.161
50	276	2013.179	1994.232	-18.947
50	277	2012.414	1996.969	-15.445
50	278	2034.881	2012.518	-22.363
55	279	2018.215	1993.336	-24.879
55	280	2002.810	1987.112	-15.698
55	281	2030.317	2010.372	-19.945
55	282	2013.490	1998.335	-15.155
55	283	2024.588	2003.416	-21.172
100	317	2022.473	2007.338	-15.135
100	318	1986.370	1965.729	-20.641
100	319	2018.391	1990.585	-27.806
100	320	2014.010	1994.256	-19.754
100	321	2033.669	2006.838	-26.831
100	322	2003.631	1982.000	-21.631
100	323	2034.581	2003.978	-30.603
100	324	2010.509	1985.272	-25.237
100	326	2051.145	2016.103	-35.042
100	327	2029.512	1989.947	-39.565
100	328	2018.733	2002.133	-16.600
100	329	2011.179	1984.485	-26.694
100	332	2033.057	2002.279	-30.778
100	333	2015.036	1981.961	-33.075
100	334	2020.186	1987.516	-32.670
100	335	2021.652	1991.793	-29.859
100	336	2023.262	1996.786	-26.476
100	337	2025.660	1993.831	-31.829
100	338	2013.172	1994.030	-19.142
100	339	2028.523	1997.816	-30.707
100	340	2017.087	1990.327	-26.760
100	341	2009.245	1991.303	-17.942
100	342	2013.559	1978.317	-35.242
100	343	1995.125	1975.919	-19.206
105	344	2049.013	2016.577	-32.436
105	346	2025.665	1976.356	-49.309
105	349	2001.553	1980.333	-21.220
105	350	2028.642	2002.001	-26.641
105	351	2013.188	1978.920	-34.268
105	352	2012.595	1986.976	-25.619
Max		2051.145	2034.643	3.390
Average		2014.701	1998.213	-16.488
Min		1960.460	1957.474	-49.309
Std Dev		15.606	15.105	12.104

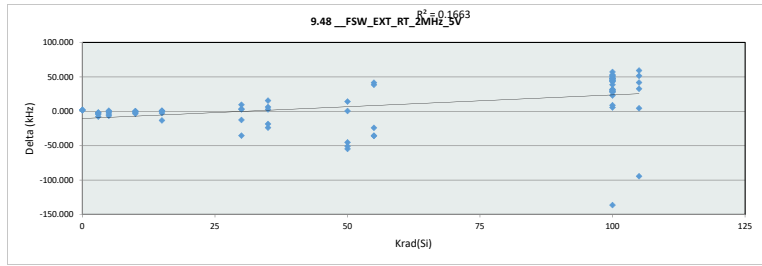


9.24 __FSW_EXT_RT_2MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	2300 kHz										
Min Limit	1700 kHz										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2002.843	1957.474	1997.634	1995.010	1990.373	1989.074	1981.827	1991.606	1987.112	1965.729	1976.356
Average	2011.792	1989.246	2013.717	2013.112	2005.571	1999.747	1994.946	2001.637	1998.514	1992.106	1990.194
Max	2025.031	2024.730	2034.643	2033.990	2016.357	2010.731	2013.222	2012.860	2010.372	2016.103	2016.577
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

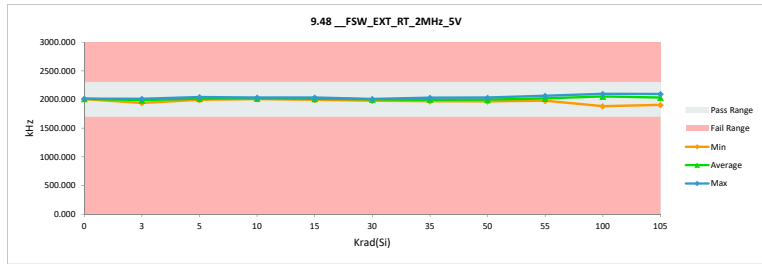


HDR TID Report  
TPS7H5004-SP

9.48 __FSW_EXT_RT_2MHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz		kHz	
Max Limit	2300		2300	
Min Limit	1700		1700	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	2006.711	2008.359	1.648
0	297	2015.402	2018.013	2.611
0	298	2013.850	2016.147	2.297
3	192	2016.771	2012.666	-4.105
3	193	2017.051	2014.915	-2.136
3	194	1996.787	1994.848	-1.939
3	196	1989.739	1988.416	-1.323
3	197	1949.282	1942.052	-7.230
5	200	1999.389	1999.088	-0.301
5	202	2046.987	2046.384	-0.603
5	203	2023.026	2019.405	-3.621
5	204	1999.471	2000.844	1.373
5	206	2029.031	2022.895	-6.136
10	207	2010.304	2008.544	-1.760
10	209	2016.408	2016.875	0.467
10	210	2012.248	2012.224	-0.024
10	211	2040.818	2037.610	-3.208
10	212	2035.389	2035.919	0.530
15	214	2005.835	2007.172	1.337
15	217	2009.726	1996.852	-12.874
15	218	2011.860	2012.292	0.432
15	219	2019.235	2016.997	-2.238
15	222	2035.689	2036.825	1.136
30	224	2005.423	2008.453	3.030
30	225	2007.343	2011.193	3.850
30	226	1998.246	2008.241	9.995
30	227	2016.715	1981.751	-34.964
30	229	2024.220	2012.012	-12.208
35	230	1997.131	1973.776	-23.355
35	231	2001.667	1983.717	-17.950
35	232	2023.702	2030.267	6.565
35	234	1982.879	1998.777	15.898
35	237	2031.152	2034.276	3.124
50	272	2021.682	2036.257	14.575
50	275	2019.697	1975.025	-44.672
50	276	2032.065	1981.775	-50.290
50	277	2024.108	1969.759	-54.349
50	278	2030.546	2031.452	0.906
55	279	2006.494	2048.401	41.907
55	280	2017.362	1981.898	-35.464
55	281	2040.986	2017.450	-23.536
55	282	2036.473	2001.483	-34.990
55	283	2029.718	2068.619	38.901
100	317	2030.235	2074.496	44.261
100	318	2000.199	2051.463	51.264
100	319	2012.166	2041.575	29.409
100	320	2025.504	2054.333	28.829
100	321	2026.918	2074.718	47.800
100	322	2020.811	2078.308	57.497
100	323	2052.298	2101.391	49.093
100	324	2024.385	2069.047	44.662
100	326	2042.562	2090.093	47.531
100	327	2018.395	2027.580	9.185
100	328	2025.566	2064.504	38.938
100	329	2022.290	1886.414	-135.876
100	332	2032.854	2065.354	32.500
100	333	2017.100	2022.985	5.885
100	334	2028.523	2071.888	43.365
100	335	2032.390	2085.817	53.427
100	336	2030.912	2078.552	47.640
100	337	2035.805	2066.573	30.768
100	338	2014.818	2061.844	47.026
100	339	2035.142	2087.549	52.407
100	340	2006.098	2037.857	31.759
100	341	2016.744	2061.734	44.990
100	342	1993.628	2021.910	28.282
100	343	2004.376	2027.703	23.327
105	344	2038.415	2043.209	4.794
105	346	2003.585	1909.588	-93.997
105	349	2014.989	2074.636	59.647
105	350	2046.400	2098.301	51.901
105	351	2000.679	2042.918	42.239
105	352	2021.203	2054.346	33.143
Max		2052.298	2101.391	59.647
Average		2018.132	2026.666	8.534
Min		1949.282	1886.414	-135.876
Std Dev		16.758	40.481	34.692



9.48 __FSW_EXT_RT_2MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	2300										
Min Limit	1700										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2008.359	1942.052	1999.088	2008.544	1996.852	1981.751	1973.776	1969.759	1981.898	1886.414	1909.588
Average	2014.173	1990.579	2017.723	2022.234	2014.028	2004.330	2004.163	1998.854	2023.570	2054.320	2037.166
Max	2018.013	2014.915	2046.384	2037.610	2036.825	2012.012	2034.276	2036.257	2068.619	2101.391	2098.301
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

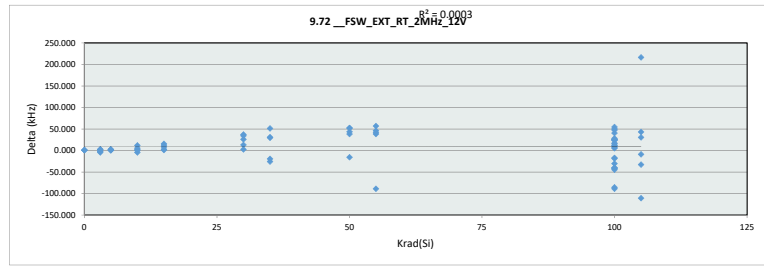




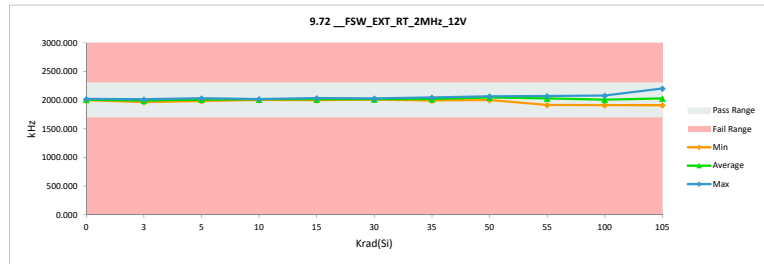
HDR TID Report  
TPS7H5004-SP

9.72 __FSW_EXT_RT_2MHz_12V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	2300
Min Limit	1700

Krad(Si)	Serial #	PRE	POST	Delta
0	295	2005.956	2007.503	1.547
0	297	2018.882	2020.614	1.732
0	298	1996.574	1998.271	1.697
3	192	2011.246	2014.723	3.477
3	193	2002.217	2000.852	-1.365
3	194	1990.146	1989.968	-0.178
3	196	1983.125	1986.509	3.384
3	197	1968.548	1964.661	-3.887
5	200	1985.867	1988.822	2.955
5	202	2030.292	2031.664	1.372
5	203	2013.489	2016.910	3.421
5	204	1983.507	1985.698	2.191
5	206	2031.318	2032.589	1.271
10	207	2000.186	2003.519	3.333
10	209	2007.078	2019.621	12.543
10	210	2000.405	2006.689	6.284
10	211	2023.878	2020.155	-3.723
10	212	2019.189	2020.044	0.855
15	214	1994.044	2010.395	16.351
15	217	2012.892	2016.519	3.627
15	218	1996.643	1998.631	1.988
15	219	2009.324	2018.409	9.085
15	222	2023.271	2036.283	13.012
30	224	1992.683	2030.733	38.050
30	225	1992.908	2019.498	26.590
30	226	1981.915	2016.867	34.952
30	227	2006.103	2009.416	3.313
30	229	2009.444	2023.279	13.835
35	230	2023.467	1998.705	-24.762
35	231	2011.893	1993.331	-18.562
35	232	2007.746	2038.084	30.338
35	234	1967.212	2019.144	51.932
35	237	2015.196	2046.934	31.738
50	272	2006.667	2050.927	44.260
50	275	2008.303	2061.450	53.147
50	276	2017.465	2002.485	-14.980
50	277	2013.377	2066.158	52.781
50	278	2014.003	2053.153	39.150
55	279	1991.294	2048.856	57.562
55	280	2003.386	1915.422	-87.964
55	281	2024.609	2071.553	46.944
55	282	2020.317	2059.388	39.071
55	283	2016.111	2058.550	42.439
100	317	2016.637	2028.462	11.825
100	318	1995.430	2020.573	25.143
100	319	1996.613	1966.980	-29.633
100	320	2009.204	1969.250	-39.954
100	321	2009.282	2027.179	17.897
100	322	2013.146	2061.286	48.140
100	323	2038.060	2079.307	41.247
100	324	2012.878	2038.898	26.020
100	326	2023.983	2039.773	15.790
100	327	2000.106	1911.914	-88.192
100	328	2011.062	2019.278	8.216
100	329	2011.394	2066.671	55.277
100	332	2015.754	1999.399	-16.355
100	333	1999.952	1915.325	-84.627
100	334	2013.615	2038.311	24.696
100	335	2020.723	2071.768	51.045
100	336	2015.741	2042.506	26.765
100	337	2021.889	2003.893	-17.996
100	338	2003.453	2014.975	11.522
100	339	2019.804	2048.900	29.096
100	340	1991.631	1952.199	-39.432
100	341	2005.097	2011.092	5.995
100	342	1976.147	1932.097	-44.050
100	343	1992.602	1951.522	-41.080
105	344	2021.765	1911.842	-109.923
105	346	1986.170	2203.168	216.998
105	349	2002.415	2046.197	43.782
105	350	2034.439	2065.601	31.162
105	351	1984.250	1976.233	-8.017
105	352	2011.465	1979.625	-31.840
Max		2038.060	2203.168	216.998
Average		2006.670	2015.989	9.319
Min		1967.212	1911.842	-109.923
Std Dev		14.842	44.849	42.492

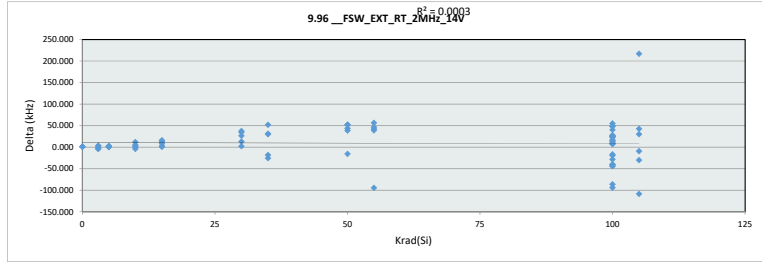


9.72 __FSW_EXT_RT_2MHz_12V		0	3	5	10	15	30	35	50	55	100	105
LL		1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min		1998.271	1964.661	1985.698	2003.519	1998.631	2009.416	1993.331	2002.485	1915.422	1911.914	1911.842
Average		2008.796	1991.343	2011.137	2014.006	2016.047	2019.959	2019.240	2046.835	2030.754	2008.815	2030.444
Max		2020.614	2014.723	2032.589	2020.155	2036.283	2030.733	2046.934	2066.158	2071.553	2079.307	2203.168
UL		2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

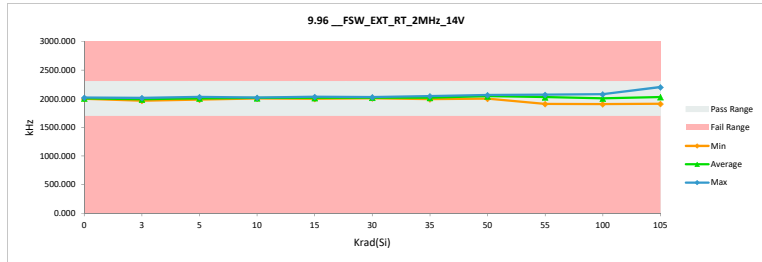


HDR TID Report  
TPS7H5004-SP

9.96_FSW_EXT_RT_2MHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	2300	2300		
Min Limit	1700	1700		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	2005.616	2007.033	1.417
0	297	2018.247	2020.383	2.136
0	298	1996.047	1997.810	1.763
3	192	2011.066	2015.100	4.034
3	193	2001.859	2000.697	-1.162
3	194	1989.894	1989.236	-0.658
3	196	1982.524	1986.161	3.637
3	197	1967.920	1964.341	-3.579
5	200	1985.559	1988.758	3.199
5	202	2030.264	2031.022	0.758
5	203	2013.195	2015.968	2.773
5	204	1983.288	1985.732	2.444
5	206	2030.994	2032.850	1.856
10	207	1999.926	2003.323	3.397
10	209	2006.676	2019.184	12.508
10	210	2000.158	2006.149	5.991
10	211	2023.460	2020.162	-3.298
10	212	2018.823	2019.790	0.967
15	214	1993.315	2010.104	16.789
15	217	2012.645	2016.289	3.644
15	218	1996.714	1998.137	1.423
15	219	2008.880	2017.990	9.110
15	222	2022.593	2035.922	13.329
30	224	1992.393	2030.618	38.225
30	225	1992.398	2019.410	27.012
30	226	1981.676	2016.915	35.239
30	227	2005.804	2008.896	3.092
30	229	2009.399	2023.225	13.826
35	230	2023.066	1998.137	-24.929
35	231	2011.454	1994.080	-17.374
35	232	2007.098	2037.836	30.738
35	234	1966.852	2019.528	52.676
35	237	2015.074	2046.963	31.889
50	272	2006.294	2050.881	44.587
50	275	2008.089	2061.180	53.091
50	276	2017.103	2002.276	-14.827
50	277	2012.996	2065.462	52.466
50	278	2013.732	2053.106	39.374
55	279	1991.121	2048.263	57.142
55	280	2003.522	1909.741	-93.781
55	281	2024.195	2071.453	47.258
55	282	2020.066	2059.814	39.748
55	283	2015.782	2058.711	42.929
100	317	2016.364	2026.447	10.083
100	318	1995.219	2020.328	25.109
100	319	1996.216	1968.556	-27.660
100	320	2009.299	1970.323	-38.976
100	321	2009.346	2026.939	17.593
100	322	2013.133	2061.820	48.687
100	323	2037.751	2078.734	40.983
100	324	2012.601	2036.404	23.803
100	326	2023.343	2038.423	15.080
100	327	1999.581	1906.261	-93.320
100	328	2010.876	2018.923	8.047
100	329	2010.751	2066.367	55.616
100	332	2015.620	1999.429	-16.191
100	333	1999.453	1913.803	-85.650
100	334	2013.551	2038.851	25.300
100	335	2020.532	2070.720	50.188
100	336	2015.766	2042.860	27.094
100	337	2021.666	2003.006	-18.660
100	338	2002.963	2015.830	12.867
100	339	2019.894	2048.622	28.728
100	340	1991.167	1951.580	-39.587
100	341	2004.732	2012.917	8.185
100	342	1975.997	1932.469	-43.528
100	343	1992.353	1949.761	-42.592
105	344	2021.275	1913.251	-108.024
105	346	1985.563	2203.540	217.977
105	349	2002.192	2045.741	43.549
105	350	2034.251	2064.991	30.740
105	351	1984.340	1975.648	-8.692
105	352	2011.035	1981.687	-29.348
Max		2037.751	2203.540	217.977
Average		2006.364	2015.656	9.291
Min		1966.852	1906.261	-108.024
Std Dev		14.861	45.156	42.828

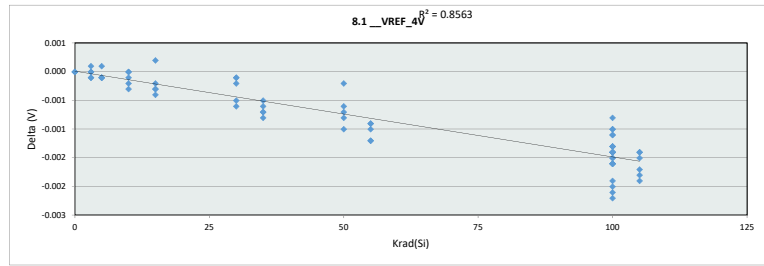


9.96_FSW_EXT_RT_2MHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	2300	kHz									
Min Limit	1700	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	1997.810	1964.341	1985.732	2003.323	1998.137	2008.896	1994.080	2002.276	1909.741	1906.261	1913.251
Average	2008.409	1991.107	2010.866	2013.722	2015.688	2019.813	2019.309	2046.581	2029.596	2008.307	2030.810
Max	2020.383	2015.100	2032.850	2020.162	2035.922	2030.618	2046.963	2065.462	2071.453	2078.734	2203.540
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000

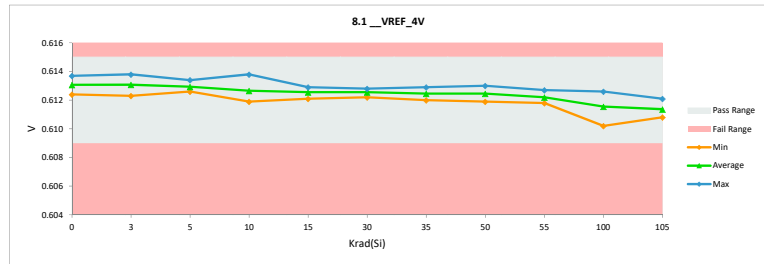


HDR TID Report  
TPS7H5004-SP

		8.1_VREF_4V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	Min Limit	0.615	0.615	
		0.609	0.609	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.613	0.613	0.000
0	297	0.612	0.612	0.000
0	298	0.614	0.614	0.000
3	192	0.614	0.614	0.000
3	193	0.613	0.613	0.000
3	194	0.614	0.614	0.000
3	196	0.613	0.613	0.000
3	197	0.612	0.612	0.000
5	200	0.613	0.613	0.000
5	202	0.613	0.613	0.000
5	203	0.613	0.613	0.000
5	204	0.613	0.613	0.000
5	206	0.613	0.613	0.000
10	207	0.613	0.612	0.000
10	209	0.612	0.612	0.000
10	210	0.614	0.614	0.000
10	211	0.613	0.613	0.000
10	212	0.612	0.612	0.000
15	214	0.613	0.613	0.000
15	217	0.613	0.613	0.000
15	218	0.613	0.613	0.000
15	219	0.612	0.612	0.000
15	222	0.613	0.613	0.000
30	224	0.613	0.612	-0.001
30	225	0.613	0.613	0.000
30	226	0.613	0.613	0.000
30	227	0.613	0.613	0.000
30	229	0.613	0.613	0.000
35	230	0.613	0.613	-0.001
35	231	0.613	0.612	-0.001
35	232	0.613	0.613	-0.001
35	234	0.613	0.613	-0.001
35	237	0.613	0.612	-0.001
50	272	0.613	0.613	-0.001
50	275	0.613	0.612	-0.001
50	276	0.613	0.612	-0.001
50	277	0.613	0.613	-0.001
50	278	0.613	0.613	0.000
55	279	0.613	0.612	-0.001
55	280	0.613	0.612	-0.001
55	281	0.613	0.612	-0.001
55	282	0.613	0.612	-0.001
55	283	0.614	0.613	-0.001
100	317	0.613	0.612	-0.001
100	318	0.612	0.610	-0.002
100	319	0.613	0.611	-0.002
100	320	0.613	0.611	-0.001
100	321	0.613	0.611	-0.002
100	322	0.613	0.613	-0.001
100	323	0.613	0.612	-0.001
100	324	0.613	0.612	-0.001
100	326	0.614	0.612	-0.001
100	327	0.613	0.612	-0.001
100	328	0.613	0.612	-0.001
100	329	0.613	0.611	-0.002
100	332	0.613	0.611	-0.001
100	333	0.613	0.611	-0.002
100	334	0.613	0.612	-0.001
100	335	0.613	0.612	-0.001
100	336	0.613	0.612	-0.001
100	337	0.613	0.612	-0.001
100	338	0.612	0.611	-0.001
100	339	0.613	0.612	-0.002
100	340	0.613	0.611	-0.002
100	341	0.613	0.612	-0.002
100	342	0.613	0.612	-0.001
100	343	0.613	0.612	-0.002
105	344	0.613	0.611	-0.002
105	346	0.613	0.612	-0.001
105	349	0.613	0.611	-0.002
105	350	0.613	0.611	-0.002
105	351	0.613	0.611	-0.002
105	352	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.612	-0.001
	Min	0.612	0.610	-0.002
	Std Dev	0.000	0.001	0.001

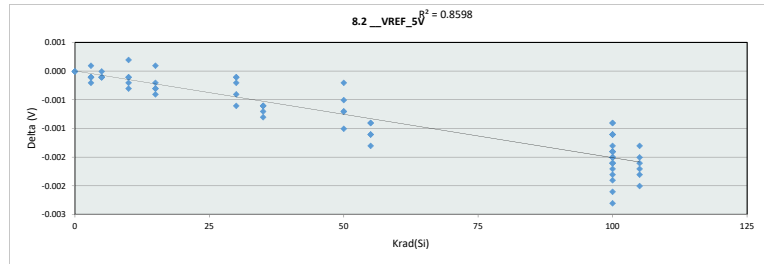


		8.1_VREF_4V										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit	Min Limit	0.615	0.609	V	V							
		0.609	0.609	V	V							
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min		0.612	0.612	0.613	0.612	0.612	0.612	0.612	0.612	0.612	0.610	0.611
Average		0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612	0.611
Max		0.614	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.612
UL		0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

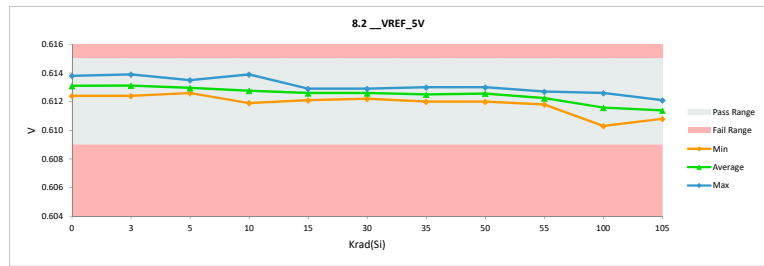


HDR TID Report  
TPS7H5004-SP

		8.2_VREF_5V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	Min Limit	0.615	0.615	
		0.609	0.609	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.613	0.613	0.000
0	297	0.612	0.612	0.000
0	298	0.614	0.614	0.000
3	192	0.614	0.614	0.000
3	193	0.613	0.613	0.000
3	194	0.614	0.614	0.000
3	196	0.613	0.613	0.000
3	197	0.613	0.612	0.000
5	200	0.613	0.613	0.000
5	202	0.614	0.613	0.000
5	203	0.613	0.613	0.000
5	204	0.613	0.613	0.000
5	206	0.613	0.613	0.000
10	207	0.613	0.613	0.000
10	209	0.612	0.613	0.000
10	210	0.614	0.614	0.000
10	211	0.613	0.613	0.000
10	212	0.612	0.612	0.000
15	214	0.613	0.613	0.000
15	217	0.613	0.613	0.000
15	218	0.613	0.613	0.000
15	219	0.613	0.612	0.000
15	222	0.613	0.613	0.000
30	224	0.613	0.612	-0.001
30	225	0.613	0.613	0.000
30	226	0.613	0.613	0.000
30	227	0.613	0.613	0.000
30	229	0.613	0.613	0.000
35	230	0.613	0.613	-0.001
35	231	0.613	0.612	-0.001
35	232	0.614	0.613	-0.001
35	234	0.613	0.613	-0.001
35	237	0.613	0.612	-0.001
50	272	0.613	0.613	-0.001
50	275	0.613	0.612	-0.001
50	276	0.613	0.612	-0.001
50	277	0.613	0.613	-0.001
50	278	0.613	0.613	0.000
55	279	0.613	0.612	-0.001
55	280	0.613	0.612	-0.001
55	281	0.613	0.612	-0.001
55	282	0.613	0.612	-0.001
55	283	0.614	0.613	-0.001
100	317	0.613	0.612	-0.002
100	318	0.612	0.610	-0.002
100	319	0.613	0.611	-0.002
100	320	0.613	0.611	-0.001
100	321	0.613	0.611	-0.002
100	322	0.613	0.613	-0.001
100	323	0.613	0.612	-0.002
100	324	0.613	0.612	-0.001
100	326	0.614	0.612	-0.002
100	327	0.613	0.612	-0.002
100	328	0.613	0.613	-0.001
100	329	0.613	0.611	-0.002
100	332	0.613	0.611	-0.001
100	333	0.613	0.611	-0.002
100	334	0.613	0.612	-0.001
100	335	0.613	0.612	-0.001
100	336	0.613	0.612	-0.002
100	337	0.613	0.612	-0.001
100	338	0.612	0.611	-0.001
100	339	0.613	0.612	-0.002
100	340	0.613	0.611	-0.002
100	341	0.613	0.612	-0.002
100	342	0.613	0.612	-0.001
100	343	0.613	0.612	-0.002
105	344	0.613	0.611	-0.002
105	346	0.613	0.612	-0.001
105	349	0.613	0.611	-0.002
105	350	0.613	0.611	-0.002
105	351	0.613	0.611	-0.002
105	352	0.614	0.612	-0.002
	Max	0.614	0.614	0.000
	Average	0.613	0.612	-0.001
	Min	0.612	0.610	-0.002
	Std Dev	0.000	0.001	0.001

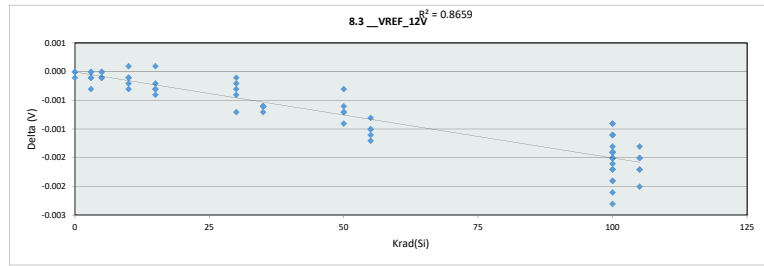


		8.2_VREF_5V										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit	Min Limit	0.615	0.609	V	V							
		0	3	5	10	15	30	35	50	55	100	105
LL		0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min		0.612	0.612	0.613	0.612	0.612	0.612	0.612	0.612	0.612	0.610	0.611
Average		0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.611
Max		0.614	0.614	0.614	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.612
UL		0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

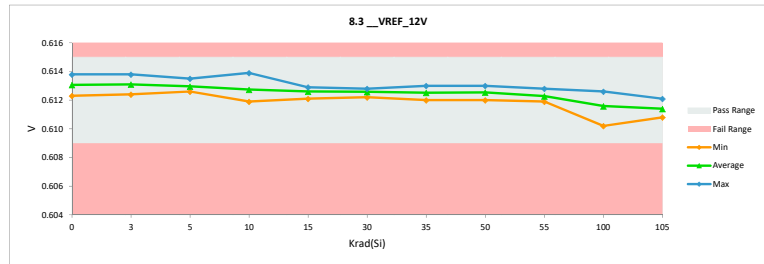


HDR TID Report  
TPS7H5004-SP

		8.3_VREF_12V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	Min Limit	0.615	0.615	
		0.609	0.609	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.613	0.613	0.000
0	297	0.612	0.612	0.000
0	298	0.614	0.614	0.000
3	192	0.614	0.614	0.000
3	193	0.613	0.613	0.000
3	194	0.614	0.614	0.000
3	196	0.613	0.613	0.000
3	197	0.613	0.612	0.000
5	200	0.613	0.613	0.000
5	202	0.613	0.613	0.000
5	203	0.613	0.613	0.000
5	204	0.613	0.613	0.000
5	206	0.613	0.613	0.000
10	207	0.613	0.613	0.000
10	209	0.612	0.613	0.000
10	210	0.614	0.614	0.000
10	211	0.613	0.613	0.000
10	212	0.612	0.612	0.000
15	214	0.613	0.613	0.000
15	217	0.613	0.613	0.000
15	218	0.613	0.613	0.000
15	219	0.612	0.612	0.000
15	222	0.613	0.613	0.000
30	224	0.613	0.612	-0.001
30	225	0.613	0.613	0.000
30	226	0.613	0.613	0.000
30	227	0.613	0.613	0.000
30	229	0.613	0.613	0.000
35	230	0.613	0.613	-0.001
35	231	0.613	0.612	-0.001
35	232	0.614	0.613	-0.001
35	234	0.613	0.613	-0.001
35	237	0.613	0.612	-0.001
50	272	0.613	0.613	-0.001
50	275	0.613	0.612	-0.001
50	276	0.613	0.612	-0.001
50	277	0.613	0.613	-0.001
50	278	0.613	0.613	0.000
55	279	0.613	0.612	-0.001
55	280	0.613	0.612	-0.001
55	281	0.613	0.612	-0.001
55	282	0.613	0.612	-0.001
55	283	0.614	0.613	-0.001
100	317	0.613	0.612	-0.001
100	318	0.612	0.610	-0.002
100	319	0.613	0.611	-0.002
100	320	0.613	0.611	-0.001
100	321	0.613	0.611	-0.002
100	322	0.613	0.613	-0.001
100	323	0.613	0.612	-0.002
100	324	0.613	0.612	-0.001
100	326	0.614	0.612	-0.002
100	327	0.613	0.612	-0.002
100	328	0.613	0.613	-0.001
100	329	0.613	0.611	-0.002
100	332	0.613	0.612	-0.001
100	333	0.613	0.611	-0.002
100	334	0.613	0.612	-0.001
100	335	0.613	0.612	-0.001
100	336	0.613	0.612	-0.001
100	337	0.613	0.612	-0.001
100	338	0.612	0.611	-0.001
100	339	0.613	0.612	-0.002
100	340	0.613	0.611	-0.002
100	341	0.613	0.612	-0.002
100	342	0.613	0.612	-0.001
100	343	0.613	0.612	-0.002
105	344	0.613	0.611	-0.002
105	346	0.613	0.612	-0.001
105	349	0.613	0.611	-0.002
105	350	0.613	0.611	-0.002
105	351	0.613	0.611	-0.002
105	352	0.614	0.612	-0.002
	Max	0.614	0.614	0.000
	Average	0.613	0.612	-0.001
	Min	0.612	0.610	-0.002
	Std Dev	0.000	0.001	0.001

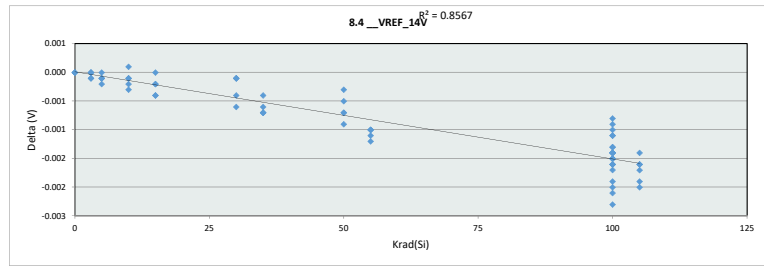


		8.3_VREF_12V										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit	Min Limit	0.615	0.609	V	V							
		0	3	5	10	15	30	35	50	55	100	105
LL		0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min		0.612	0.612	0.613	0.612	0.612	0.612	0.612	0.612	0.612	0.610	0.611
Average		0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.611
Max		0.614	0.614	0.614	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.612
UL		0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

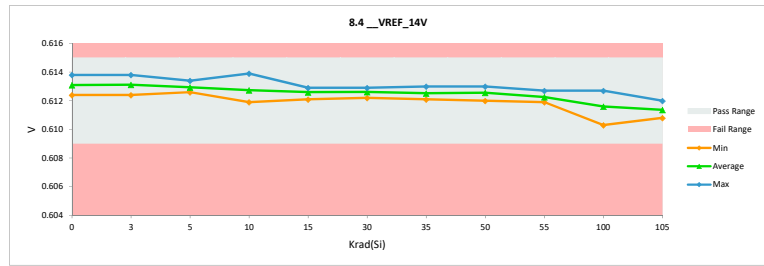


HDR TID Report  
TPS7H5004-SP

		8.4_VREF_14V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	Min Limit	0.615	0.615	
		0.609	0.609	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.613	0.613	0.000
0	297	0.612	0.612	0.000
0	298	0.614	0.614	0.000
3	192	0.614	0.614	0.000
3	193	0.613	0.613	0.000
3	194	0.614	0.614	0.000
3	196	0.613	0.613	0.000
3	197	0.612	0.612	0.000
5	200	0.613	0.613	0.000
5	202	0.614	0.613	0.000
5	203	0.613	0.613	0.000
5	204	0.613	0.613	0.000
5	206	0.613	0.613	0.000
10	207	0.613	0.613	0.000
10	209	0.612	0.613	0.000
10	210	0.614	0.614	0.000
10	211	0.613	0.613	0.000
10	212	0.612	0.612	0.000
15	214	0.613	0.613	0.000
15	217	0.613	0.613	0.000
15	218	0.613	0.613	0.000
15	219	0.613	0.612	0.000
15	222	0.613	0.613	0.000
30	224	0.613	0.612	-0.001
30	225	0.613	0.613	0.000
30	226	0.613	0.613	0.000
30	227	0.613	0.613	0.000
30	229	0.613	0.613	0.000
35	230	0.613	0.613	-0.001
35	231	0.613	0.612	0.000
35	232	0.614	0.613	-0.001
35	234	0.613	0.613	-0.001
35	237	0.613	0.612	-0.001
50	272	0.613	0.613	-0.001
50	275	0.613	0.612	-0.001
50	276	0.613	0.612	-0.001
50	277	0.613	0.613	-0.001
50	278	0.613	0.613	0.000
55	279	0.613	0.612	-0.001
55	280	0.613	0.612	-0.001
55	281	0.613	0.612	-0.001
55	282	0.613	0.612	-0.001
55	283	0.614	0.613	-0.001
100	317	0.613	0.612	-0.002
100	318	0.612	0.610	-0.002
100	319	0.613	0.611	-0.002
100	320	0.613	0.611	-0.001
100	321	0.613	0.611	-0.002
100	322	0.613	0.613	-0.001
100	323	0.613	0.612	-0.002
100	324	0.613	0.612	-0.001
100	326	0.614	0.612	-0.002
100	327	0.613	0.612	-0.002
100	328	0.613	0.613	-0.001
100	329	0.613	0.611	-0.002
100	332	0.613	0.612	-0.001
100	333	0.613	0.611	-0.002
100	334	0.613	0.612	-0.001
100	335	0.613	0.612	-0.001
100	336	0.613	0.612	-0.001
100	337	0.613	0.612	-0.001
100	338	0.612	0.611	-0.001
100	339	0.613	0.612	-0.002
100	340	0.613	0.611	-0.002
100	341	0.613	0.612	-0.002
100	342	0.613	0.612	-0.001
100	343	0.613	0.612	-0.001
105	344	0.613	0.611	-0.002
105	346	0.613	0.612	-0.001
105	349	0.613	0.611	-0.002
105	350	0.613	0.611	-0.002
105	351	0.613	0.611	-0.002
105	352	0.614	0.612	-0.002
	Max	0.614	0.614	0.000
	Average	0.613	0.612	-0.001
	Min	0.612	0.610	-0.002
	Std Dev	0.000	0.001	0.001

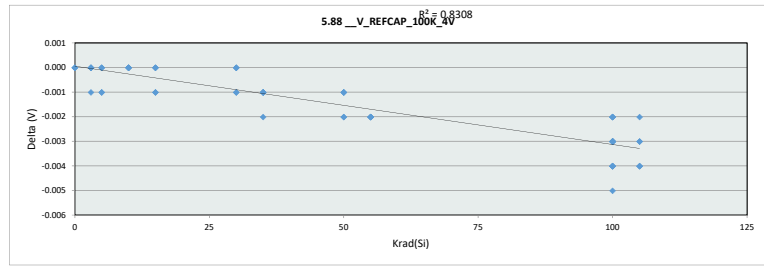


		8.4_VREF_14V										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit	Min Limit	0.615	0.609	V	V							
		0.609	0.609	V	V							
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min		0.612	0.612	0.613	0.612	0.612	0.612	0.612	0.612	0.612	0.610	0.611
Average		0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.611
Max		0.614	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.612
UL		0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615

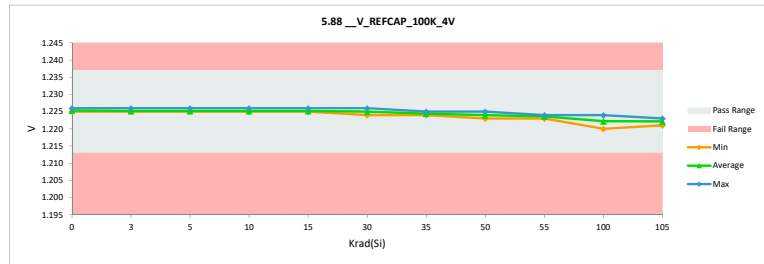


HDR TID Report  
TPS7H5004-SP

5.88 __V_REFCAP_100K_4V					
Test Site					
Tester					
Test Number					
Unit	V		V		
Max Limit	1.237		1.237		
Min Limit	1.213		1.213		
Krad(Si)	Serial #	PRE	POST	Delta	
0	295	1.225	1.225	0.000	
0	297	1.225	1.225	0.000	
0	298	1.226	1.226	0.000	
3	192	1.226	1.226	0.000	
3	193	1.225	1.225	0.000	
3	194	1.225	1.225	0.000	
3	196	1.226	1.225	-0.001	
3	197	1.225	1.225	0.000	
5	200	1.225	1.225	0.000	
5	202	1.226	1.225	-0.001	
5	203	1.225	1.225	0.000	
5	204	1.226	1.226	0.000	
5	206	1.226	1.225	-0.001	
10	207	1.225	1.225	0.000	
10	209	1.225	1.225	0.000	
10	210	1.226	1.226	0.000	
10	211	1.225	1.225	0.000	
10	212	1.225	1.225	0.000	
15	214	1.226	1.226	0.000	
15	217	1.225	1.225	0.000	
15	218	1.226	1.225	-0.001	
15	219	1.226	1.225	-0.001	
15	222	1.225	1.225	0.000	
30	224	1.225	1.224	-0.001	
30	225	1.226	1.225	-0.001	
30	226	1.225	1.225	0.000	
30	227	1.225	1.225	0.000	
30	229	1.226	1.226	0.000	
35	230	1.226	1.224	-0.002	
35	231	1.225	1.224	-0.001	
35	232	1.226	1.225	-0.001	
35	234	1.226	1.225	-0.001	
35	237	1.225	1.224	-0.001	
50	272	1.225	1.224	-0.001	
50	275	1.225	1.223	-0.002	
50	276	1.225	1.224	-0.001	
50	277	1.226	1.224	-0.002	
50	278	1.226	1.225	-0.001	
55	279	1.225	1.223	-0.002	
55	280	1.226	1.224	-0.002	
55	281	1.225	1.223	-0.002	
55	282	1.226	1.224	-0.002	
55	283	1.226	1.224	-0.002	
100	317	1.225	1.222	-0.003	
100	318	1.225	1.221	-0.004	
100	319	1.226	1.222	-0.004	
100	320	1.225	1.222	-0.003	
100	321	1.225	1.220	-0.005	
100	322	1.226	1.224	-0.002	
100	323	1.225	1.222	-0.003	
100	324	1.226	1.223	-0.003	
100	326	1.226	1.223	-0.003	
100	327	1.225	1.222	-0.003	
100	328	1.225	1.223	-0.002	
100	329	1.225	1.221	-0.004	
100	332	1.225	1.223	-0.002	
100	333	1.225	1.222	-0.003	
100	334	1.225	1.222	-0.003	
100	335	1.225	1.223	-0.002	
100	336	1.226	1.223	-0.003	
100	337	1.226	1.223	-0.003	
100	338	1.225	1.222	-0.003	
100	339	1.226	1.222	-0.004	
100	340	1.226	1.222	-0.004	
100	341	1.225	1.222	-0.003	
100	342	1.225	1.222	-0.003	
100	343	1.225	1.222	-0.003	
105	344	1.225	1.221	-0.004	
105	346	1.225	1.223	-0.002	
105	349	1.226	1.222	-0.004	
105	350	1.226	1.222	-0.004	
105	351	1.225	1.222	-0.003	
105	352	1.226	1.223	-0.003	
Max		1.226	1.226	0.000	
Average		1.225	1.224	-0.002	
Min		1.225	1.220	-0.005	
Std Dev		0.000	0.001	0.001	

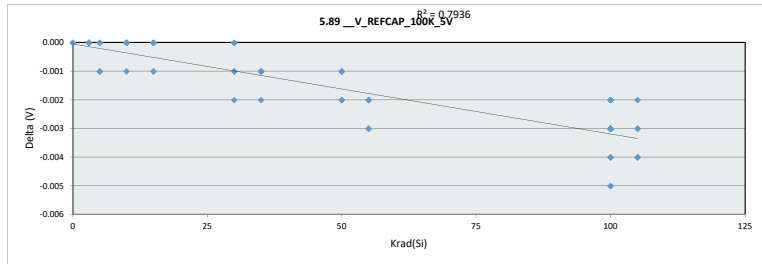


5.88 __V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.220	1.221
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

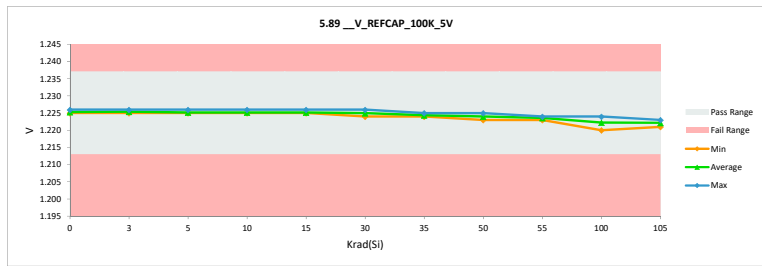


HDR TID Report  
TPS7H5004-SP

5.89 __V_REFCAP_100K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.225	1.225	0.000
5	200	1.226	1.225	-0.001
5	202	1.226	1.225	-0.001
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.225	-0.001
10	207	1.225	1.225	0.000
10	209	1.225	1.225	0.000
10	210	1.226	1.226	0.000
10	211	1.225	1.225	0.000
10	212	1.226	1.225	-0.001
15	214	1.226	1.226	0.000
15	217	1.225	1.225	0.000
15	218	1.226	1.225	-0.001
15	219	1.226	1.225	-0.001
15	222	1.225	1.225	0.000
30	224	1.226	1.224	-0.002
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.226	1.225	-0.001
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.225	1.224	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.225	1.224	-0.001
50	272	1.225	1.224	-0.001
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.224	-0.002
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.223	-0.003
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.225	1.222	-0.003
100	318	1.225	1.221	-0.004
100	319	1.226	1.222	-0.004
100	320	1.225	1.222	-0.003
100	321	1.225	1.220	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.225	1.223	-0.002
100	329	1.226	1.221	-0.005
100	332	1.225	1.223	-0.002
100	333	1.225	1.222	-0.003
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.223	-0.003
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.225	1.222	-0.003
100	342	1.225	1.222	-0.003
100	343	1.225	1.222	-0.003
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.222	-0.004
105	350	1.226	1.222	-0.004
105	351	1.225	1.222	-0.003
105	352	1.226	1.223	-0.003
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.002
Min		1.225	1.220	-0.005
Std Dev		0.001	0.002	0.001



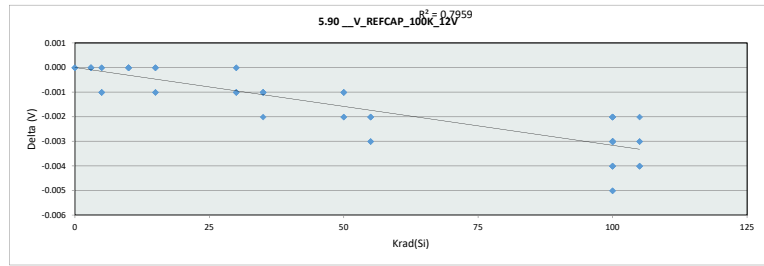
5.89 __V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.220	1.221
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



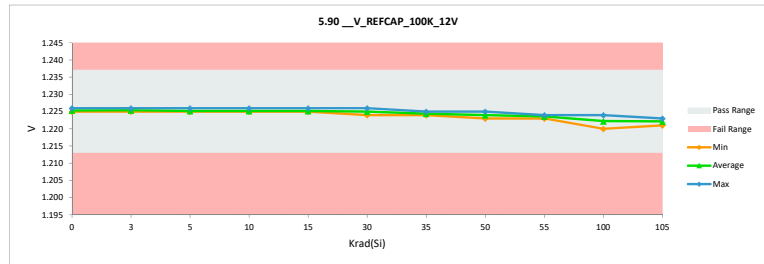


HDR TID Report  
TPS7H5004-SP

5.90_V_REFCAP_100K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.225	1.225	0.000
5	200	1.226	1.225	-0.001
5	202	1.226	1.225	-0.001
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.225	-0.001
10	207	1.225	1.225	0.000
10	209	1.225	1.225	0.000
10	210	1.226	1.226	0.000
10	211	1.225	1.225	0.000
10	212	1.225	1.225	0.000
15	214	1.226	1.226	0.000
15	217	1.225	1.225	0.000
15	218	1.226	1.225	-0.001
15	219	1.226	1.225	-0.001
15	222	1.225	1.225	0.000
30	224	1.225	1.224	-0.001
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.226	1.225	-0.001
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.225	1.224	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.225	1.224	-0.001
50	272	1.225	1.224	-0.001
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.224	-0.002
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.223	-0.003
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.225	1.222	-0.003
100	318	1.225	1.221	-0.004
100	319	1.226	1.222	-0.004
100	320	1.225	1.222	-0.003
100	321	1.225	1.220	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.225	1.223	-0.002
100	329	1.226	1.221	-0.005
100	332	1.225	1.223	-0.002
100	333	1.225	1.222	-0.003
100	334	1.225	1.223	-0.002
100	335	1.225	1.223	-0.002
100	336	1.226	1.223	-0.003
100	337	1.226	1.223	-0.003
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.225	1.222	-0.003
100	342	1.225	1.222	-0.003
100	343	1.225	1.222	-0.003
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.222	-0.004
105	350	1.226	1.222	-0.004
105	351	1.225	1.222	-0.003
105	352	1.226	1.223	-0.003
Max		1.226	1.226	0.000
Average		1.225	1.224	-0.002
Min		1.225	1.220	-0.005
Std Dev		0.001	0.002	0.001



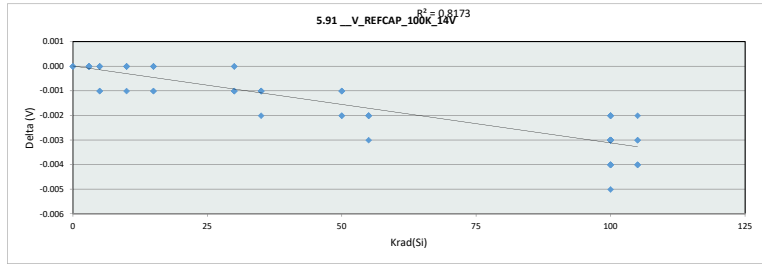
5.90_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.220	1.221
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



HDR TID Report  
TPS7H5004-SP

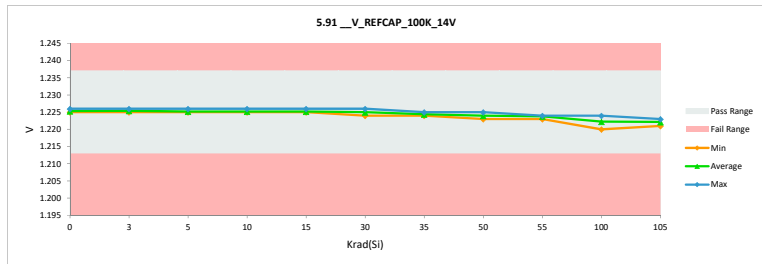
5.91_V_REFCAP_100K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(S)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.225	1.225	0.000
5	200	1.225	1.225	0.000
5	202	1.226	1.225	-0.001
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.225	-0.001
10	207	1.225	1.225	0.000
10	209	1.225	1.225	0.000
10	210	1.226	1.226	0.000
10	211	1.225	1.225	0.000
10	212	1.226	1.225	-0.001
15	214	1.226	1.226	0.000
15	217	1.225	1.225	0.000
15	218	1.226	1.225	-0.001
15	219	1.226	1.225	-0.001
15	222	1.225	1.225	0.000
30	224	1.225	1.224	-0.001
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.226	1.225	-0.001
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.225	1.224	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.225	1.224	-0.001
50	272	1.225	1.224	-0.001
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.224	-0.002
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.225	1.222	-0.003
100	318	1.225	1.221	-0.004
100	319	1.226	1.222	-0.004
100	320	1.225	1.222	-0.003
100	321	1.225	1.220	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.225	1.223	-0.002
100	329	1.225	1.221	-0.004
100	332	1.225	1.223	-0.002
100	333	1.225	1.222	-0.003
100	334	1.225	1.223	-0.002
100	335	1.225	1.223	-0.002
100	336	1.226	1.223	-0.003
100	337	1.226	1.223	-0.003
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.225	1.222	-0.003
100	342	1.225	1.222	-0.003
100	343	1.225	1.222	-0.003
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.222	-0.004
105	350	1.226	1.222	-0.004
105	351	1.225	1.222	-0.003
105	352	1.226	1.223	-0.003
Max		1.226	1.226	0.000
Average		1.225	1.224	-0.002
Min		1.225	1.220	-0.005
Std Dev		0.001	0.001	0.001



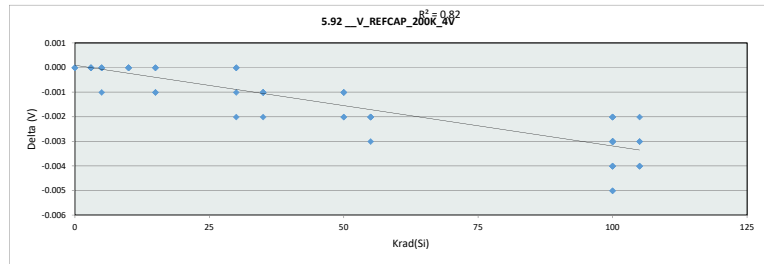
5.91_V_REFCAP	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.220	1.221
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

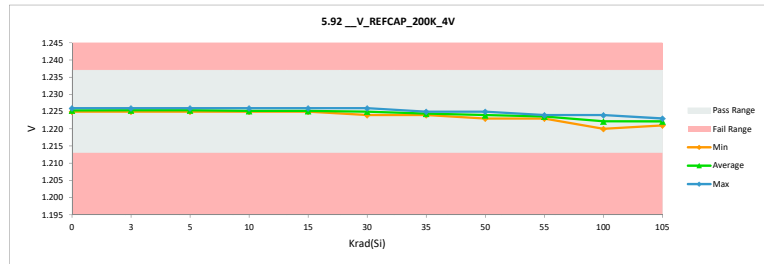


HDR TID Report  
TPS7H5004-SP

5.92 __V_REFCAP_200K_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.225	1.225	0.000
5	200	1.225	1.225	0.000
5	202	1.226	1.226	0.000
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.225	-0.001
10	207	1.225	1.225	0.000
10	209	1.225	1.225	0.000
10	210	1.226	1.226	0.000
10	211	1.225	1.225	0.000
10	212	1.225	1.225	0.000
15	214	1.226	1.226	0.000
15	217	1.225	1.225	0.000
15	218	1.226	1.225	-0.001
15	219	1.226	1.225	-0.001
15	222	1.225	1.225	0.000
30	224	1.226	1.224	-0.002
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.225	1.225	0.000
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.225	1.224	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.225	1.224	-0.001
50	272	1.225	1.224	-0.001
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.224	-0.002
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.225	1.223	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.225	1.222	-0.003
100	318	1.225	1.221	-0.004
100	319	1.226	1.222	-0.004
100	320	1.225	1.222	-0.003
100	321	1.225	1.220	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.225	1.223	-0.002
100	329	1.226	1.221	-0.005
100	332	1.225	1.223	-0.002
100	333	1.225	1.222	-0.003
100	334	1.225	1.222	-0.003
100	335	1.225	1.223	-0.002
100	336	1.226	1.223	-0.003
100	337	1.226	1.223	-0.003
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.225	1.222	-0.003
100	342	1.225	1.222	-0.003
100	343	1.225	1.222	-0.003
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.222	-0.004
105	350	1.226	1.222	-0.004
105	351	1.225	1.222	-0.003
105	352	1.226	1.223	-0.003
Max		1.226	1.226	0.000
Average		1.225	1.224	-0.002
Min		1.225	1.220	-0.005
Std Dev		0.001	0.002	0.001

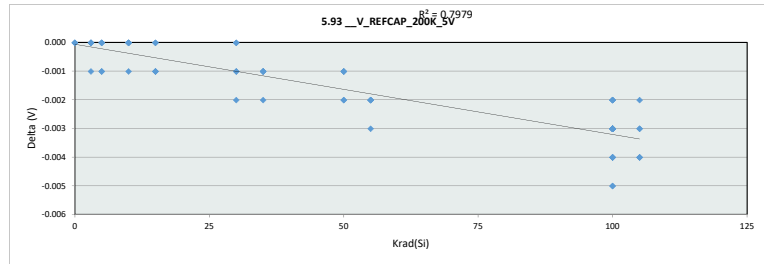


5.92 __V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.220	1.221
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

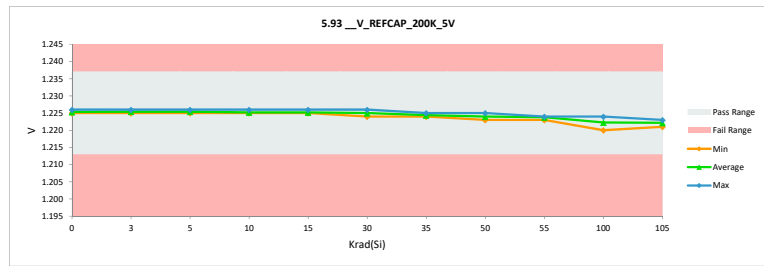


HDR TID Report  
TPS7H5004-SP

5.93 __V_REFCAP_200K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.225	-0.001
5	200	1.226	1.225	-0.001
5	202	1.226	1.226	0.000
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.225	-0.001
10	207	1.225	1.225	0.000
10	209	1.225	1.225	0.000
10	210	1.226	1.226	0.000
10	211	1.225	1.225	0.000
10	212	1.226	1.225	-0.001
15	214	1.226	1.226	0.000
15	217	1.225	1.225	0.000
15	218	1.226	1.225	-0.001
15	219	1.226	1.225	-0.001
15	222	1.226	1.225	-0.001
30	224	1.226	1.224	-0.002
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.226	1.225	-0.001
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.225	1.224	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.225	1.224	-0.001
50	272	1.225	1.224	-0.001
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.224	-0.002
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.225	1.222	-0.003
100	318	1.225	1.221	-0.004
100	319	1.226	1.222	-0.004
100	320	1.225	1.222	-0.003
100	321	1.225	1.220	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.225	1.223	-0.002
100	329	1.226	1.221	-0.005
100	332	1.225	1.223	-0.002
100	333	1.225	1.222	-0.003
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.223	-0.003
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.225	1.222	-0.003
100	342	1.225	1.222	-0.003
100	343	1.226	1.222	-0.004
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.222	-0.004
105	350	1.226	1.222	-0.004
105	351	1.225	1.222	-0.003
105	352	1.226	1.223	-0.003
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.002
Min		1.225	1.220	-0.005
Std Dev		0.000	0.002	0.001

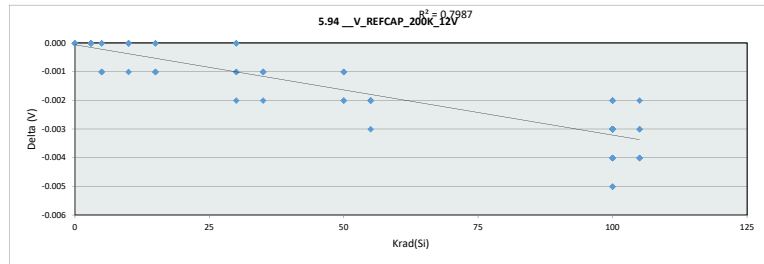


5.93 __V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.220	1.221
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

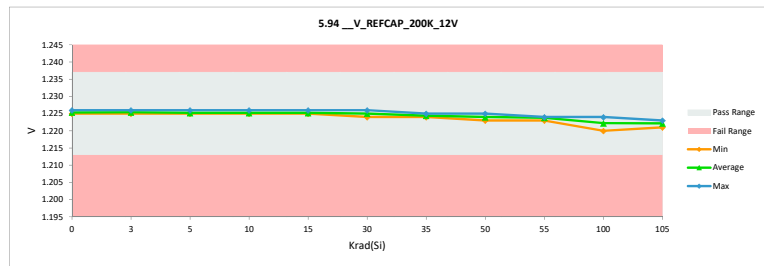


HDR TID Report  
TPS7H5004-SP

5.94 __V_REFCAP_200K_12V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.225	1.225	0.000
5	200	1.226	1.225	-0.001
5	202	1.226	1.225	-0.001
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.225	-0.001
10	207	1.225	1.225	0.000
10	209	1.225	1.225	0.000
10	210	1.226	1.226	0.000
10	211	1.225	1.225	0.000
10	212	1.226	1.225	-0.001
15	214	1.226	1.226	0.000
15	217	1.225	1.225	0.000
15	218	1.226	1.225	-0.001
15	219	1.226	1.225	-0.001
15	222	1.226	1.225	-0.001
30	224	1.226	1.224	-0.002
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.226	1.225	-0.001
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.225	1.224	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.225	1.224	-0.001
50	272	1.225	1.224	-0.001
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.224	-0.002
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.225	1.222	-0.003
100	318	1.225	1.221	-0.004
100	319	1.226	1.222	-0.004
100	320	1.225	1.222	-0.003
100	321	1.225	1.220	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.225	1.223	-0.002
100	329	1.226	1.221	-0.005
100	332	1.225	1.223	-0.002
100	333	1.225	1.222	-0.003
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.223	-0.003
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.225	1.222	-0.003
100	342	1.225	1.222	-0.003
100	343	1.226	1.222	-0.004
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.222	-0.004
105	350	1.226	1.222	-0.004
105	351	1.225	1.222	-0.003
105	352	1.226	1.223	-0.003
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.002
Min		1.225	1.220	-0.005
Std Dev		0.000	0.001	0.001

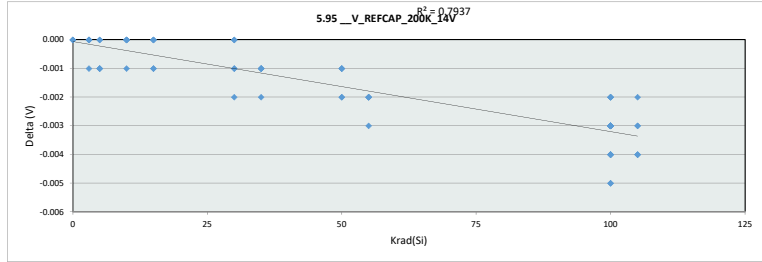


5.94 __V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.220	1.221
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

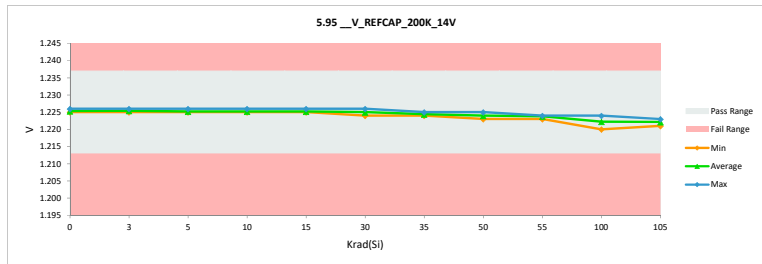


HDR TID Report  
TPS7H5004-SP

5.95_V_REFCAP_200K_14V				
Test Site	Tester	Test Number	Unit	
			V	V
			1.237	1.237
			1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.225	-0.001
5	200	1.226	1.225	-0.001
5	202	1.226	1.225	-0.001
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.225	-0.001
10	207	1.225	1.225	0.000
10	209	1.225	1.225	0.000
10	210	1.226	1.226	0.000
10	211	1.225	1.225	0.000
10	212	1.226	1.225	-0.001
15	214	1.226	1.226	0.000
15	217	1.225	1.225	0.000
15	218	1.226	1.225	-0.001
15	219	1.226	1.225	-0.001
15	222	1.225	1.225	0.000
30	224	1.226	1.224	-0.002
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.226	1.225	-0.001
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.225	1.224	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.225	1.224	-0.001
50	272	1.225	1.224	-0.001
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.224	-0.002
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.225	1.222	-0.003
100	318	1.225	1.221	-0.004
100	319	1.226	1.222	-0.004
100	320	1.225	1.222	-0.003
100	321	1.225	1.220	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.225	1.223	-0.002
100	329	1.226	1.221	-0.005
100	332	1.225	1.223	-0.002
100	333	1.225	1.222	-0.003
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.223	-0.003
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.225	1.222	-0.003
100	342	1.225	1.222	-0.003
100	343	1.226	1.222	-0.004
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.222	-0.004
105	350	1.226	1.222	-0.004
105	351	1.225	1.222	-0.003
105	352	1.226	1.223	-0.003
	Max	1.226	1.226	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.220	-0.005
	Std Dev	0.000	0.001	0.001

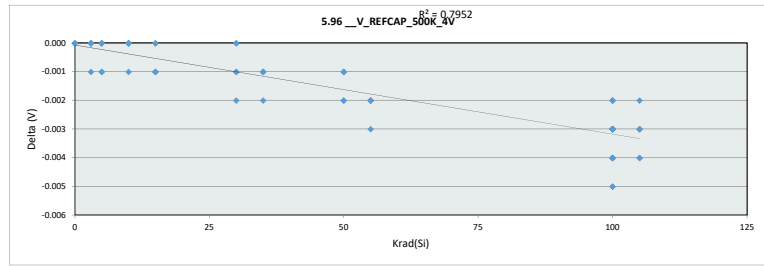


5.95_V_REFCAP											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			1.237	V							
			1.213	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.220	1.221
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

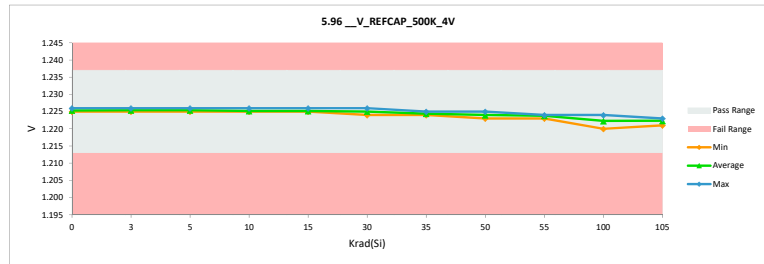


HDR TID Report  
TPS7H5004-SP

5.96 __V_REFCAP_500K_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.225	-0.001
5	200	1.226	1.225	-0.001
5	202	1.226	1.226	0.000
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.225	-0.001
10	207	1.225	1.225	0.000
10	209	1.225	1.225	0.000
10	210	1.226	1.226	0.000
10	211	1.225	1.225	0.000
10	212	1.226	1.225	-0.001
15	214	1.226	1.226	0.000
15	217	1.225	1.225	0.000
15	218	1.226	1.225	-0.001
15	219	1.226	1.225	-0.001
15	222	1.226	1.225	-0.001
30	224	1.226	1.224	-0.002
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.226	1.225	-0.001
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.225	1.224	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.225	1.224	-0.001
50	272	1.225	1.224	-0.001
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.224	-0.002
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.226	1.222	-0.004
100	318	1.225	1.221	-0.004
100	319	1.226	1.223	-0.003
100	320	1.225	1.222	-0.003
100	321	1.225	1.220	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.225	1.223	-0.002
100	329	1.226	1.221	-0.005
100	332	1.225	1.223	-0.002
100	333	1.225	1.222	-0.003
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.223	-0.003
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.225	1.222	-0.003
100	342	1.225	1.222	-0.003
100	343	1.226	1.222	-0.004
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.223	-0.003
105	350	1.226	1.222	-0.004
105	351	1.225	1.222	-0.003
105	352	1.226	1.223	-0.003
Max		1.226	1.226	0.000
Average		1.226	1.224	-0.002
Min		1.225	1.220	-0.005
Std Dev		0.000	0.001	0.001

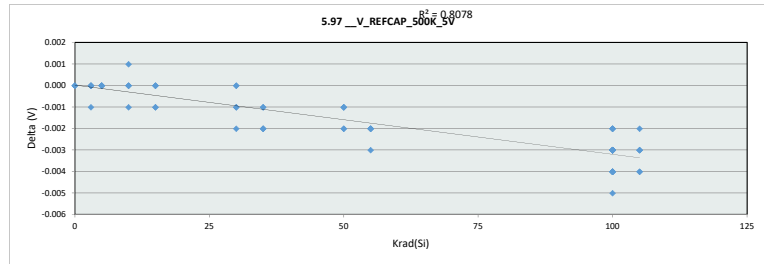


5.96 __V_REFCAP_500K_4V											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.220	1.221
Average	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

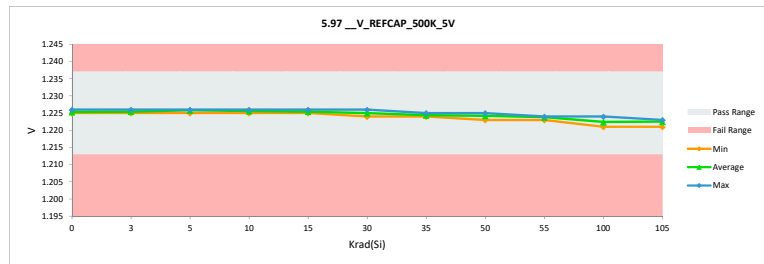


HDR TID Report  
TPS7H5004-SP

5.97 __V_REFCAP_500K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.225	-0.001
5	200	1.226	1.226	0.000
5	202	1.226	1.226	0.000
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.225	1.225	0.000
10	209	1.225	1.226	0.001
10	210	1.226	1.226	0.000
10	211	1.226	1.225	-0.001
10	212	1.226	1.226	0.000
15	214	1.226	1.226	0.000
15	217	1.225	1.225	0.000
15	218	1.226	1.226	0.000
15	219	1.226	1.225	-0.001
15	222	1.226	1.225	-0.001
30	224	1.226	1.224	-0.002
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.226	1.225	-0.001
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.226	1.224	-0.002
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.226	1.224	-0.002
50	272	1.226	1.224	-0.002
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.225	-0.001
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.226	1.222	-0.004
100	318	1.225	1.221	-0.004
100	319	1.226	1.223	-0.003
100	320	1.225	1.222	-0.003
100	321	1.225	1.221	-0.004
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.226	1.223	-0.003
100	329	1.226	1.221	-0.005
100	332	1.225	1.223	-0.002
100	333	1.225	1.222	-0.003
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.224	-0.002
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.226	1.223	-0.003
100	343	1.226	1.222	-0.004
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.223	-0.003
105	350	1.226	1.223	-0.003
105	351	1.226	1.222	-0.004
105	352	1.226	1.223	-0.003
Max		1.226	1.226	0.001
Average		1.226	1.224	-0.002
Min		1.225	1.221	-0.005
Std Dev		0.000	0.002	0.001



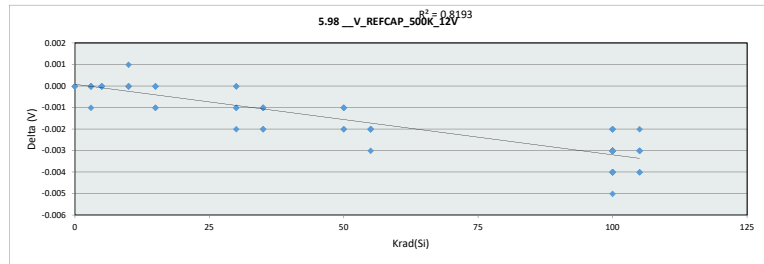
5.97 __V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.221	1.221
Average	1.225	1.225	1.226	1.226	1.225	1.224	1.224	1.224	1.224	1.222	1.223
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



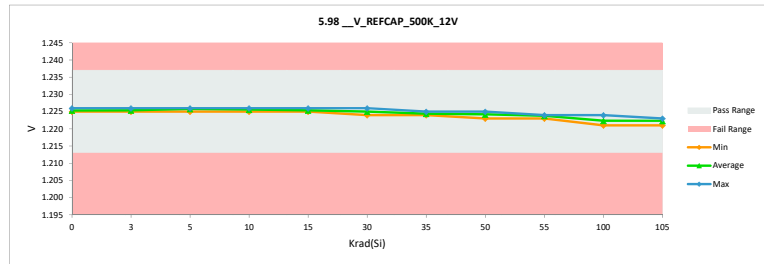


HDR TID Report  
TPS7H5004-SP

5.98 __V_REFCAP_500K_12V				
Test Site	Tester	Test Number	Unit	
			V	V
			Max Limit	1.237
			Min Limit	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.225	-0.001
5	200	1.226	1.226	0.000
5	202	1.226	1.226	0.000
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.225	1.225	0.000
10	209	1.225	1.226	0.001
10	210	1.226	1.226	0.000
10	211	1.225	1.225	0.000
10	212	1.226	1.226	0.000
15	214	1.226	1.226	0.000
15	217	1.225	1.225	0.000
15	218	1.226	1.226	0.000
15	219	1.226	1.225	-0.001
15	222	1.226	1.225	-0.001
30	224	1.226	1.224	-0.002
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.226	1.225	-0.001
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.225	1.224	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.226	1.224	-0.002
50	272	1.226	1.224	-0.002
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.225	-0.001
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.226	1.222	-0.004
100	318	1.225	1.221	-0.004
100	319	1.226	1.223	-0.003
100	320	1.225	1.222	-0.003
100	321	1.225	1.221	-0.004
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.226	1.223	-0.003
100	329	1.226	1.221	-0.005
100	332	1.225	1.223	-0.002
100	333	1.225	1.222	-0.003
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.224	-0.002
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.225	1.222	-0.003
100	343	1.226	1.222	-0.004
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.223	-0.003
105	350	1.226	1.222	-0.004
105	351	1.225	1.222	-0.003
105	352	1.226	1.223	-0.003
	Max	1.226	1.226	0.001
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.002	0.001

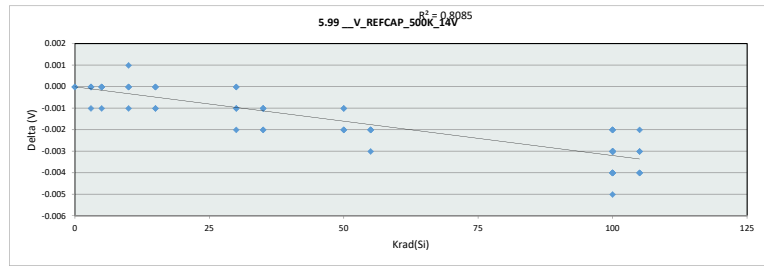


5.98 __V_REFCAP											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			1.237	1.213	V	V					
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.221	1.221
Average	1.225	1.225	1.226	1.226	1.225	1.224	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

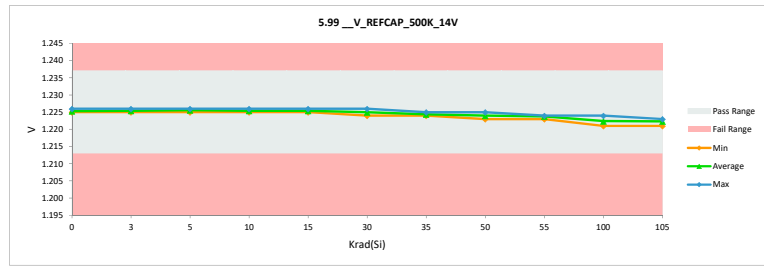


HDR TID Report  
TPS7H5004-SP

5.99 _V_REFCAP_500K_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.225	-0.001
5	200	1.226	1.225	-0.001
5	202	1.226	1.226	0.000
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.225	1.225	0.000
10	209	1.225	1.226	0.001
10	210	1.226	1.226	0.000
10	211	1.225	1.225	0.000
10	212	1.226	1.225	-0.001
15	214	1.226	1.226	0.000
15	217	1.225	1.225	0.000
15	218	1.226	1.226	0.000
15	219	1.226	1.225	-0.001
15	222	1.226	1.225	-0.001
30	224	1.226	1.224	-0.002
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.226	1.225	-0.001
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.225	1.224	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.226	1.224	-0.002
50	272	1.226	1.224	-0.002
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.224	-0.002
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.226	1.223	-0.003
100	318	1.225	1.221	-0.004
100	319	1.226	1.223	-0.003
100	320	1.225	1.222	-0.003
100	321	1.225	1.221	-0.004
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.226	1.223	-0.003
100	329	1.226	1.221	-0.005
100	332	1.225	1.223	-0.002
100	333	1.225	1.222	-0.003
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.224	-0.002
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.225	1.222	-0.003
100	343	1.226	1.222	-0.004
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.223	-0.003
105	350	1.226	1.222	-0.004
105	351	1.226	1.222	-0.004
105	352	1.226	1.223	-0.003
Max		1.226	1.226	0.001
Average		1.226	1.224	-0.002
Min		1.225	1.221	-0.005
Std Dev		0.000	0.001	0.001

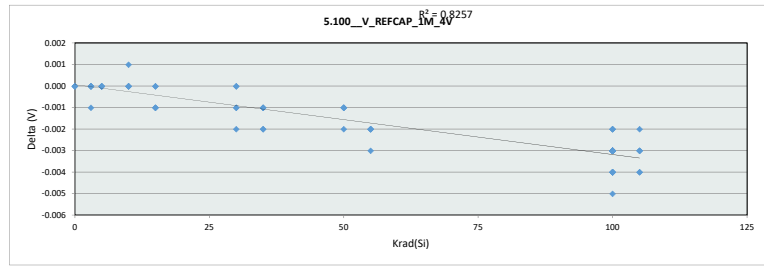


5.99 _V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.221	1.221
Average	1.225	1.225	1.226	1.225	1.225	1.225	1.224	1.224	1.224	1.222	1.222
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

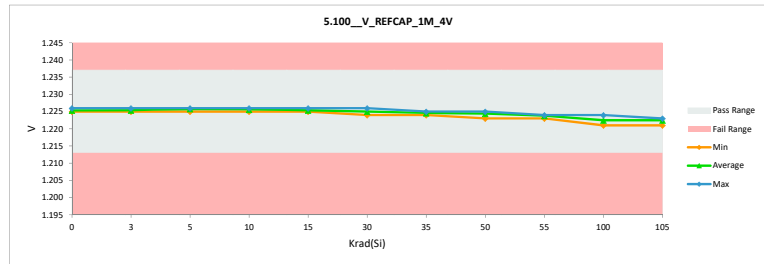


HDR TID Report  
TPS7H5004-SP

		5.100_V_REFCAP_1M_4V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		1.237	1.237	
Min Limit		1.213	1.213	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.225	1.225	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.225	1.225	0.000
3	194	1.225	1.225	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.225	-0.001
5	200	1.226	1.226	0.000
5	202	1.226	1.226	0.000
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.225	1.225	0.000
10	209	1.225	1.226	0.001
10	210	1.226	1.226	0.000
10	211	1.225	1.225	0.000
10	212	1.226	1.226	0.000
15	214	1.226	1.226	0.000
15	217	1.226	1.225	-0.001
15	218	1.226	1.226	0.000
15	219	1.226	1.225	-0.001
15	222	1.226	1.225	-0.001
30	224	1.226	1.224	-0.002
30	225	1.226	1.225	-0.001
30	226	1.225	1.225	0.000
30	227	1.226	1.225	-0.001
30	229	1.226	1.226	0.000
35	230	1.226	1.224	-0.002
35	231	1.226	1.225	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.226	1.224	-0.002
50	272	1.226	1.225	-0.001
50	275	1.225	1.223	-0.002
50	276	1.225	1.224	-0.001
50	277	1.226	1.225	-0.001
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.226	1.223	-0.003
100	318	1.225	1.221	-0.004
100	319	1.226	1.223	-0.003
100	320	1.225	1.222	-0.003
100	321	1.225	1.221	-0.004
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.225	1.222	-0.003
100	328	1.226	1.223	-0.003
100	329	1.226	1.221	-0.005
100	332	1.226	1.223	-0.003
100	333	1.226	1.222	-0.004
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.224	-0.002
100	338	1.225	1.222	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.226	1.223	-0.003
100	343	1.226	1.223	-0.003
105	344	1.225	1.221	-0.004
105	346	1.225	1.223	-0.002
105	349	1.226	1.223	-0.003
105	350	1.226	1.223	-0.003
105	351	1.226	1.222	-0.004
105	352	1.226	1.223	-0.003
	Max	1.226	1.226	0.001
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.001	0.001

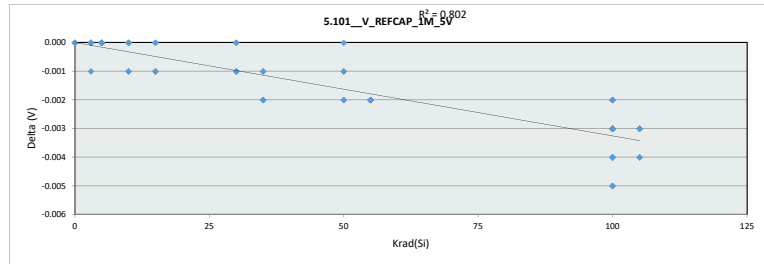


		5.100_V_REFCAP											
Test Site													
Tester													
Test Number													
Max Limit		1.237	V										
Min Limit		1.213	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213		
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.223	1.221	1.221		
Average	1.225	1.225	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.223		
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223		
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237		

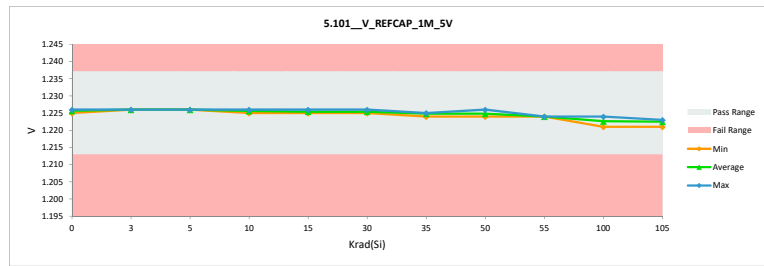


HDR TID Report  
TPS7H5004-SP

5.101_V_REFCAP_1M_5V				
Test Site	Tester	Test Number	Unit	
			V	V
			1.237	1.237
			1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.226	1.226	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.227	1.226	-0.001
3	193	1.226	1.226	0.000
3	194	1.226	1.226	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.226	0.000
5	200	1.226	1.226	0.000
5	202	1.226	1.226	0.000
5	203	1.226	1.226	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.226	1.225	-0.001
10	209	1.226	1.226	0.000
10	210	1.226	1.226	0.000
10	211	1.226	1.225	-0.001
10	212	1.226	1.226	0.000
15	214	1.226	1.226	0.000
15	217	1.226	1.225	-0.001
15	218	1.226	1.226	0.000
15	219	1.226	1.225	-0.001
15	222	1.226	1.225	-0.001
30	224	1.226	1.225	-0.001
30	225	1.226	1.225	-0.001
30	226	1.226	1.225	-0.001
30	227	1.226	1.226	0.000
30	229	1.226	1.226	0.000
35	230	1.226	1.225	-0.001
35	231	1.226	1.225	-0.001
35	232	1.227	1.225	-0.002
35	234	1.227	1.225	-0.002
35	237	1.226	1.224	-0.002
50	272	1.226	1.225	-0.001
50	275	1.226	1.224	-0.002
50	276	1.226	1.224	-0.002
50	277	1.226	1.225	-0.001
50	278	1.226	1.226	0.000
55	279	1.226	1.224	-0.002
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.226	1.223	-0.003
100	318	1.226	1.221	-0.005
100	319	1.226	1.223	-0.003
100	320	1.226	1.223	-0.003
100	321	1.226	1.221	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.226	1.222	-0.004
100	328	1.226	1.224	-0.002
100	329	1.226	1.221	-0.005
100	332	1.226	1.223	-0.003
100	333	1.226	1.222	-0.004
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.224	-0.002
100	338	1.226	1.223	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.226	1.223	-0.003
100	343	1.226	1.223	-0.003
105	344	1.225	1.221	-0.004
105	346	1.226	1.223	-0.003
105	349	1.226	1.223	-0.003
105	350	1.226	1.223	-0.003
105	351	1.226	1.222	-0.004
105	352	1.226	1.223	-0.003
	Max	1.227	1.226	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.002	0.001

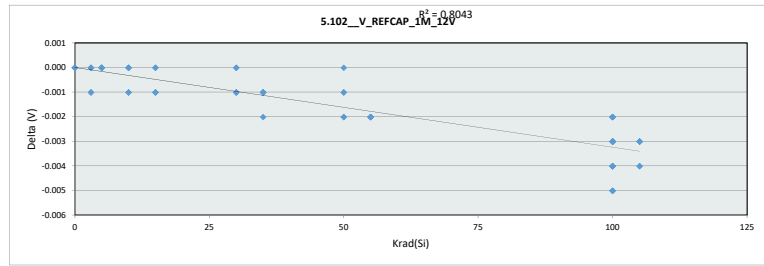


5.101_V_REFCAP															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL			1.237	V	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min			1.225		1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.224	1.221	1.221
Average			1.226		1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.225	1.224	1.223	1.223
Max			1.226		1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.226	1.224	1.224	1.223
UL			1.237	V	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

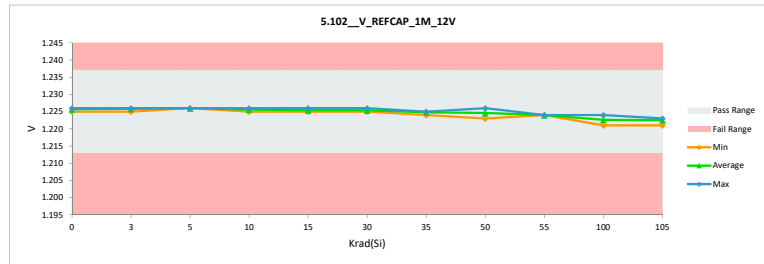


HDR TID Report  
TPS7H5004-SP

5.102_V_REFCAP_1M_12V				
Test Site	Tester	Test Number	Unit	
			V	V
			Max Limit	1.237
			Min Limit	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.226	1.226	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.227	1.226	-0.001
3	193	1.226	1.225	-0.001
3	194	1.226	1.226	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.226	0.000
5	200	1.226	1.226	0.000
5	202	1.226	1.226	0.000
5	203	1.226	1.226	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.226	1.225	-0.001
10	209	1.226	1.226	0.000
10	210	1.226	1.226	0.000
10	211	1.226	1.225	-0.001
10	212	1.226	1.226	0.000
15	214	1.226	1.226	0.000
15	217	1.226	1.225	-0.001
15	218	1.226	1.226	0.000
15	219	1.226	1.225	-0.001
15	222	1.226	1.225	-0.001
30	224	1.226	1.225	-0.001
30	225	1.226	1.225	-0.001
30	226	1.226	1.225	-0.001
30	227	1.226	1.226	0.000
30	229	1.226	1.226	0.000
35	230	1.226	1.225	-0.001
35	231	1.226	1.225	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.226	1.224	-0.002
50	272	1.226	1.225	-0.001
50	275	1.225	1.223	-0.002
50	276	1.226	1.224	-0.002
50	277	1.226	1.225	-0.001
50	278	1.226	1.226	0.000
55	279	1.226	1.224	-0.002
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.226	1.223	-0.003
100	318	1.226	1.221	-0.005
100	319	1.226	1.223	-0.003
100	320	1.226	1.223	-0.003
100	321	1.226	1.221	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.226	1.222	-0.004
100	328	1.226	1.224	-0.002
100	329	1.226	1.221	-0.005
100	332	1.226	1.223	-0.003
100	333	1.226	1.222	-0.004
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.224	-0.002
100	338	1.226	1.223	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.226	1.223	-0.003
100	343	1.226	1.223	-0.003
105	344	1.225	1.221	-0.004
105	346	1.226	1.223	-0.003
105	349	1.226	1.223	-0.003
105	350	1.226	1.223	-0.003
105	351	1.226	1.222	-0.004
105	352	1.226	1.223	-0.003
	Max	1.227	1.226	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.002	0.001

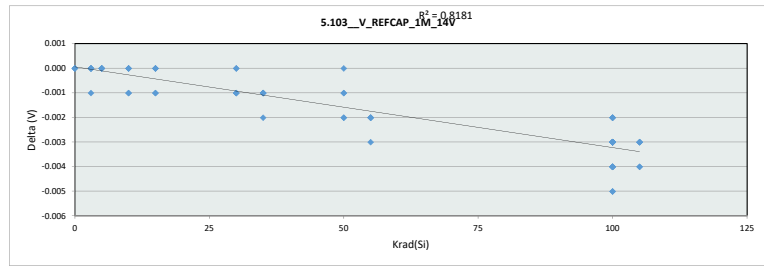


5.102_V_REFCAP															
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105
LL			1.237	V	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min			1.225	V	1.225	1.225	1.226	1.225	1.225	1.225	1.224	1.223	1.224	1.221	1.221
Average			1.226	V	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.223
Max			1.226	V	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.226	1.224	1.224	1.223
UL			1.237	V	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

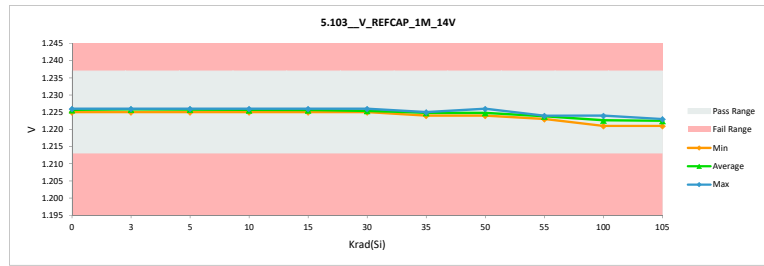


HDR TID Report  
TPS7H5004-SP

5.103_V_REFCAP_1M_14V				
Test Site	Tester	Test Number	Unit	
			V	V
			1.237	1.237
			1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.226	1.226	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.226	1.226	0.000
3	193	1.226	1.225	-0.001
3	194	1.226	1.226	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.226	0.000
5	200	1.226	1.226	0.000
5	202	1.226	1.226	0.000
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.226	1.225	-0.001
10	209	1.226	1.226	0.000
10	210	1.226	1.226	0.000
10	211	1.226	1.225	-0.001
10	212	1.226	1.226	0.000
15	214	1.226	1.226	0.000
15	217	1.226	1.225	-0.001
15	218	1.226	1.226	0.000
15	219	1.226	1.226	0.000
15	222	1.226	1.225	-0.001
30	224	1.226	1.225	-0.001
30	225	1.226	1.225	-0.001
30	226	1.226	1.225	-0.001
30	227	1.226	1.226	0.000
30	229	1.226	1.226	0.000
35	230	1.226	1.225	-0.001
35	231	1.226	1.225	-0.001
35	232	1.226	1.225	-0.001
35	234	1.226	1.225	-0.001
35	237	1.226	1.224	-0.002
50	272	1.226	1.225	-0.001
50	275	1.226	1.224	-0.002
50	276	1.226	1.224	-0.002
50	277	1.226	1.225	-0.001
50	278	1.226	1.226	0.000
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.226	1.223	-0.003
100	318	1.225	1.221	-0.004
100	319	1.226	1.223	-0.003
100	320	1.226	1.223	-0.003
100	321	1.226	1.221	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.226	1.222	-0.004
100	328	1.226	1.224	-0.002
100	329	1.226	1.221	-0.005
100	332	1.226	1.223	-0.003
100	333	1.226	1.222	-0.004
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.224	-0.002
100	338	1.226	1.223	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.226	1.223	-0.003
100	343	1.226	1.223	-0.003
105	344	1.225	1.221	-0.004
105	346	1.226	1.223	-0.003
105	349	1.226	1.223	-0.003
105	350	1.226	1.223	-0.003
105	351	1.226	1.222	-0.004
105	352	1.226	1.223	-0.003
	Max	1.226	1.226	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.002	0.001

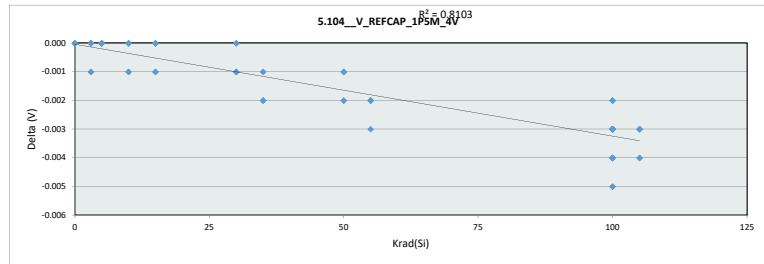


5.103_V_REFCAP																
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105	
LL			1.237	V	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min			1.225		1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.221	1.221	
Average			1.226		1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.223	1.223	1.223	
Max			1.226		1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.226	1.224	1.224	1.223	
UL			1.237	V	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	

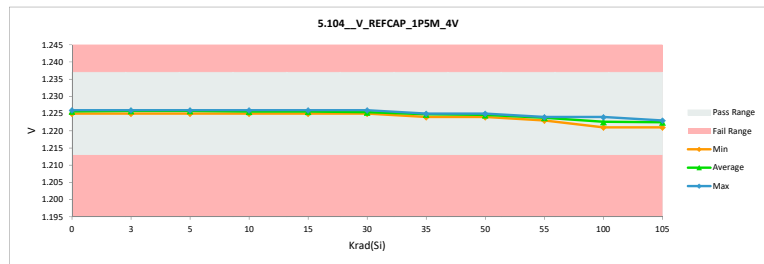


HDR TID Report  
TPS7H5004-SP

5.104_V_REFCAP_1P5M_4V				
Test Site	Tester	Test Number	Unit	
			V	V
			1.237	1.237
			1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.226	1.226	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.227	1.226	-0.001
3	193	1.226	1.225	-0.001
3	194	1.226	1.226	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.226	0.000
5	200	1.226	1.226	0.000
5	202	1.226	1.226	0.000
5	203	1.225	1.225	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.226	1.225	-0.001
10	209	1.226	1.226	0.000
10	210	1.226	1.226	0.000
10	211	1.226	1.225	-0.001
10	212	1.226	1.226	0.000
15	214	1.226	1.226	0.000
15	217	1.226	1.225	-0.001
15	218	1.226	1.226	0.000
15	219	1.226	1.226	0.000
15	222	1.226	1.225	-0.001
30	224	1.226	1.225	-0.001
30	225	1.226	1.225	-0.001
30	226	1.226	1.225	-0.001
30	227	1.226	1.226	0.000
30	229	1.226	1.226	0.000
35	230	1.226	1.225	-0.001
35	231	1.226	1.225	-0.001
35	232	1.227	1.225	-0.002
35	234	1.227	1.225	-0.002
35	237	1.226	1.224	-0.002
50	272	1.226	1.225	-0.001
50	275	1.226	1.224	-0.002
50	276	1.226	1.224	-0.002
50	277	1.226	1.225	-0.001
50	278	1.226	1.225	-0.001
55	279	1.226	1.223	-0.003
55	280	1.226	1.224	-0.002
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.226	1.223	-0.003
100	318	1.225	1.221	-0.004
100	319	1.226	1.223	-0.003
100	320	1.226	1.223	-0.003
100	321	1.226	1.221	-0.005
100	322	1.226	1.224	-0.002
100	323	1.225	1.222	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.226	1.222	-0.004
100	328	1.226	1.224	-0.002
100	329	1.226	1.221	-0.005
100	332	1.226	1.223	-0.003
100	333	1.226	1.222	-0.004
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.226	1.223	-0.003
100	337	1.226	1.224	-0.002
100	338	1.226	1.223	-0.003
100	339	1.226	1.222	-0.004
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.226	1.223	-0.003
100	343	1.226	1.223	-0.003
105	344	1.225	1.221	-0.004
105	346	1.226	1.223	-0.003
105	349	1.226	1.223	-0.003
105	350	1.226	1.223	-0.003
105	351	1.226	1.222	-0.004
105	352	1.226	1.223	-0.003
	Max	1.227	1.226	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.002	0.001

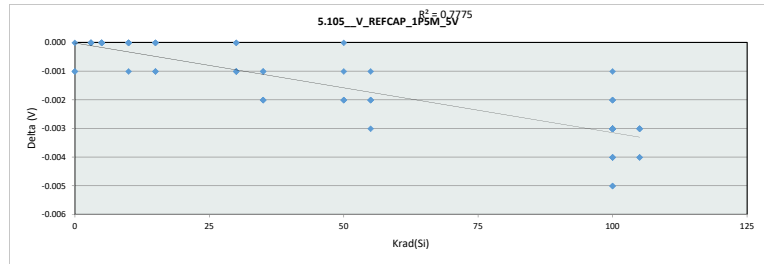


5.104_V_REFCAP											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			1.237	1.213	V						
			1.237	1.213	V						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.221	1.221
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.223	1.223
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

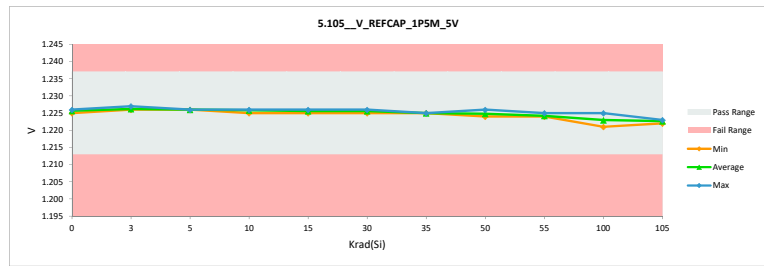


HDR TID Report  
TPS7H5004-SP

5.105_V_REFCAP_1P5M_5V				
Test Site	Tester	Test Number	Unit	
			V	V
			1.237	1.237
			1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.226	1.226	0.000
0	297	1.226	1.225	-0.001
0	298	1.227	1.226	-0.001
3	192	1.227	1.227	0.000
3	193	1.226	1.226	0.000
3	194	1.226	1.226	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.226	0.000
5	200	1.226	1.226	0.000
5	202	1.226	1.226	0.000
5	203	1.226	1.226	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.226	1.226	0.000
10	209	1.226	1.226	0.000
10	210	1.226	1.226	0.000
10	211	1.226	1.225	-0.001
10	212	1.226	1.226	0.000
15	214	1.226	1.226	0.000
15	217	1.226	1.225	-0.001
15	218	1.226	1.226	0.000
15	219	1.226	1.226	0.000
15	222	1.226	1.225	-0.001
30	224	1.226	1.225	-0.001
30	225	1.226	1.225	-0.001
30	226	1.226	1.226	0.000
30	227	1.226	1.226	0.000
30	229	1.227	1.226	-0.001
35	230	1.227	1.225	-0.002
35	231	1.226	1.225	-0.001
35	232	1.227	1.225	-0.002
35	234	1.227	1.225	-0.002
35	237	1.226	1.225	-0.001
50	272	1.226	1.225	-0.001
50	275	1.226	1.224	-0.002
50	276	1.226	1.224	-0.002
50	277	1.227	1.225	-0.002
50	278	1.226	1.226	0.000
55	279	1.226	1.224	-0.002
55	280	1.227	1.224	-0.003
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.225	-0.001
100	317	1.226	1.223	-0.003
100	318	1.226	1.222	-0.004
100	319	1.226	1.223	-0.003
100	320	1.226	1.223	-0.003
100	321	1.226	1.221	-0.005
100	322	1.226	1.225	-0.001
100	323	1.226	1.223	-0.003
100	324	1.226	1.223	-0.003
100	326	1.227	1.223	-0.004
100	327	1.226	1.223	-0.003
100	328	1.226	1.224	-0.002
100	329	1.226	1.221	-0.005
100	332	1.226	1.224	-0.002
100	333	1.226	1.223	-0.003
100	334	1.226	1.223	-0.003
100	335	1.226	1.223	-0.003
100	336	1.227	1.224	-0.003
100	337	1.226	1.224	-0.002
100	338	1.226	1.223	-0.003
100	339	1.226	1.223	-0.003
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.226	1.223	-0.003
100	343	1.226	1.223	-0.003
105	344	1.226	1.222	-0.004
105	346	1.226	1.223	-0.003
105	349	1.226	1.223	-0.003
105	350	1.226	1.223	-0.003
105	351	1.226	1.222	-0.004
105	352	1.226	1.223	-0.003
	Max	1.227	1.227	0.000
	Average	1.226	1.224	-0.002
	Min	1.226	1.221	-0.005
	Std Dev	0.000	0.001	0.001



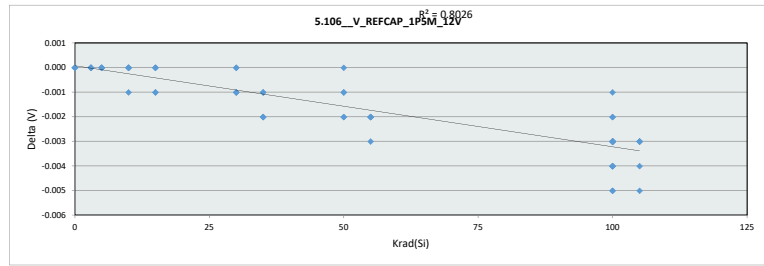
5.105_V_REFCAP											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			1.237	V							
			1.213	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.221	1.222
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.224	1.223	1.223
Max	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.226	1.225	1.225	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



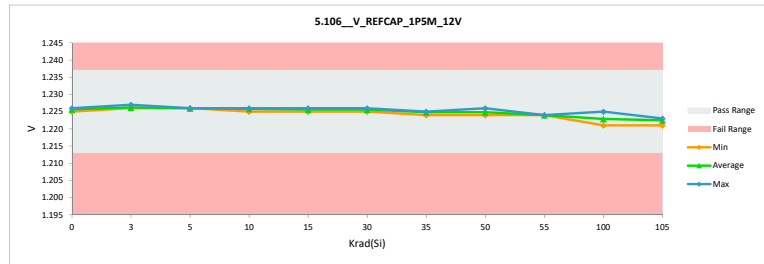


HDR TID Report  
TPS7H5004-SP

5.106_V_REFCAP_1P5M_12V				
Test Site	Tester	Test Number	Unit	
			V	V
			1.237	1.237
			1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.226	1.226	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.227	1.227	0.000
3	193	1.226	1.226	0.000
3	194	1.226	1.226	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.226	0.000
5	200	1.226	1.226	0.000
5	202	1.226	1.226	0.000
5	203	1.226	1.226	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.226	1.226	0.000
10	209	1.226	1.226	0.000
10	210	1.226	1.226	0.000
10	211	1.226	1.225	-0.001
10	212	1.226	1.226	0.000
15	214	1.226	1.226	0.000
15	217	1.226	1.225	-0.001
15	218	1.226	1.226	0.000
15	219	1.226	1.226	0.000
15	222	1.226	1.225	-0.001
30	224	1.226	1.225	-0.001
30	225	1.226	1.225	-0.001
30	226	1.226	1.226	0.000
30	227	1.226	1.226	0.000
30	229	1.227	1.226	-0.001
35	230	1.226	1.225	-0.001
35	231	1.226	1.225	-0.001
35	232	1.227	1.225	-0.002
35	234	1.227	1.225	-0.002
35	237	1.226	1.224	-0.002
50	272	1.226	1.225	-0.001
50	275	1.226	1.224	-0.002
50	276	1.226	1.224	-0.002
50	277	1.226	1.225	-0.001
50	278	1.226	1.226	0.000
55	279	1.226	1.224	-0.002
55	280	1.227	1.224	-0.003
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.226	1.223	-0.003
100	318	1.226	1.222	-0.004
100	319	1.226	1.223	-0.003
100	320	1.226	1.223	-0.003
100	321	1.226	1.221	-0.005
100	322	1.226	1.225	-0.001
100	323	1.226	1.223	-0.003
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.226	1.223	-0.003
100	328	1.226	1.224	-0.002
100	329	1.226	1.221	-0.005
100	332	1.226	1.223	-0.003
100	333	1.226	1.222	-0.004
100	334	1.226	1.223	-0.003
100	335	1.226	1.223	-0.003
100	336	1.227	1.224	-0.003
100	337	1.226	1.224	-0.002
100	338	1.226	1.223	-0.003
100	339	1.226	1.223	-0.003
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.226	1.223	-0.003
100	343	1.226	1.223	-0.003
105	344	1.226	1.221	-0.005
105	346	1.226	1.223	-0.003
105	349	1.226	1.223	-0.003
105	350	1.226	1.223	-0.003
105	351	1.226	1.222	-0.004
105	352	1.226	1.223	-0.003
	Max	1.227	1.227	0.000
	Average	1.226	1.224	-0.002
	Min	1.225	1.221	-0.005
	Std Dev	0.000	0.002	0.002



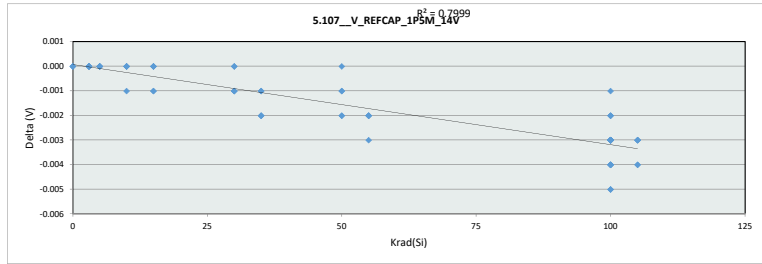
5.106_V_REFCAP																
Test Site	Tester	Test Number	Max Limit	Min Limit	0	3	5	10	15	30	35	50	55	100	105	
LL			1.237	V	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min			1.225		1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.224	1.221	1.221	
Average			1.226		1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.223	1.223	1.223	
Max			1.226		1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.226	1.224	1.225	1.223	
UL			1.237	V	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	



HDR TID Report  
TPS7H5004-SP

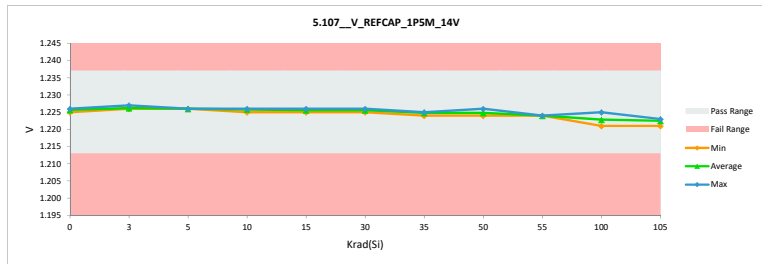
5.107_V_REFCAP_1P5M_14V				
Test Site				
Tester				
Test Number				
Unit	V		V	
Max Limit	1.237		1.237	
Min Limit	1.213		1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.226	1.226	0.000
0	297	1.225	1.225	0.000
0	298	1.226	1.226	0.000
3	192	1.227	1.227	0.000
3	193	1.226	1.226	0.000
3	194	1.226	1.226	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.226	0.000
5	200	1.226	1.226	0.000
5	202	1.226	1.226	0.000
5	203	1.226	1.226	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.226	1.226	0.000
10	209	1.226	1.226	0.000
10	210	1.226	1.226	0.000
10	211	1.226	1.225	-0.001
10	212	1.226	1.226	0.000
15	214	1.226	1.226	0.000
15	217	1.226	1.225	-0.001
15	218	1.226	1.226	0.000
15	219	1.226	1.226	0.000
15	222	1.226	1.225	-0.001
30	224	1.226	1.225	-0.001
30	225	1.226	1.225	-0.001
30	226	1.226	1.226	0.000
30	227	1.226	1.226	0.000
30	229	1.227	1.226	-0.001
35	230	1.226	1.225	-0.001
35	231	1.226	1.225	-0.001
35	232	1.227	1.225	-0.002
35	234	1.227	1.225	-0.002
35	237	1.226	1.224	-0.002
50	272	1.226	1.225	-0.001
50	275	1.226	1.224	-0.002
50	276	1.226	1.224	-0.002
50	277	1.226	1.225	-0.001
50	278	1.226	1.226	0.000
55	279	1.226	1.224	-0.002
55	280	1.227	1.224	-0.003
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.224	-0.002
100	317	1.226	1.223	-0.003
100	318	1.226	1.222	-0.004
100	319	1.226	1.223	-0.003
100	320	1.226	1.223	-0.003
100	321	1.226	1.221	-0.005
100	322	1.226	1.225	-0.001
100	323	1.226	1.222	-0.004
100	324	1.226	1.223	-0.003
100	326	1.226	1.223	-0.003
100	327	1.226	1.223	-0.003
100	328	1.226	1.224	-0.002
100	329	1.226	1.221	-0.005
100	332	1.226	1.223	-0.003
100	333	1.226	1.222	-0.004
100	334	1.225	1.223	-0.002
100	335	1.226	1.223	-0.003
100	336	1.227	1.224	-0.003
100	337	1.226	1.224	-0.002
100	338	1.226	1.223	-0.003
100	339	1.226	1.223	-0.003
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.226	1.223	-0.003
100	343	1.226	1.223	-0.003
105	344	1.225	1.221	-0.004
105	346	1.226	1.223	-0.003
105	349	1.226	1.223	-0.003
105	350	1.226	1.223	-0.003
105	351	1.226	1.222	-0.004
105	352	1.226	1.223	-0.003
Max		1.227	1.227	0.000
Average		1.226	1.224	-0.002
Min		1.225	1.221	-0.005
Std Dev		0.000	0.002	0.002



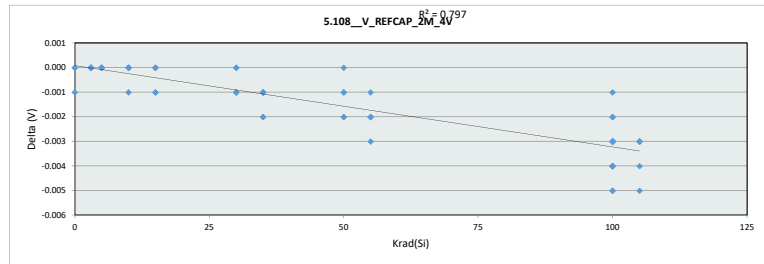
5.107_V_REFCAP				
Test Site				
Tester				
Test Number				
Max Limit	1.237	V		
Min Limit	1.213	V		

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.224	1.221	1.221
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.223	1.223
Max	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.226	1.224	1.225	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

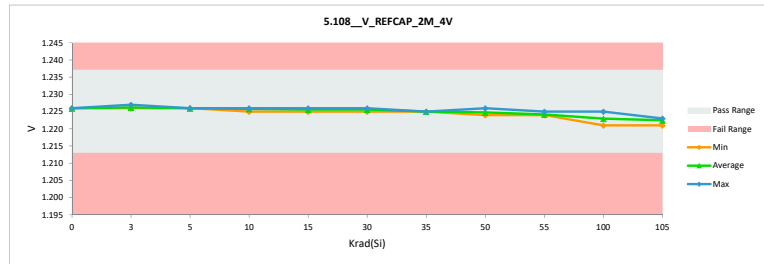


HDR TID Report  
TPS7H5004-SP

5.108_V_REFCAP_2M_4V				
Test Site	Tester	Test Number	Unit	
			V	V
Max Limit			1.237	1.237
Min Limit			1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.226	1.226	0.000
0	297	1.226	1.226	0.000
0	298	1.227	1.226	-0.001
3	192	1.227	1.227	0.000
3	193	1.226	1.226	0.000
3	194	1.226	1.226	0.000
3	196	1.226	1.226	0.000
3	197	1.226	1.226	0.000
5	200	1.226	1.226	0.000
5	202	1.226	1.226	0.000
5	203	1.226	1.226	0.000
5	204	1.226	1.226	0.000
5	206	1.226	1.226	0.000
10	207	1.226	1.226	0.000
10	209	1.226	1.226	0.000
10	210	1.226	1.226	0.000
10	211	1.226	1.225	-0.001
10	212	1.226	1.226	0.000
15	214	1.226	1.226	0.000
15	217	1.226	1.225	-0.001
15	218	1.226	1.226	0.000
15	219	1.226	1.226	0.000
15	222	1.226	1.225	-0.001
30	224	1.226	1.225	-0.001
30	225	1.226	1.225	-0.001
30	226	1.226	1.226	0.000
30	227	1.226	1.226	0.000
30	229	1.227	1.226	-0.001
35	230	1.226	1.225	-0.001
35	231	1.226	1.225	-0.001
35	232	1.227	1.225	-0.002
35	234	1.227	1.225	-0.002
35	237	1.226	1.225	-0.001
50	272	1.226	1.225	-0.001
50	275	1.226	1.224	-0.002
50	276	1.226	1.224	-0.002
50	277	1.226	1.225	-0.001
50	278	1.226	1.226	0.000
55	279	1.226	1.224	-0.002
55	280	1.227	1.224	-0.003
55	281	1.226	1.224	-0.002
55	282	1.226	1.224	-0.002
55	283	1.226	1.225	-0.001
100	317	1.226	1.223	-0.003
100	318	1.226	1.222	-0.004
100	319	1.227	1.223	-0.004
100	320	1.226	1.223	-0.003
100	321	1.226	1.221	-0.005
100	322	1.226	1.225	-0.001
100	323	1.226	1.223	-0.003
100	324	1.226	1.223	-0.003
100	326	1.227	1.223	-0.004
100	327	1.226	1.223	-0.003
100	328	1.226	1.224	-0.002
100	329	1.226	1.221	-0.005
100	332	1.226	1.224	-0.002
100	333	1.226	1.223	-0.003
100	334	1.226	1.223	-0.003
100	335	1.226	1.223	-0.003
100	336	1.227	1.224	-0.003
100	337	1.227	1.224	-0.003
100	338	1.226	1.223	-0.003
100	339	1.226	1.223	-0.003
100	340	1.226	1.222	-0.004
100	341	1.226	1.222	-0.004
100	342	1.226	1.223	-0.003
100	343	1.226	1.223	-0.003
105	344	1.226	1.221	-0.005
105	346	1.226	1.223	-0.003
105	349	1.226	1.223	-0.003
105	350	1.226	1.223	-0.003
105	351	1.226	1.222	-0.004
105	352	1.226	1.223	-0.003
Max		1.227	1.227	0.000
Average		1.226	1.224	-0.002
Min		1.226	1.221	-0.005
Std Dev		0.000	0.002	0.002

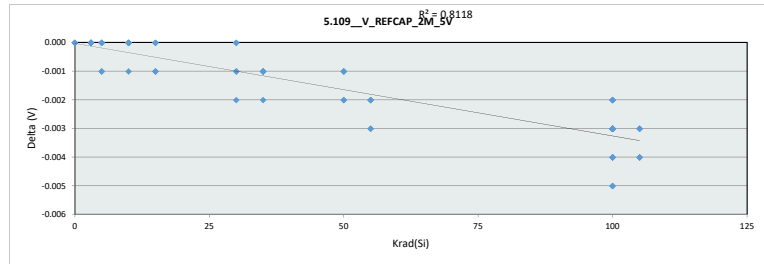


5.108_V_REFCAP											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			1.237	V							
			1.213	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.221	1.221
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.224	1.223	1.223
Max	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.226	1.225	1.225	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

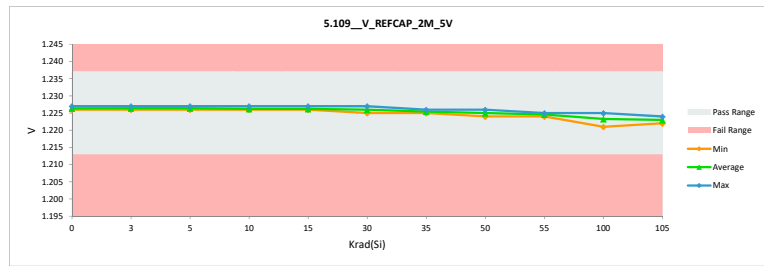


HDR TID Report  
TPS7H5004-SP

5.109_V_REFCAP_2M_5V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	295	1.226	1.226	0.000	
0	297	1.226	1.226	0.000	
0	298	1.227	1.227	0.000	
3	192	1.227	1.227	0.000	
3	193	1.226	1.226	0.000	
3	194	1.226	1.226	0.000	
3	196	1.227	1.227	0.000	
3	197	1.226	1.226	0.000	
5	200	1.227	1.226	-0.001	
5	202	1.227	1.227	0.000	
5	203	1.226	1.226	0.000	
5	204	1.227	1.227	0.000	
5	206	1.227	1.226	-0.001	
10	207	1.226	1.226	0.000	
10	209	1.226	1.226	0.000	
10	210	1.227	1.227	0.000	
10	211	1.226	1.226	0.000	
10	212	1.227	1.226	-0.001	
15	214	1.227	1.227	0.000	
15	217	1.226	1.226	0.000	
15	218	1.227	1.226	-0.001	
15	219	1.227	1.226	-0.001	
15	222	1.227	1.226	-0.001	
30	224	1.227	1.225	-0.002	
30	225	1.227	1.226	-0.001	
30	226	1.226	1.226	0.000	
30	227	1.227	1.226	-0.001	
30	229	1.227	1.227	0.000	
35	230	1.227	1.225	-0.002	
35	231	1.226	1.225	-0.001	
35	232	1.227	1.226	-0.001	
35	234	1.227	1.226	-0.001	
35	237	1.226	1.225	-0.001	
50	272	1.226	1.225	-0.001	
50	275	1.226	1.224	-0.002	
50	276	1.226	1.225	-0.001	
50	277	1.227	1.225	-0.002	
50	278	1.227	1.226	-0.001	
55	279	1.227	1.224	-0.003	
55	280	1.227	1.225	-0.002	
55	281	1.227	1.224	-0.003	
55	282	1.227	1.225	-0.002	
55	283	1.227	1.225	-0.002	
100	317	1.227	1.223	-0.004	
100	318	1.226	1.222	-0.004	
100	319	1.227	1.224	-0.003	
100	320	1.226	1.223	-0.003	
100	321	1.226	1.221	-0.005	
100	322	1.227	1.225	-0.002	
100	323	1.226	1.223	-0.003	
100	324	1.227	1.224	-0.003	
100	326	1.227	1.224	-0.003	
100	327	1.226	1.223	-0.003	
100	328	1.226	1.224	-0.002	
100	329	1.227	1.222	-0.005	
100	332	1.226	1.224	-0.002	
100	333	1.226	1.223	-0.003	
100	334	1.226	1.223	-0.003	
100	335	1.227	1.224	-0.003	
100	336	1.227	1.224	-0.003	
100	337	1.227	1.225	-0.002	
100	338	1.226	1.223	-0.003	
100	339	1.227	1.223	-0.004	
100	340	1.227	1.223	-0.004	
100	341	1.226	1.223	-0.003	
100	342	1.226	1.223	-0.003	
100	343	1.227	1.223	-0.004	
105	344	1.226	1.222	-0.004	
105	346	1.226	1.223	-0.003	
105	349	1.227	1.223	-0.004	
105	350	1.227	1.223	-0.004	
105	351	1.226	1.223	-0.003	
105	352	1.227	1.224	-0.003	
Max		1.227	1.227	0.000	
Average		1.227	1.225	-0.002	
Min		1.226	1.221	-0.005	
Std Dev		0.000	0.002	0.001	

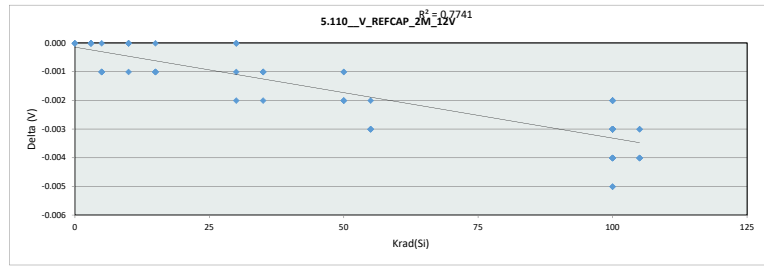


5.109_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.221	1.222
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.223	1.223
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

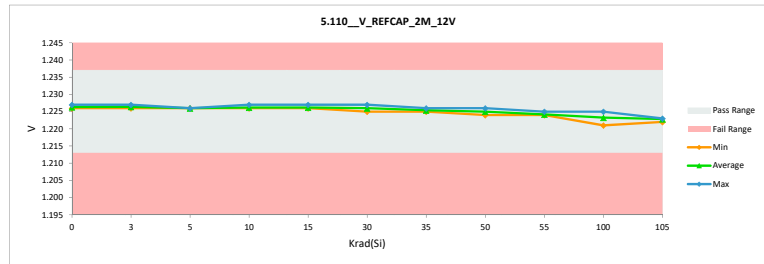


HDR TID Report  
TPS7H5004-SP

5.110_V_REFCAP_2M_12V				
Test Site	Tester	Test Number	Unit	
			V	V
			1.237	1.237
			1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.226	1.226	0.000
0	297	1.226	1.226	0.000
0	298	1.227	1.227	0.000
3	192	1.227	1.227	0.000
3	193	1.226	1.226	0.000
3	194	1.226	1.226	0.000
3	196	1.227	1.227	0.000
3	197	1.226	1.226	0.000
5	200	1.227	1.226	-0.001
5	202	1.227	1.226	-0.001
5	203	1.226	1.226	0.000
5	204	1.227	1.226	-0.001
5	206	1.227	1.226	-0.001
10	207	1.226	1.226	0.000
10	209	1.226	1.226	0.000
10	210	1.227	1.227	0.000
10	211	1.226	1.226	0.000
10	212	1.227	1.226	-0.001
15	214	1.227	1.227	0.000
15	217	1.227	1.226	-0.001
15	218	1.227	1.226	-0.001
15	219	1.227	1.226	-0.001
15	222	1.227	1.226	-0.001
30	224	1.227	1.225	-0.002
30	225	1.226	1.226	0.000
30	226	1.226	1.226	0.000
30	227	1.227	1.226	-0.001
30	229	1.227	1.227	0.000
35	230	1.227	1.225	-0.002
35	231	1.226	1.225	-0.001
35	232	1.227	1.226	-0.001
35	234	1.227	1.226	-0.001
35	237	1.226	1.225	-0.001
50	272	1.227	1.225	-0.002
50	275	1.226	1.224	-0.002
50	276	1.226	1.225	-0.001
50	277	1.227	1.225	-0.002
50	278	1.227	1.226	-0.001
55	279	1.227	1.224	-0.003
55	280	1.227	1.224	-0.003
55	281	1.227	1.224	-0.003
55	282	1.227	1.224	-0.003
55	283	1.227	1.225	-0.002
100	317	1.227	1.223	-0.004
100	318	1.226	1.222	-0.004
100	319	1.227	1.223	-0.004
100	320	1.226	1.223	-0.003
100	321	1.226	1.221	-0.005
100	322	1.227	1.225	-0.002
100	323	1.226	1.223	-0.003
100	324	1.227	1.224	-0.003
100	326	1.227	1.224	-0.003
100	327	1.226	1.223	-0.003
100	328	1.226	1.224	-0.002
100	329	1.227	1.222	-0.005
100	332	1.226	1.224	-0.002
100	333	1.226	1.223	-0.003
100	334	1.226	1.223	-0.003
100	335	1.226	1.224	-0.002
100	336	1.227	1.224	-0.003
100	337	1.227	1.225	-0.002
100	338	1.226	1.223	-0.003
100	339	1.227	1.223	-0.004
100	340	1.227	1.223	-0.004
100	341	1.226	1.223	-0.003
100	342	1.226	1.223	-0.003
100	343	1.227	1.223	-0.004
105	344	1.226	1.222	-0.004
105	346	1.226	1.223	-0.003
105	349	1.227	1.223	-0.004
105	350	1.227	1.223	-0.004
105	351	1.226	1.223	-0.003
105	352	1.227	1.223	-0.004
	Max	1.227	1.227	0.000
	Average	1.227	1.225	-0.002
	Min	1.226	1.221	-0.005
	Std Dev	0.000	0.002	0.001

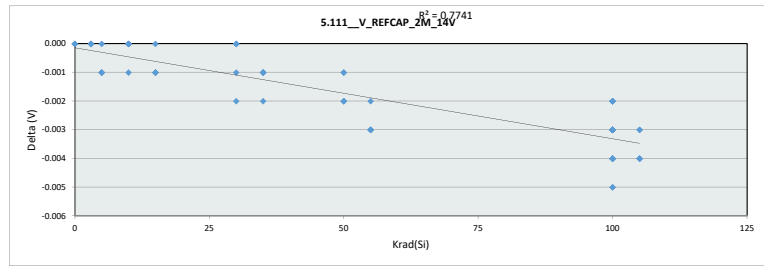


5.110_V_REFCAP											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			1.237	V							
			1.213	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.221	1.222
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223	1.223
Max	1.227	1.227	1.226	1.227	1.227	1.227	1.226	1.226	1.225	1.225	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

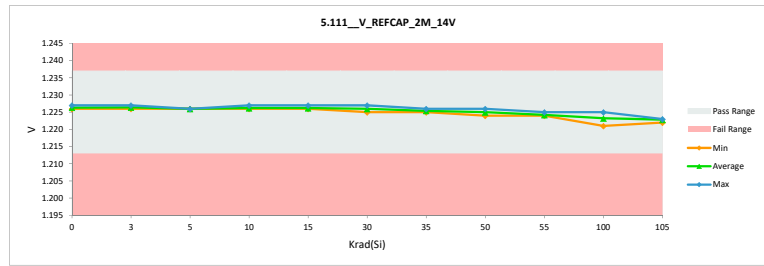


HDR TID Report  
TPS7H5004-SP

5.111_V_REFCAP_2M_14V				
Test Site	Tester	Test Number	Unit	
			V	V
			1.237	1.237
			1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.226	1.226	0.000
0	297	1.226	1.226	0.000
0	298	1.227	1.227	0.000
3	192	1.227	1.227	0.000
3	193	1.226	1.226	0.000
3	194	1.226	1.226	0.000
3	196	1.227	1.227	0.000
3	197	1.226	1.226	0.000
5	200	1.227	1.226	-0.001
5	202	1.227	1.226	-0.001
5	203	1.226	1.226	0.000
5	204	1.227	1.226	-0.001
5	206	1.227	1.226	-0.001
10	207	1.226	1.226	0.000
10	209	1.226	1.226	0.000
10	210	1.227	1.227	0.000
10	211	1.226	1.226	0.000
10	212	1.227	1.226	-0.001
15	214	1.227	1.227	0.000
15	217	1.227	1.226	-0.001
15	218	1.227	1.226	-0.001
15	219	1.227	1.226	-0.001
15	222	1.227	1.226	-0.001
30	224	1.227	1.225	-0.002
30	225	1.226	1.226	0.000
30	226	1.226	1.226	0.000
30	227	1.227	1.226	-0.001
30	229	1.227	1.227	0.000
35	230	1.227	1.225	-0.002
35	231	1.226	1.225	-0.001
35	232	1.227	1.226	-0.001
35	234	1.227	1.226	-0.001
35	237	1.226	1.225	-0.001
50	272	1.227	1.225	-0.002
50	275	1.226	1.224	-0.002
50	276	1.226	1.225	-0.001
50	277	1.227	1.225	-0.002
50	278	1.227	1.226	-0.001
55	279	1.227	1.224	-0.003
55	280	1.227	1.224	-0.003
55	281	1.227	1.224	-0.003
55	282	1.227	1.224	-0.003
55	283	1.227	1.225	-0.002
100	317	1.227	1.223	-0.004
100	318	1.226	1.222	-0.004
100	319	1.227	1.223	-0.004
100	320	1.226	1.223	-0.003
100	321	1.226	1.221	-0.005
100	322	1.227	1.225	-0.002
100	323	1.226	1.223	-0.003
100	324	1.227	1.224	-0.003
100	326	1.227	1.224	-0.003
100	327	1.226	1.223	-0.003
100	328	1.226	1.224	-0.002
100	329	1.227	1.222	-0.005
100	332	1.226	1.224	-0.002
100	333	1.226	1.223	-0.003
100	334	1.226	1.223	-0.003
100	335	1.226	1.224	-0.002
100	336	1.227	1.224	-0.003
100	337	1.227	1.225	-0.002
100	338	1.226	1.223	-0.003
100	339	1.227	1.223	-0.004
100	340	1.227	1.223	-0.004
100	341	1.226	1.223	-0.003
100	342	1.226	1.223	-0.003
100	343	1.227	1.223	-0.004
105	344	1.226	1.222	-0.004
105	346	1.226	1.223	-0.003
105	349	1.227	1.223	-0.004
105	350	1.227	1.223	-0.004
105	351	1.226	1.223	-0.003
105	352	1.227	1.223	-0.004
	Max	1.227	1.227	0.000
	Average	1.227	1.225	-0.002
	Min	1.226	1.221	-0.005
	Std Dev	0.000	0.002	0.001



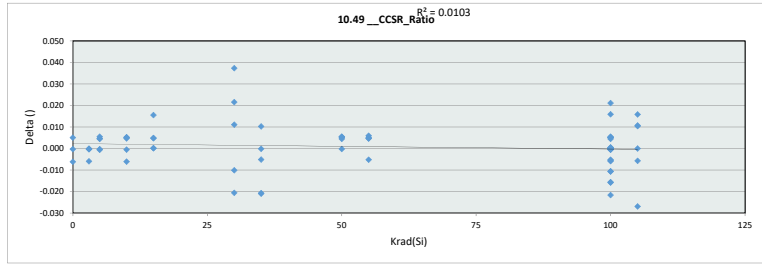
5.111_V_REFCAP											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			1.237	V							
			1.213	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.221	1.222
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.224	1.223	1.223
Max	1.227	1.227	1.226	1.227	1.227	1.227	1.226	1.226	1.225	1.225	1.223
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



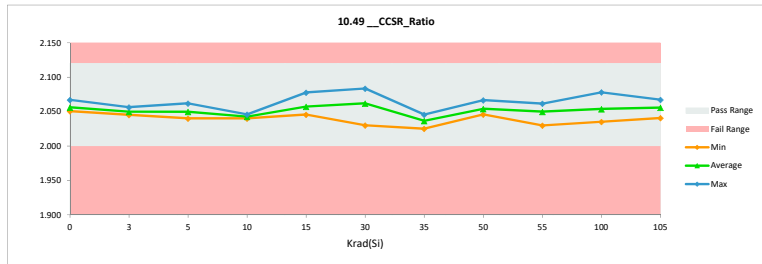
HDR TID Report  
TPS7H5004-SP

10.49_CCSR_Ratio	
Test Site	
Tester	
Test Number	
Unit	
Max Limit	2.12
Min Limit	2

Krad(Si)	Serial #	PRE	POST	Delta
0	295	2.057	2.051	-0.006
0	297	2.062	2.067	0.005
0	298	2.051	2.051	0.000
3	192	2.051	2.045	-0.006
3	193	2.056	2.056	0.000
3	194	2.051	2.051	0.000
3	196	2.046	2.045	0.000
3	197	2.051	2.051	0.000
5	200	2.041	2.046	0.005
5	202	2.056	2.056	0.000
5	203	2.041	2.045	0.005
5	204	2.040	2.040	0.000
5	206	2.056	2.062	0.006
10	207	2.041	2.046	0.005
10	209	2.046	2.040	-0.006
10	210	2.036	2.041	0.005
10	211	2.041	2.046	0.005
10	212	2.041	2.041	0.000
15	214	2.045	2.046	0.000
15	217	2.051	2.056	0.005
15	218	2.035	2.051	0.016
15	219	2.056	2.056	0.000
15	222	2.073	2.078	0.005
30	224	2.056	2.078	0.022
30	225	2.051	2.030	-0.020
30	226	2.046	2.083	0.037
30	227	2.045	2.035	-0.010
30	229	2.072	2.084	0.011
35	230	2.031	2.041	0.010
35	231	2.046	2.046	0.000
35	232	2.051	2.031	-0.021
35	234	2.045	2.040	-0.005
35	237	2.046	2.025	-0.021
50	272	2.046	2.051	0.005
50	275	2.051	2.056	0.006
50	276	2.040	2.046	0.006
50	277	2.067	2.067	0.000
50	278	2.046	2.051	0.005
55	279	2.035	2.030	-0.005
55	280	2.045	2.051	0.006
55	281	2.056	2.062	0.005
55	282	2.046	2.051	0.005
55	283	2.051	2.056	0.005
100	317	2.051	2.052	0.001
100	318	2.067	2.072	0.005
100	319	2.057	2.046	-0.011
100	320	2.051	2.051	0.000
100	321	2.051	2.041	-0.010
100	322	2.051	2.051	0.000
100	323	2.062	2.062	0.000
100	324	2.062	2.046	-0.016
100	326	2.067	2.051	-0.016
100	327	2.051	2.056	0.005
100	328	2.057	2.051	-0.006
100	329	2.057	2.061	0.005
100	332	2.056	2.056	0.000
100	333	2.072	2.073	0.000
100	334	2.041	2.062	0.021
100	335	2.051	2.051	0.000
100	336	2.040	2.046	0.006
100	337	2.051	2.046	-0.005
100	338	2.051	2.046	-0.005
100	339	2.062	2.078	0.016
100	340	2.056	2.057	0.000
100	341	2.057	2.035	-0.022
100	342	2.051	2.051	0.000
100	343	2.046	2.051	0.005
105	344	2.041	2.051	0.011
105	346	2.051	2.067	0.016
105	349	2.051	2.052	0.000
105	350	2.046	2.041	-0.006
105	351	2.057	2.067	0.011
105	352	2.083	2.057	-0.027
Max		2.083	2.084	0.037
Average		2.051	2.052	0.001
Min		2.031	2.025	-0.027
Std Dev		0.010	0.012	0.010



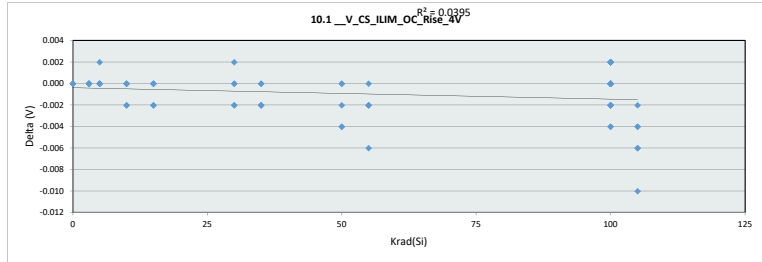
10.49_CCSR_Ratio		0	3	5	10	15	30	35	50	55	100	105
LL		2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000
Min		2.051	2.045	2.040	2.040	2.046	2.030	2.025	2.046	2.030	2.035	2.041
Average		2.056	2.050	2.050	2.043	2.057	2.062	2.037	2.054	2.050	2.054	2.056
Max		2.067	2.056	2.062	2.046	2.078	2.084	2.046	2.067	2.062	2.078	2.067
UL		2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120



HDR TID Report  
TPS7H5004-SP

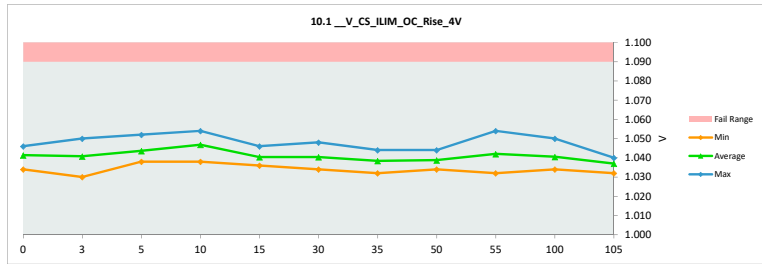
10.1_V_CS_ILIM_OC_Rise_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(S)	Serial #	PRE	POST	Delta
0	295	1.034	1.034	0.000
0	297	1.044	1.044	0.000
0	298	1.046	1.046	0.000
3	192	1.050	1.050	0.000
3	193	1.048	1.048	0.000
3	194	1.030	1.030	0.000
3	196	1.040	1.040	0.000
3	197	1.036	1.036	0.000
5	200	1.044	1.044	0.000
5	202	1.040	1.040	0.000
5	203	1.052	1.052	0.000
5	204	1.042	1.044	0.002
5	206	1.038	1.038	0.000
10	207	1.046	1.044	-0.002
10	209	1.038	1.038	0.000
10	210	1.052	1.052	0.000
10	211	1.056	1.054	-0.002
10	212	1.046	1.046	0.000
15	214	1.040	1.040	0.000
15	217	1.048	1.046	-0.002
15	218	1.038	1.038	0.000
15	219	1.042	1.042	0.000
15	222	1.038	1.036	-0.002
30	224	1.040	1.042	0.002
30	225	1.048	1.048	0.000
30	226	1.040	1.038	-0.002
30	227	1.040	1.040	0.000
30	229	1.036	1.034	-0.002
35	230	1.040	1.038	-0.002
35	231	1.034	1.032	-0.002
35	232	1.044	1.044	0.000
35	234	1.038	1.036	-0.002
35	237	1.042	1.042	0.000
50	272	1.038	1.034	-0.004
50	275	1.038	1.036	-0.002
50	276	1.044	1.040	-0.004
50	277	1.040	1.040	0.000
50	278	1.044	1.044	0.000
55	279	1.036	1.034	-0.002
55	280	1.032	1.032	0.000
55	281	1.060	1.054	-0.006
55	282	1.046	1.044	-0.002
55	283	1.048	1.046	-0.002
100	317	1.050	1.048	-0.002
100	318	1.036	1.034	-0.002
100	319	1.040	1.040	0.000
100	320	1.038	1.038	0.000
100	321	1.048	1.050	0.002
100	322	1.034	1.036	0.002
100	323	1.046	1.048	0.002
100	324	1.038	1.040	0.002
100	326	1.044	1.046	0.002
100	327	1.044	1.042	-0.002
100	328	1.038	1.038	0.000
100	329	1.040	1.036	-0.004
100	332	1.044	1.044	0.000
100	333	1.038	1.038	0.000
100	334	1.042	1.042	0.000
100	335	1.042	1.038	-0.004
100	336	1.044	1.044	0.000
100	337	1.040	1.038	-0.002
100	338	1.040	1.040	0.000
100	339	1.034	1.034	0.000
100	340	1.040	1.038	-0.002
100	341	1.044	1.046	0.002
100	342	1.034	1.034	0.000
100	343	1.038	1.040	0.002
105	344	1.042	1.040	-0.002
105	346	1.046	1.040	-0.006
105	349	1.044	1.040	-0.004
105	350	1.040	1.036	-0.004
105	351	1.040	1.034	-0.006
105	352	1.042	1.032	-0.010
Max		1.060	1.054	0.002
Average		1.042	1.041	-0.001
Min		1.030	1.030	-0.010
Std Dev		0.005	0.006	0.002



10.1_V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09 V
Min Limit	V

Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.034	1.030	1.038	1.038	1.036	1.034	1.032	1.034	1.032	1.034	1.032
Average	1.041	1.041	1.044	1.047	1.040	1.040	1.038	1.039	1.042	1.041	1.037
Max	1.046	1.050	1.052	1.054	1.046	1.048	1.044	1.044	1.054	1.050	1.040
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

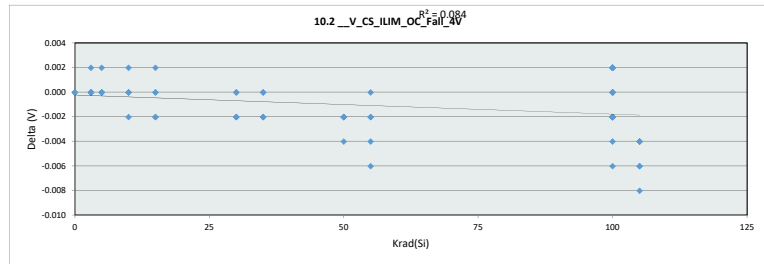




HDR TID Report  
TPS7H5004-SP

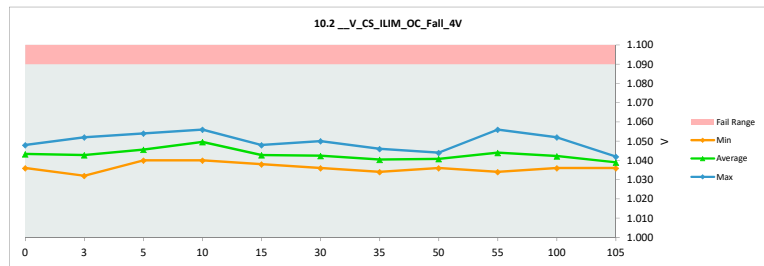
10.2 __V_CS_ILIM_OC_Fall_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(S)	Serial #	PRE	POST	Delta
0	295	1.036	1.036	0.000
0	297	1.046	1.046	0.000
0	298	1.048	1.048	0.000
3	192	1.052	1.052	0.000
3	193	1.048	1.050	0.002
3	194	1.032	1.032	0.000
3	196	1.042	1.042	0.000
3	197	1.038	1.038	0.000
5	200	1.046	1.046	0.000
5	202	1.042	1.042	0.000
5	203	1.054	1.054	0.000
5	204	1.044	1.046	0.002
5	206	1.040	1.040	0.000
10	207	1.048	1.048	0.000
10	209	1.040	1.040	0.000
10	210	1.054	1.056	0.002
10	211	1.058	1.056	-0.002
10	212	1.048	1.048	0.000
15	214	1.042	1.042	0.000
15	217	1.050	1.048	-0.002
15	218	1.040	1.042	0.002
15	219	1.044	1.044	0.000
15	222	1.040	1.038	-0.002
30	224	1.044	1.044	0.000
30	225	1.050	1.050	0.000
30	226	1.042	1.040	-0.002
30	227	1.044	1.042	-0.002
30	229	1.038	1.036	-0.002
35	230	1.042	1.040	-0.002
35	231	1.036	1.034	-0.002
35	232	1.046	1.046	0.000
35	234	1.040	1.038	-0.002
35	237	1.044	1.044	0.000
50	272	1.040	1.036	-0.004
50	275	1.040	1.038	-0.002
50	276	1.046	1.044	-0.002
50	277	1.044	1.042	-0.002
50	278	1.046	1.044	-0.002
55	279	1.038	1.036	-0.002
55	280	1.034	1.034	0.000
55	281	1.062	1.056	-0.006
55	282	1.048	1.046	-0.002
55	283	1.052	1.048	-0.004
100	317	1.052	1.050	-0.002
100	318	1.038	1.036	-0.002
100	319	1.042	1.040	-0.002
100	320	1.040	1.040	0.000
100	321	1.050	1.052	0.002
100	322	1.036	1.038	0.002
100	323	1.048	1.048	0.000
100	324	1.040	1.042	0.002
100	326	1.046	1.048	0.002
100	327	1.046	1.044	-0.002
100	328	1.040	1.040	0.000
100	329	1.044	1.038	-0.006
100	332	1.046	1.046	0.000
100	333	1.040	1.040	0.000
100	334	1.044	1.042	-0.002
100	335	1.044	1.040	-0.004
100	336	1.048	1.046	-0.002
100	337	1.042	1.040	-0.002
100	338	1.042	1.042	0.000
100	339	1.034	1.036	0.002
100	340	1.042	1.040	-0.002
100	341	1.046	1.048	0.002
100	342	1.036	1.036	0.000
100	343	1.040	1.042	0.002
105	344	1.044	1.040	-0.004
105	346	1.048	1.042	-0.006
105	349	1.046	1.042	-0.004
105	350	1.042	1.038	-0.004
105	351	1.042	1.036	-0.006
105	352	1.044	1.036	-0.008
Max		1.062	1.056	0.002
Average		1.044	1.043	-0.001
Min		1.032	1.032	-0.008
Std Dev		0.005	0.006	0.002



10.2 __V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09 V
Min Limit	V

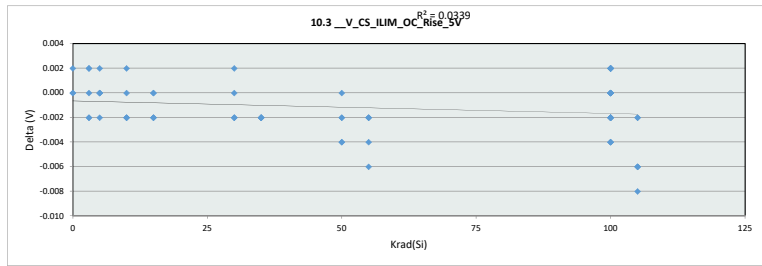
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.036	1.032	1.040	1.040	1.038	1.036	1.034	1.036	1.034	1.036	1.036
Average	1.043	1.043	1.046	1.050	1.043	1.042	1.040	1.041	1.044	1.042	1.039
Max	1.048	1.052	1.054	1.056	1.048	1.050	1.046	1.044	1.056	1.052	1.042
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



HDR TID Report  
TPS7H5004-SP

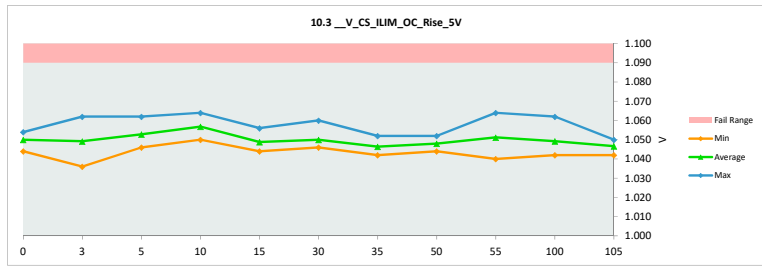
10.3 _V_CS_ILIM_OC_Rise_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.042	1.044	0.002
0	297	1.052	1.052	0.000
0	298	1.054	1.054	0.000
3	192	1.060	1.062	0.002
3	193	1.054	1.056	0.002
3	194	1.038	1.036	-0.002
3	196	1.048	1.048	0.000
3	197	1.046	1.044	-0.002
5	200	1.052	1.052	0.000
5	202	1.048	1.050	0.002
5	203	1.062	1.062	0.000
5	204	1.054	1.054	0.000
5	206	1.048	1.046	-0.002
10	207	1.054	1.052	-0.002
10	209	1.048	1.050	0.002
10	210	1.064	1.064	0.000
10	211	1.066	1.064	-0.002
10	212	1.056	1.054	-0.002
15	214	1.048	1.046	-0.002
15	217	1.056	1.056	0.000
15	218	1.048	1.046	-0.002
15	219	1.052	1.052	0.000
15	222	1.046	1.044	-0.002
30	224	1.050	1.048	-0.002
30	225	1.058	1.060	0.002
30	226	1.048	1.048	0.000
30	227	1.050	1.048	-0.002
30	229	1.048	1.046	-0.002
35	230	1.048	1.046	-0.002
35	231	1.044	1.042	-0.002
35	232	1.054	1.052	-0.002
35	234	1.046	1.044	-0.002
35	237	1.050	1.048	-0.002
50	272	1.050	1.046	-0.004
50	275	1.046	1.044	-0.002
50	276	1.054	1.050	-0.004
50	277	1.050	1.048	-0.002
50	278	1.052	1.052	0.000
55	279	1.044	1.042	-0.002
55	280	1.042	1.040	-0.002
55	281	1.070	1.064	-0.006
55	282	1.056	1.054	-0.002
55	283	1.060	1.056	-0.004
100	317	1.060	1.056	-0.004
100	318	1.044	1.044	0.000
100	319	1.052	1.050	-0.002
100	320	1.048	1.048	0.000
100	321	1.060	1.062	0.002
100	322	1.042	1.044	0.002
100	323	1.054	1.056	0.002
100	324	1.048	1.050	0.002
100	326	1.052	1.054	0.002
100	327	1.052	1.050	-0.002
100	328	1.044	1.046	0.002
100	329	1.048	1.044	-0.004
100	332	1.054	1.052	-0.002
100	333	1.048	1.046	-0.002
100	334	1.052	1.050	-0.002
100	335	1.050	1.046	-0.004
100	336	1.054	1.054	0.000
100	337	1.048	1.046	-0.002
100	338	1.048	1.048	0.000
100	339	1.042	1.044	0.002
100	340	1.050	1.048	-0.002
100	341	1.054	1.056	0.002
100	342	1.042	1.042	0.000
100	343	1.046	1.046	0.000
105	344	1.050	1.048	-0.002
105	346	1.056	1.050	-0.006
105	349	1.056	1.050	-0.006
105	350	1.048	1.046	-0.002
105	351	1.048	1.042	-0.006
105	352	1.052	1.044	-0.008
	Max	1.070	1.064	0.002
	Average	1.051	1.050	-0.001
	Min	1.038	1.036	-0.008
	Std Dev	0.006	0.006	0.002



10.3 _V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09 V
Min Limit	V

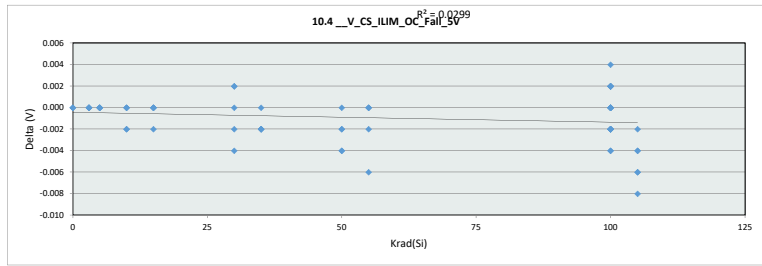
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.036	1.046	1.050	1.044	1.046	1.042	1.044	1.040	1.042	1.042
Average	1.050	1.049	1.053	1.057	1.049	1.050	1.046	1.048	1.051	1.049	1.047
Max	1.054	1.062	1.062	1.064	1.056	1.060	1.052	1.052	1.064	1.062	1.050
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



HDR TID Report  
TPS7H5004-SP

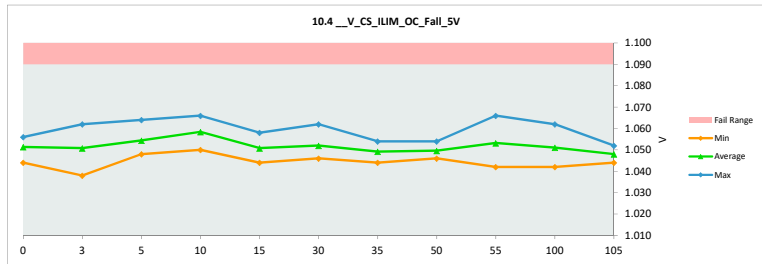
10.4 __V_CS_ILIM_OC_Fall_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(S)	Serial #	PRE	POST	Delta
0	295	1.044	1.044	0.000
0	297	1.054	1.054	0.000
0	298	1.056	1.056	0.000
3	192	1.062	1.062	0.000
3	193	1.058	1.058	0.000
3	194	1.038	1.038	0.000
3	196	1.050	1.050	0.000
3	197	1.046	1.046	0.000
5	200	1.054	1.054	0.000
5	202	1.052	1.052	0.000
5	203	1.064	1.064	0.000
5	204	1.054	1.054	0.000
5	206	1.048	1.048	0.000
10	207	1.056	1.054	-0.002
10	209	1.050	1.050	0.000
10	210	1.066	1.066	0.000
10	211	1.068	1.066	-0.002
10	212	1.056	1.056	0.000
15	214	1.048	1.048	0.000
15	217	1.058	1.058	0.000
15	218	1.050	1.050	0.000
15	219	1.054	1.054	0.000
15	222	1.046	1.044	-0.002
30	224	1.050	1.052	0.002
30	225	1.060	1.062	0.002
30	226	1.050	1.050	0.000
30	227	1.052	1.050	-0.002
30	229	1.050	1.046	-0.004
35	230	1.050	1.048	-0.002
35	231	1.046	1.044	-0.002
35	232	1.056	1.054	-0.002
35	234	1.050	1.048	-0.002
35	237	1.052	1.052	0.000
50	272	1.050	1.046	-0.004
50	275	1.048	1.046	-0.002
50	276	1.056	1.052	-0.004
50	277	1.052	1.050	-0.002
50	278	1.054	1.054	0.000
55	279	1.044	1.044	0.000
55	280	1.042	1.042	0.000
55	281	1.072	1.066	-0.006
55	282	1.056	1.056	0.000
55	283	1.060	1.058	-0.002
100	317	1.060	1.058	-0.002
100	318	1.046	1.044	-0.002
100	319	1.054	1.052	-0.002
100	320	1.048	1.050	0.002
100	321	1.060	1.062	0.002
100	322	1.042	1.046	0.004
100	323	1.056	1.058	0.002
100	324	1.050	1.052	0.002
100	326	1.054	1.056	0.002
100	327	1.054	1.054	0.000
100	328	1.048	1.046	-0.002
100	329	1.050	1.046	-0.004
100	332	1.054	1.054	0.000
100	333	1.050	1.050	0.000
100	334	1.052	1.052	0.000
100	335	1.052	1.048	-0.004
100	336	1.056	1.056	0.000
100	337	1.050	1.048	-0.002
100	338	1.050	1.050	0.000
100	339	1.044	1.044	0.000
100	340	1.052	1.050	-0.002
100	341	1.056	1.058	0.002
100	342	1.044	1.042	-0.002
100	343	1.048	1.050	0.002
105	344	1.052	1.050	-0.002
105	346	1.058	1.052	-0.006
105	349	1.056	1.052	-0.004
105	350	1.050	1.046	-0.004
105	351	1.050	1.044	-0.006
105	352	1.052	1.044	-0.008
Max		1.072	1.066	0.004
Average		1.052	1.052	-0.001
Min		1.038	1.038	-0.008
Std Dev		0.006	0.006	0.002



10.4 __V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09
Min Limit	V

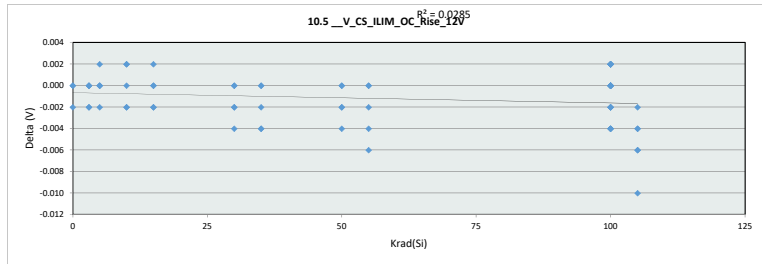
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.038	1.048	1.050	1.044	1.046	1.044	1.046	1.042	1.042	1.044
Average	1.051	1.051	1.054	1.058	1.051	1.052	1.049	1.050	1.053	1.051	1.048
Max	1.056	1.062	1.064	1.066	1.058	1.062	1.054	1.054	1.066	1.062	1.052
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



HDR TID Report  
TPS7H5004-SP

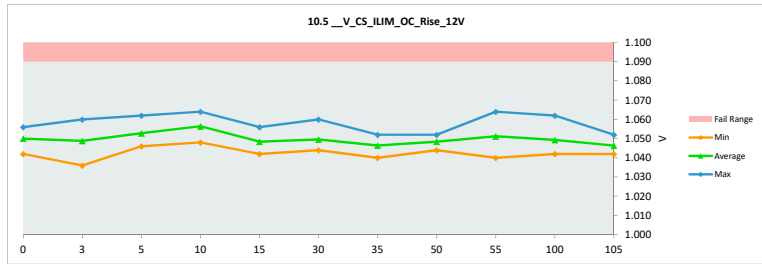
		10.5_V_CS_ILIM_OC_Rise_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.09	1.09	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.044	1.042	-0.002
0	297	1.052	1.052	0.000
0	298	1.056	1.056	0.000
3	192	1.062	1.060	-0.002
3	193	1.056	1.056	0.000
3	194	1.036	1.036	0.000
3	196	1.048	1.048	0.000
3	197	1.046	1.044	-0.002
5	200	1.050	1.052	0.002
5	202	1.050	1.050	0.000
5	203	1.062	1.062	0.000
5	204	1.054	1.054	0.000
5	206	1.048	1.046	-0.002
10	207	1.054	1.052	-0.002
10	209	1.046	1.048	0.002
10	210	1.064	1.064	0.000
10	211	1.066	1.064	-0.002
10	212	1.052	1.054	0.002
15	214	1.046	1.046	0.000
15	217	1.054	1.056	0.002
15	218	1.048	1.048	0.000
15	219	1.052	1.050	-0.002
15	222	1.044	1.042	-0.002
30	224	1.050	1.048	-0.002
30	225	1.060	1.060	0.000
30	226	1.048	1.046	-0.002
30	227	1.050	1.050	0.000
30	229	1.048	1.044	-0.004
35	230	1.048	1.046	-0.002
35	231	1.044	1.040	-0.004
35	232	1.052	1.052	0.000
35	234	1.048	1.044	-0.004
35	237	1.050	1.050	0.000
50	272	1.050	1.046	-0.004
50	275	1.046	1.044	-0.002
50	276	1.054	1.052	-0.002
50	277	1.048	1.048	0.000
50	278	1.052	1.052	0.000
55	279	1.044	1.042	-0.002
55	280	1.040	1.040	0.000
55	281	1.070	1.064	-0.006
55	282	1.054	1.054	0.000
55	283	1.060	1.056	-0.004
100	317	1.060	1.056	-0.004
100	318	1.044	1.044	0.000
100	319	1.052	1.050	-0.002
100	320	1.048	1.048	0.000
100	321	1.060	1.062	0.002
100	322	1.042	1.044	0.002
100	323	1.054	1.056	0.002
100	324	1.048	1.050	0.002
100	326	1.052	1.054	0.002
100	327	1.050	1.052	0.002
100	328	1.046	1.046	0.000
100	329	1.048	1.044	-0.004
100	332	1.054	1.052	-0.002
100	333	1.048	1.048	0.000
100	334	1.052	1.050	-0.002
100	335	1.050	1.046	-0.004
100	336	1.054	1.054	0.000
100	337	1.048	1.046	-0.002
100	338	1.048	1.048	0.000
100	339	1.042	1.042	0.000
100	340	1.052	1.048	-0.004
100	341	1.054	1.054	0.000
100	342	1.042	1.042	0.000
100	343	1.046	1.048	0.002
105	344	1.052	1.048	-0.004
105	346	1.056	1.050	-0.006
105	349	1.054	1.052	-0.002
105	350	1.048	1.044	-0.004
105	351	1.048	1.042	-0.006
105	352	1.052	1.042	-0.010
	Max	1.070	1.064	0.002
	Average	1.051	1.050	-0.001
	Min	1.036	1.036	-0.010
	Std Dev	0.006	0.006	0.002



		10.5_V_CS_ILIM	
Test Site			
Tester			
Test Number			
Max Limit	1.09	V	
Min Limit		V	

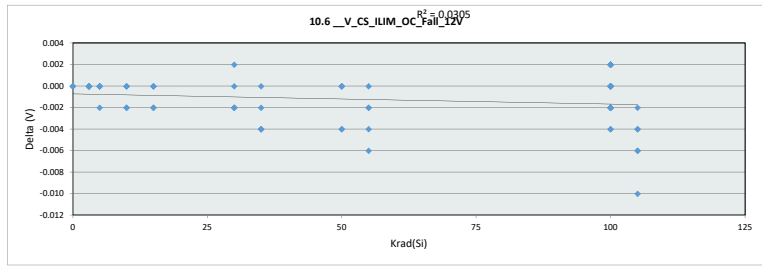
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.042	1.036	1.046	1.048	1.042	1.044	1.040	1.044	1.040	1.042	1.042
Average	1.050	1.049	1.053	1.056	1.048	1.050	1.046	1.048	1.051	1.049	1.046
Max	1.056	1.060	1.062	1.064	1.056	1.060	1.052	1.052	1.064	1.062	1.052
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



# HDR TID Report TPS7H5004-SP

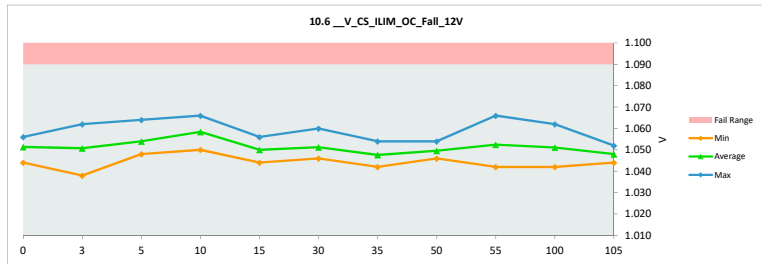
10.6_V_CS_ILIM_OC_Fall_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(S)	Serial #	PRE	POST	Delta
0	295	1.044	1.044	0.000
0	297	1.054	1.054	0.000
0	298	1.056	1.056	0.000
3	192	1.062	1.062	0.000
3	193	1.058	1.058	0.000
3	194	1.038	1.038	0.000
3	196	1.050	1.050	0.000
3	197	1.046	1.046	0.000
5	200	1.054	1.052	-0.002
5	202	1.052	1.052	0.000
5	203	1.064	1.064	0.000
5	204	1.054	1.054	0.000
5	206	1.048	1.048	0.000
10	207	1.056	1.054	-0.002
10	209	1.050	1.050	0.000
10	210	1.066	1.066	0.000
10	211	1.068	1.066	-0.002
10	212	1.056	1.056	0.000
15	214	1.048	1.048	0.000
15	217	1.058	1.056	-0.002
15	218	1.050	1.050	0.000
15	219	1.052	1.052	0.000
15	222	1.046	1.044	-0.002
30	224	1.050	1.052	0.002
30	225	1.060	1.060	0.000
30	226	1.050	1.048	-0.002
30	227	1.052	1.050	-0.002
30	229	1.048	1.046	-0.002
35	230	1.050	1.046	-0.004
35	231	1.046	1.042	-0.004
35	232	1.054	1.054	0.000
35	234	1.050	1.046	-0.004
35	237	1.052	1.050	-0.002
50	272	1.050	1.046	-0.004
50	275	1.046	1.046	0.000
50	276	1.056	1.052	-0.004
50	277	1.050	1.050	0.000
50	278	1.054	1.054	0.000
55	279	1.044	1.042	-0.002
55	280	1.042	1.042	0.000
55	281	1.072	1.066	-0.006
55	282	1.056	1.054	-0.002
55	283	1.062	1.058	-0.004
100	317	1.060	1.058	-0.002
100	318	1.046	1.046	0.000
100	319	1.054	1.052	-0.002
100	320	1.048	1.050	0.002
100	321	1.062	1.062	0.000
100	322	1.044	1.046	0.002
100	323	1.056	1.058	0.002
100	324	1.050	1.052	0.002
100	326	1.054	1.056	0.002
100	327	1.054	1.054	0.000
100	328	1.048	1.046	-0.002
100	329	1.050	1.046	-0.004
100	332	1.054	1.054	0.000
100	333	1.050	1.050	0.000
100	334	1.054	1.052	-0.002
100	335	1.052	1.048	-0.004
100	336	1.056	1.056	0.000
100	337	1.050	1.048	-0.002
100	338	1.050	1.050	0.000
100	339	1.044	1.044	0.000
100	340	1.052	1.050	-0.002
100	341	1.056	1.056	0.000
100	342	1.044	1.042	-0.002
100	343	1.048	1.050	0.002
105	344	1.052	1.050	-0.002
105	346	1.058	1.052	-0.006
105	349	1.056	1.052	-0.004
105	350	1.050	1.046	-0.004
105	351	1.050	1.044	-0.006
105	352	1.054	1.044	-0.010
	Max	1.072	1.066	0.002
	Average	1.052	1.051	-0.001
	Min	1.038	1.038	-0.010
	Std Dev	0.006	0.006	0.002



10.6_V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09 V
Min Limit	V

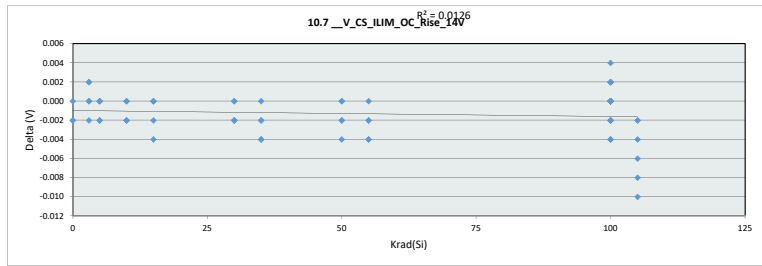
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.038	1.048	1.050	1.044	1.046	1.042	1.046	1.042	1.042	1.044
Average	1.051	1.051	1.054	1.058	1.050	1.051	1.048	1.050	1.052	1.051	1.048
Max	1.056	1.062	1.064	1.066	1.056	1.060	1.054	1.054	1.066	1.062	1.052
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



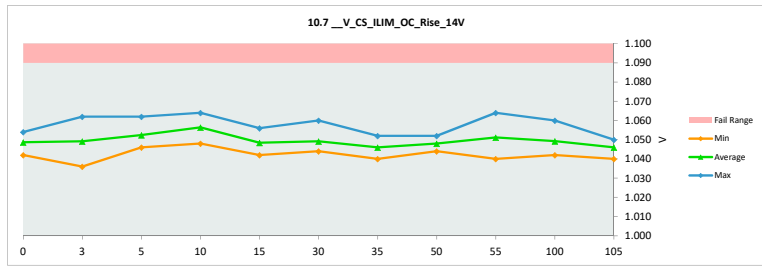
HDR TID Report  
TPS7H5004-SP

		10.7 __V_CS_ILIM_OC_Rise_14V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.09	1.09	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	295	1.042	1.042	0.000
0	297	1.052	1.050	-0.002
0	298	1.056	1.054	-0.002
3	192	1.060	1.062	0.002
3	193	1.056	1.056	0.000
3	194	1.036	1.036	0.000
3	196	1.046	1.048	0.002
3	197	1.046	1.044	-0.002
5	200	1.052	1.050	-0.002
5	202	1.050	1.050	0.000
5	203	1.062	1.062	0.000
5	204	1.054	1.054	0.000
5	206	1.048	1.046	-0.002
10	207	1.054	1.052	-0.002
10	209	1.048	1.048	0.000
10	210	1.064	1.064	0.000
10	211	1.066	1.064	-0.002
10	212	1.056	1.054	-0.002
15	214	1.046	1.046	0.000
15	217	1.056	1.056	0.000
15	218	1.048	1.048	0.000
15	219	1.052	1.050	-0.002
15	222	1.046	1.042	-0.004
30	224	1.050	1.048	-0.002
30	225	1.060	1.060	0.000
30	226	1.046	1.046	0.000
30	227	1.050	1.048	-0.002
30	229	1.046	1.044	-0.002
35	230	1.048	1.046	-0.002
35	231	1.044	1.040	-0.004
35	232	1.052	1.052	0.000
35	234	1.048	1.044	-0.004
35	237	1.050	1.048	-0.002
50	272	1.048	1.044	-0.004
50	275	1.044	1.044	0.000
50	276	1.054	1.052	-0.002
50	277	1.050	1.048	-0.002
50	278	1.052	1.052	0.000
55	279	1.044	1.042	-0.002
55	280	1.042	1.040	-0.002
55	281	1.068	1.064	-0.004
55	282	1.054	1.054	0.000
55	283	1.060	1.056	-0.004
100	317	1.058	1.056	-0.002
100	318	1.044	1.044	0.000
100	319	1.050	1.050	0.000
100	320	1.048	1.048	0.000
100	321	1.060	1.060	0.000
100	322	1.042	1.042	0.000
100	323	1.054	1.056	0.002
100	324	1.048	1.048	0.000
100	326	1.052	1.056	0.004
100	327	1.054	1.052	-0.002
100	328	1.046	1.046	0.000
100	329	1.048	1.044	-0.004
100	332	1.054	1.052	-0.002
100	333	1.048	1.048	0.000
100	334	1.050	1.050	0.000
100	335	1.050	1.046	-0.004
100	336	1.054	1.054	0.000
100	337	1.048	1.046	-0.002
100	338	1.050	1.048	-0.002
100	339	1.042	1.044	0.002
100	340	1.052	1.048	-0.004
100	341	1.054	1.056	0.002
100	342	1.042	1.042	0.000
100	343	1.044	1.046	0.002
105	344	1.050	1.048	-0.002
105	346	1.054	1.050	-0.004
105	349	1.056	1.050	-0.006
105	350	1.048	1.046	-0.002
105	351	1.048	1.040	-0.008
105	352	1.052	1.042	-0.010
	Max	1.068	1.064	0.004
	Average	1.051	1.049	-0.001
	Min	1.036	1.036	-0.010
	Std Dev	0.006	0.006	0.002



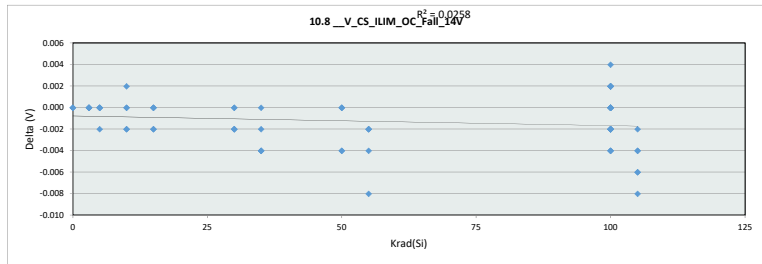
		10.7 __V_CS_ILIM												
Test Site														
Tester														
Test Number														
Max Limit	1.09	V												
Min Limit		V												
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105			
LL														
Min	1.042	1.036	1.046	1.048	1.042	1.044	1.040	1.044	1.040	1.042	1.040			
Average	1.049	1.049	1.052	1.056	1.048	1.049	1.046	1.048	1.051	1.049	1.046			
Max	1.054	1.062	1.062	1.064	1.056	1.060	1.052	1.052	1.064	1.060	1.050			
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090			



HDR TID Report  
TPS7H5004-SP

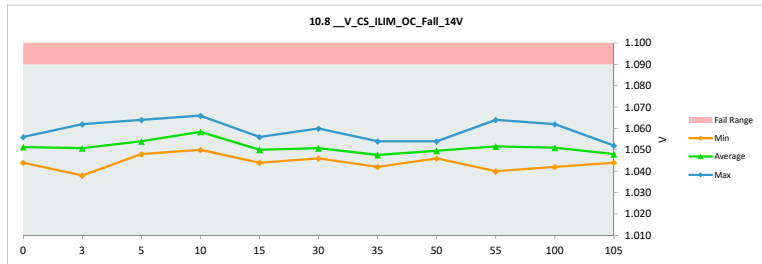
10.8_V_CS_ILIM_OC_Fall_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(S)	Serial #	PRE	POST	Delta
0	295	1.044	1.044	0.000
0	297	1.054	1.054	0.000
0	298	1.056	1.056	0.000
3	192	1.062	1.062	0.000
3	193	1.058	1.058	0.000
3	194	1.038	1.038	0.000
3	196	1.050	1.050	0.000
3	197	1.046	1.046	0.000
5	200	1.054	1.052	-0.002
5	202	1.052	1.052	0.000
5	203	1.064	1.064	0.000
5	204	1.054	1.054	0.000
5	206	1.048	1.048	0.000
10	207	1.056	1.054	-0.002
10	209	1.048	1.050	0.002
10	210	1.066	1.066	0.000
10	211	1.068	1.066	-0.002
10	212	1.056	1.056	0.000
15	214	1.048	1.048	0.000
15	217	1.058	1.056	-0.002
15	218	1.050	1.050	0.000
15	219	1.052	1.052	0.000
15	222	1.046	1.044	-0.002
30	224	1.050	1.050	0.000
30	225	1.060	1.060	0.000
30	226	1.050	1.048	-0.002
30	227	1.052	1.050	-0.002
30	229	1.048	1.046	-0.002
35	230	1.050	1.046	-0.004
35	231	1.046	1.042	-0.004
35	232	1.054	1.054	0.000
35	234	1.050	1.046	-0.004
35	237	1.052	1.050	-0.002
50	272	1.050	1.046	-0.004
50	275	1.046	1.046	0.000
50	276	1.056	1.052	-0.004
50	277	1.050	1.050	0.000
50	278	1.054	1.054	0.000
55	279	1.044	1.042	-0.002
55	280	1.042	1.040	-0.002
55	281	1.072	1.064	-0.008
55	282	1.056	1.054	-0.002
55	283	1.062	1.058	-0.004
100	317	1.062	1.058	-0.004
100	318	1.046	1.046	0.000
100	319	1.054	1.052	-0.002
100	320	1.048	1.048	0.000
100	321	1.060	1.062	0.002
100	322	1.042	1.046	0.004
100	323	1.056	1.058	0.002
100	324	1.050	1.052	0.002
100	326	1.054	1.056	0.002
100	327	1.054	1.054	0.000
100	328	1.048	1.046	-0.002
100	329	1.050	1.046	-0.004
100	332	1.054	1.054	0.000
100	333	1.050	1.048	-0.002
100	334	1.054	1.052	-0.002
100	335	1.052	1.048	-0.004
100	336	1.056	1.056	0.000
100	337	1.050	1.048	-0.002
100	338	1.050	1.050	0.000
100	339	1.044	1.044	0.000
100	340	1.052	1.050	-0.002
100	341	1.056	1.058	0.002
100	342	1.044	1.042	-0.002
100	343	1.048	1.050	0.002
105	344	1.052	1.050	-0.002
105	346	1.058	1.052	-0.006
105	349	1.056	1.052	-0.004
105	350	1.050	1.046	-0.004
105	351	1.050	1.044	-0.006
105	352	1.052	1.044	-0.008
	Max	1.072	1.066	0.004
	Average	1.052	1.051	-0.001
	Min	1.038	1.038	-0.008
	Std Dev	0.006	0.006	0.002



10.8_V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09 V
Min Limit	V

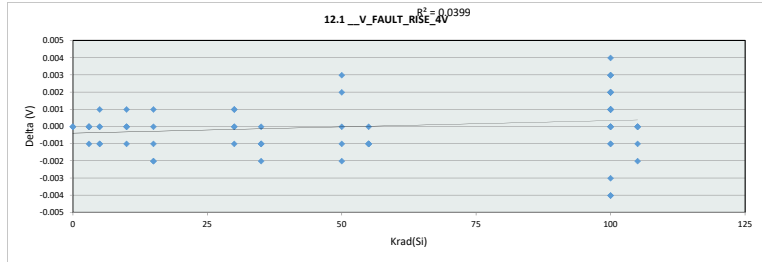
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.038	1.048	1.050	1.044	1.046	1.042	1.046	1.040	1.042	1.044
Average	1.051	1.051	1.054	1.058	1.050	1.051	1.048	1.050	1.052	1.051	1.048
Max	1.056	1.062	1.064	1.066	1.056	1.060	1.054	1.054	1.064	1.062	1.052
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



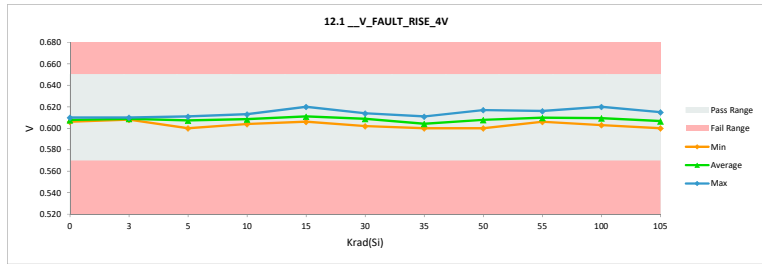
HDR TID Report  
TPS7H5004-SP

12.1 __V_FAULT_RISE_4V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.65 0.65
Min Limit	0.57 0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.606	0.606	0.000
0	297	0.610	0.610	0.000
0	298	0.607	0.607	0.000
3	192	0.609	0.609	0.000
3	193	0.608	0.608	0.000
3	194	0.610	0.610	0.000
3	196	0.610	0.609	-0.001
3	197	0.608	0.608	0.000
5	200	0.601	0.600	-0.001
5	202	0.611	0.611	0.000
5	203	0.608	0.607	-0.001
5	204	0.610	0.610	0.000
5	206	0.607	0.608	0.001
10	207	0.607	0.607	0.000
10	209	0.609	0.609	0.000
10	210	0.608	0.609	0.001
10	211	0.613	0.613	0.000
10	212	0.605	0.604	-0.001
15	214	0.609	0.607	-0.002
15	217	0.619	0.620	0.001
15	218	0.612	0.610	-0.002
15	219	0.606	0.606	0.000
15	222	0.613	0.612	-0.001
30	224	0.609	0.610	0.001
30	225	0.609	0.609	0.000
30	226	0.608	0.609	0.001
30	227	0.615	0.614	-0.001
30	229	0.602	0.602	0.000
35	230	0.608	0.607	-0.001
35	231	0.603	0.601	-0.002
35	232	0.603	0.602	-0.001
35	234	0.601	0.600	-0.001
35	237	0.611	0.611	0.000
50	272	0.609	0.611	0.002
50	275	0.602	0.600	-0.002
50	276	0.604	0.604	0.000
50	277	0.608	0.607	-0.001
50	278	0.614	0.617	0.003
55	279	0.617	0.616	-0.001
55	280	0.611	0.611	0.000
55	281	0.607	0.606	-0.001
55	282	0.608	0.607	-0.001
55	283	0.610	0.609	-0.001
100	317	0.609	0.611	0.002
100	318	0.608	0.610	0.002
100	319	0.612	0.613	0.001
100	320	0.615	0.616	0.001
100	321	0.607	0.607	0.000
100	322	0.608	0.611	0.003
100	323	0.614	0.615	0.001
100	324	0.606	0.606	0.000
100	326	0.606	0.607	0.001
100	327	0.606	0.603	-0.003
100	328	0.609	0.609	0.000
100	329	0.609	0.612	0.003
100	332	0.607	0.607	0.000
100	333	0.600	0.604	0.004
100	334	0.612	0.614	0.002
100	335	0.610	0.611	0.001
100	336	0.611	0.610	-0.001
100	337	0.608	0.604	-0.004
100	338	0.606	0.608	0.002
100	339	0.606	0.605	-0.001
100	340	0.613	0.609	-0.004
100	341	0.607	0.609	0.002
100	342	0.602	0.605	0.003
100	343	0.619	0.620	0.001
105	344	0.615	0.615	0.000
105	346	0.608	0.608	0.000
105	349	0.604	0.602	-0.002
105	350	0.609	0.609	0.000
105	351	0.606	0.606	0.000
105	352	0.601	0.600	-0.001
Max		0.619	0.620	0.004
Average		0.608	0.608	0.000
Min		0.600	0.600	-0.004
Std Dev		0.004	0.004	0.002



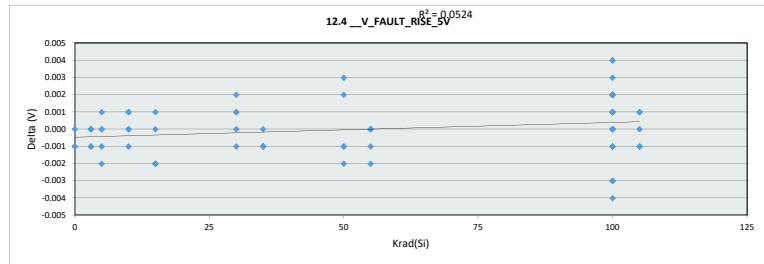
12.1 __V_FAULT_R		0	3	5	10	15	30	35	50	55	100	105
LL		0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min		0.606	0.608	0.600	0.604	0.606	0.602	0.600	0.600	0.606	0.603	0.600
Average		0.608	0.609	0.607	0.608	0.611	0.609	0.604	0.608	0.610	0.609	0.607
Max		0.610	0.610	0.611	0.613	0.620	0.614	0.611	0.617	0.616	0.620	0.615
UL		0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



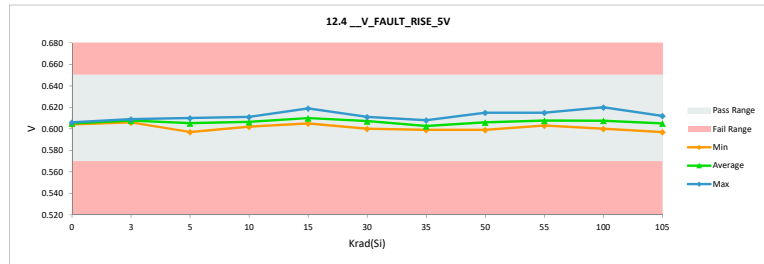


HDR TID Report  
TPS7H5004-SP

12.4 __V_FAULT_RISE_5V				
Test Site	Tester	Test Number	Unit	
			V	V
			Max Limit	0.65
			Min Limit	0.57
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.605	0.604	-0.001
0	297	0.607	0.606	-0.001
0	298	0.605	0.605	0.000
3	192	0.608	0.607	-0.001
3	193	0.607	0.607	0.000
3	194	0.609	0.609	0.000
3	196	0.609	0.609	0.000
3	197	0.607	0.606	-0.001
5	200	0.599	0.597	-0.002
5	202	0.610	0.610	0.000
5	203	0.606	0.605	-0.001
5	204	0.609	0.609	0.000
5	206	0.604	0.605	0.001
10	207	0.604	0.605	0.001
10	209	0.608	0.608	0.000
10	210	0.605	0.606	0.001
10	211	0.611	0.611	0.000
10	212	0.603	0.602	-0.001
15	214	0.608	0.606	-0.002
15	217	0.618	0.619	0.001
15	218	0.612	0.610	-0.002
15	219	0.605	0.605	0.000
15	222	0.612	0.610	-0.002
30	224	0.608	0.610	0.002
30	225	0.607	0.607	0.000
30	226	0.607	0.608	0.001
30	227	0.612	0.611	-0.001
30	229	0.599	0.600	0.001
35	230	0.606	0.605	-0.001
35	231	0.601	0.600	-0.001
35	232	0.602	0.601	-0.001
35	234	0.600	0.599	-0.001
35	237	0.608	0.608	0.000
50	272	0.607	0.609	0.002
50	275	0.600	0.599	-0.001
50	276	0.602	0.601	-0.001
50	277	0.608	0.606	-0.002
50	278	0.612	0.615	0.003
55	279	0.617	0.615	-0.002
55	280	0.608	0.608	0.000
55	281	0.604	0.603	-0.001
55	282	0.605	0.605	0.000
55	283	0.607	0.607	0.000
100	317	0.608	0.611	0.003
100	318	0.606	0.608	0.002
100	319	0.610	0.610	0.000
100	320	0.613	0.614	0.001
100	321	0.605	0.606	0.001
100	322	0.607	0.609	0.002
100	323	0.616	0.616	0.000
100	324	0.604	0.603	-0.001
100	326	0.604	0.604	0.000
100	327	0.603	0.600	-0.003
100	328	0.607	0.607	0.000
100	329	0.608	0.612	0.004
100	332	0.606	0.605	-0.001
100	333	0.597	0.601	0.004
100	334	0.610	0.612	0.002
100	335	0.609	0.610	0.001
100	336	0.608	0.608	0.000
100	337	0.604	0.600	-0.004
100	338	0.604	0.606	0.002
100	339	0.604	0.603	-0.001
100	340	0.611	0.608	-0.003
100	341	0.605	0.606	0.001
100	342	0.600	0.602	0.002
100	343	0.618	0.620	0.002
105	344	0.611	0.612	0.001
105	346	0.606	0.606	0.000
105	349	0.601	0.600	-0.001
105	350	0.609	0.610	0.001
105	351	0.606	0.605	-0.001
105	352	0.598	0.597	-0.001
	Max	0.618	0.620	0.004
	Average	0.607	0.607	0.000
	Min	0.597	0.597	-0.004
	Std Dev	0.004	0.005	0.002

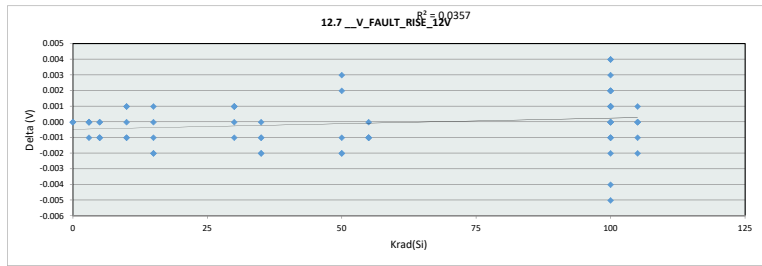


12.4 __V_FAULT_R											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			0.65	V							
			0.57	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.604	0.606	0.597	0.602	0.605	0.600	0.599	0.599	0.603	0.600	0.597
Average	0.605	0.608	0.605	0.606	0.610	0.607	0.603	0.606	0.608	0.608	0.605
Max	0.606	0.609	0.610	0.611	0.619	0.611	0.608	0.615	0.615	0.620	0.612
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

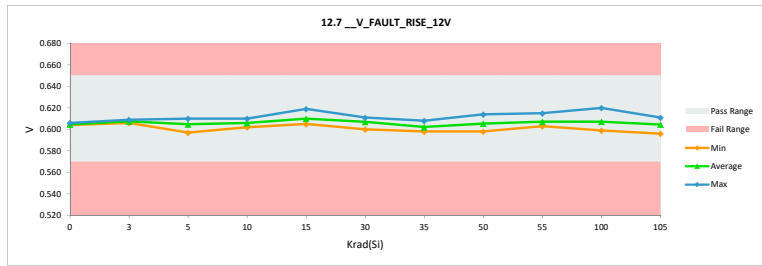


HDR TID Report  
TPS7H5004-SP

12.7 __V_FAULT_RISE_12V				
Test Site	Tester	Test Number	Unit	
			V	V
			Max Limit	0.65
			Min Limit	0.57
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.604	0.604	0.000
0	297	0.606	0.606	0.000
0	298	0.604	0.604	0.000
3	192	0.607	0.607	0.000
3	193	0.606	0.606	0.000
3	194	0.609	0.609	0.000
3	196	0.609	0.608	-0.001
3	197	0.607	0.607	0.000
5	200	0.598	0.597	-0.001
5	202	0.610	0.610	0.000
5	203	0.606	0.605	-0.001
5	204	0.609	0.608	-0.001
5	206	0.604	0.604	0.000
10	207	0.604	0.605	0.001
10	209	0.608	0.608	0.000
10	210	0.604	0.605	0.001
10	211	0.611	0.610	-0.001
10	212	0.603	0.602	-0.001
15	214	0.608	0.606	-0.002
15	217	0.618	0.619	0.001
15	218	0.612	0.610	-0.002
15	219	0.605	0.605	0.000
15	222	0.611	0.610	-0.001
30	224	0.608	0.609	0.001
30	225	0.607	0.607	0.000
30	226	0.607	0.608	0.001
30	227	0.612	0.611	-0.001
30	229	0.599	0.600	0.001
35	230	0.606	0.604	-0.002
35	231	0.601	0.600	-0.001
35	232	0.602	0.601	-0.001
35	234	0.600	0.598	-0.002
35	237	0.608	0.608	0.000
50	272	0.607	0.609	0.002
50	275	0.600	0.598	-0.002
50	276	0.601	0.600	-0.001
50	277	0.608	0.606	-0.002
50	278	0.611	0.614	0.003
55	279	0.616	0.615	-0.001
55	280	0.608	0.608	0.000
55	281	0.604	0.603	-0.001
55	282	0.605	0.604	-0.001
55	283	0.607	0.606	-0.001
100	317	0.608	0.611	0.003
100	318	0.606	0.607	0.001
100	319	0.610	0.610	0.000
100	320	0.613	0.614	0.001
100	321	0.605	0.606	0.001
100	322	0.607	0.609	0.002
100	323	0.615	0.616	0.001
100	324	0.603	0.603	0.000
100	326	0.604	0.603	-0.001
100	327	0.603	0.599	-0.004
100	328	0.607	0.607	0.000
100	329	0.607	0.611	0.004
100	332	0.606	0.605	-0.001
100	333	0.597	0.601	0.004
100	334	0.610	0.611	0.001
100	335	0.609	0.610	0.001
100	336	0.607	0.607	0.000
100	337	0.604	0.599	-0.005
100	338	0.604	0.606	0.002
100	339	0.604	0.603	-0.001
100	340	0.610	0.608	-0.002
100	341	0.604	0.605	0.001
100	342	0.600	0.602	0.002
100	343	0.618	0.620	0.002
105	344	0.610	0.611	0.001
105	346	0.606	0.606	0.000
105	349	0.601	0.599	-0.002
105	350	0.609	0.609	0.000
105	351	0.606	0.606	0.000
105	352	0.597	0.596	-0.001
	Max	0.618	0.620	0.004
	Average	0.606	0.606	0.000
	Min	0.597	0.596	-0.005
	Std Dev	0.004	0.005	0.002

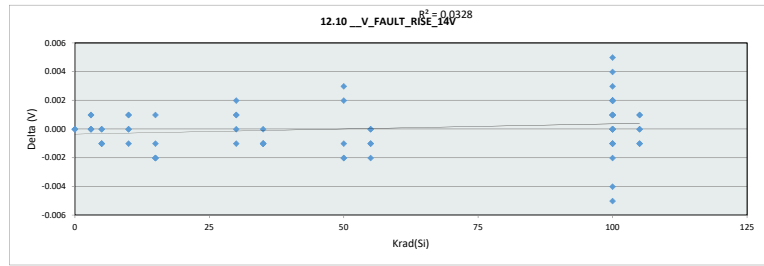


12.7 __V_FAULT_R											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			0.65	0.57	V						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.604	0.606	0.597	0.602	0.605	0.600	0.598	0.598	0.603	0.599	0.596
Average	0.605	0.607	0.605	0.606	0.610	0.607	0.602	0.605	0.607	0.607	0.605
Max	0.606	0.609	0.610	0.610	0.619	0.611	0.608	0.614	0.615	0.620	0.611
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

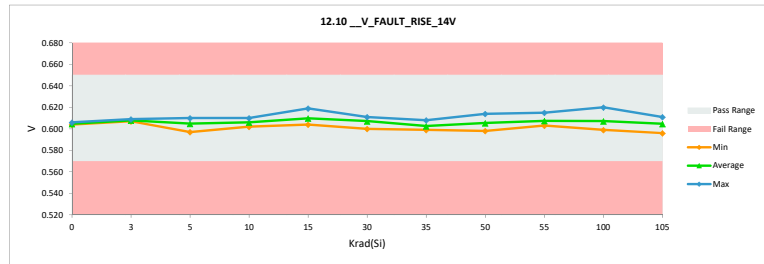


HDR TID Report  
TPS7H5004-SP

		12.10 __V_FAULT_RISE_14V		
Test Site	Tester			
Test Number	Unit	V	V	
Max Limit	0.65		0.65	
Min Limit	0.57		0.57	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.604	0.604	0.000
0	297	0.606	0.606	0.000
0	298	0.604	0.604	0.000
3	192	0.607	0.607	0.000
3	193	0.606	0.607	0.001
3	194	0.609	0.609	0.000
3	196	0.609	0.609	0.000
3	197	0.606	0.607	0.001
5	200	0.598	0.597	-0.001
5	202	0.610	0.610	0.000
5	203	0.606	0.605	-0.001
5	204	0.609	0.608	-0.001
5	206	0.604	0.604	0.000
10	207	0.604	0.605	0.001
10	209	0.608	0.608	0.000
10	210	0.604	0.605	0.001
10	211	0.610	0.610	0.000
10	212	0.603	0.602	-0.001
15	214	0.608	0.606	-0.002
15	217	0.618	0.619	0.001
15	218	0.612	0.610	-0.002
15	219	0.605	0.604	-0.001
15	222	0.612	0.610	-0.002
30	224	0.608	0.610	0.002
30	225	0.607	0.607	0.000
30	226	0.607	0.608	0.001
30	227	0.612	0.611	-0.001
30	229	0.599	0.600	0.001
35	230	0.606	0.605	-0.001
35	231	0.601	0.600	-0.001
35	232	0.602	0.601	-0.001
35	234	0.600	0.599	-0.001
35	237	0.608	0.608	0.000
50	272	0.607	0.609	0.002
50	275	0.600	0.598	-0.002
50	276	0.601	0.600	-0.001
50	277	0.608	0.606	-0.002
50	278	0.611	0.614	0.003
55	279	0.617	0.615	-0.002
55	280	0.608	0.608	0.000
55	281	0.604	0.603	-0.001
55	282	0.605	0.605	0.000
55	283	0.607	0.606	-0.001
100	317	0.608	0.610	0.002
100	318	0.606	0.608	0.002
100	319	0.610	0.610	0.000
100	320	0.613	0.614	0.001
100	321	0.605	0.606	0.001
100	322	0.607	0.609	0.002
100	323	0.615	0.615	0.000
100	324	0.604	0.603	-0.001
100	326	0.603	0.603	0.000
100	327	0.603	0.599	-0.004
100	328	0.607	0.606	-0.001
100	329	0.607	0.612	0.005
100	332	0.605	0.605	0.000
100	333	0.597	0.601	0.004
100	334	0.610	0.611	0.001
100	335	0.609	0.610	0.001
100	336	0.607	0.607	0.000
100	337	0.604	0.599	-0.005
100	338	0.603	0.606	0.003
100	339	0.604	0.603	-0.001
100	340	0.610	0.608	-0.002
100	341	0.604	0.606	0.002
100	342	0.600	0.602	0.002
100	343	0.618	0.620	0.002
105	344	0.610	0.611	0.001
105	346	0.606	0.606	0.000
105	349	0.600	0.599	-0.001
105	350	0.609	0.610	0.001
105	351	0.606	0.606	0.000
105	352	0.597	0.596	-0.001
	Max	0.618	0.620	0.005
	Average	0.606	0.606	0.000
	Min	0.597	0.596	-0.005
	Std Dev	0.004	0.005	0.002



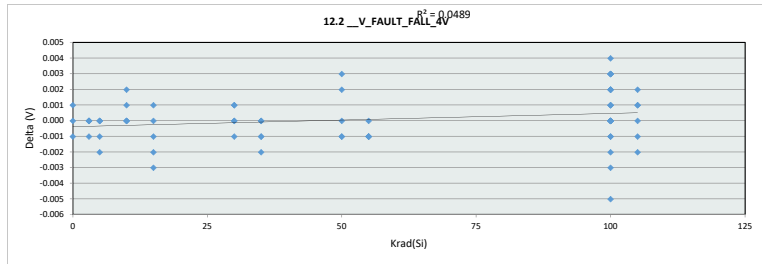
		12.10 __V_FAULT										
Test Site	Tester											
Test Number	Unit	V	V									
Max Limit	0.65		0.65									
Min Limit	0.57		0.57									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.604	0.607	0.597	0.602	0.604	0.600	0.599	0.598	0.603	0.599	0.596	0.570
Average	0.605	0.608	0.605	0.606	0.610	0.607	0.603	0.605	0.607	0.607	0.605	0.605
Max	0.606	0.609	0.610	0.610	0.619	0.611	0.608	0.614	0.615	0.620	0.611	0.611
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



HDR TID Report  
TPS7H5004-SP

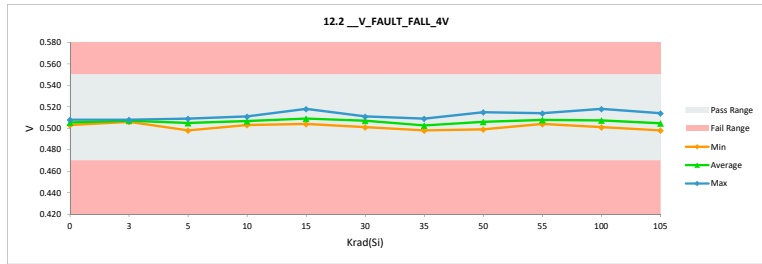
12.2 __V_FAULT_FALL_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(S)	Serial #	PRE	POST	Delta
0	295	0.504	0.503	-0.001
0	297	0.507	0.508	0.001
0	298	0.505	0.505	0.000
3	192	0.507	0.507	0.000
3	193	0.507	0.507	0.000
3	194	0.508	0.508	0.000
3	196	0.508	0.507	-0.001
3	197	0.506	0.506	0.000
5	200	0.500	0.498	-0.002
5	202	0.509	0.509	0.000
5	203	0.506	0.505	-0.001
5	204	0.508	0.508	0.000
5	206	0.505	0.505	0.000
10	207	0.504	0.505	0.001
10	209	0.507	0.507	0.000
10	210	0.506	0.508	0.002
10	211	0.511	0.511	0.000
10	212	0.503	0.503	0.000
15	214	0.507	0.505	-0.002
15	217	0.517	0.518	0.001
15	218	0.511	0.508	-0.003
15	219	0.504	0.504	0.000
15	222	0.511	0.510	-0.001
30	224	0.508	0.509	0.001
30	225	0.507	0.507	0.000
30	226	0.507	0.507	0.000
30	227	0.512	0.511	-0.001
30	229	0.500	0.501	0.001
35	230	0.506	0.505	-0.001
35	231	0.501	0.500	-0.001
35	232	0.501	0.501	0.000
35	234	0.500	0.498	-0.002
35	237	0.509	0.509	0.000
50	272	0.507	0.509	0.002
50	275	0.500	0.499	-0.001
50	276	0.502	0.502	0.000
50	277	0.506	0.505	-0.001
50	278	0.512	0.515	0.003
55	279	0.515	0.514	-0.001
55	280	0.509	0.509	0.000
55	281	0.505	0.504	-0.001
55	282	0.506	0.505	-0.001
55	283	0.508	0.507	-0.001
100	317	0.507	0.509	0.002
100	318	0.507	0.508	0.001
100	319	0.510	0.510	0.000
100	320	0.514	0.514	0.000
100	321	0.504	0.505	0.001
100	322	0.506	0.509	0.003
100	323	0.513	0.513	0.000
100	324	0.504	0.504	0.000
100	326	0.504	0.504	0.000
100	327	0.504	0.501	-0.003
100	328	0.507	0.507	0.000
100	329	0.507	0.511	0.004
100	332	0.505	0.505	0.000
100	333	0.499	0.502	0.003
100	334	0.510	0.512	0.002
100	335	0.508	0.509	0.001
100	336	0.509	0.508	-0.001
100	337	0.506	0.501	-0.005
100	338	0.504	0.506	0.002
100	339	0.504	0.503	-0.001
100	340	0.510	0.508	-0.002
100	341	0.504	0.507	0.003
100	342	0.500	0.503	0.003
100	343	0.516	0.518	0.002
105	344	0.512	0.514	0.002
105	346	0.505	0.505	0.000
105	349	0.502	0.500	-0.002
105	350	0.506	0.507	0.001
105	351	0.503	0.504	0.001
105	352	0.499	0.498	-0.001
Max		0.517	0.518	0.004
Average		0.506	0.507	0.000
Min		0.499	0.498	-0.005
Std Dev		0.004	0.004	0.002



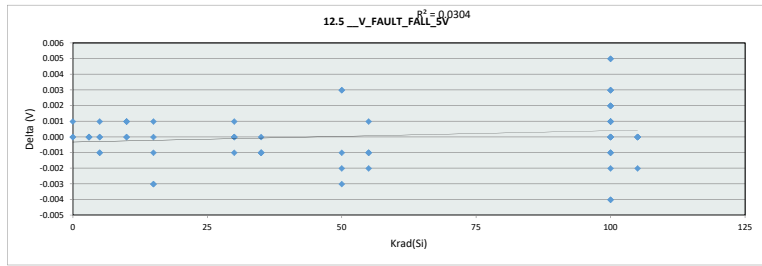
12.2 __V_FAULT_F	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.503	0.506	0.498	0.503	0.504	0.501	0.498	0.499	0.504	0.501	0.498
Average	0.505	0.507	0.505	0.507	0.509	0.507	0.503	0.506	0.508	0.507	0.505
Max	0.508	0.508	0.509	0.511	0.518	0.511	0.509	0.515	0.514	0.518	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550

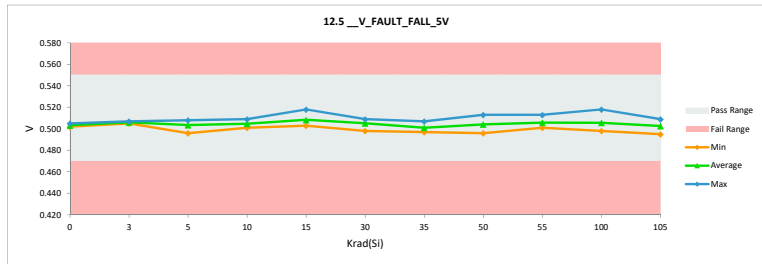


HDR TID Report  
TPS7H5004-SP

12.5 __V_FAULT_FALL_5V				
Test Site	Tester	Test Number	Unit	
			V	V
			Max Limit	0.55
			Min Limit	0.47
Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.502	0.502	0.000
0	297	0.504	0.505	0.001
0	298	0.503	0.503	0.000
3	192	0.506	0.506	0.000
3	193	0.505	0.505	0.000
3	194	0.507	0.507	0.000
3	196	0.507	0.507	0.000
3	197	0.505	0.505	0.000
5	200	0.497	0.496	-0.001
5	202	0.508	0.508	0.000
5	203	0.505	0.504	-0.001
5	204	0.507	0.507	0.000
5	206	0.502	0.503	0.001
10	207	0.502	0.503	0.001
10	209	0.506	0.507	0.001
10	210	0.503	0.504	0.001
10	211	0.509	0.509	0.000
10	212	0.501	0.501	0.000
15	214	0.507	0.504	-0.003
15	217	0.517	0.518	0.001
15	218	0.511	0.508	-0.003
15	219	0.503	0.503	0.000
15	222	0.510	0.509	-0.001
30	224	0.507	0.508	0.001
30	225	0.505	0.505	0.000
30	226	0.506	0.506	0.000
30	227	0.510	0.509	-0.001
30	229	0.498	0.498	0.000
35	230	0.504	0.503	-0.001
35	231	0.500	0.499	-0.001
35	232	0.500	0.499	-0.001
35	234	0.498	0.497	-0.001
35	237	0.507	0.507	0.000
50	272	0.505	0.508	0.003
50	275	0.499	0.496	-0.003
50	276	0.500	0.499	-0.001
50	277	0.507	0.505	-0.002
50	278	0.510	0.513	0.003
55	279	0.515	0.513	-0.002
55	280	0.506	0.507	0.001
55	281	0.502	0.501	-0.001
55	282	0.504	0.503	-0.001
55	283	0.506	0.505	-0.001
100	317	0.506	0.509	0.003
100	318	0.504	0.506	0.002
100	319	0.508	0.508	0.000
100	320	0.511	0.512	0.001
100	321	0.503	0.504	0.001
100	322	0.506	0.507	0.001
100	323	0.514	0.514	0.000
100	324	0.502	0.502	0.000
100	326	0.502	0.502	0.000
100	327	0.502	0.498	-0.004
100	328	0.506	0.505	-0.001
100	329	0.505	0.510	0.005
100	332	0.504	0.503	-0.001
100	333	0.495	0.500	0.005
100	334	0.508	0.510	0.002
100	335	0.507	0.508	0.001
100	336	0.506	0.506	0.000
100	337	0.502	0.498	-0.004
100	338	0.502	0.505	0.003
100	339	0.502	0.501	-0.001
100	340	0.508	0.506	-0.002
100	341	0.502	0.504	0.002
100	342	0.498	0.500	0.002
100	343	0.515	0.518	0.003
105	344	0.509	0.509	0.000
105	346	0.504	0.504	0.000
105	349	0.499	0.497	-0.002
105	350	0.507	0.507	0.000
105	351	0.504	0.504	0.000
105	352	0.495	0.495	0.000
	Max	0.517	0.518	0.005
	Average	0.505	0.505	0.000
	Min	0.495	0.495	-0.004
	Std Dev	0.004	0.005	0.002



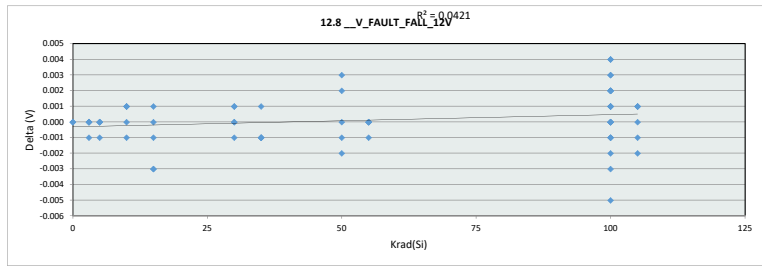
12.5 __V_FAULT_F											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			0.55	0.47							
			V	V							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.502	0.505	0.496	0.501	0.503	0.498	0.497	0.496	0.501	0.498	0.495
Average	0.503	0.506	0.504	0.505	0.508	0.505	0.501	0.504	0.506	0.506	0.503
Max	0.505	0.507	0.508	0.509	0.518	0.509	0.507	0.513	0.513	0.518	0.509
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



HDR TID Report  
TPS7H5004-SP

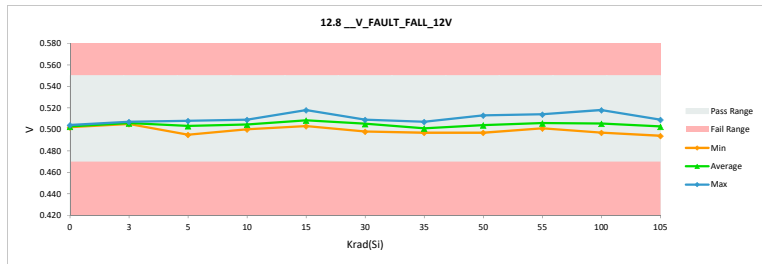
12.8 __V_FAULT_FALL_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.502	0.502	0.000
0	297	0.504	0.504	0.000
0	298	0.502	0.502	0.000
3	192	0.506	0.505	-0.001
3	193	0.505	0.505	0.000
3	194	0.507	0.507	0.000
3	196	0.507	0.507	0.000
3	197	0.505	0.505	0.000
5	200	0.496	0.495	-0.001
5	202	0.508	0.508	0.000
5	203	0.504	0.504	0.000
5	204	0.507	0.507	0.000
5	206	0.502	0.502	0.000
10	207	0.502	0.503	0.001
10	209	0.506	0.507	0.001
10	210	0.503	0.504	0.001
10	211	0.509	0.509	0.000
10	212	0.501	0.500	-0.001
15	214	0.507	0.504	-0.003
15	217	0.517	0.518	0.001
15	218	0.511	0.508	-0.003
15	219	0.503	0.503	0.000
15	222	0.510	0.509	-0.001
30	224	0.507	0.508	0.001
30	225	0.505	0.505	0.000
30	226	0.506	0.506	0.000
30	227	0.510	0.509	-0.001
30	229	0.497	0.498	0.001
35	230	0.504	0.503	-0.001
35	231	0.500	0.499	-0.001
35	232	0.500	0.499	-0.001
35	234	0.498	0.497	-0.001
35	237	0.506	0.507	0.001
50	272	0.505	0.507	0.002
50	275	0.498	0.497	-0.001
50	276	0.499	0.499	0.000
50	277	0.506	0.504	-0.002
50	278	0.510	0.513	0.003
55	279	0.514	0.514	0.000
55	280	0.506	0.506	0.000
55	281	0.502	0.501	-0.001
55	282	0.503	0.503	0.000
55	283	0.505	0.505	0.000
100	317	0.506	0.509	0.003
100	318	0.504	0.506	0.002
100	319	0.508	0.508	0.000
100	320	0.511	0.512	0.001
100	321	0.503	0.504	0.001
100	322	0.505	0.507	0.002
100	323	0.514	0.514	0.000
100	324	0.501	0.501	0.000
100	326	0.502	0.501	-0.001
100	327	0.501	0.498	-0.003
100	328	0.505	0.505	0.000
100	329	0.506	0.510	0.004
100	332	0.504	0.503	-0.001
100	333	0.495	0.499	0.004
100	334	0.508	0.509	0.001
100	335	0.507	0.508	0.001
100	336	0.505	0.505	0.000
100	337	0.502	0.497	-0.005
100	338	0.501	0.504	0.003
100	339	0.502	0.501	-0.001
100	340	0.508	0.506	-0.002
100	341	0.502	0.504	0.002
100	342	0.498	0.500	0.002
100	343	0.516	0.518	0.002
105	344	0.508	0.509	0.001
105	346	0.504	0.504	0.000
105	349	0.499	0.497	-0.002
105	350	0.507	0.508	0.001
105	351	0.503	0.504	0.001
105	352	0.495	0.494	-0.001
Max		0.517	0.518	0.004
Average		0.505	0.505	0.000
Min		0.495	0.494	-0.005
Std Dev		0.004	0.005	0.002



12.8 __V_FAULT_F	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

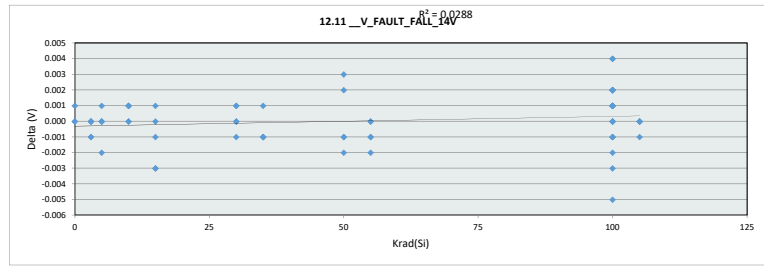
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.502	0.505	0.495	0.500	0.503	0.498	0.497	0.497	0.501	0.497	0.494
Average	0.503	0.506	0.503	0.505	0.508	0.505	0.501	0.504	0.506	0.505	0.503
Max	0.504	0.507	0.508	0.509	0.518	0.509	0.507	0.513	0.514	0.518	0.509
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



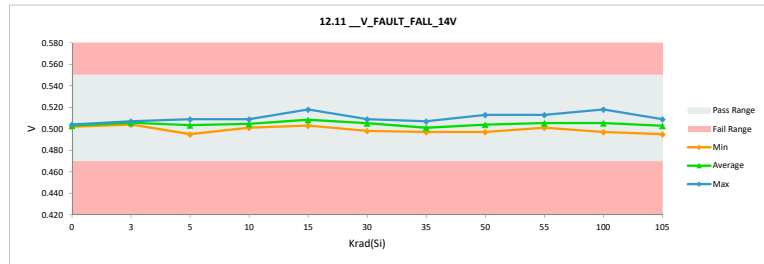
HDR TID Report  
TPS7H5004-SP

12.11 __V_FAULT_FALL_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.55	0.55		
Min Limit	0.47	0.47		

Krad(S)	Serial #	PRE	POST	Delta
0	295	0.502	0.502	0.000
0	297	0.504	0.504	0.000
0	298	0.502	0.503	0.001
3	192	0.506	0.505	-0.001
3	193	0.505	0.505	0.000
3	194	0.507	0.507	0.000
3	196	0.507	0.507	0.000
3	197	0.505	0.504	-0.001
5	200	0.497	0.495	-0.002
5	202	0.508	0.509	0.001
5	203	0.504	0.504	0.000
5	204	0.507	0.507	0.000
5	206	0.502	0.502	0.000
10	207	0.502	0.503	0.001
10	209	0.506	0.507	0.001
10	210	0.502	0.503	0.001
10	211	0.509	0.509	0.000
10	212	0.501	0.501	0.000
15	214	0.507	0.504	-0.003
15	217	0.517	0.518	0.001
15	218	0.511	0.508	-0.003
15	219	0.503	0.503	0.000
15	222	0.510	0.509	-0.001
30	224	0.507	0.508	0.001
30	225	0.505	0.505	0.000
30	226	0.506	0.506	0.000
30	227	0.510	0.509	-0.001
30	229	0.497	0.498	0.001
35	230	0.504	0.503	-0.001
35	231	0.500	0.499	-0.001
35	232	0.500	0.499	-0.001
35	234	0.498	0.497	-0.001
35	237	0.506	0.507	0.001
50	272	0.505	0.507	0.002
50	275	0.498	0.497	-0.001
50	276	0.499	0.498	-0.001
50	277	0.506	0.504	-0.002
50	278	0.510	0.513	0.003
55	279	0.515	0.513	-0.002
55	280	0.506	0.506	0.000
55	281	0.502	0.501	-0.001
55	282	0.503	0.503	0.000
55	283	0.505	0.504	-0.001
100	317	0.506	0.508	0.002
100	318	0.504	0.506	0.002
100	319	0.508	0.508	0.000
100	320	0.511	0.513	0.002
100	321	0.503	0.504	0.001
100	322	0.506	0.507	0.001
100	323	0.513	0.514	0.001
100	324	0.502	0.502	0.000
100	326	0.502	0.501	-0.001
100	327	0.501	0.498	-0.003
100	328	0.505	0.505	0.000
100	329	0.506	0.510	0.004
100	332	0.504	0.503	-0.001
100	333	0.495	0.499	0.004
100	334	0.508	0.509	0.001
100	335	0.507	0.508	0.001
100	336	0.505	0.505	0.000
100	337	0.502	0.497	-0.005
100	338	0.502	0.504	0.002
100	339	0.502	0.501	-0.001
100	340	0.508	0.506	-0.002
100	341	0.503	0.504	0.001
100	342	0.498	0.500	0.002
100	343	0.516	0.518	0.002
105	344	0.509	0.509	0.000
105	346	0.504	0.504	0.000
105	349	0.499	0.498	-0.001
105	350	0.507	0.507	0.000
105	351	0.504	0.504	0.000
105	352	0.495	0.495	0.000
Max		0.517	0.518	0.004
Average		0.505	0.505	0.000
Min		0.495	0.495	-0.005
Std Dev		0.004	0.005	0.002



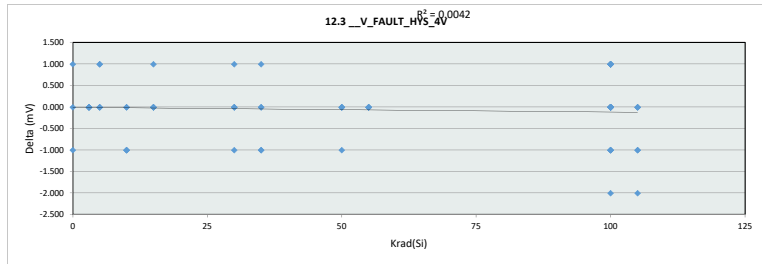
12.11 __V_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	0.55	V									
Min Limit	0.47	V									
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.502	0.504	0.495	0.501	0.503	0.498	0.497	0.497	0.501	0.497	0.495
Average	0.503	0.506	0.503	0.505	0.508	0.505	0.501	0.504	0.505	0.505	0.503
Max	0.504	0.507	0.509	0.509	0.518	0.509	0.507	0.513	0.513	0.518	0.509
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



HDR TID Report  
TPS7H5004-SP

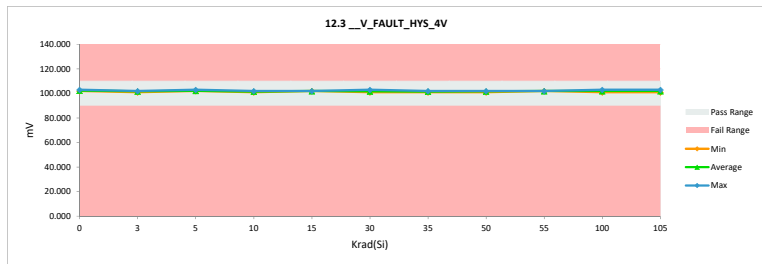
12.3 __V_FAULT_HYS_4V	
Test Site	
Tester	
Test Number	
Unit	mV mV
Max Limit	110 110
Min Limit	90 90

Krad(Si)	Serial #	PRE	POST	Delta
0	295	102.000	103.000	1.000
0	297	103.000	102.000	-1.000
0	298	102.000	102.000	0.000
3	192	102.000	102.000	0.000
3	193	101.000	101.000	0.000
3	194	102.000	102.000	0.000
3	196	102.000	102.000	0.000
3	197	102.000	102.000	0.000
5	200	101.000	102.000	1.000
5	202	102.000	102.000	0.000
5	203	102.000	102.000	0.000
5	204	102.000	102.000	0.000
5	206	102.000	103.000	1.000
10	207	103.000	102.000	-1.000
10	209	102.000	102.000	0.000
10	210	102.000	101.000	-1.000
10	211	102.000	102.000	0.000
10	212	102.000	101.000	-1.000
15	214	102.000	102.000	0.000
15	217	102.000	102.000	0.000
15	218	101.000	102.000	1.000
15	219	102.000	102.000	0.000
15	222	102.000	102.000	0.000
30	224	101.000	101.000	0.000
30	225	102.000	102.000	0.000
30	226	101.000	102.000	1.000
30	227	103.000	103.000	0.000
30	229	102.000	101.000	-1.000
35	230	102.000	102.000	0.000
35	231	102.000	101.000	-1.000
35	232	102.000	101.000	-1.000
35	234	101.000	102.000	1.000
35	237	102.000	102.000	0.000
50	272	102.000	102.000	0.000
50	275	102.000	101.000	-1.000
50	276	102.000	102.000	0.000
50	277	102.000	102.000	0.000
50	278	102.000	102.000	0.000
55	279	102.000	102.000	0.000
55	280	102.000	102.000	0.000
55	281	102.000	102.000	0.000
55	282	102.000	102.000	0.000
55	283	102.000	102.000	0.000
100	317	102.000	102.000	0.000
100	318	101.000	102.000	1.000
100	319	102.000	103.000	1.000
100	320	101.000	102.000	1.000
100	321	103.000	102.000	-1.000
100	322	102.000	102.000	0.000
100	323	101.000	102.000	1.000
100	324	102.000	102.000	0.000
100	326	102.000	103.000	1.000
100	327	102.000	102.000	0.000
100	328	102.000	102.000	0.000
100	329	102.000	101.000	-1.000
100	332	102.000	102.000	0.000
100	333	101.000	102.000	1.000
100	334	102.000	102.000	0.000
100	335	102.000	102.000	0.000
100	336	102.000	102.000	0.000
100	337	102.000	103.000	1.000
100	338	102.000	102.000	0.000
100	339	102.000	102.000	0.000
100	340	103.000	101.000	-2.000
100	341	103.000	102.000	-1.000
100	342	102.000	102.000	0.000
100	343	103.000	102.000	-1.000
105	344	103.000	101.000	-2.000
105	346	103.000	103.000	0.000
105	349	102.000	102.000	0.000
105	350	103.000	102.000	-1.000
105	351	103.000	102.000	-1.000
105	352	102.000	102.000	0.000
Max		103.000	103.000	1.000
Average		102.014	101.945	-0.068
Min		101.000	101.000	-2.000
Std Dev		0.540	0.497	0.694



12.3 __V_FAULT_H	
Test Site	
Tester	
Test Number	
Max Limit	110 mV
Min Limit	90 mV

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	101.000	102.000	101.000	102.000	101.000	101.000	101.000	102.000	101.000	101.000
Average	102.333	101.800	102.200	101.600	102.000	101.800	101.600	101.800	102.000	102.042	102.000
Max	103.000	102.000	103.000	102.000	102.000	103.000	102.000	102.000	102.000	103.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

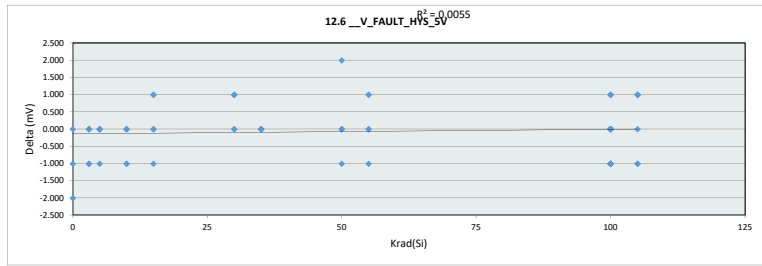




HDR TID Report  
TPS7H5004-SP

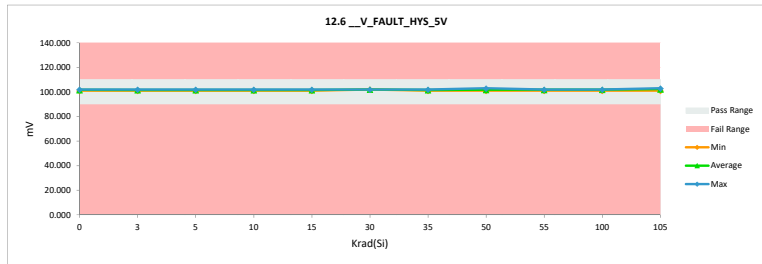
		12.6 __V_FAULT_HYS_5V	
Test Site			
Tester			
Test Number			
Unit	mV		mV
Max Limit	110		110
Min Limit	90		90

Krad(Si)	Serial #	PRE	POST	Delta
0	295	103.000	102.000	-1.000
0	297	103.000	101.000	-2.000
0	298	102.000	102.000	0.000
3	192	102.000	101.000	-1.000
3	193	102.000	102.000	0.000
3	194	102.000	102.000	0.000
3	196	102.000	102.000	0.000
3	197	102.000	101.000	-1.000
5	200	102.000	101.000	-1.000
5	202	102.000	102.000	0.000
5	203	101.000	101.000	0.000
5	204	102.000	102.000	0.000
5	206	102.000	102.000	0.000
10	207	102.000	102.000	0.000
10	209	102.000	101.000	-1.000
10	210	102.000	102.000	0.000
10	211	102.000	102.000	0.000
10	212	102.000	101.000	-1.000
15	214	101.000	102.000	1.000
15	217	101.000	101.000	0.000
15	218	101.000	102.000	1.000
15	219	102.000	102.000	0.000
15	222	102.000	101.000	-1.000
30	224	101.000	102.000	1.000
30	225	102.000	102.000	0.000
30	226	101.000	102.000	1.000
30	227	102.000	102.000	0.000
30	229	101.000	102.000	1.000
35	230	102.000	102.000	0.000
35	231	101.000	101.000	0.000
35	232	102.000	102.000	0.000
35	234	102.000	102.000	0.000
35	237	101.000	101.000	0.000
50	272	102.000	101.000	-1.000
50	275	101.000	103.000	2.000
50	276	102.000	102.000	0.000
50	277	101.000	101.000	0.000
50	278	102.000	102.000	0.000
55	279	102.000	102.000	0.000
55	280	102.000	101.000	-1.000
55	281	102.000	102.000	0.000
55	282	101.000	102.000	1.000
55	283	101.000	102.000	1.000
100	317	102.000	102.000	0.000
100	318	102.000	102.000	0.000
100	319	102.000	102.000	0.000
100	320	102.000	102.000	0.000
100	321	102.000	102.000	0.000
100	322	101.000	102.000	1.000
100	323	102.000	102.000	0.000
100	324	102.000	101.000	-1.000
100	326	102.000	102.000	0.000
100	327	101.000	102.000	1.000
100	328	101.000	102.000	1.000
100	329	103.000	102.000	-1.000
100	332	102.000	102.000	0.000
100	333	102.000	101.000	-1.000
100	334	102.000	102.000	0.000
100	335	102.000	102.000	0.000
100	336	102.000	102.000	0.000
100	337	102.000	102.000	0.000
100	338	102.000	101.000	-1.000
100	339	102.000	102.000	0.000
100	340	103.000	102.000	-1.000
100	341	103.000	102.000	-1.000
100	342	102.000	102.000	0.000
100	343	103.000	102.000	-1.000
105	344	102.000	103.000	1.000
105	346	102.000	102.000	0.000
105	349	102.000	103.000	1.000
105	350	102.000	103.000	1.000
105	351	102.000	101.000	-1.000
105	352	103.000	102.000	-1.000
Max		103.000	103.000	2.000
Average		101.877	101.808	-0.068
Min		101.000	101.000	-2.000
Std Dev		0.551	0.518	0.733



		12.6 __V_FAULT_H	
Test Site			
Tester			
Test Number			
Max Limit	110	mV	
Min Limit	90	mV	

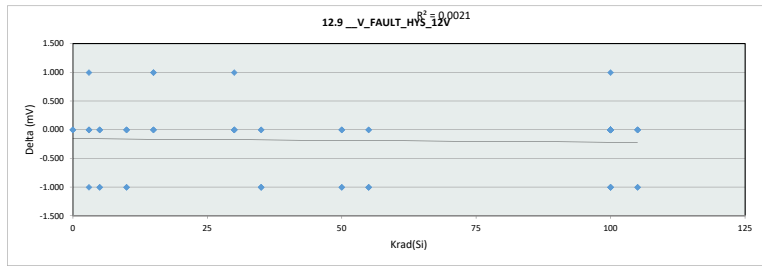
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	101.000	101.000	101.000	101.000	102.000	101.000	101.000	101.000	101.000	101.000
Average	101.667	101.600	101.600	101.600	101.600	102.000	101.600	101.800	101.800	101.875	102.333
Max	102.000	102.000	102.000	102.000	102.000	102.000	102.000	103.000	102.000	102.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



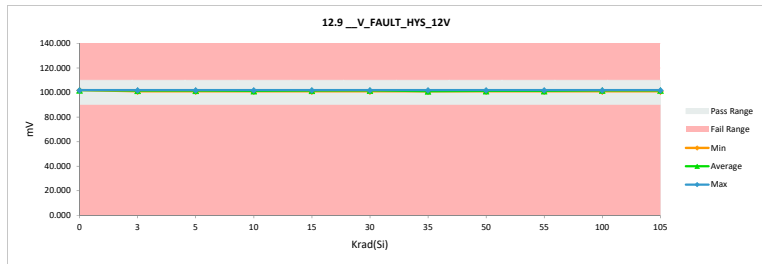
HDR TID Report  
TPS7H5004-SP

12.9 __V_FAULT_HYS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	295	102.000	102.000	0.000
0	297	102.000	102.000	0.000
0	298	102.000	102.000	0.000
3	192	101.000	102.000	1.000
3	193	101.000	101.000	0.000
3	194	102.000	102.000	0.000
3	196	102.000	101.000	-1.000
3	197	102.000	102.000	0.000
5	200	102.000	102.000	0.000
5	202	102.000	102.000	0.000
5	203	102.000	101.000	-1.000
5	204	102.000	101.000	-1.000
5	206	102.000	102.000	0.000
10	207	102.000	102.000	0.000
10	209	102.000	101.000	-1.000
10	210	101.000	101.000	0.000
10	211	102.000	101.000	-1.000
10	212	102.000	102.000	0.000
15	214	101.000	102.000	1.000
15	217	101.000	101.000	0.000
15	218	101.000	102.000	1.000
15	219	102.000	102.000	0.000
15	222	101.000	101.000	0.000
30	224	101.000	101.000	0.000
30	225	102.000	102.000	0.000
30	226	101.000	102.000	1.000
30	227	102.000	102.000	0.000
30	229	102.000	102.000	0.000
35	230	102.000	101.000	-1.000
35	231	101.000	101.000	0.000
35	232	102.000	102.000	0.000
35	234	102.000	101.000	-1.000
35	237	102.000	101.000	-1.000
50	272	102.000	102.000	0.000
50	275	102.000	101.000	-1.000
50	276	102.000	101.000	-1.000
50	277	102.000	102.000	0.000
50	278	101.000	101.000	0.000
55	279	102.000	101.000	-1.000
55	280	102.000	102.000	0.000
55	281	102.000	102.000	0.000
55	282	102.000	101.000	-1.000
55	283	102.000	101.000	-1.000
100	317	102.000	102.000	0.000
100	318	102.000	101.000	-1.000
100	319	102.000	102.000	0.000
100	320	102.000	102.000	0.000
100	321	102.000	102.000	0.000
100	322	102.000	102.000	0.000
100	323	101.000	102.000	1.000
100	324	102.000	102.000	0.000
100	326	102.000	102.000	0.000
100	327	102.000	101.000	-1.000
100	328	102.000	102.000	0.000
100	329	101.000	101.000	0.000
100	332	102.000	102.000	0.000
100	333	102.000	102.000	0.000
100	334	102.000	102.000	0.000
100	335	102.000	102.000	0.000
100	336	102.000	102.000	0.000
100	337	102.000	102.000	0.000
100	338	103.000	102.000	-1.000
100	339	102.000	102.000	0.000
100	340	102.000	102.000	0.000
100	341	102.000	101.000	-1.000
100	342	102.000	102.000	0.000
100	343	102.000	102.000	0.000
105	344	102.000	102.000	0.000
105	346	102.000	102.000	0.000
105	349	102.000	102.000	0.000
105	350	102.000	101.000	-1.000
105	351	103.000	102.000	-1.000
105	352	102.000	102.000	0.000
Max		103.000	102.000	1.000
Average		101.849	101.658	-0.192
Min		101.000	101.000	-1.000
Std Dev		0.430	0.478	0.544



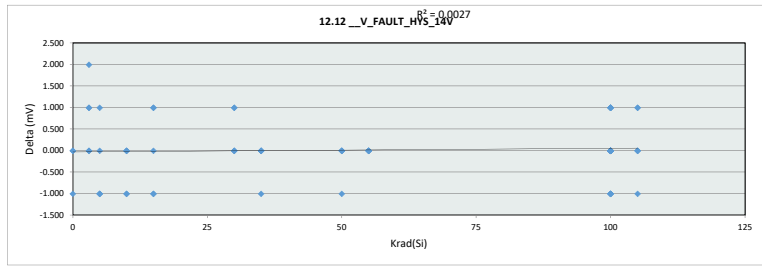
12.9 __V_FAULT_H		0	3	5	10	15	30	35	50	55	100	105
LL		90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min		102.000	101.000	101.000	101.000	101.000	101.000	101.000	101.000	101.000	101.000	101.000
Average		102.000	101.600	101.600	101.400	101.600	101.800	101.200	101.400	101.400	101.833	101.833
Max		102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000
UL		110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



HDR TID Report  
TPS7H5004-SP

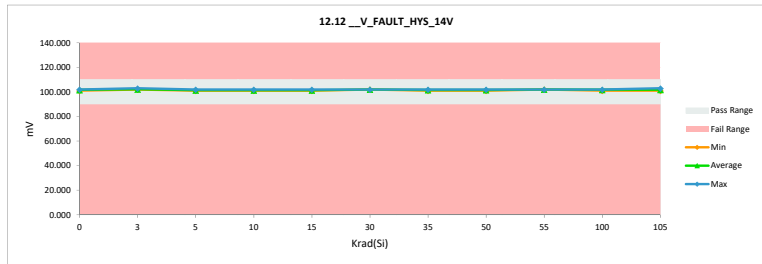
12.12 __V_FAULT_HYS_14V	
Test Site	
Tester	
Test Number	
Unit	mV mV
Max Limit	110 110
Min Limit	90 90

Krad(Si)	Serial #	PRE	POST	Delta
0	295	102.000	102.000	0.000
0	297	102.000	102.000	0.000
0	298	102.000	101.000	-1.000
3	192	101.000	102.000	1.000
3	193	101.000	102.000	1.000
3	194	102.000	102.000	0.000
3	196	102.000	102.000	0.000
3	197	101.000	103.000	2.000
5	200	101.000	102.000	1.000
5	202	102.000	101.000	-1.000
5	203	102.000	101.000	-1.000
5	204	102.000	101.000	-1.000
5	206	102.000	102.000	0.000
10	207	102.000	102.000	0.000
10	209	102.000	101.000	-1.000
10	210	102.000	102.000	0.000
10	211	101.000	101.000	0.000
10	212	102.000	101.000	-1.000
15	214	101.000	102.000	1.000
15	217	101.000	101.000	0.000
15	218	101.000	102.000	1.000
15	219	102.000	101.000	-1.000
15	222	102.000	101.000	-1.000
30	224	101.000	102.000	1.000
30	225	102.000	102.000	0.000
30	226	101.000	102.000	1.000
30	227	102.000	102.000	0.000
30	229	102.000	102.000	0.000
35	230	102.000	102.000	0.000
35	231	101.000	101.000	0.000
35	232	102.000	102.000	0.000
35	234	102.000	102.000	0.000
35	237	102.000	101.000	-1.000
50	272	102.000	102.000	0.000
50	275	102.000	101.000	-1.000
50	276	102.000	102.000	0.000
50	277	102.000	102.000	0.000
50	278	101.000	101.000	0.000
55	279	102.000	102.000	0.000
55	280	102.000	102.000	0.000
55	281	102.000	102.000	0.000
55	282	102.000	102.000	0.000
55	283	102.000	102.000	0.000
100	317	102.000	102.000	0.000
100	318	102.000	102.000	0.000
100	319	102.000	102.000	0.000
100	320	102.000	101.000	-1.000
100	321	102.000	102.000	0.000
100	322	101.000	102.000	1.000
100	323	102.000	101.000	-1.000
100	324	102.000	101.000	-1.000
100	326	101.000	102.000	1.000
100	327	102.000	101.000	-1.000
100	328	102.000	101.000	-1.000
100	329	101.000	102.000	1.000
100	332	101.000	102.000	1.000
100	333	102.000	102.000	0.000
100	334	102.000	102.000	0.000
100	335	102.000	102.000	0.000
100	336	102.000	102.000	0.000
100	337	102.000	102.000	0.000
100	338	101.000	102.000	1.000
100	339	102.000	102.000	0.000
100	340	102.000	102.000	0.000
100	341	101.000	102.000	1.000
100	342	102.000	102.000	0.000
100	343	102.000	102.000	0.000
105	344	101.000	102.000	1.000
105	346	102.000	102.000	0.000
105	349	101.000	101.000	0.000
105	350	102.000	103.000	1.000
105	351	102.000	102.000	0.000
105	352	102.000	101.000	-1.000
Max		102.000	103.000	2.000
Average		101.726	101.740	0.014
Min		101.000	101.000	-1.000
Std Dev		0.449	0.501	0.697



12.12 __V_FAULT	
Test Site	
Tester	
Test Number	
Max Limit	110 mV
Min Limit	90 mV

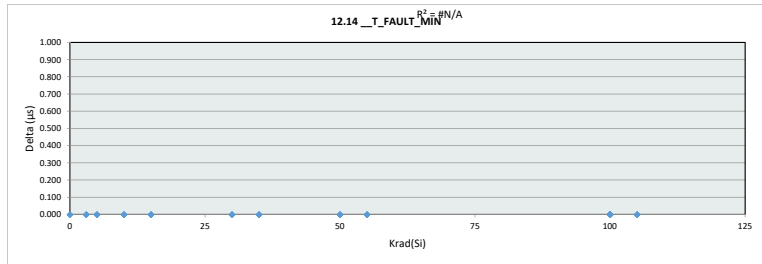
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	102.000	101.000	101.000	101.000	102.000	101.000	101.000	102.000	101.000	101.000
Average	101.667	102.200	101.400	101.400	101.400	102.000	101.600	101.600	102.000	101.792	101.833
Max	102.000	103.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	102.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



HDR TID Report  
TPS7H5004-SP

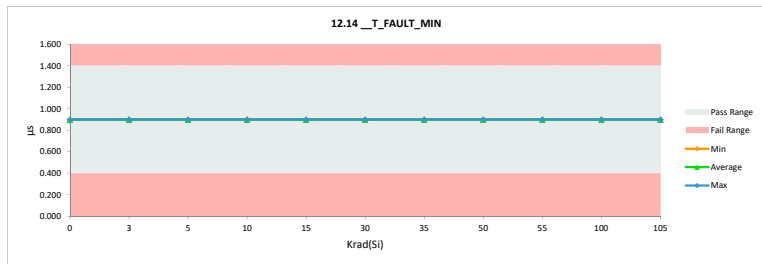
12.14 __T_FAULT_MIN	
Test Site	
Tester	
Test Number	
Unit	µs
Max Limit	1.4
Min Limit	0.4

Krad(Si)	Serial #	PRE	POST	Delta
0	295	0.900	0.900	0.000
0	297	0.900	0.900	0.000
0	298	0.900	0.900	0.000
3	192	0.900	0.900	0.000
3	193	0.900	0.900	0.000
3	194	0.900	0.900	0.000
3	196	0.900	0.900	0.000
3	197	0.900	0.900	0.000
5	200	0.900	0.900	0.000
5	202	0.900	0.900	0.000
5	203	0.900	0.900	0.000
5	204	0.900	0.900	0.000
5	206	0.900	0.900	0.000
10	207	0.900	0.900	0.000
10	209	0.900	0.900	0.000
10	210	0.900	0.900	0.000
10	211	0.900	0.900	0.000
10	212	0.900	0.900	0.000
15	214	0.900	0.900	0.000
15	217	0.900	0.900	0.000
15	218	0.900	0.900	0.000
15	219	0.900	0.900	0.000
15	222	0.900	0.900	0.000
30	224	0.900	0.900	0.000
30	225	0.900	0.900	0.000
30	226	0.900	0.900	0.000
30	227	0.900	0.900	0.000
30	229	0.900	0.900	0.000
35	230	0.900	0.900	0.000
35	231	0.900	0.900	0.000
35	232	0.900	0.900	0.000
35	234	0.900	0.900	0.000
35	237	0.900	0.900	0.000
50	272	0.900	0.900	0.000
50	275	0.900	0.900	0.000
50	276	0.900	0.900	0.000
50	277	0.900	0.900	0.000
50	278	0.900	0.900	0.000
55	279	0.900	0.900	0.000
55	280	0.900	0.900	0.000
55	281	0.900	0.900	0.000
55	282	0.900	0.900	0.000
55	283	0.900	0.900	0.000
100	317	0.900	0.900	0.000
100	318	0.900	0.900	0.000
100	319	0.900	0.900	0.000
100	320	0.900	0.900	0.000
100	321	0.900	0.900	0.000
100	322	0.900	0.900	0.000
100	323	0.900	0.900	0.000
100	324	0.900	0.900	0.000
100	326	0.900	0.900	0.000
100	327	0.900	0.900	0.000
100	328	0.900	0.900	0.000
100	329	0.900	0.900	0.000
100	332	0.900	0.900	0.000
100	333	0.900	0.900	0.000
100	334	0.900	0.900	0.000
100	335	0.900	0.900	0.000
100	336	0.900	0.900	0.000
100	337	0.900	0.900	0.000
100	338	0.900	0.900	0.000
100	339	0.900	0.900	0.000
100	340	0.900	0.900	0.000
100	341	0.900	0.900	0.000
100	342	0.900	0.900	0.000
100	343	0.900	0.900	0.000
105	344	0.900	0.900	0.000
105	346	0.900	0.900	0.000
105	349	0.900	0.900	0.000
105	350	0.900	0.900	0.000
105	351	0.900	0.900	0.000
105	352	0.900	0.900	0.000
Max		0.900	0.900	0.000
Average		0.900	0.900	0.000
Min		0.900	0.900	0.000
Std Dev		0.000	0.000	0.000



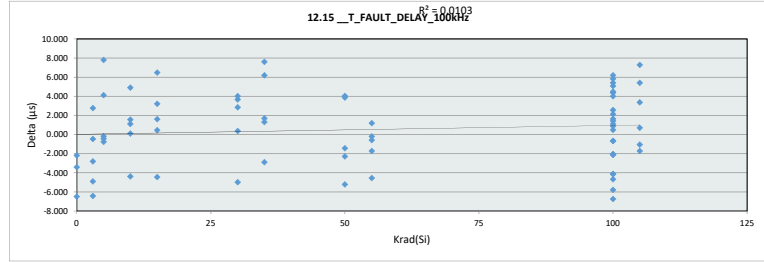
12.14 __T_FAULT	
Test Site	
Tester	
Test Number	
Max Limit	1.4 µs
Min Limit	0.4 µs

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400
Min	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900
Average	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900
Max	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900
UL	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400

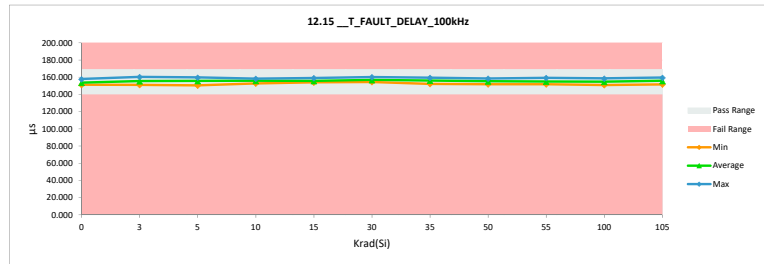


HDR TID Report  
TPS7H5004-SP

12.15 __T_FAULT_DELAY_100kHz				
Test Site				
Tester				
Test Number				
Unit	µs	µs		
Max Limit	169	169		
Min Limit	140	140		
Krad(Si)	Serial #	PRE	POST	Delta
0	295	157.697	151.235	-6.462
0	297	155.422	152.052	-3.370
0	298	160.079	157.918	-2.161
3	192	159.684	156.917	-2.767
3	193	157.499	151.111	-6.388
3	194	157.622	160.423	2.801
3	196	159.825	154.958	-4.867
3	197	155.064	154.629	-0.435
5	200	150.644	150.492	-0.152
5	202	155.641	159.790	4.149
5	203	152.241	151.502	-0.739
5	204	158.035	157.615	-0.420
5	206	151.630	159.467	7.837
10	207	153.405	158.351	4.946
10	209	155.784	155.924	0.140
10	210	154.898	156.496	1.598
10	211	157.110	152.757	-4.353
10	212	155.430	156.576	1.146
15	214	154.702	155.195	0.493
15	217	151.638	154.896	3.258
15	218	160.262	155.842	-4.420
15	219	152.358	154.000	1.642
15	222	152.626	159.144	6.518
30	224	152.308	156.369	4.061
30	225	160.329	155.373	-4.956
30	226	154.026	154.434	0.408
30	227	156.553	160.252	3.699
30	229	155.592	158.468	2.876
35	230	156.917	154.055	-2.862
35	231	155.800	157.138	1.338
35	232	150.510	158.142	7.632
35	234	150.552	152.283	1.731
35	237	153.277	159.507	6.230
50	272	157.066	151.891	-5.175
50	275	157.372	155.964	-1.408
50	276	156.203	153.953	-2.250
50	277	152.383	156.460	4.077
50	278	154.673	158.582	3.909
55	279	159.439	159.284	-0.155
55	280	157.761	157.207	-0.554
55	281	156.782	152.270	-4.512
55	282	156.477	154.795	-1.682
55	283	150.637	151.856	1.219
100	317	154.036	155.498	1.462
100	318	154.498	155.020	0.522
100	319	152.901	157.303	4.402
100	320	157.220	153.143	-4.077
100	321	158.754	154.103	-4.651
100	322	160.016	158.004	-2.012
100	323	151.447	155.976	4.529
100	324	157.825	153.707	-4.118
100	326	157.450	151.704	-5.746
100	327	155.449	153.368	-2.081
100	328	153.409	155.999	2.590
100	329	151.628	157.068	5.440
100	332	153.340	152.715	-0.625
100	333	152.705	153.610	0.905
100	334	150.940	157.160	6.220
100	335	152.805	158.668	5.863
100	336	157.764	151.066	-6.698
100	337	154.306	155.449	1.143
100	338	155.372	153.325	-2.047
100	339	152.932	156.965	4.033
100	340	151.460	156.562	5.102
100	341	151.505	153.183	1.678
100	342	152.795	154.962	2.167
100	343	151.543	150.913	-0.630
105	344	150.701	154.105	3.404
105	346	154.149	159.596	5.447
105	349	153.284	154.027	0.743
105	350	152.685	151.662	-1.023
105	351	159.541	157.855	-1.686
105	352	150.894	158.202	7.308
Max		160.329	160.423	7.837
Average		154.895	155.431	0.537
Min		150.510	150.492	-6.698
Std Dev		2.871	2.646	3.786

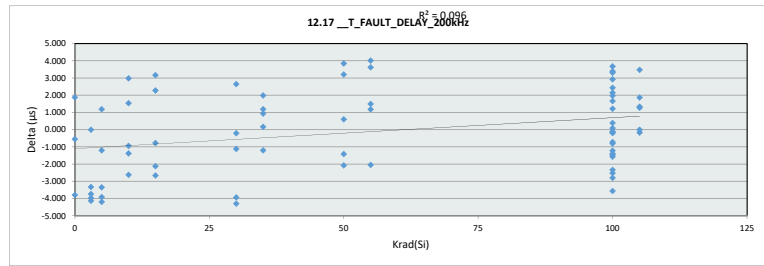


12.15 __T_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	169	µs									
Min Limit	140	µs									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000
Min	151.235	151.111	150.492	152.757	154.000	154.434	152.283	151.891	151.856	150.913	151.662
Average	153.735	155.608	155.773	156.021	155.815	156.979	156.225	155.370	155.082	154.811	155.908
Max	157.918	160.423	159.790	158.351	159.144	160.252	159.507	158.582	159.284	158.668	159.596
UL	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000

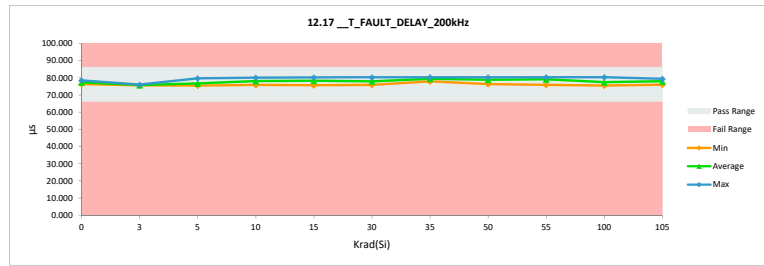


HDR TID Report  
TPS7H5004-SP

12.17 __T_FAULT_DELAY_200kHz				
Test Site				
Tester				
Test Number				
Unit	µs		µs	
Max Limit	86		86	
Min Limit	66		66	
Krad(Si)	Serial #	PRE	POST	Delta
0	295	80.135	76.354	-3.781
0	297	77.823	77.284	-0.539
0	298	76.602	78.496	1.894
3	192	79.890	75.936	-3.954
3	193	75.715	75.727	0.012
3	194	79.482	75.758	-3.724
3	196	80.183	76.065	-4.118
3	197	78.934	75.616	-3.318
5	200	80.251	76.352	-3.899
5	202	79.649	75.466	-4.183
5	203	79.406	76.076	-3.330
5	204	77.435	76.246	-1.189
5	206	78.569	79.771	1.202
10	207	78.307	76.944	-1.363
10	209	78.481	75.878	-2.603
10	210	80.174	79.251	-0.923
10	211	75.538	78.537	2.999
10	212	78.575	80.134	1.559
15	214	78.922	78.162	-0.760
15	217	79.965	77.316	-2.649
15	218	77.874	80.164	2.290
15	219	77.790	75.688	-2.102
15	222	77.088	80.270	3.182
30	224	78.640	78.450	-0.190
30	225	80.372	76.452	-3.920
30	226	77.607	80.271	2.664
30	227	79.862	78.758	-1.104
30	229	80.247	75.963	-4.284
35	230	78.814	80.020	1.206
35	231	80.236	80.414	0.178
35	232	79.151	77.964	-1.187
35	234	77.546	79.545	1.999
35	237	78.338	79.294	0.956
50	272	79.497	80.114	0.617
50	275	77.095	80.313	3.218
50	276	75.792	79.658	3.866
50	277	79.614	78.218	-1.396
50	278	78.425	76.362	-2.063
55	279	76.568	80.210	3.642
55	280	77.982	75.954	-2.028
55	281	78.468	79.663	1.195
55	282	75.532	79.560	4.028
55	283	78.868	80.381	1.513
100	317	76.798	78.958	2.160
100	318	78.982	77.425	-1.557
100	319	76.230	79.551	3.321
100	320	76.964	80.373	3.409
100	321	76.654	79.104	2.450
100	322	77.709	76.330	-1.379
100	323	77.262	77.372	0.110
100	324	80.452	77.948	-2.504
100	326	77.515	76.810	-0.705
100	327	77.920	75.598	-2.322
100	328	77.232	80.178	2.946
100	329	75.876	76.282	0.406
100	332	77.851	77.683	-0.168
100	333	77.180	77.014	-0.166
100	334	80.306	77.518	-2.788
100	335	79.816	76.269	-3.547
100	336	75.947	79.642	3.695
100	337	77.051	75.602	-1.449
100	338	77.242	76.452	-0.790
100	339	75.759	77.433	1.674
100	340	77.787	76.580	-1.207
100	341	75.810	77.039	1.229
100	342	75.634	75.544	-0.090
100	343	75.599	77.581	1.982
105	344	76.174	76.024	-0.150
105	346	78.110	79.464	1.354
105	349	75.909	79.398	3.489
105	350	77.389	77.399	0.010
105	351	76.339	77.628	1.289
105	352	76.324	78.204	1.880
Max		80.452	80.414	4.028
Average		77.963	77.856	-0.107
Min		75.532	75.466	-4.284
Std Dev		1.503	1.650	2.396

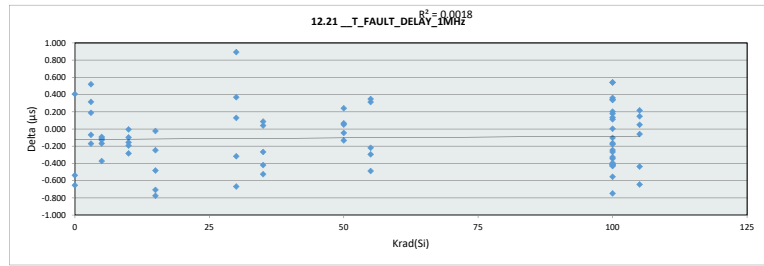


12.17 __T_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	86		µs								
Min Limit	66		µs								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
Min	76.354	75.616	75.466	75.878	75.688	75.963	77.964	76.362	75.954	75.544	76.024
Average	77.378	75.820	76.782	78.149	78.320	77.979	79.447	78.933	79.154	77.512	78.020
Max	78.496	76.065	79.771	80.134	80.270	80.271	80.414	80.313	80.381	80.373	79.464
UL	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000

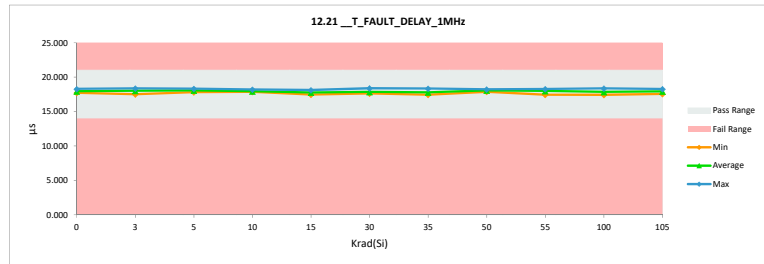


HDR TID Report  
TPS7H5004-SP

12.21_T_FAULT_DELAY_1MHz				
Test Site	Tester	Test Number	Unit	
			µs	µs
			Max Limit	21
			Min Limit	14
Krad(Si)	Serial #	PRE	POST	Delta
0	295	18.380	17.846	-0.534
0	297	17.893	18.302	0.409
0	298	18.388	17.738	-0.650
3	192	18.074	18.393	0.319
3	193	17.590	17.782	0.192
3	194	17.760	18.283	0.523
3	196	17.578	17.514	-0.064
3	197	18.373	18.207	-0.166
5	200	18.448	18.344	-0.104
5	202	17.924	17.835	-0.089
5	203	18.220	17.851	-0.369
5	204	18.333	18.168	-0.165
5	206	18.347	18.223	-0.124
10	207	17.873	17.872	-0.001
10	209	18.376	18.225	-0.151
10	210	18.105	17.916	-0.189
10	211	18.029	17.935	-0.094
10	212	18.190	17.911	-0.279
15	214	18.433	17.661	-0.772
15	217	18.373	18.130	-0.243
15	218	18.224	17.521	-0.703
15	219	17.952	17.474	-0.478
15	222	18.154	18.134	-0.020
30	224	18.294	17.629	-0.665
30	225	18.023	17.709	-0.314
30	226	17.506	18.403	0.897
30	227	17.693	17.826	0.133
30	229	17.461	17.833	0.372
35	230	17.978	17.456	-0.522
35	231	18.213	17.798	-0.415
35	232	17.655	17.698	0.043
35	234	18.003	17.739	-0.264
35	237	18.264	18.355	0.091
50	272	18.023	18.092	0.069
50	275	17.642	17.886	0.244
50	276	18.233	18.191	-0.042
50	277	17.938	17.993	0.055
50	278	18.372	18.243	-0.129
55	279	18.433	18.218	-0.215
55	280	17.978	18.294	0.316
55	281	17.937	17.452	-0.485
55	282	18.419	18.128	-0.291
55	283	17.599	17.951	0.352
100	317	18.010	17.588	-0.422
100	318	17.904	17.563	-0.341
100	319	18.042	18.158	0.116
100	320	18.169	18.375	0.206
100	321	18.423	17.872	-0.551
100	322	17.627	18.172	0.545
100	323	17.488	18.032	0.544
100	324	17.477	17.658	0.181
100	326	18.373	18.379	0.006
100	327	18.342	17.933	-0.409
100	328	18.254	18.093	-0.161
100	329	18.402	17.658	-0.744
100	332	17.524	17.663	0.139
100	333	18.127	18.027	-0.100
100	334	18.147	17.824	-0.323
100	335	17.701	17.437	-0.264
100	336	17.707	17.466	-0.241
100	337	17.804	18.146	0.342
100	338	18.375	17.984	-0.391
100	339	18.036	17.609	-0.427
100	340	17.829	18.195	0.366
100	341	17.959	17.550	-0.409
100	342	17.642	17.469	-0.173
100	343	17.823	18.168	0.345
105	344	17.736	17.957	0.221
105	346	18.214	17.573	-0.641
105	349	17.914	17.966	0.052
105	350	17.981	18.132	0.151
105	351	18.122	17.689	-0.433
105	352	18.355	18.300	-0.055
	Max	18.448	18.403	0.897
	Average	18.030	17.929	-0.101
	Min	17.461	17.437	-0.772
	Std Dev	0.292	0.284	0.351

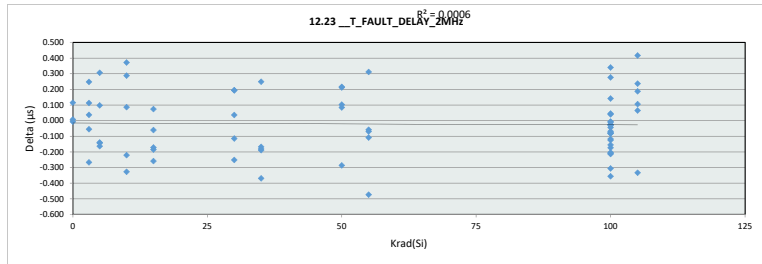


12.21_T_FAULT											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			21	14							
			µs	µs							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000
Min	17.738	17.514	17.835	17.872	17.474	17.629	17.456	17.886	17.452	17.437	17.573
Average	17.962	18.036	18.084	17.972	17.784	17.880	17.809	18.081	18.009	17.876	17.936
Max	18.302	18.393	18.344	18.225	18.134	18.403	18.355	18.243	18.294	18.379	18.300
UL	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000

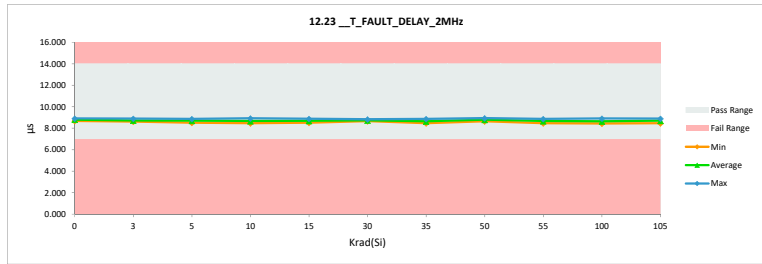


HDR TID Report  
TPS7H5004-SP

12.23 __T_FAULT_DELAY_2MHz				
Test Site	Tester	Test Number	Unit	
			µs	µs
			Max Limit	14
			Min Limit	7
Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.805	8.814	0.009
0	297	8.686	8.681	-0.005
0	298	8.805	8.922	0.117
3	192	8.802	8.749	-0.053
3	193	8.924	8.659	-0.265
3	194	8.860	8.899	0.039
3	196	8.466	8.716	0.250
3	197	8.499	8.614	0.115
5	200	8.776	8.875	0.099
5	202	8.736	8.595	-0.141
5	203	8.669	8.507	-0.162
5	204	8.829	8.691	-0.138
5	206	8.556	8.864	0.308
10	207	8.565	8.855	0.290
10	209	8.811	8.486	-0.325
10	210	8.589	8.677	0.088
10	211	8.554	8.928	0.374
10	212	8.690	8.471	-0.219
15	214	8.704	8.534	-0.170
15	217	8.806	8.882	0.076
15	218	8.949	8.766	-0.183
15	219	8.796	8.738	-0.058
15	222	8.776	8.519	-0.257
30	224	8.616	8.812	0.196
30	225	8.681	8.719	0.038
30	226	8.770	8.658	-0.112
30	227	8.640	8.837	0.197
30	229	8.941	8.691	-0.250
35	230	8.948	8.581	-0.367
35	231	8.826	8.639	-0.187
35	232	8.619	8.870	0.251
35	234	8.877	8.698	-0.179
35	237	8.634	8.468	-0.166
50	272	8.938	8.654	-0.284
50	275	8.512	8.726	0.214
50	276	8.724	8.942	0.218
50	277	8.808	8.913	0.105
50	278	8.677	8.763	0.086
55	279	8.937	8.465	-0.472
55	280	8.936	8.869	-0.067
55	281	8.704	8.598	-0.106
55	282	8.539	8.852	0.313
55	283	8.694	8.638	-0.056
100	317	8.470	8.512	0.042
100	318	8.498	8.474	-0.024
100	319	8.794	8.641	-0.153
100	320	8.523	8.443	-0.080
100	321	8.891	8.719	-0.172
100	322	8.954	8.746	-0.208
100	323	8.936	8.582	-0.354
100	324	8.844	8.721	-0.123
100	326	8.862	8.788	-0.074
100	327	8.894	8.590	-0.304
100	328	8.556	8.476	-0.080
100	329	8.550	8.542	-0.008
100	332	8.613	8.590	-0.023
100	333	8.581	8.515	-0.066
100	334	8.664	8.660	-0.004
100	335	8.711	8.758	0.047
100	336	8.794	8.582	-0.212
100	337	8.704	8.588	-0.116
100	338	8.465	8.743	0.278
100	339	8.458	8.800	0.342
100	340	8.934	8.892	-0.042
100	341	8.688	8.659	-0.029
100	342	8.832	8.630	-0.202
100	343	8.776	8.919	0.143
105	344	8.539	8.728	0.189
105	346	8.738	8.846	0.108
105	349	8.527	8.766	0.239
105	350	8.796	8.465	-0.331
105	351	8.489	8.908	0.419
105	352	8.496	8.563	0.067
	Max	8.954	8.942	0.419
	Average	8.716	8.694	-0.022
	Min	8.458	8.443	-0.472
	Std Dev	0.150	0.142	0.200



12.23 __T_FAULT											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			14	7							
			µs	µs							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000
Min	8.681	8.614	8.507	8.471	8.519	8.658	8.468	8.654	8.465	8.443	8.465
Average	8.806	8.727	8.706	8.683	8.688	8.743	8.651	8.800	8.684	8.649	8.713
Max	8.922	8.899	8.875	8.928	8.882	8.837	8.870	8.942	8.869	8.919	8.908
UL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000

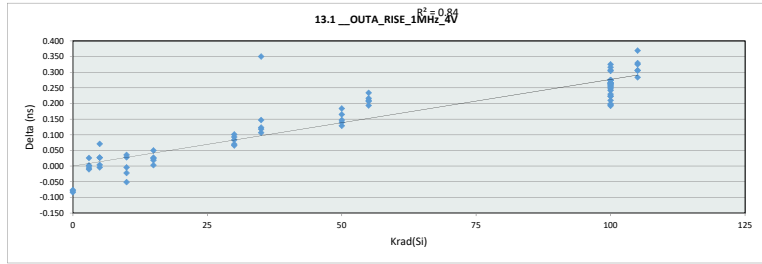




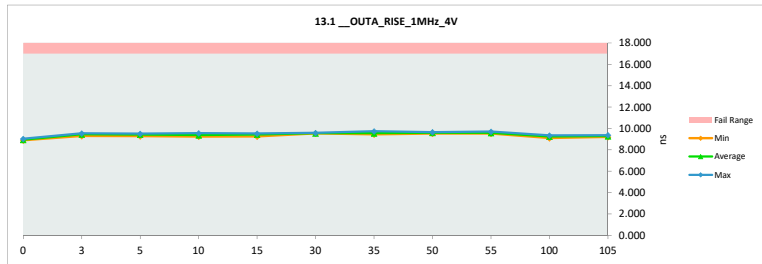
HDR TID Report  
TPS7H5004-SP

13.1 __OUTA_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(S)	Serial #	PRE	POST	Delta
0	295	9.117	9.039	-0.078
0	297	8.971	8.888	-0.083
0	298	9.001	8.926	-0.075
3	192	9.447	9.449	0.002
3	193	9.494	9.497	0.003
3	194	9.307	9.298	-0.009
3	196	9.550	9.545	-0.005
3	197	9.465	9.492	0.027
5	200	9.392	9.420	0.028
5	202	9.277	9.282	0.005
5	203	9.489	9.486	-0.003
5	204	9.490	9.518	0.028
5	206	9.397	9.469	0.072
10	207	9.502	9.531	0.029
10	209	9.528	9.565	0.037
10	210	9.316	9.313	-0.003
10	211	9.262	9.241	-0.021
10	212	9.327	9.277	-0.050
15	214	9.388	9.413	0.025
15	217	9.447	9.475	0.028
15	218	9.486	9.537	0.051
15	219	9.491	9.495	0.004
15	222	9.233	9.253	0.020
30	224	9.457	9.542	0.085
30	225	9.491	9.593	0.102
30	226	9.471	9.538	0.067
30	227	9.477	9.571	0.094
30	229	9.495	9.566	0.071
35	230	9.319	9.438	0.119
35	231	9.393	9.744	0.351
35	232	9.418	9.542	0.124
35	234	9.453	9.601	0.148
35	237	9.360	9.468	0.108
50	272	9.475	9.623	0.148
50	275	9.529	9.659	0.130
50	276	9.429	9.595	0.166
50	277	9.491	9.632	0.141
50	278	9.329	9.514	0.185
55	279	9.322	9.557	0.235
55	280	9.387	9.605	0.218
55	281	9.312	9.523	0.211
55	282	9.339	9.547	0.208
55	283	9.517	9.712	0.195
100	317	8.930	9.141	0.211
100	318	9.094	9.337	0.243
100	319	9.032	9.285	0.253
100	320	9.042	9.301	0.259
100	321	8.956	9.204	0.248
100	322	8.900	9.208	0.308
100	323	8.828	9.104	0.276
100	324	9.037	9.301	0.264
100	326	8.783	9.109	0.326
100	327	9.002	9.307	0.305
100	328	8.894	9.162	0.268
100	329	8.976	9.232	0.256
100	332	9.065	9.330	0.265
100	333	8.990	9.184	0.194
100	334	8.965	9.240	0.275
100	335	9.008	9.270	0.262
100	336	9.013	9.289	0.276
100	337	8.953	9.270	0.317
100	338	9.024	9.224	0.200
100	339	9.040	9.237	0.197
100	340	9.068	9.299	0.231
100	341	9.133	9.357	0.224
100	342	9.004	9.271	0.267
100	343	9.064	9.289	0.225
105	344	8.868	9.198	0.330
105	346	9.090	9.375	0.285
105	349	8.952	9.322	0.370
105	350	8.934	9.260	0.326
105	351	9.001	9.307	0.306
105	352	8.967	9.274	0.307
	Max	9.550	9.744	0.370
	Average	9.225	9.379	0.154
	Min	8.783	8.888	-0.083
	Std Dev	0.229	0.180	0.124



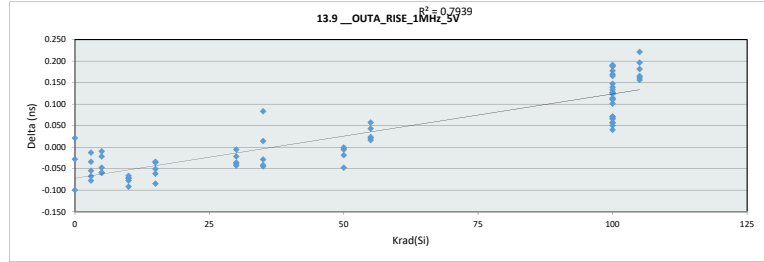
13.1 __OUTA_RISE		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		8.888	9.298	9.282	9.241	9.253	9.538	9.438	9.514	9.523	9.104	9.198
Average		8.951	9.456	9.435	9.385	9.435	9.562	9.559	9.605	9.589	9.248	9.289
Max		9.039	9.545	9.518	9.565	9.537	9.593	9.744	9.659	9.712	9.357	9.375
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



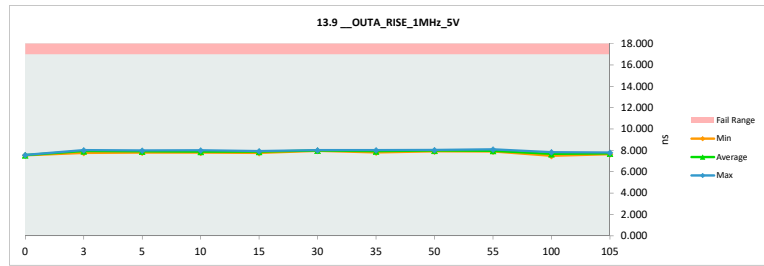
HDR TID Report  
TPS7H5004-SP

13.9 __OUTA_RISE_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	7.658	7.559	-0.099
0	297	7.550	7.572	0.022
0	298	7.567	7.540	-0.027
3	192	7.958	7.946	-0.012
3	193	8.055	7.978	-0.077
3	194	7.832	7.765	-0.067
3	196	8.058	8.025	-0.033
3	197	8.017	7.963	-0.054
5	200	7.911	7.902	-0.009
5	202	7.844	7.797	-0.047
5	203	7.988	7.929	-0.059
5	204	8.050	7.992	-0.058
5	206	7.939	7.918	-0.021
10	207	8.061	7.990	-0.071
10	209	8.086	8.009	-0.077
10	210	7.894	7.828	-0.066
10	211	7.886	7.795	-0.091
10	212	7.863	7.791	-0.072
15	214	7.897	7.836	-0.061
15	217	7.970	7.937	-0.033
15	218	8.010	7.926	-0.084
15	219	7.988	7.938	-0.050
15	222	7.790	7.755	-0.035
30	224	7.993	7.972	-0.021
30	225	8.061	8.023	-0.038
30	226	8.001	7.959	-0.042
30	227	8.036	8.001	-0.035
30	229	8.013	8.008	-0.005
35	230	7.856	7.812	-0.044
35	231	7.886	7.970	0.084
35	232	7.965	7.924	-0.041
35	234	8.013	8.028	0.015
35	237	7.901	7.873	-0.028
50	272	8.058	8.011	-0.047
50	275	8.047	8.043	-0.004
50	276	7.979	7.961	-0.018
50	277	8.009	8.005	-0.004
50	278	7.904	7.904	0.000
55	279	7.867	7.925	0.058
55	280	7.928	7.952	0.024
55	281	7.879	7.901	0.022
55	282	7.919	7.936	0.017
55	283	8.050	8.094	0.044
100	317	7.423	7.491	0.068
100	318	7.621	7.749	0.128
100	319	7.598	7.709	0.111
100	320	7.634	7.758	0.124
100	321	7.503	7.651	0.148
100	322	7.412	7.582	0.170
100	323	7.405	7.545	0.140
100	324	7.512	7.702	0.190
100	326	7.403	7.595	0.192
100	327	7.567	7.682	0.115
100	328	7.546	7.603	0.057
100	329	7.462	7.628	0.166
100	332	7.650	7.691	0.041
100	333	7.572	7.685	0.113
100	334	7.564	7.615	0.051
100	335	7.590	7.662	0.072
100	336	7.591	7.769	0.178
100	337	7.558	7.616	0.058
100	338	7.548	7.675	0.127
100	339	7.497	7.685	0.188
100	340	7.634	7.736	0.102
100	341	7.707	7.841	0.134
100	342	7.600	7.666	0.066
100	343	7.610	7.681	0.071
105	344	7.489	7.646	0.157
105	346	7.623	7.805	0.182
105	349	7.526	7.748	0.222
105	350	7.464	7.661	0.197
105	351	7.540	7.702	0.162
105	352	7.517	7.683	0.166
Max		8.086	8.094	0.222
Average		7.775	7.812	0.036
Min		7.403	7.491	-0.099
Std Dev		0.220	0.157	0.091



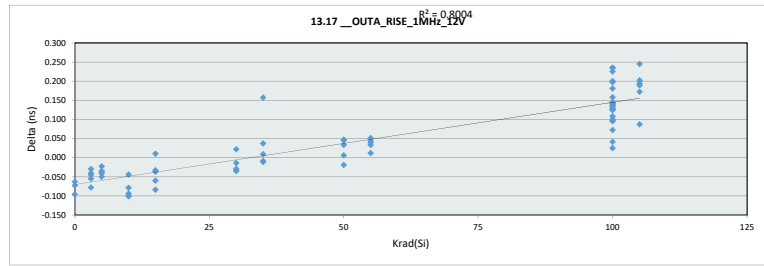
13.9 __OUTA_RISE		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.540	7.765	7.797	7.791	7.755	7.959	7.812	7.904	7.901	7.491	7.646
Average		7.557	7.935	7.908	7.883	7.878	7.993	7.921	7.985	7.962	7.667	7.708
Max		7.572	8.025	7.992	8.009	7.938	8.023	8.028	8.043	8.094	7.841	7.805
UL		17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

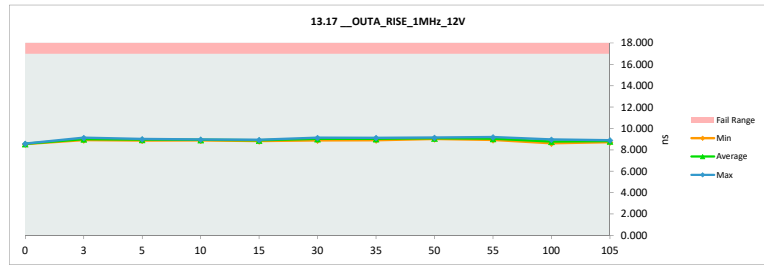
13.17 __OUTA_RISE_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.642	8.570	-0.072
0	297	8.680	8.585	-0.095
0	298	8.595	8.533	-0.062
3	192	9.043	8.999	-0.044
3	193	9.004	8.964	-0.040
3	194	8.955	8.901	-0.054
3	196	9.163	9.134	-0.029
3	197	9.086	9.009	-0.077
5	200	8.942	8.905	-0.037
5	202	8.892	8.870	-0.022
5	203	9.051	9.011	-0.040
5	204	9.055	9.006	-0.049
5	206	8.979	8.945	-0.034
10	207	9.066	8.966	-0.100
10	209	9.051	8.973	-0.078
10	210	8.985	8.886	-0.099
10	211	9.013	8.970	-0.043
10	212	8.967	8.874	-0.093
15	214	8.963	8.931	-0.032
15	217	8.888	8.899	0.011
15	218	9.022	8.939	-0.083
15	219	8.938	8.879	-0.059
15	222	8.850	8.814	-0.036
30	224	8.914	8.882	-0.032
30	225	9.039	9.062	0.023
30	226	9.162	9.128	-0.034
30	227	8.984	8.956	-0.028
30	229	9.122	9.109	-0.013
35	230	8.909	8.899	-0.010
35	231	8.937	9.095	0.158
35	232	9.114	9.124	0.010
35	234	9.042	9.034	-0.008
35	237	8.929	8.967	0.038
50	272	9.057	9.105	0.048
50	275	9.136	9.143	0.007
50	276	9.010	9.044	0.034
50	277	9.057	9.103	0.046
50	278	9.031	9.013	-0.018
55	279	8.874	8.919	0.045
55	280	9.154	9.194	0.040
55	281	8.969	8.982	0.013
55	282	9.018	9.052	0.034
55	283	9.013	9.065	0.052
100	317	8.575	8.601	0.026
100	318	8.697	8.836	0.139
100	319	8.653	8.796	0.143
100	320	8.742	8.870	0.128
100	321	8.742	8.968	0.226
100	322	8.623	8.753	0.130
100	323	8.567	8.803	0.236
100	324	8.601	8.736	0.135
100	326	8.477	8.713	0.236
100	327	8.716	8.814	0.098
100	328	8.586	8.711	0.125
100	329	8.579	8.780	0.201
100	332	8.644	8.826	0.182
100	333	8.704	8.841	0.137
100	334	8.621	8.819	0.198
100	335	8.558	8.686	0.128
100	336	8.724	8.833	0.109
100	337	8.555	8.656	0.101
100	338	8.667	8.740	0.073
100	339	8.515	8.658	0.143
100	340	8.583	8.742	0.159
100	341	8.711	8.857	0.146
100	342	8.698	8.740	0.042
100	343	8.652	8.748	0.096
105	344	8.662	8.750	0.088
105	346	8.726	8.899	0.173
105	349	8.651	8.846	0.195
105	350	8.559	8.805	0.246
105	351	8.708	8.898	0.190
105	352	8.503	8.706	0.203
Max		9.163	9.194	0.246
Average		8.840	8.889	0.049
Min		8.477	8.533	-0.100
Std Dev		0.203	0.151	0.099



13.17 __OUTA_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

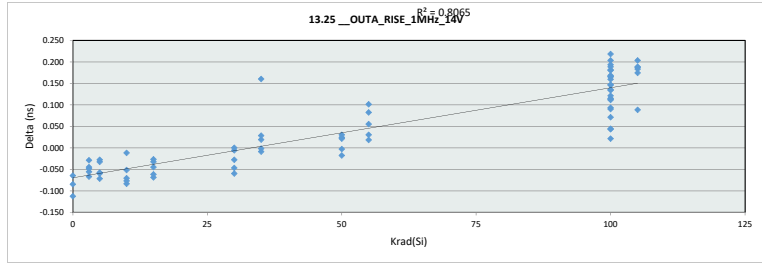
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.533	8.901	8.870	8.874	8.814	8.882	8.899	9.013	8.919	8.601	8.706
Average	8.563	9.001	8.947	8.934	8.892	9.027	9.024	9.082	9.042	8.772	8.817
Max	8.585	9.134	9.011	8.973	8.939	9.128	9.124	9.143	9.194	8.968	8.899
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

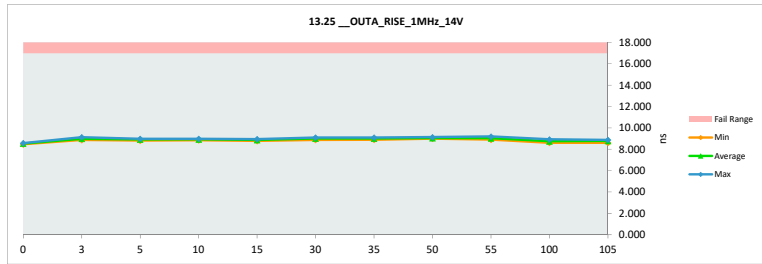
13.25 _OUTA_RISE_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.637	8.573	-0.064
0	297	8.639	8.555	-0.084
0	298	8.580	8.468	-0.112
3	192	9.049	9.001	-0.048
3	193	8.985	8.930	-0.055
3	194	8.933	8.867	-0.066
3	196	9.156	9.128	-0.028
3	197	9.029	8.985	-0.044
5	200	8.909	8.882	-0.027
5	202	8.882	8.811	-0.071
5	203	9.014	8.982	-0.032
5	204	9.043	8.984	-0.059
5	206	8.967	8.910	-0.057
10	207	9.050	8.980	-0.070
10	209	9.037	8.961	-0.076
10	210	8.939	8.888	-0.051
10	211	8.981	8.970	-0.011
10	212	8.928	8.845	-0.083
15	214	8.960	8.892	-0.068
15	217	8.917	8.856	-0.061
15	218	8.975	8.949	-0.026
15	219	8.889	8.857	-0.032
15	222	8.820	8.776	-0.044
30	224	8.878	8.873	-0.005
30	225	9.038	9.011	-0.027
30	226	9.150	9.104	-0.046
30	227	8.963	8.964	0.001
30	229	9.110	9.051	-0.059
35	230	8.890	8.889	-0.001
35	231	8.915	9.076	0.161
35	232	9.072	9.101	0.029
35	234	9.022	9.014	-0.008
35	237	8.919	8.939	0.020
50	272	9.059	9.042	-0.017
50	275	9.110	9.135	0.025
50	276	8.994	9.025	0.031
50	277	9.044	9.066	0.022
50	278	8.995	8.993	-0.002
55	279	8.840	8.896	0.056
55	280	9.116	9.199	0.083
55	281	8.949	8.980	0.031
55	282	8.950	9.052	0.102
55	283	9.004	9.023	0.019
100	317	8.488	8.604	0.116
100	318	8.652	8.787	0.135
100	319	8.694	8.716	0.022
100	320	8.676	8.845	0.169
100	321	8.736	8.930	0.194
100	322	8.571	8.760	0.189
100	323	8.597	8.801	0.204
100	324	8.582	8.764	0.182
100	326	8.487	8.668	0.181
100	327	8.671	8.807	0.136
100	328	8.550	8.716	0.166
100	329	8.570	8.730	0.160
100	332	8.578	8.797	0.219
100	333	8.701	8.823	0.122
100	334	8.604	8.770	0.166
100	335	8.545	8.636	0.091
100	336	8.694	8.808	0.114
100	337	8.565	8.610	0.045
100	338	8.600	8.712	0.112
100	339	8.568	8.640	0.072
100	340	8.539	8.686	0.147
100	341	8.716	8.810	0.094
100	342	8.663	8.707	0.044
100	343	8.625	8.772	0.147
105	344	8.603	8.787	0.184
105	346	8.701	8.876	0.175
105	349	8.658	8.846	0.188
105	350	8.520	8.709	0.189
105	351	8.643	8.847	0.204
105	352	8.515	8.604	0.089
Max		9.156	9.199	0.219
Average		8.817	8.864	0.047
Min		8.487	8.468	-0.112
Std Dev		0.203	0.155	0.096



13.25 _OUTA_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

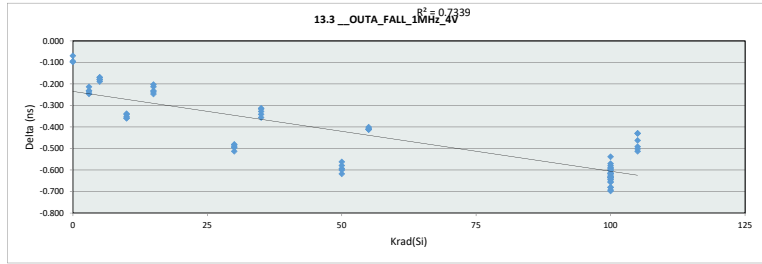
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.468	8.867	8.811	8.845	8.776	8.873	8.889	8.993	8.896	8.604	8.604
Average	8.532	8.982	8.914	8.929	8.866	9.001	9.004	9.052	9.030	8.746	8.778
Max	8.573	9.128	8.984	8.980	8.949	9.104	9.101	9.135	9.199	8.930	8.876
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



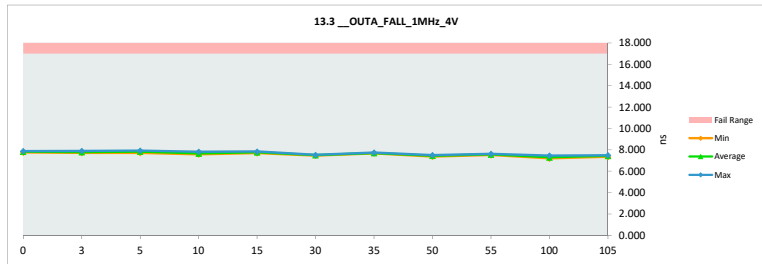
HDR TID Report  
TPS7H5004-SP

13.3_OUTA_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(S)	Serial #	PRE	POST	Delta
0	295	7.947	7.879	-0.068
0	297	7.947	7.854	-0.093
0	298	7.846	7.749	-0.097
3	192	8.126	7.895	-0.231
3	193	8.044	7.813	-0.231
3	194	7.959	7.717	-0.242
3	196	8.014	7.769	-0.245
3	197	7.964	7.752	-0.212
5	200	7.978	7.810	-0.168
5	202	7.903	7.715	-0.188
5	203	8.112	7.933	-0.179
5	204	8.056	7.877	-0.179
5	206	8.011	7.841	-0.170
10	207	8.181	7.823	-0.358
10	209	8.036	7.678	-0.358
10	210	7.930	7.579	-0.351
10	211	7.984	7.643	-0.341
10	212	7.927	7.590	-0.337
15	214	8.043	7.797	-0.246
15	217	7.942	7.730	-0.212
15	218	8.038	7.807	-0.231
15	219	8.080	7.844	-0.236
15	222	7.893	7.691	-0.202
30	224	7.994	7.514	-0.480
30	225	8.015	7.520	-0.495
30	226	7.952	7.461	-0.491
30	227	8.044	7.532	-0.512
30	229	7.987	7.505	-0.482
35	230	8.028	7.688	-0.340
35	231	7.989	7.674	-0.315
35	232	8.098	7.743	-0.355
35	234	8.062	7.750	-0.312
35	237	7.997	7.670	-0.327
50	272	7.975	7.376	-0.599
50	275	8.052	7.435	-0.617
50	276	8.088	7.496	-0.592
50	277	8.089	7.511	-0.578
50	278	7.964	7.403	-0.561
55	279	7.921	7.513	-0.408
55	280	7.978	7.567	-0.411
55	281	8.026	7.626	-0.400
55	282	8.000	7.591	-0.409
55	283	7.996	7.586	-0.410
100	317	8.039	7.343	-0.696
100	318	7.834	7.200	-0.634
100	319	7.900	7.245	-0.655
100	320	7.845	7.216	-0.629
100	321	7.850	7.218	-0.632
100	322	7.976	7.341	-0.635
100	323	7.908	7.308	-0.600
100	324	7.918	7.381	-0.537
100	326	7.808	7.239	-0.569
100	327	7.931	7.344	-0.587
100	328	7.926	7.304	-0.622
100	329	8.009	7.365	-0.644
100	332	7.916	7.317	-0.599
100	333	7.907	7.228	-0.679
100	334	7.859	7.245	-0.614
100	335	7.937	7.296	-0.641
100	336	8.075	7.460	-0.615
100	337	8.008	7.374	-0.634
100	338	7.983	7.291	-0.692
100	339	7.868	7.255	-0.613
100	340	8.067	7.412	-0.655
100	341	8.013	7.334	-0.679
100	342	7.894	7.302	-0.592
100	343	7.894	7.315	-0.579
105	344	8.005	7.493	-0.512
105	346	7.843	7.353	-0.490
105	349	7.933	7.472	-0.461
105	350	7.805	7.376	-0.429
105	351	7.952	7.451	-0.501
105	352	7.922	7.493	-0.429
Max		8.181	7.933	-0.068
Average		7.973	7.533	-0.440
Min		7.805	7.200	-0.696
Std Dev		0.079	0.210	0.178



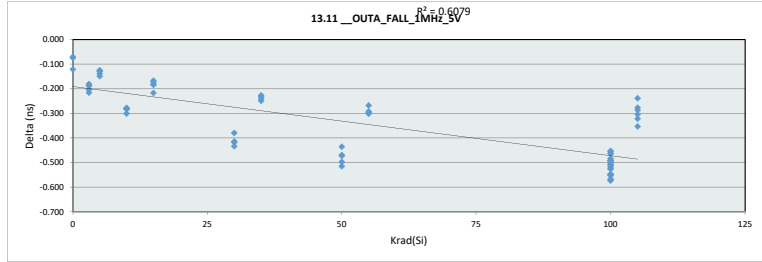
13.3_OUTA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.749	7.717	7.715	7.579	7.691	7.461	7.670	7.376	7.513	7.200	7.353
Average	7.827	7.789	7.835	7.663	7.774	7.506	7.705	7.444	7.577	7.306	7.440
Max	7.879	7.895	7.933	7.823	7.844	7.532	7.750	7.511	7.626	7.460	7.493
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

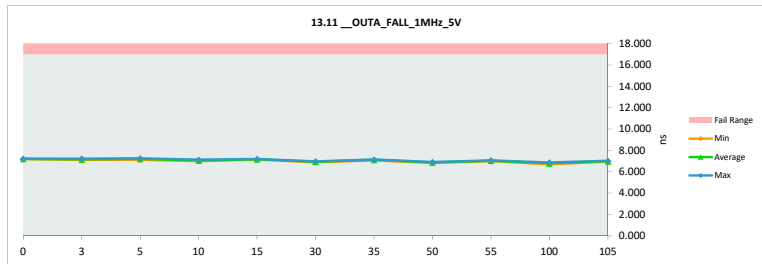
13.11 __OUTA_FALL_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	7.330	7.210	-0.120
0	297	7.293	7.220	-0.073
0	298	7.251	7.182	-0.069
3	192	7.407	7.227	-0.180
3	193	7.368	7.180	-0.188
3	194	7.314	7.111	-0.203
3	196	7.342	7.157	-0.185
3	197	7.350	7.135	-0.215
5	200	7.325	7.197	-0.128
5	202	7.255	7.117	-0.138
5	203	7.379	7.252	-0.127
5	204	7.388	7.264	-0.124
5	206	7.339	7.191	-0.148
10	207	7.413	7.132	-0.281
10	209	7.381	7.081	-0.300
10	210	7.274	6.997	-0.277
10	211	7.318	7.036	-0.282
10	212	7.270	6.991	-0.279
15	214	7.383	7.167	-0.216
15	217	7.314	7.131	-0.183
15	218	7.375	7.203	-0.172
15	219	7.384	7.202	-0.182
15	222	7.304	7.138	-0.166
30	224	7.336	6.922	-0.414
30	225	7.356	6.942	-0.414
30	226	7.314	6.881	-0.433
30	227	7.395	6.978	-0.417
30	229	7.314	6.935	-0.379
35	230	7.332	7.101	-0.231
35	231	7.345	7.116	-0.229
35	232	7.379	7.139	-0.240
35	234	7.374	7.148	-0.226
35	237	7.317	7.069	-0.248
50	272	7.320	6.823	-0.497
50	275	7.377	6.863	-0.514
50	276	7.347	6.912	-0.435
50	277	7.375	6.906	-0.469
50	278	7.309	6.837	-0.472
55	279	7.283	6.983	-0.300
55	280	7.327	7.028	-0.299
55	281	7.330	7.063	-0.267
55	282	7.330	7.039	-0.291
55	283	7.346	7.050	-0.296
100	317	7.328	6.835	-0.493
100	318	7.248	6.701	-0.547
100	319	7.340	6.791	-0.549
100	320	7.268	6.759	-0.509
100	321	7.285	6.739	-0.546
100	322	7.326	6.810	-0.516
100	323	7.295	6.789	-0.506
100	324	7.313	6.821	-0.492
100	326	7.225	6.762	-0.463
100	327	7.282	6.784	-0.498
100	328	7.281	6.791	-0.490
100	329	7.320	6.813	-0.507
100	332	7.350	6.798	-0.552
100	333	7.249	6.759	-0.490
100	334	7.318	6.751	-0.567
100	335	7.305	6.822	-0.483
100	336	7.328	6.866	-0.462
100	337	7.294	6.839	-0.455
100	338	7.275	6.749	-0.526
100	339	7.245	6.723	-0.522
100	340	7.317	6.865	-0.452
100	341	7.336	6.873	-0.463
100	342	7.292	6.719	-0.573
100	343	7.329	6.762	-0.567
105	344	7.301	6.980	-0.321
105	346	7.293	6.941	-0.352
105	349	7.281	7.005	-0.276
105	350	7.256	6.953	-0.303
105	351	7.264	7.026	-0.238
105	352	7.264	6.978	-0.286
Max		7.413	7.264	-0.069
Average		7.320	6.973	-0.347
Min		7.225	6.701	-0.573
Std Dev		0.043	0.164	0.148



13.11 __OUTA_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

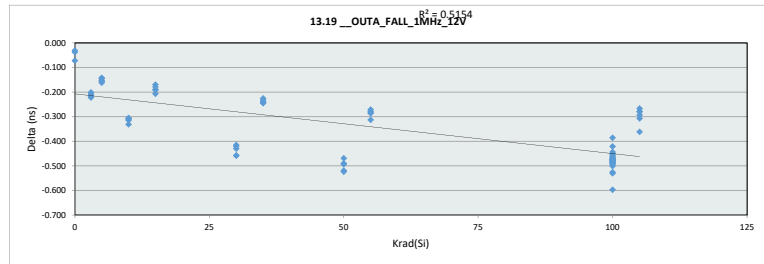
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.182	7.111	7.117	6.991	7.131	6.881	7.069	6.823	6.983	6.701	6.941
Average	7.204	7.162	7.204	7.047	7.168	6.932	7.115	6.868	7.033	6.788	6.981
Max	7.220	7.227	7.264	7.132	7.203	6.978	7.148	6.912	7.063	6.873	7.026
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

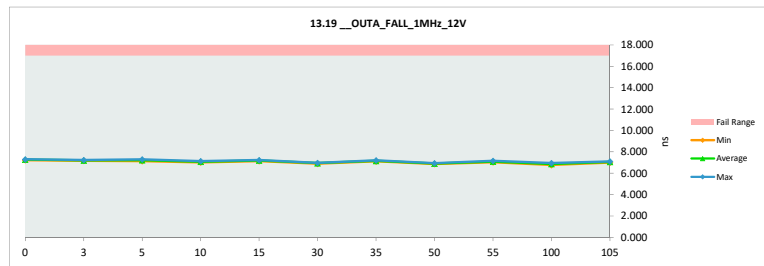
13.19 __OUTA_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	7.364	7.293	-0.071
0	297	7.338	7.308	-0.030
0	298	7.258	7.221	-0.037
3	192	7.450	7.240	-0.210
3	193	7.381	7.161	-0.220
3	194	7.387	7.167	-0.220
3	196	7.438	7.237	-0.201
3	197	7.412	7.210	-0.202
5	200	7.342	7.186	-0.156
5	202	7.301	7.140	-0.161
5	203	7.407	7.263	-0.144
5	204	7.448	7.297	-0.151
5	206	7.369	7.228	-0.141
10	207	7.445	7.132	-0.313
10	209	7.409	7.079	-0.330
10	210	7.340	7.032	-0.308
10	211	7.404	7.096	-0.308
10	212	7.319	7.015	-0.304
15	214	7.436	7.230	-0.206
15	217	7.308	7.139	-0.169
15	218	7.422	7.232	-0.190
15	219	7.380	7.191	-0.189
15	222	7.341	7.163	-0.178
30	224	7.344	6.887	-0.457
30	225	7.373	6.954	-0.419
30	226	7.395	6.966	-0.429
30	227	7.411	6.954	-0.457
30	229	7.381	6.967	-0.414
35	230	7.376	7.144	-0.232
35	231	7.384	7.140	-0.244
35	232	7.457	7.215	-0.242
35	234	7.398	7.173	-0.225
35	237	7.351	7.111	-0.240
50	272	7.379	6.861	-0.518
50	275	7.439	6.916	-0.523
50	276	7.393	6.925	-0.468
50	277	7.433	6.944	-0.489
50	278	7.362	6.869	-0.493
55	279	7.318	7.033	-0.285
55	280	7.449	7.169	-0.280
55	281	7.373	7.094	-0.279
55	282	7.373	7.103	-0.270
55	283	7.376	7.064	-0.312
100	317	7.354	6.862	-0.492
100	318	7.281	6.800	-0.481
100	319	7.375	6.779	-0.596
100	320	7.334	6.849	-0.485
100	321	7.412	6.912	-0.500
100	322	7.406	6.934	-0.472
100	323	7.407	6.933	-0.474
100	324	7.302	6.917	-0.385
100	326	7.263	6.843	-0.420
100	327	7.362	6.872	-0.490
100	328	7.334	6.884	-0.450
100	329	7.340	6.871	-0.469
100	332	7.343	6.867	-0.476
100	333	7.328	6.868	-0.460
100	334	7.295	6.852	-0.443
100	335	7.302	6.822	-0.480
100	336	7.412	6.957	-0.455
100	337	7.322	6.848	-0.474
100	338	7.292	6.819	-0.473
100	339	7.238	6.763	-0.475
100	340	7.329	6.864	-0.465
100	341	7.408	6.921	-0.487
100	342	7.339	6.810	-0.529
100	343	7.366	6.841	-0.525
105	344	7.446	7.085	-0.361
105	346	7.325	7.033	-0.292
105	349	7.341	7.063	-0.278
105	350	7.315	7.049	-0.266
105	351	7.384	7.105	-0.279
105	352	7.311	7.005	-0.306
Max		7.457	7.308	-0.030
Average		7.366	7.024	-0.342
Min		7.238	6.763	-0.596
Std Dev		0.051	0.153	0.139



13.19 __OUTA_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

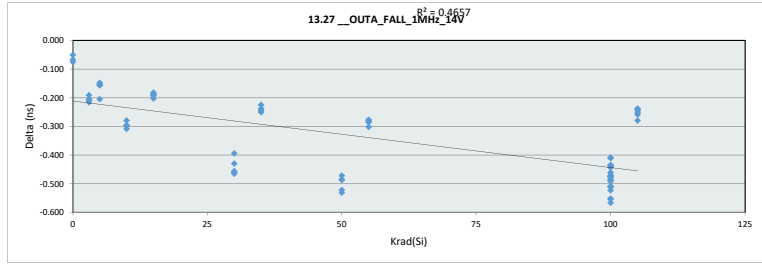
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.221	7.161	7.140	7.015	7.139	6.887	7.111	6.861	7.033	6.763	7.005
Average	7.274	7.203	7.223	7.071	7.191	6.946	7.157	6.903	7.093	6.862	7.057
Max	7.308	7.240	7.297	7.132	7.232	6.967	7.215	6.944	7.169	6.957	7.105
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

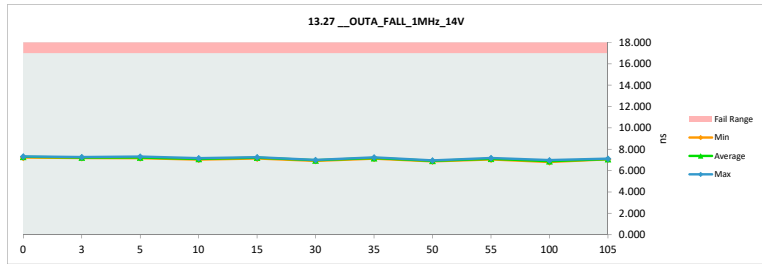
13.27 __OUTA_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	295	7.406	7.332	-0.074
0	297	7.377	7.327	-0.050
0	298	7.287	7.221	-0.066
3	192	7.465	7.275	-0.190
3	193	7.405	7.190	-0.215
3	194	7.403	7.198	-0.205
3	196	7.451	7.245	-0.206
3	197	7.430	7.222	-0.208
5	200	7.356	7.209	-0.147
5	202	7.319	7.166	-0.153
5	203	7.425	7.221	-0.204
5	204	7.469	7.315	-0.154
5	206	7.406	7.250	-0.156
10	207	7.462	7.166	-0.296
10	209	7.412	7.104	-0.308
10	210	7.348	7.049	-0.299
10	211	7.412	7.117	-0.295
10	212	7.319	7.040	-0.279
15	214	7.448	7.262	-0.186
15	217	7.338	7.146	-0.192
15	218	7.430	7.249	-0.181
15	219	7.412	7.210	-0.202
15	222	7.374	7.185	-0.189
30	224	7.379	6.915	-0.464
30	225	7.407	6.978	-0.429
30	226	7.435	6.980	-0.455
30	227	7.417	6.958	-0.459
30	229	7.403	7.010	-0.393
35	230	7.411	7.161	-0.250
35	231	7.416	7.179	-0.237
35	232	7.485	7.241	-0.244
35	234	7.418	7.194	-0.224
35	237	7.364	7.120	-0.244
50	272	7.400	6.870	-0.530
50	275	7.455	6.934	-0.521
50	276	7.415	6.944	-0.471
50	277	7.443	6.957	-0.486
50	278	7.388	6.904	-0.484
55	279	7.331	7.053	-0.278
55	280	7.473	7.189	-0.284
55	281	7.398	7.114	-0.284
55	282	7.398	7.122	-0.276
55	283	7.387	7.086	-0.301
100	317	7.406	6.855	-0.551
100	318	7.328	6.806	-0.522
100	319	7.406	6.853	-0.553
100	320	7.362	6.854	-0.508
100	321	7.417	6.975	-0.442
100	322	7.389	6.954	-0.435
100	323	7.370	6.962	-0.408
100	324	7.316	6.906	-0.410
100	326	7.297	6.808	-0.489
100	327	7.425	6.914	-0.511
100	328	7.319	6.882	-0.437
100	329	7.348	6.906	-0.442
100	332	7.361	6.891	-0.470
100	333	7.385	6.896	-0.489
100	334	7.340	6.869	-0.471
100	335	7.339	6.866	-0.473
100	336	7.417	6.983	-0.434
100	337	7.337	6.877	-0.460
100	338	7.356	6.880	-0.476
100	339	7.304	6.796	-0.508
100	340	7.359	6.875	-0.484
100	341	7.414	6.905	-0.509
100	342	7.335	6.859	-0.476
100	343	7.404	6.838	-0.566
105	344	7.340	7.089	-0.251
105	346	7.324	7.066	-0.258
105	349	7.331	7.094	-0.237
105	350	7.324	7.079	-0.245
105	351	7.407	7.128	-0.279
105	352	7.301	7.062	-0.239
Max		7.485	7.332	-0.050
Average		7.385	7.046	-0.340
Min		7.287	6.796	-0.566
Std Dev		0.048	0.152	0.140



13.27 __OUTA_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.221	7.190	7.166	7.040	7.146	6.915	7.120	6.870	7.053	6.796	7.062
Average	7.293	7.226	7.232	7.095	7.210	6.968	7.179	6.922	7.113	6.884	7.086
Max	7.332	7.275	7.315	7.166	7.262	7.010	7.241	6.957	7.189	6.983	7.128
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

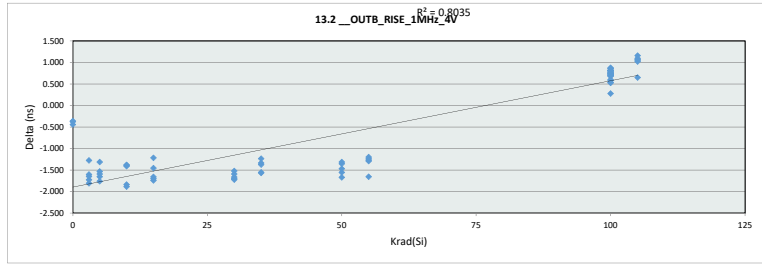




HDR TID Report  
TPS7H5004-SP

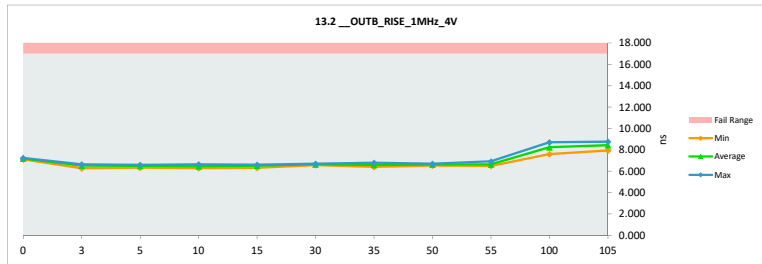
13.2 __OUTB_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	7.675	7.235	-0.440
0	297	7.474	7.103	-0.371
0	298	7.562	7.205	-0.357
3	192	8.136	6.537	-1.599
3	193	8.220	6.576	-1.644
3	194	7.548	6.275	-1.273
3	196	8.446	6.642	-1.804
3	197	8.332	6.611	-1.721
5	200	8.062	6.474	-1.588
5	202	7.636	6.326	-1.310
5	203	8.242	6.590	-1.652
5	204	8.323	6.569	-1.754
5	206	8.047	6.518	-1.529
10	207	8.470	6.637	-1.833
10	209	8.523	6.643	-1.880
10	210	7.658	6.284	-1.374
10	211	7.737	6.352	-1.385
10	212	7.794	6.390	-1.404
15	214	7.845	6.395	-1.450
15	217	8.256	6.601	-1.655
15	218	8.351	6.616	-1.735
15	219	8.269	6.574	-1.695
15	222	7.530	6.317	-1.213
30	224	8.180	6.593	-1.587
30	225	8.391	6.695	-1.696
30	226	8.078	6.562	-1.516
30	227	8.305	6.648	-1.657
30	229	8.408	6.691	-1.717
35	230	7.737	6.407	-1.330
35	231	8.018	6.787	-1.231
35	232	8.104	6.543	-1.561
35	234	8.202	6.653	-1.549
35	237	7.895	6.526	-1.369
50	272	8.193	6.643	-1.550
50	275	8.320	6.654	-1.666
50	276	8.058	6.708	-1.350
50	277	8.049	6.583	-1.466
50	278	7.825	6.516	-1.309
55	279	7.768	6.535	-1.233
55	280	7.935	6.648	-1.287
55	281	7.690	6.497	-1.193
55	282	7.894	6.630	-1.264
55	283	8.568	6.919	-1.649
100	317	7.315	7.598	0.283
100	318	7.725	8.331	0.606
100	319	7.673	8.386	0.713
100	320	7.791	8.645	0.854
100	321	7.446	8.186	0.740
100	322	7.184	7.905	0.721
100	323	6.974	7.671	0.697
100	324	7.636	8.450	0.814
100	326	7.162	7.976	0.814
100	327	7.523	8.208	0.685
100	328	7.351	7.885	0.534
100	329	7.358	8.173	0.815
100	332	7.628	8.389	0.761
100	333	7.640	8.347	0.707
100	334	7.510	8.273	0.763
100	335	7.622	8.153	0.531
100	336	7.724	8.604	0.880
100	337	7.547	8.348	0.801
100	338	7.721	8.303	0.582
100	339	7.453	8.198	0.745
100	340	7.687	8.448	0.761
100	341	7.838	8.702	0.864
100	342	7.644	8.466	0.822
100	343	7.575	8.388	0.813
105	344	7.280	7.936	0.656
105	346	7.736	8.762	1.026
105	349	7.303	8.465	1.162
105	350	7.261	8.324	1.063
105	351	7.608	8.670	1.062
105	352	7.337	8.438	1.101
Max		8.568	8.762	1.162
Average		7.822	7.295	-0.527
Min		6.974	6.275	-1.880
Std Dev		0.376	0.869	1.136



13.2 __OUTB_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

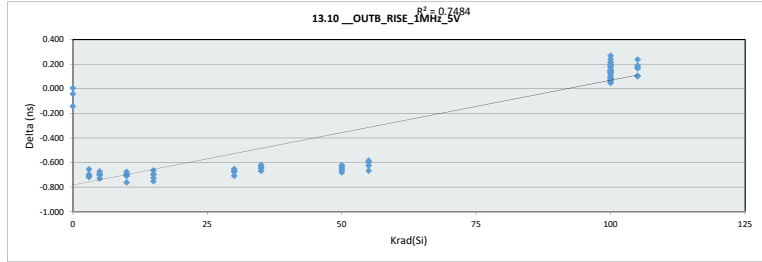
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.103	6.275	6.326	6.284	6.317	6.562	6.407	6.516	6.497	7.598	7.936
Average	7.181	6.528	6.495	6.461	6.501	6.638	6.583	6.621	6.646	8.251	8.433
Max	7.235	6.642	6.590	6.643	6.616	6.695	6.787	6.708	6.919	8.702	8.762
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

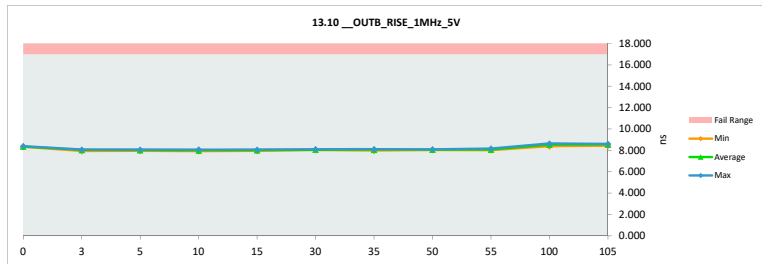
13.10_OUTB_RISE_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.471	8.330	-0.141
0	297	8.349	8.357	0.008
0	298	8.444	8.403	-0.041
3	192	8.756	8.053	-0.703
3	193	8.772	8.056	-0.716
3	194	8.589	7.936	-0.653
3	196	8.788	8.076	-0.712
3	197	8.771	8.076	-0.695
5	200	8.713	8.014	-0.699
5	202	8.616	7.946	-0.670
5	203	8.766	8.065	-0.701
5	204	8.803	8.076	-0.727
5	206	8.725	8.036	-0.689
10	207	8.834	8.074	-0.760
10	209	8.800	8.092	-0.708
10	210	8.604	7.930	-0.674
10	211	8.641	7.942	-0.699
10	212	8.655	7.964	-0.691
15	214	8.651	7.957	-0.694
15	217	8.807	8.057	-0.750
15	218	8.772	8.078	-0.694
15	219	8.770	8.047	-0.723
15	222	8.619	7.957	-0.662
30	224	8.713	8.048	-0.665
30	225	8.787	8.112	-0.675
30	226	8.739	8.032	-0.707
30	227	8.750	8.078	-0.672
30	229	8.767	8.116	-0.651
35	230	8.601	7.976	-0.625
35	231	8.701	8.083	-0.618
35	232	8.704	8.038	-0.666
35	234	8.757	8.111	-0.646
35	237	8.688	8.047	-0.641
50	272	8.763	8.104	-0.659
50	275	8.769	8.091	-0.678
50	276	8.717	8.096	-0.621
50	277	8.702	8.059	-0.643
50	278	8.659	8.033	-0.626
55	279	8.625	8.031	-0.594
55	280	8.712	8.047	-0.665
55	281	8.602	8.019	-0.583
55	282	8.653	8.058	-0.595
55	283	8.806	8.183	-0.623
100	317	8.320	8.388	0.068
100	318	8.393	8.572	0.179
100	319	8.474	8.598	0.124
100	320	8.495	8.645	0.150
100	321	8.317	8.534	0.217
100	322	8.291	8.447	0.156
100	323	8.190	8.460	0.270
100	324	8.407	8.605	0.198
100	326	8.273	8.516	0.243
100	327	8.433	8.519	0.086
100	328	8.320	8.467	0.147
100	329	8.369	8.453	0.084
100	332	8.410	8.627	0.217
100	333	8.434	8.613	0.179
100	334	8.398	8.487	0.089
100	335	8.438	8.488	0.050
100	336	8.488	8.637	0.149
100	337	8.429	8.501	0.072
100	338	8.455	8.508	0.053
100	339	8.417	8.507	0.090
100	340	8.404	8.598	0.194
100	341	8.447	8.586	0.139
100	342	8.499	8.606	0.107
100	343	8.435	8.569	0.134
105	344	8.330	8.433	0.103
105	346	8.505	8.613	0.108
105	349	8.364	8.604	0.240
105	350	8.348	8.516	0.168
105	351	8.411	8.601	0.190
105	352	8.379	8.555	0.176
Max		8.834	8.645	0.270
Average		8.573	8.262	-0.311
Min		8.190	7.930	-0.760
Std Dev		0.174	0.251	0.406



13.10_OUTB_RIS	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

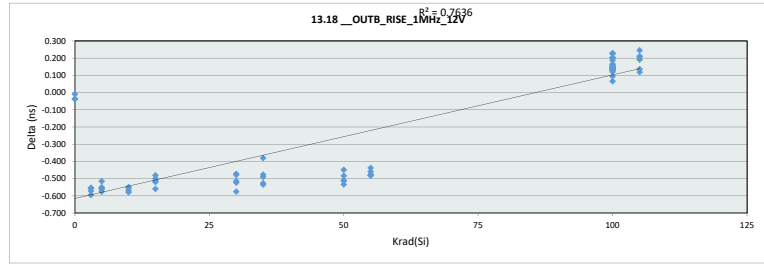
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.330	7.936	7.946	7.930	7.957	8.032	7.976	8.033	8.019	8.388	8.433
Average	8.363	8.039	8.027	8.000	8.019	8.077	8.051	8.077	8.068	8.539	8.554
Max	8.403	8.076	8.076	8.092	8.078	8.116	8.111	8.104	8.183	8.645	8.613
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

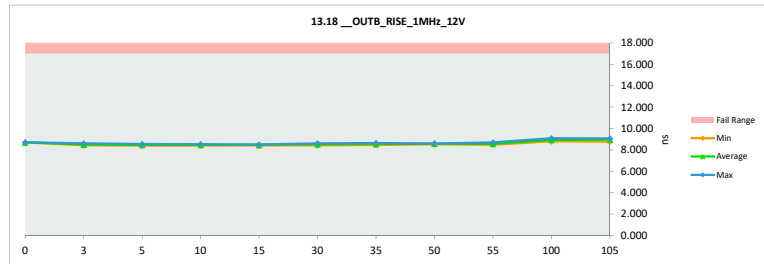
13.18 __OUTB_RISE_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.739	8.704	-0.035
0	297	8.730	8.693	-0.037
0	298	8.703	8.695	-0.008
3	192	9.077	8.507	-0.570
3	193	9.011	8.456	-0.555
3	194	8.972	8.420	-0.552
3	196	9.195	8.601	-0.594
3	197	9.099	8.544	-0.555
5	200	8.980	8.431	-0.549
5	202	8.958	8.399	-0.559
5	203	9.054	8.540	-0.514
5	204	9.068	8.512	-0.556
5	206	9.046	8.472	-0.574
10	207	9.078	8.526	-0.552
10	209	9.056	8.478	-0.578
10	210	8.959	8.411	-0.548
10	211	9.058	8.491	-0.567
10	212	8.974	8.424	-0.550
15	214	8.999	8.481	-0.518
15	217	8.955	8.457	-0.498
15	218	9.057	8.499	-0.558
15	219	8.932	8.453	-0.479
15	222	8.910	8.398	-0.512
30	224	8.897	8.420	-0.477
30	225	9.025	8.554	-0.471
30	226	9.176	8.602	-0.574
30	227	8.978	8.465	-0.513
30	229	9.132	8.610	-0.522
35	230	8.984	8.458	-0.526
35	231	9.011	8.632	-0.379
35	232	9.152	8.619	-0.533
35	234	9.033	8.544	-0.489
35	237	8.974	8.498	-0.476
50	272	9.074	8.562	-0.512
50	275	9.131	8.599	-0.532
50	276	9.062	8.580	-0.482
50	277	9.036	8.588	-0.448
50	278	9.026	8.517	-0.509
55	279	8.913	8.476	-0.437
55	280	9.152	8.694	-0.458
55	281	8.994	8.514	-0.480
55	282	9.042	8.563	-0.479
55	283	9.073	8.598	-0.475
100	317	8.670	8.799	0.129
100	318	8.769	8.922	0.153
100	319	8.821	8.888	0.067
100	320	8.853	8.974	0.121
100	321	8.852	9.079	0.227
100	322	8.707	8.908	0.201
100	323	8.798	9.004	0.206
100	324	8.778	8.909	0.131
100	326	8.637	8.868	0.231
100	327	8.842	8.994	0.152
100	328	8.728	8.870	0.142
100	329	8.724	8.952	0.228
100	332	8.781	8.936	0.155
100	333	8.814	8.953	0.139
100	334	8.819	8.965	0.146
100	335	8.695	8.857	0.162
100	336	8.889	8.985	0.096
100	337	8.730	8.864	0.134
100	338	8.723	8.912	0.189
100	339	8.658	8.809	0.151
100	340	8.720	8.866	0.146
100	341	8.810	9.013	0.203
100	342	8.779	8.917	0.138
100	343	8.792	8.959	0.167
105	344	8.777	8.915	0.138
105	346	8.812	9.024	0.212
105	349	8.750	8.997	0.247
105	350	8.762	8.955	0.193
105	351	8.864	9.073	0.209
105	352	8.639	8.759	0.120
Max		9.195	9.079	0.247
Average		8.911	8.693	-0.217
Min		8.637	8.398	-0.594
Std Dev		0.152	0.215	0.338



13.18 __OUTB_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

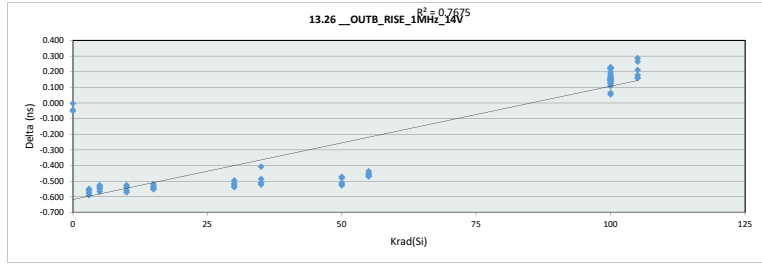
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.693	8.420	8.399	8.411	8.398	8.420	8.458	8.517	8.476	8.799	8.759
Average	8.697	8.506	8.471	8.466	8.458	8.530	8.550	8.569	8.569	8.925	8.954
Max	8.704	8.601	8.540	8.526	8.499	8.610	8.632	8.599	8.694	9.079	9.073
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

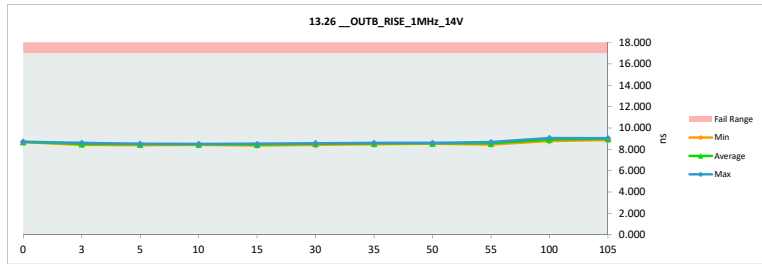
13.26_OUTB_RISE_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	8.712	8.710	-0.002
0	297	8.714	8.670	-0.044
0	298	8.705	8.655	-0.050
3	192	9.055	8.507	-0.548
3	193	8.984	8.430	-0.554
3	194	8.972	8.413	-0.559
3	196	9.187	8.599	-0.588
3	197	9.100	8.526	-0.574
5	200	8.980	8.433	-0.547
5	202	8.925	8.382	-0.543
5	203	9.058	8.525	-0.533
5	204	9.078	8.512	-0.566
5	206	8.996	8.471	-0.525
10	207	9.076	8.506	-0.570
10	209	9.012	8.487	-0.525
10	210	8.944	8.406	-0.538
10	211	9.027	8.500	-0.527
10	212	8.976	8.417	-0.559
15	214	9.022	8.472	-0.550
15	217	8.956	8.439	-0.517
15	218	9.031	8.495	-0.536
15	219	8.963	8.432	-0.531
15	222	8.898	8.350	-0.548
30	224	8.898	8.404	-0.494
30	225	9.049	8.528	-0.521
30	226	9.120	8.583	-0.537
30	227	8.980	8.467	-0.513
30	229	9.118	8.585	-0.533
35	230	8.976	8.467	-0.509
35	231	8.996	8.590	-0.406
35	232	9.113	8.592	-0.521
35	234	9.040	8.531	-0.509
35	237	8.979	8.494	-0.485
50	272	9.065	8.539	-0.526
50	275	9.104	8.595	-0.509
50	276	9.035	8.565	-0.470
50	277	9.036	8.557	-0.479
50	278	9.025	8.508	-0.517
55	279	8.879	8.443	-0.436
55	280	9.133	8.677	-0.456
55	281	8.966	8.498	-0.468
55	282	9.016	8.565	-0.451
55	283	9.041	8.575	-0.466
100	317	8.639	8.766	0.127
100	318	8.766	8.913	0.147
100	319	8.818	8.928	0.110
100	320	8.899	8.965	0.066
100	321	8.825	9.056	0.231
100	322	8.737	8.865	0.128
100	323	8.788	8.942	0.154
100	324	8.701	8.922	0.221
100	326	8.612	8.835	0.223
100	327	8.782	8.968	0.186
100	328	8.683	8.908	0.225
100	329	8.722	8.887	0.165
100	332	8.741	8.960	0.219
100	333	8.834	8.973	0.139
100	334	8.754	8.928	0.174
100	335	8.647	8.806	0.159
100	336	8.803	8.960	0.157
100	337	8.661	8.809	0.148
100	338	8.789	8.846	0.057
100	339	8.681	8.842	0.161
100	340	8.677	8.877	0.200
100	341	8.822	8.948	0.126
100	342	8.789	8.942	0.153
100	343	8.786	8.929	0.143
105	344	8.699	8.964	0.265
105	346	8.859	9.020	0.161
105	349	8.833	8.995	0.162
105	350	8.658	8.946	0.288
105	351	8.826	9.038	0.212
105	352	8.682	8.863	0.181
Max		9.187	9.056	0.288
Average		8.897	8.681	-0.216
Min		8.612	8.350	-0.588
Std Dev		0.154	0.215	0.341



13.26_OUTB_RIS	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

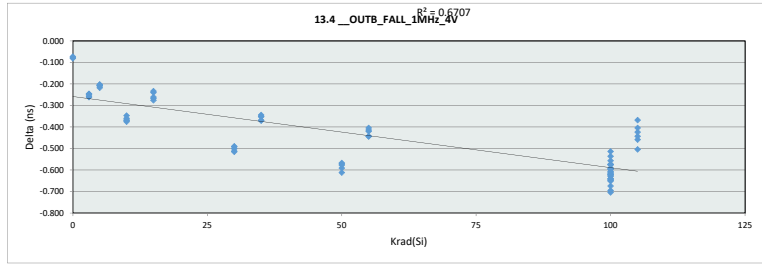
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.655	8.413	8.382	8.406	8.350	8.404	8.467	8.508	8.443	8.766	8.863
Average	8.678	8.495	8.465	8.463	8.438	8.513	8.535	8.553	8.552	8.907	8.971
Max	8.710	8.599	8.525	8.506	8.495	8.585	8.592	8.595	8.677	9.056	9.038
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

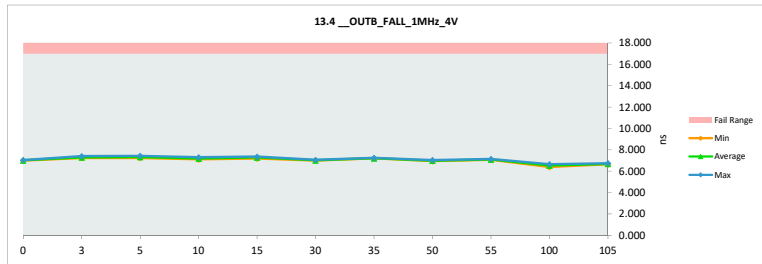
13.4 __OUTB_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(S)	Serial #	PRE	POST	Delta
0	295	7.091	7.019	-0.072
0	297	7.121	7.044	-0.077
0	298	7.080	7.000	-0.080
3	192	7.657	7.411	-0.246
3	193	7.511	7.252	-0.259
3	194	7.498	7.238	-0.260
3	196	7.566	7.321	-0.245
3	197	7.522	7.272	-0.250
5	200	7.499	7.291	-0.208
5	202	7.435	7.234	-0.201
5	203	7.643	7.436	-0.207
5	204	7.558	7.353	-0.205
5	206	7.521	7.304	-0.217
10	207	7.681	7.307	-0.374
10	209	7.548	7.182	-0.366
10	210	7.488	7.119	-0.369
10	211	7.541	7.182	-0.359
10	212	7.457	7.111	-0.346
15	214	7.533	7.301	-0.232
15	217	7.511	7.245	-0.266
15	218	7.591	7.331	-0.260
15	219	7.643	7.368	-0.275
15	222	7.429	7.190	-0.239
30	224	7.496	6.996	-0.500
30	225	7.577	7.066	-0.511
30	226	7.497	7.006	-0.491
30	227	7.519	7.005	-0.514
30	229	7.523	7.034	-0.489
35	230	7.576	7.208	-0.368
35	231	7.547	7.204	-0.343
35	232	7.617	7.248	-0.369
35	234	7.572	7.222	-0.350
35	237	7.556	7.205	-0.351
50	272	7.534	6.944	-0.590
50	275	7.566	6.955	-0.611
50	276	7.615	7.044	-0.571
50	277	7.592	7.026	-0.566
50	278	7.521	6.945	-0.576
55	279	7.485	7.066	-0.419
55	280	7.482	7.069	-0.413
55	281	7.545	7.142	-0.403
55	282	7.541	7.098	-0.443
55	283	7.531	7.089	-0.442
100	317	7.255	6.609	-0.646
100	318	7.053	6.444	-0.609
100	319	7.129	6.507	-0.622
100	320	7.032	6.392	-0.640
100	321	7.132	6.510	-0.622
100	322	7.204	6.610	-0.594
100	323	7.091	6.556	-0.535
100	324	7.172	6.576	-0.596
100	326	7.070	6.464	-0.606
100	327	7.142	6.523	-0.619
100	328	7.092	6.579	-0.513
100	329	7.180	6.608	-0.572
100	332	7.127	6.554	-0.573
100	333	7.119	6.416	-0.703
100	334	7.061	6.505	-0.556
100	335	7.178	6.549	-0.629
100	336	7.272	6.657	-0.615
100	337	7.163	6.524	-0.639
100	338	7.217	6.544	-0.673
100	339	7.151	6.457	-0.694
100	340	7.201	6.611	-0.590
100	341	7.170	6.525	-0.645
100	342	7.072	6.497	-0.575
100	343	7.159	6.509	-0.650
105	344	7.192	6.689	-0.503
105	346	7.023	6.656	-0.367
105	349	7.134	6.676	-0.458
105	350	7.066	6.643	-0.423
105	351	7.147	6.744	-0.403
105	352	7.144	6.702	-0.442
	Max	7.681	7.436	-0.072
	Average	7.358	6.917	-0.442
	Min	7.023	6.392	-0.703
	Std Dev	0.213	0.321	0.166



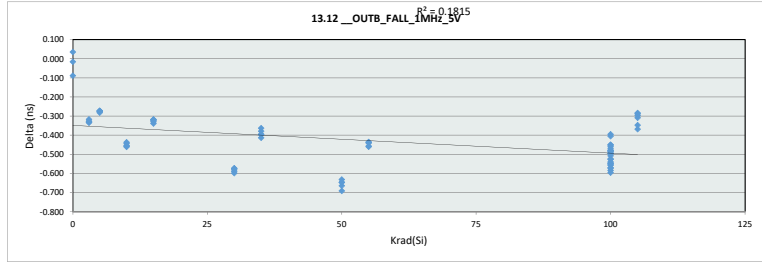
13.4 __OUTB_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.000	7.238	7.234	7.111	7.190	6.996	7.204	6.944	7.066	6.392	6.643
Average	7.021	7.299	7.324	7.180	7.287	7.021	7.217	6.983	7.093	6.530	6.685
Max	7.044	7.411	7.436	7.307	7.368	7.066	7.248	7.044	7.142	6.657	6.744
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

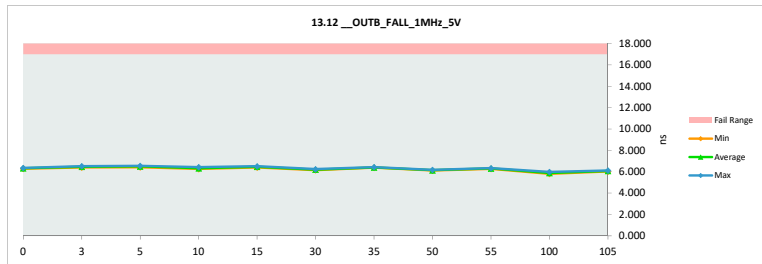


HDR TID Report  
TPS7H5004-SP

13.12_OUTB_FALL_1MHz_5V				
Test Site	Tester	Test Number	Unit	Max Limit
			ns	ns
			17	17
Krad(Si)	Serial #	PRE	POST	Delta
0	295	6.375	6.287	-0.088
0	297	6.349	6.334	-0.015
0	298	6.327	6.363	0.036
3	192	6.859	6.527	-0.332
3	193	6.765	6.436	-0.329
3	194	6.734	6.401	-0.333
3	196	6.796	6.471	-0.325
3	197	6.787	6.471	-0.316
5	200	6.738	6.464	-0.274
5	202	6.692	6.419	-0.273
5	203	6.842	6.561	-0.281
5	204	6.818	6.545	-0.273
5	206	6.778	6.507	-0.271
10	207	6.870	6.434	-0.436
10	209	6.819	6.364	-0.455
10	210	6.721	6.266	-0.455
10	211	6.788	6.328	-0.460
10	212	6.708	6.265	-0.443
15	214	6.803	6.465	-0.338
15	217	6.741	6.424	-0.317
15	218	6.831	6.502	-0.329
15	219	6.842	6.520	-0.322
15	222	6.720	6.403	-0.317
30	224	6.750	6.153	-0.597
30	225	6.819	6.247	-0.572
30	226	6.737	6.165	-0.572
30	227	6.784	6.196	-0.588
30	229	6.780	6.200	-0.580
35	230	6.793	6.399	-0.394
35	231	6.797	6.435	-0.362
35	232	6.830	6.418	-0.412
35	234	6.798	6.402	-0.396
35	237	6.763	6.385	-0.378
50	272	6.776	6.113	-0.663
50	275	6.814	6.124	-0.690
50	276	6.812	6.181	-0.631
50	277	6.815	6.169	-0.646
50	278	6.770	6.125	-0.645
55	279	6.732	6.273	-0.459
55	280	6.728	6.289	-0.439
55	281	6.757	6.323	-0.434
55	282	6.758	6.302	-0.456
55	283	6.774	6.336	-0.438
100	317	6.457	5.934	-0.523
100	318	6.331	5.835	-0.496
100	319	6.375	5.830	-0.545
100	320	6.374	5.829	-0.545
100	321	6.402	5.833	-0.569
100	322	6.411	5.963	-0.448
100	323	6.303	5.909	-0.394
100	324	6.361	5.910	-0.451
100	326	6.336	5.866	-0.470
100	327	6.373	5.914	-0.459
100	328	6.386	5.803	-0.583
100	329	6.440	5.845	-0.595
100	332	6.389	5.985	-0.404
100	333	6.372	5.865	-0.507
100	334	6.412	5.858	-0.554
100	335	6.402	5.831	-0.571
100	336	6.428	5.945	-0.483
100	337	6.438	5.960	-0.478
100	338	6.393	5.868	-0.525
100	339	6.367	5.812	-0.555
100	340	6.399	5.905	-0.494
100	341	6.392	5.905	-0.487
100	342	6.319	5.839	-0.480
100	343	6.433	5.894	-0.539
105	344	6.370	6.083	-0.287
105	346	6.391	6.045	-0.346
105	349	6.371	6.063	-0.308
105	350	6.311	6.028	-0.283
105	351	6.418	6.123	-0.295
105	352	6.464	6.097	-0.367
	Max	6.870	6.561	0.036
	Average	6.601	6.172	-0.429
	Min	6.303	5.803	-0.690
	Std Dev	0.203	0.241	0.140



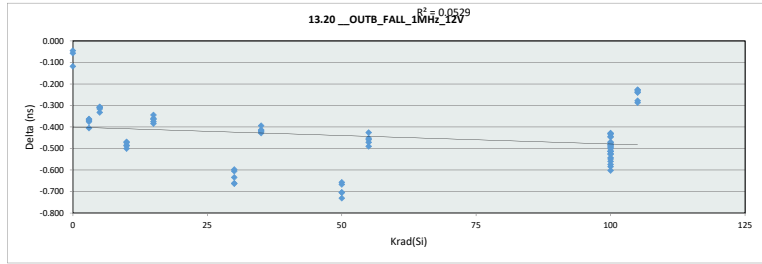
13.12_OUTB_FALL											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			17	ns							
			ns	ns							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.287	6.401	6.419	6.265	6.403	6.153	6.385	6.113	6.273	5.803	6.028
Average	6.328	6.461	6.499	6.331	6.463	6.192	6.408	6.142	6.305	5.881	6.073
Max	6.363	6.527	6.561	6.434	6.520	6.247	6.435	6.181	6.336	5.985	6.123
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

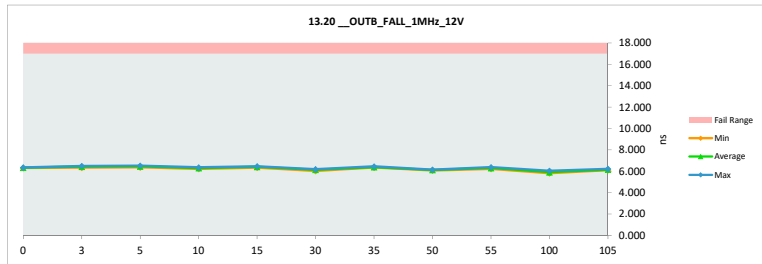
13.20_OUTB_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	6.420	6.303	-0.117
0	297	6.412	6.356	-0.056
0	298	6.377	6.333	-0.044
3	192	6.848	6.478	-0.370
3	193	6.734	6.330	-0.404
3	194	6.752	6.388	-0.364
3	196	6.853	6.491	-0.362
3	197	6.805	6.431	-0.374
5	200	6.713	6.404	-0.309
5	202	6.695	6.364	-0.331
5	203	6.829	6.521	-0.308
5	204	6.811	6.506	-0.305
5	206	6.780	6.465	-0.315
10	207	6.828	6.360	-0.468
10	209	6.778	6.279	-0.499
10	210	6.735	6.248	-0.487
10	211	6.820	6.348	-0.472
10	212	6.709	6.225	-0.484
15	214	6.805	6.462	-0.343
15	217	6.723	6.340	-0.383
15	218	6.819	6.445	-0.374
15	219	6.779	6.416	-0.363
15	222	6.736	6.377	-0.359
30	224	6.692	6.030	-0.662
30	225	6.785	6.181	-0.604
30	226	6.785	6.152	-0.633
30	227	6.752	6.091	-0.661
30	229	6.786	6.189	-0.597
35	230	6.795	6.378	-0.417
35	231	6.798	6.386	-0.412
35	232	6.870	6.451	-0.419
35	234	6.793	6.366	-0.427
35	237	6.740	6.347	-0.393
50	272	6.776	6.073	-0.703
50	275	6.847	6.117	-0.730
50	276	6.803	6.147	-0.656
50	277	6.813	6.147	-0.666
50	278	6.790	6.086	-0.704
55	279	6.695	6.225	-0.470
55	280	6.805	6.380	-0.425
55	281	6.774	6.316	-0.458
55	282	6.769	6.318	-0.451
55	283	6.766	6.277	-0.489
100	317	6.492	5.909	-0.583
100	318	6.330	5.817	-0.513
100	319	6.407	5.859	-0.548
100	320	6.411	5.899	-0.512
100	321	6.461	5.982	-0.479
100	322	6.447	6.016	-0.431
100	323	6.475	6.044	-0.431
100	324	6.382	5.936	-0.446
100	326	6.349	5.870	-0.479
100	327	6.443	5.948	-0.495
100	328	6.375	5.906	-0.469
100	329	6.474	5.932	-0.542
100	332	6.334	5.907	-0.427
100	333	6.481	5.880	-0.601
100	334	6.422	5.863	-0.559
100	335	6.318	5.818	-0.500
100	336	6.488	5.977	-0.511
100	337	6.332	5.843	-0.489
100	338	6.439	5.866	-0.573
100	339	6.295	5.852	-0.443
100	340	6.384	5.912	-0.472
100	341	6.413	5.928	-0.485
100	342	6.402	5.879	-0.523
100	343	6.452	5.926	-0.526
105	344	6.417	6.131	-0.286
105	346	6.381	6.104	-0.277
105	349	6.364	6.136	-0.228
105	350	6.360	6.134	-0.226
105	351	6.456	6.220	-0.236
105	352	6.360	6.121	-0.239
Max		6.870	6.521	-0.044
Average		6.610	6.166	-0.444
Min		6.295	5.817	-0.730
Std Dev		0.194	0.216	0.141



13.20_OUTB_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

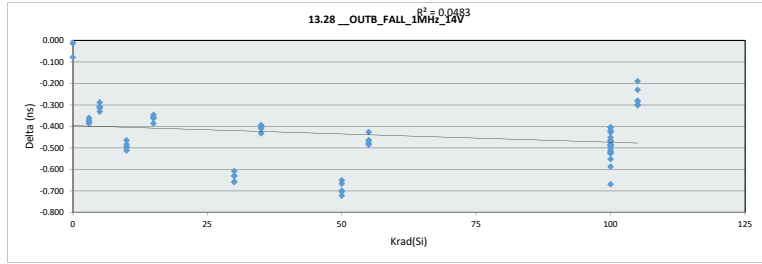
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.303	6.330	6.364	6.225	6.340	6.030	6.347	6.073	6.225	5.817	6.104
Average	6.331	6.424	6.452	6.292	6.408	6.129	6.386	6.114	6.303	5.907	6.141
Max	6.356	6.491	6.521	6.360	6.462	6.189	6.451	6.147	6.380	6.044	6.220
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



HDR TID Report  
TPS7H5004-SP

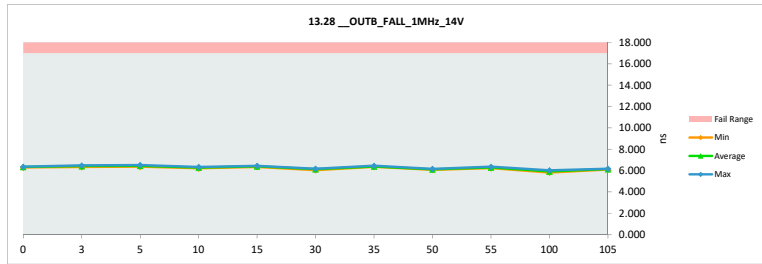
13.28_OUTB_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	295	6.375	6.363	-0.012
0	297	6.381	6.370	-0.011
0	298	6.372	6.295	-0.077
3	192	6.857	6.482	-0.375
3	193	6.724	6.339	-0.385
3	194	6.771	6.389	-0.382
3	196	6.849	6.490	-0.359
3	197	6.805	6.435	-0.370
5	200	6.717	6.409	-0.308
5	202	6.696	6.366	-0.330
5	203	6.809	6.522	-0.287
5	204	6.807	6.499	-0.308
5	206	6.769	6.455	-0.314
10	207	6.828	6.346	-0.482
10	209	6.777	6.266	-0.511
10	210	6.733	6.235	-0.498
10	211	6.812	6.349	-0.463
10	212	6.714	6.220	-0.494
15	214	6.817	6.432	-0.385
15	217	6.697	6.341	-0.356
15	218	6.814	6.454	-0.360
15	219	6.765	6.420	-0.345
15	222	6.723	6.363	-0.360
30	224	6.709	6.052	-0.657
30	225	6.799	6.171	-0.628
30	226	6.797	6.167	-0.630
30	227	6.757	6.100	-0.657
30	229	6.793	6.186	-0.607
35	230	6.792	6.388	-0.404
35	231	6.796	6.365	-0.431
35	232	6.853	6.459	-0.394
35	234	6.782	6.373	-0.409
35	237	6.750	6.356	-0.394
50	272	6.774	6.072	-0.702
50	275	6.835	6.114	-0.721
50	276	6.814	6.165	-0.649
50	277	6.813	6.148	-0.665
50	278	6.798	6.100	-0.698
55	279	6.719	6.235	-0.484
55	280	6.795	6.370	-0.425
55	281	6.770	6.307	-0.463
55	282	6.780	6.316	-0.464
55	283	6.766	6.289	-0.477
100	317	6.481	5.930	-0.551
100	318	6.380	5.858	-0.522
100	319	6.487	5.819	-0.668
100	320	6.350	5.864	-0.486
100	321	6.434	6.016	-0.418
100	322	6.483	5.995	-0.488
100	323	6.452	5.964	-0.488
100	324	6.362	5.958	-0.404
100	326	6.297	5.871	-0.426
100	327	6.425	5.936	-0.489
100	328	6.404	5.939	-0.465
100	329	6.367	5.944	-0.423
100	332	6.350	5.947	-0.403
100	333	6.412	5.945	-0.467
100	334	6.388	5.875	-0.513
100	335	6.351	5.869	-0.482
100	336	6.481	6.031	-0.450
100	337	6.446	5.924	-0.522
100	338	6.395	5.894	-0.501
100	339	6.336	5.850	-0.486
100	340	6.412	5.887	-0.525
100	341	6.425	5.960	-0.465
100	342	6.396	5.882	-0.514
100	343	6.477	5.891	-0.586
105	344	6.458	6.161	-0.297
105	346	6.392	6.112	-0.280
105	349	6.352	6.163	-0.189
105	350	6.407	6.107	-0.300
105	351	6.467	6.186	-0.281
105	352	6.355	6.126	-0.229
Max		6.857	6.522	-0.011
Average		6.610	6.171	-0.439
Min		6.297	5.819	-0.721
Std Dev		0.193	0.210	0.145



13.28_OUTB_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.295	6.339	6.366	6.220	6.341	6.052	6.356	6.072	6.235	5.819	6.107
Average	6.343	6.427	6.450	6.283	6.402	6.135	6.388	6.120	6.303	5.919	6.143
Max	6.370	6.490	6.522	6.349	6.454	6.186	6.459	6.165	6.370	6.031	6.186
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

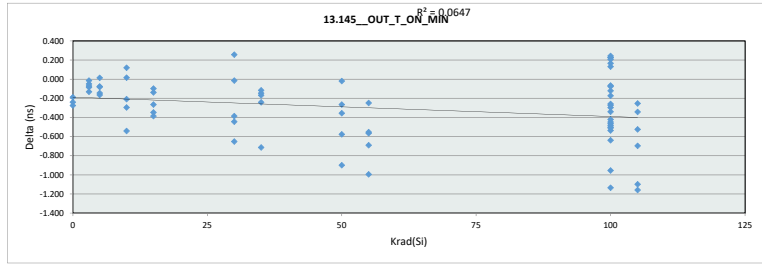




HDR TID Report  
TPS7H5004-SP

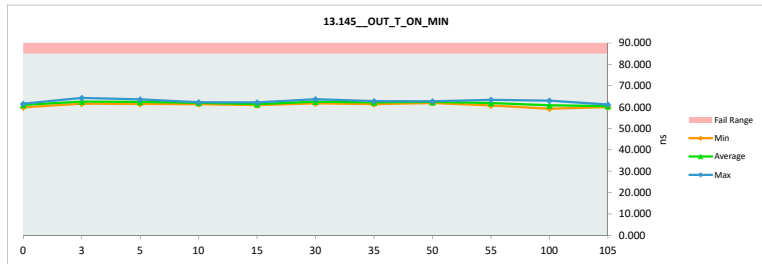
13.145_OUT_T_ON_MIN	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	85 85
Min Limit	

Krad(S)	Serial #	PRE	POST	Delta
0	295	61.702	61.516	-0.186
0	297	61.775	61.537	-0.238
0	298	60.132	59.859	-0.273
3	192	61.915	61.785	-0.130
3	193	64.322	64.275	-0.047
3	194	61.637	61.567	-0.070
3	196	62.168	62.086	-0.082
3	197	62.918	62.905	-0.013
5	200	62.900	62.737	-0.163
5	202	61.435	61.451	0.016
5	203	61.949	61.875	-0.074
5	204	63.757	63.615	-0.142
5	206	62.307	62.227	-0.080
10	207	62.176	61.637	-0.539
10	209	61.338	61.355	0.017
10	210	62.414	62.120	-0.294
10	211	62.152	62.274	0.122
10	212	62.462	62.255	-0.207
15	214	61.187	60.923	-0.264
15	217	62.059	61.963	-0.096
15	218	62.521	62.175	-0.346
15	219	61.026	60.890	-0.136
15	222	61.306	60.923	-0.383
30	224	62.876	62.226	-0.650
30	225	62.996	62.554	-0.442
30	226	63.680	63.667	-0.013
30	227	61.500	61.758	0.258
30	229	62.832	62.448	-0.384
35	230	61.621	61.477	-0.144
35	231	61.562	61.322	-0.240
35	232	62.708	62.593	-0.115
35	234	63.502	62.791	-0.711
35	237	62.712	62.546	-0.166
50	272	63.138	62.241	-0.897
50	275	63.225	62.650	-0.575
50	276	62.680	62.327	-0.353
50	277	62.685	62.667	-0.018
50	278	62.190	61.926	-0.264
55	279	62.236	61.243	-0.993
55	280	62.914	62.351	-0.563
55	281	61.459	60.770	-0.689
55	282	62.338	61.787	-0.551
55	283	63.594	63.348	-0.246
100	317	59.847	59.428	-0.419
100	318	62.837	63.005	0.168
100	319	62.060	61.525	-0.535
100	320	62.170	62.303	0.133
100	321	61.064	60.726	-0.338
100	322	59.401	59.229	-0.172
100	323	60.528	60.029	-0.499
100	324	62.719	62.273	-0.446
100	326	60.726	60.456	-0.270
100	327	61.760	61.252	-0.508
100	328	60.116	59.997	-0.119
100	329	60.583	59.631	-0.952
100	332	61.708	60.575	-1.133
100	333	62.662	62.598	-0.064
100	334	60.217	60.446	0.229
100	335	60.187	59.706	-0.481
100	336	59.920	59.284	-0.636
100	337	62.041	62.269	0.228
100	338	61.482	61.187	-0.295
100	339	59.873	59.411	-0.462
100	340	61.448	61.658	0.210
100	341	61.007	60.750	-0.257
100	342	61.509	61.435	-0.074
100	343	60.949	61.195	0.246
105	344	61.178	60.021	-1.157
105	346	60.979	60.727	-0.252
105	349	60.817	60.294	-0.523
105	350	60.541	60.200	-0.341
105	351	62.277	61.181	-1.096
105	352	60.877	60.182	-0.695
	Max	64.322	64.275	0.258
	Average	61.829	61.529	-0.300
	Min	59.401	59.229	-1.157
	Std Dev	1.057	1.120	0.328



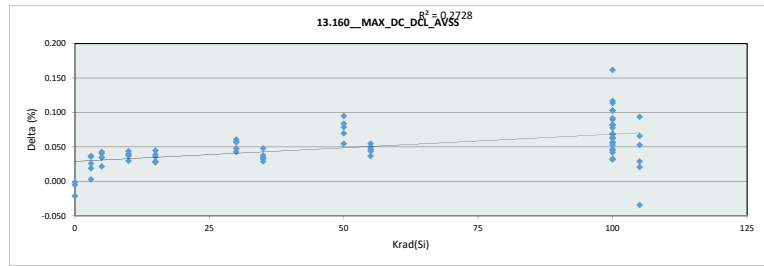
13.145_OUT_T_O	
Test Site	
Tester	
Test Number	
Max Limit	85 ns
Min Limit	ns

Krad(S)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	59.859	61.567	61.451	61.355	60.890	61.758	61.322	61.926	60.770	59.229	60.021
Average	60.971	62.524	62.381	61.928	61.375	62.531	62.146	62.362	61.900	60.849	60.434
Max	61.537	64.275	63.615	62.274	62.175	63.667	62.791	62.667	63.348	63.005	61.181
UL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000

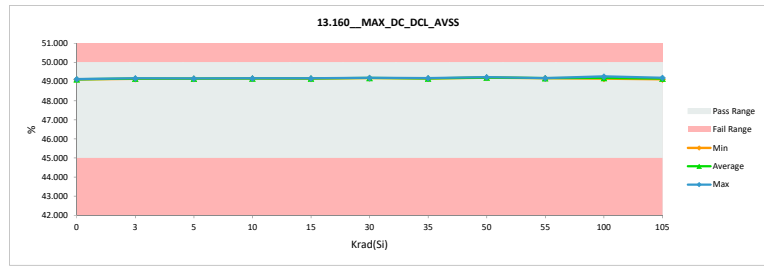


HDR TID Report  
TPS7H5004-SP

13.160_MAX_DC_DCL_AVSS				
Test Site	Tester	Test Number	Unit	
			%	%
			50	50
			45	45
Krad(Si)	Serial #	PRE	POST	Delta
0	295	49.122	49.117	-0.005
0	297	49.123	49.122	-0.001
0	298	49.155	49.134	-0.021
3	192	49.140	49.176	0.036
3	193	49.138	49.157	0.019
3	194	49.119	49.145	0.026
3	196	49.136	49.173	0.037
3	197	49.148	49.151	0.003
5	200	49.126	49.161	0.035
5	202	49.116	49.157	0.041
5	203	49.125	49.168	0.043
5	204	49.126	49.161	0.035
5	206	49.127	49.149	0.022
10	207	49.117	49.161	0.044
10	209	49.151	49.181	0.030
10	210	49.120	49.158	0.038
10	211	49.130	49.168	0.038
10	212	49.139	49.179	0.040
15	214	49.126	49.165	0.039
15	217	49.134	49.170	0.036
15	218	49.136	49.165	0.029
15	219	49.133	49.178	0.045
15	222	49.121	49.149	0.028
30	224	49.141	49.202	0.061
30	225	49.159	49.202	0.043
30	226	49.151	49.199	0.048
30	227	49.127	49.185	0.058
30	229	49.131	49.188	0.057
35	230	49.134	49.169	0.035
35	231	49.137	49.170	0.033
35	232	49.133	49.162	0.029
35	234	49.139	49.187	0.048
35	237	49.133	49.171	0.038
50	272	49.145	49.240	0.095
50	275	49.138	49.222	0.084
50	276	49.154	49.209	0.055
50	277	49.137	49.207	0.070
50	278	49.132	49.211	0.079
55	279	49.130	49.185	0.055
55	280	49.134	49.184	0.050
55	281	49.150	49.187	0.037
55	282	49.130	49.177	0.047
55	283	49.139	49.183	0.044
100	317	49.139	49.184	0.045
100	318	49.131	49.194	0.063
100	319	49.127	49.209	0.082
100	320	49.107	49.163	0.056
100	321	49.095	49.209	0.114
100	322	49.108	49.200	0.092
100	323	49.091	49.208	0.117
100	324	49.134	49.187	0.053
100	326	49.116	49.194	0.078
100	327	49.136	49.226	0.090
100	328	49.133	49.190	0.057
100	329	49.146	49.178	0.032
100	332	49.176	49.209	0.033
100	333	49.133	49.202	0.069
100	334	49.141	49.205	0.064
100	335	49.123	49.186	0.063
100	336	49.137	49.206	0.069
100	337	49.099	49.181	0.082
100	338	49.097	49.200	0.103
100	339	49.111	49.273	0.162
100	340	49.144	49.186	0.042
100	341	49.085	49.188	0.103
100	342	49.137	49.184	0.047
100	343	49.096	49.153	0.057
105	344	49.090	49.119	0.029
105	346	49.118	49.171	0.053
105	349	49.108	49.202	0.094
105	350	49.154	49.175	0.021
105	351	49.163	49.129	-0.034
105	352	49.105	49.171	0.066
	Max	49.176	49.273	0.162
	Average	49.130	49.180	0.051
	Min	49.085	49.117	-0.034
	Std Dev	0.018	0.027	0.031



13.160_MAX_DC											
Test Site	Tester	Test Number	Max Limit	Min Limit							
			50	45							
			%	%							
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000
Min	49.117	49.145	49.149	49.158	49.149	49.185	49.162	49.207	49.177	49.153	49.119
Average	49.124	49.160	49.159	49.169	49.165	49.195	49.172	49.218	49.183	49.196	49.161
Max	49.134	49.176	49.168	49.181	49.178	49.202	49.187	49.240	49.187	49.273	49.202
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



## C Appendix: LDR TID Report Data

### Description of identifiers for LDR TID report

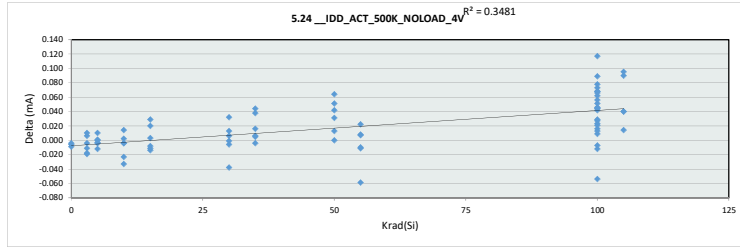
IDENTIFIER - Krad(Si)	DESCRIPTION
0	Control Unit
3	3 krad(Si) biased
5	3 krad(Si) unbiased
10	10 krad(Si) biased
15	10 krad(Si) unbiased
30	30 krad(Si) biased
35	30 krad(Si) unbiased
50	50 krad(Si) biased
55	50 krad(Si) unbiased
100	100 krad(Si) biased
105	100 krad(Si) unbiased

LDR TID Report  
TPS7H5001-SP

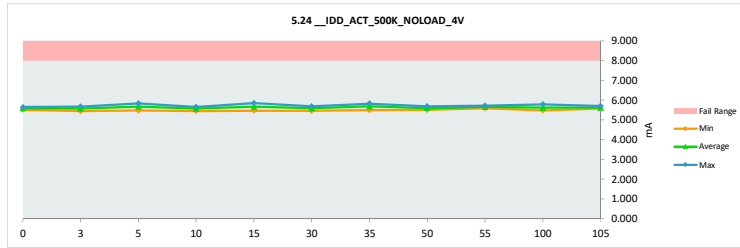
LDR TID Report  
TPS7H5001-SP

5.24 IDD_ACT_500K_NOLOAD_4V			
Test Site			
Tester			
Test Number			
Unit	mA	mA	
Max Limit	8	8	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.527	5.521	-0.006
0	118	5.630	5.626	-0.004
0	122	5.659	5.650	-0.009
3	61	5.652	5.633	-0.019
3	62	5.685	5.681	-0.004
3	63	5.604	5.610	0.006
3	64	5.652	5.641	-0.011
3	65	5.460	5.442	-0.018
3	66	5.478	5.488	0.010
5	67	5.562	5.562	0.000
5	68	5.709	5.705	-0.004
5	69	5.487	5.488	0.001
5	70	5.834	5.844	0.010
5	71	5.678	5.666	-0.012
5	72	5.758	5.755	-0.003
10	73	5.652	5.619	-0.033
10	74	5.685	5.662	-0.023
10	75	5.604	5.618	0.014
10	76	5.652	5.648	-0.004
10	77	5.460	5.456	-0.004
10	78	5.478	5.480	0.002
15	79	5.562	5.591	0.029
15	80	5.709	5.701	-0.008
15	81	5.487	5.473	-0.014
15	82	5.834	5.854	0.020
15	83	5.678	5.667	-0.011
15	84	5.758	5.761	0.003
30	86	5.652	5.665	0.013
30	89	5.685	5.691	0.006
30	91	5.604	5.636	0.032
30	92	5.652	5.614	-0.038
30	94	5.460	5.459	-0.001
30	95	5.478	5.472	-0.006
35	100	5.562	5.606	0.044
35	101	5.709	5.747	0.038
35	102	5.487	5.493	0.006
35	103	5.834	5.830	-0.004
35	104	5.678	5.683	0.005
35	105	5.758	5.774	0.016
50	106	5.656	5.687	0.031
50	107	5.560	5.602	0.042
50	108	5.451	5.515	0.064
50	109	5.608	5.659	0.051
50	110	5.628	5.641	0.013
50	111	5.510	5.510	0.000
55	113	5.675	5.682	0.007
55	114	5.611	5.600	-0.011
55	115	5.662	5.684	0.022
55	116	5.619	5.627	0.008
55	126	5.741	5.731	-0.010
55	128	5.668	5.609	-0.059
100	129	5.669	5.747	0.078
100	131	5.550	5.496	-0.054
100	132	5.672	5.665	-0.007
100	133	5.670	5.714	0.044
100	134	5.559	5.604	0.045
100	135	5.598	5.586	-0.012
100	138	5.614	5.680	0.066
100	139	5.476	5.544	0.068
100	140	5.587	5.633	0.046
100	141	5.500	5.521	0.021
100	142	5.650	5.723	0.073
100	143	5.625	5.693	0.068
100	144	5.556	5.598	0.042
100	220	5.664	5.677	0.013
100	221	5.540	5.556	0.016
100	224	5.578	5.634	0.056
100	225	5.453	5.480	0.027
100	226	5.736	5.787	0.051
100	228	5.544	5.661	0.117
100	232	5.603	5.692	0.089
100	233	5.611	5.634	0.023
100	236	5.469	5.498	0.029
100	238	5.659	5.721	0.062
100	243	5.520	5.529	0.009
105	244	5.594	5.608	0.014
105	246	5.494	5.589	0.095
105	248	5.642	5.682	0.040
105	374	5.614	5.704	0.090
105	376	5.532	5.572	0.040
105	Max	5.834	5.854	0.020
105	Average	5.611	5.629	0.018
105	Min	5.451	5.442	-0.009
105	Std Dev	0.094	0.095	0.034



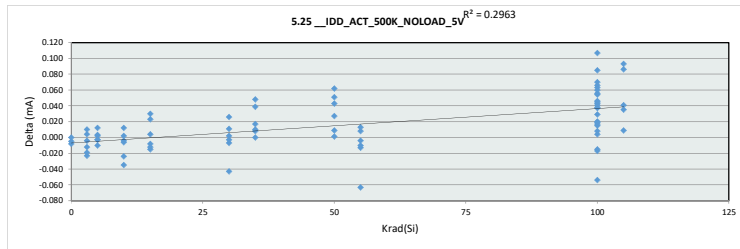
5.24 IDD_ACT_500K_NOLOAD_4V											
Test Site											
Tester											
Test Number											
Max Limit	8	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	5.521	5.442	5.488	5.456	5.473	5.459	5.493	5.510	5.600	5.480	5.572
Average	5.599	5.583	5.670	5.581	5.675	5.590	5.689	5.602	5.656	5.628	5.631
Max	5.650	5.681	5.844	5.662	5.854	5.691	5.830	5.687	5.731	5.787	5.704
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000



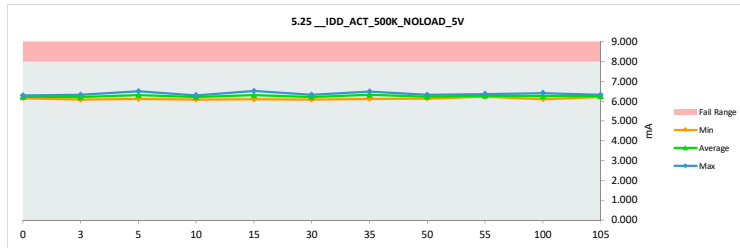
LDR TID Report  
TPS7H5001-SP

		5.25 IDD_ACT_500K_NOLOAD_5V	
Test Site			
Tester			
Test Number			
Unit		mA	mA
Max Limit		8	8
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.164	6.159	-0.005
0	118	6.264	6.264	0.000
0	122	6.300	6.292	-0.008
3	61	6.297	6.274	-0.023
3	62	6.329	6.325	-0.004
3	63	6.238	6.242	0.004
3	64	6.295	6.283	-0.012
3	65	6.098	6.079	-0.019
3	66	6.117	6.127	0.010
5	67	6.196	6.199	0.003
5	68	6.344	6.341	-0.003
5	69	6.117	6.119	0.002
5	70	6.492	6.504	0.012
5	71	6.316	6.306	-0.010
5	72	6.394	6.391	-0.003
10	73	6.297	6.262	-0.035
10	74	6.329	6.305	-0.024
10	75	6.238	6.250	0.012
10	76	6.295	6.289	-0.006
10	77	6.098	6.094	-0.004
10	78	6.117	6.119	0.002
15	79	6.196	6.226	0.030
15	80	6.344	6.336	-0.008
15	81	6.117	6.102	-0.015
15	82	6.492	6.515	0.023
15	83	6.316	6.304	-0.012
15	84	6.394	6.398	0.004
30	86	6.297	6.308	0.011
30	89	6.329	6.331	0.002
30	91	6.238	6.264	0.026
30	92	6.295	6.252	-0.043
30	94	6.098	6.095	-0.003
30	95	6.117	6.110	-0.007
35	100	6.196	6.244	0.048
35	101	6.344	6.383	0.039
35	102	6.117	6.127	0.010
35	103	6.492	6.492	0.000
35	104	6.316	6.324	0.008
35	105	6.394	6.411	0.017
50	106	6.299	6.326	0.027
50	107	6.201	6.244	0.043
50	108	6.088	6.150	0.062
50	109	6.248	6.299	0.051
50	110	6.266	6.275	0.009
50	111	6.146	6.147	0.001
55	113	6.312	6.308	-0.004
55	114	6.244	6.231	-0.013
55	115	6.296	6.309	0.013
55	116	6.256	6.264	0.008
55	126	6.382	6.372	-0.010
55	128	6.312	6.249	-0.063
100	129	6.307	6.377	0.070
100	131	6.184	6.130	-0.054
100	132	6.312	6.297	-0.015
100	133	6.309	6.350	0.041
100	134	6.198	6.241	0.043
100	135	6.239	6.222	-0.017
100	138	6.249	6.315	0.066
100	139	6.107	6.167	0.060
100	140	6.221	6.266	0.045
100	141	6.138	6.153	0.015
100	142	6.285	6.341	0.056
100	143	6.260	6.323	0.063
100	144	6.190	6.227	0.037
100	220	6.301	6.309	0.008
100	221	6.177	6.194	0.017
100	224	6.213	6.259	0.046
100	225	6.093	6.112	0.019
100	226	6.382	6.420	0.038
100	228	6.179	6.286	0.107
100	232	6.242	6.327	0.085
100	233	6.267	6.287	0.020
100	236	6.105	6.134	0.029
100	238	6.299	6.353	0.054
100	243	6.154	6.158	0.004
105	244	6.229	6.238	0.009
105	246	6.127	6.220	0.093
105	248	6.279	6.314	0.035
105	374	6.248	6.334	0.086
105	376	6.172	6.213	0.041
105	Max	6.492	6.515	0.023
105	Average	6.249	6.265	0.016
105	Min	6.088	6.079	-0.009
105	Std Dev	0.097	0.097	0.032



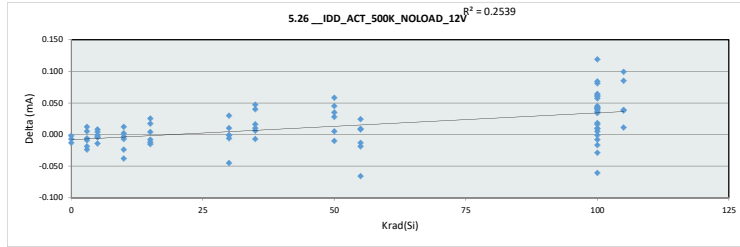
		5.25 IDD_ACT_5V									
Test Site											
Tester											
Test Number											
Max Limit		8	mA								
Min Limit			mA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.159	6.079	6.119	6.094	6.102	6.095	6.127	6.147	6.231	6.112	6.213
Average	6.238	6.222	6.310	6.220	6.314	6.227	6.330	6.240	6.289	6.260	6.264
Max	6.292	6.325	6.504	6.305	6.515	6.331	6.492	6.326	6.372	6.420	6.334
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000



LDR TID Report  
TPS7H5001-SP

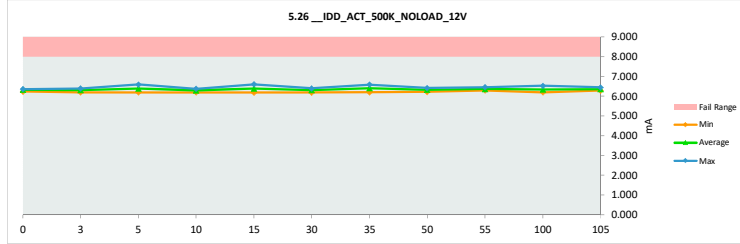
5.26 IDD_ACT_500K_NOLOAD_12V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	8
Min Limit	8

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.237	6.230	-0.007
0	118	6.359	6.357	-0.002
0	122	6.374	6.381	-0.013
3	61	6.383	6.359	-0.024
3	62	6.393	6.387	-0.006
3	63	6.298	6.303	0.005
3	64	6.376	6.367	-0.009
3	65	6.206	6.187	-0.019
3	66	6.188	6.200	0.012
5	67	6.270	6.269	-0.001
5	68	6.415	6.411	-0.004
5	69	6.185	6.189	0.004
5	70	6.582	6.590	0.008
5	71	6.394	6.380	-0.014
5	72	6.469	6.465	-0.004
10	73	6.383	6.345	-0.038
10	74	6.393	6.369	-0.024
10	75	6.298	6.310	0.012
10	76	6.376	6.369	-0.007
10	77	6.206	6.203	-0.003
10	78	6.188	6.190	0.002
15	79	6.270	6.295	0.025
15	80	6.415	6.407	-0.008
15	81	6.185	6.173	-0.012
15	82	6.582	6.599	0.017
15	83	6.394	6.379	-0.015
15	84	6.469	6.473	0.004
30	86	6.383	6.393	0.010
30	89	6.393	6.392	-0.001
30	91	6.398	6.328	-0.070
30	92	6.376	6.331	-0.045
30	94	6.206	6.205	-0.001
30	95	6.188	6.182	-0.006
35	100	6.270	6.317	0.047
35	101	6.415	6.455	0.040
35	102	6.185	6.195	0.010
35	103	6.582	6.575	-0.007
35	104	6.394	6.400	0.006
35	105	6.469	6.485	0.016
50	106	6.381	6.409	0.028
50	107	6.290	6.325	0.035
50	108	6.179	6.237	0.058
50	109	6.309	6.354	0.045
50	110	6.361	6.366	0.005
50	111	6.222	6.212	-0.010
55	113	6.408	6.416	0.008
55	114	6.308	6.289	-0.019
55	115	6.357	6.381	0.024
55	116	6.323	6.332	0.009
55	126	6.462	6.449	-0.013
55	128	6.367	6.301	-0.066
100	129	6.404	6.488	0.084
100	131	6.255	6.194	-0.061
100	132	6.384	6.367	-0.017
100	133	6.411	6.448	0.037
100	134	6.274	6.314	0.040
100	135	6.308	6.279	-0.029
100	138	6.326	6.383	0.057
100	139	6.186	6.246	0.060
100	140	6.305	6.347	0.042
100	141	6.207	6.216	0.009
100	142	6.360	6.422	0.062
100	143	6.332	6.396	0.064
100	144	6.266	6.300	0.034
100	220	6.376	6.375	-0.001
100	221	6.246	6.251	0.005
100	224	6.294	6.339	0.045
100	225	6.205	6.215	0.010
100	226	6.481	6.524	0.043
100	228	6.263	6.382	0.119
100	232	6.324	6.405	0.081
100	233	6.358	6.374	0.016
100	236	6.192	6.210	0.018
100	238	6.367	6.409	0.042
100	243	6.241	6.233	-0.008
105	244	6.313	6.324	0.011
105	246	6.193	6.278	0.085
105	248	6.370	6.408	0.038
105	374	6.345	6.444	0.099
105	376	6.245	6.287	0.039
	Max	6.582	6.590	0.119
	Average	6.328	6.342	0.014
	Min	6.179	6.173	-0.066
	Std Dev	0.097	0.099	0.034



5.26 IDD_ACT_500K_NOLOAD_12V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	8
Min Limit	8

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.230	6.187	6.189	6.190	6.173	6.182	6.195	6.212	6.289	6.194	6.278
Average	6.316	6.301	6.384	6.298	6.388	6.305	6.405	6.317	6.361	6.338	6.348
Max	6.361	6.387	6.590	6.369	6.599	6.393	6.575	6.409	6.449	6.524	6.444
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

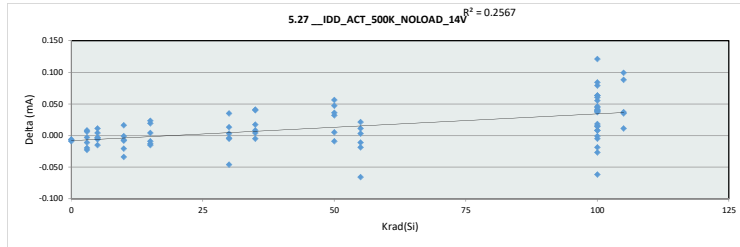


LDR TID Report  
TPS7H5001-SP

5.27 IDD\_ACT\_500K\_NOLOAD\_14V

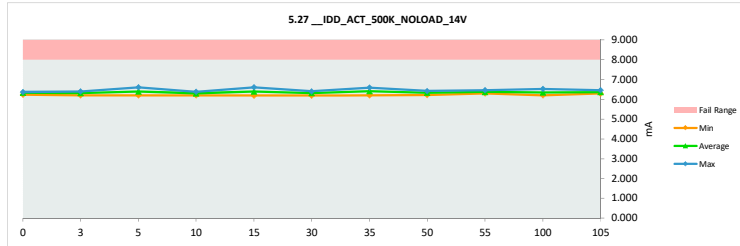
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	8	8
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.241	6.234	-0.007
0	118	6.365	6.359	-0.006
0	122	6.377	6.368	-0.009
3	61	6.385	6.362	-0.023
3	62	6.395	6.392	-0.003
3	63	6.301	6.307	0.006
3	64	6.381	6.370	-0.011
3	65	6.212	6.192	-0.020
3	66	6.194	6.202	0.008
5	67	6.276	6.272	-0.004
5	68	6.420	6.414	-0.006
5	69	6.191	6.195	0.004
5	70	6.585	6.596	0.011
5	71	6.396	6.381	-0.015
5	72	6.472	6.469	-0.003
10	73	6.385	6.351	-0.034
10	74	6.395	6.374	-0.021
10	75	6.301	6.317	0.016
10	76	6.381	6.373	-0.008
10	77	6.212	6.205	-0.007
10	78	6.194	6.193	-0.001
15	79	6.276	6.299	0.023
15	80	6.420	6.411	-0.009
15	81	6.191	6.178	-0.013
15	82	6.585	6.604	0.019
15	83	6.396	6.381	-0.015
15	84	6.472	6.476	0.004
30	86	6.385	6.398	0.013
30	89	6.395	6.398	0.003
30	91	6.301	6.336	0.035
30	92	6.381	6.335	-0.046
30	94	6.212	6.208	-0.004
30	95	6.194	6.189	-0.005
35	100	6.276	6.316	0.040
35	101	6.420	6.461	0.041
35	102	6.191	6.199	0.008
35	103	6.585	6.580	-0.005
35	104	6.396	6.400	0.004
35	105	6.472	6.489	0.017
50	106	6.384	6.416	0.032
50	107	6.294	6.330	0.036
50	108	6.186	6.242	0.056
50	109	6.313	6.360	0.047
50	110	6.365	6.370	0.005
50	111	6.226	6.217	-0.009
55	113	6.416	6.419	0.003
55	114	6.312	6.293	-0.019
55	115	6.364	6.385	0.021
55	116	6.326	6.337	0.011
55	126	6.466	6.455	-0.011
55	128	6.373	6.307	-0.066
100	129	6.408	6.492	0.084
100	131	6.257	6.195	-0.062
100	132	6.389	6.370	-0.019
100	133	6.413	6.452	0.039
100	134	6.279	6.317	0.038
100	135	6.310	6.283	-0.027
100	138	6.330	6.390	0.060
100	139	6.194	6.249	0.055
100	140	6.309	6.349	0.040
100	141	6.211	6.219	0.008
100	142	6.366	6.429	0.063
100	143	6.337	6.400	0.063
100	144	6.267	6.304	0.037
100	220	6.380	6.379	-0.001
100	221	6.246	6.254	0.008
100	224	6.298	6.342	0.044
100	225	6.207	6.221	0.014
100	226	6.485	6.526	0.041
100	228	6.264	6.385	0.121
100	232	6.329	6.408	0.079
100	233	6.363	6.378	0.015
100	236	6.196	6.214	0.018
100	238	6.370	6.416	0.046
100	243	6.243	6.238	-0.005
105	244	6.318	6.329	0.011
105	246	6.196	6.284	0.088
105	248	6.374	6.409	0.035
105	374	6.350	6.449	0.099
105	376	6.253	6.290	0.037
	Max	6.585	6.604	0.019
	Average	6.332	6.346	0.014
	Min	6.186	6.178	-0.008
	Std Dev	0.097	0.099	0.034



Test Site		
Tester		
Test Number		
Max Limit	8	mA
Min Limit		

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.234	6.192	6.195	6.193	6.178	6.189	6.199	6.217	6.293	6.195	6.284
Average	6.320	6.304	6.388	6.302	6.392	6.311	6.408	6.323	6.366	6.342	6.352
Max	6.368	6.392	6.596	6.374	6.604	6.398	6.580	6.416	6.455	6.526	6.449
UL	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000	8.000

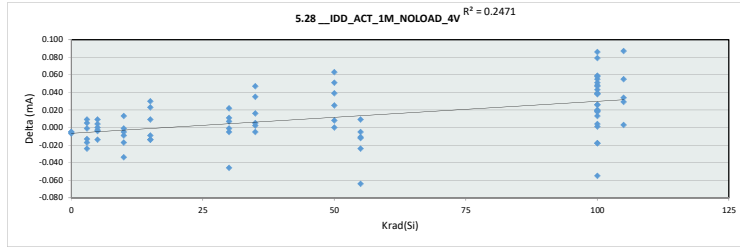




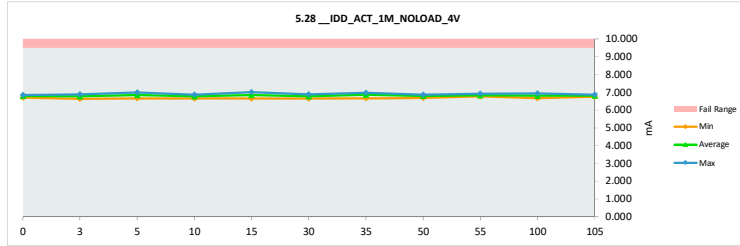
LDR TID Report  
TPS7H5001-SP

5.28 IDD_ACT_1M_NOLOAD_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.713	6.706	-0.007
0	118	6.814	6.809	-0.005
0	122	6.853	6.848	-0.005
3	61	6.844	6.820	-0.024
3	62	6.880	6.879	-0.001
3	63	6.780	6.785	0.005
3	64	6.842	6.829	-0.013
3	65	6.650	6.633	-0.017
3	66	6.674	6.683	0.009
5	67	6.738	6.738	0.000
5	68	6.898	6.894	-0.004
5	69	6.662	6.666	0.004
5	70	6.985	6.994	0.009
5	71	6.861	6.847	-0.014
5	72	6.940	6.937	-0.003
10	73	6.844	6.810	-0.034
10	74	6.880	6.863	-0.017
10	75	6.780	6.793	0.013
10	76	6.842	6.833	-0.009
10	77	6.650	6.645	-0.005
10	78	6.674	6.673	-0.001
15	79	6.738	6.768	0.030
15	80	6.898	6.889	-0.009
15	81	6.662	6.648	-0.014
15	82	6.985	7.008	0.023
15	83	6.861	6.847	-0.014
15	84	6.940	6.949	0.009
30	86	6.844	6.855	0.011
30	89	6.880	6.887	0.007
30	91	6.780	6.802	0.022
30	92	6.842	6.796	-0.046
30	94	6.650	6.649	-0.001
30	95	6.674	6.669	-0.005
35	100	6.738	6.785	0.047
35	101	6.898	6.933	0.035
35	102	6.662	6.667	0.005
35	103	6.985	6.980	-0.005
35	104	6.861	6.863	0.002
35	105	6.940	6.956	0.016
50	106	6.842	6.867	0.025
50	107	6.747	6.786	0.039
50	108	6.636	6.699	0.063
50	109	6.796	6.847	0.051
50	110	6.814	6.822	0.008
50	111	6.692	6.692	0.000
55	113	6.858	6.834	-0.024
55	114	6.784	6.772	-0.012
55	115	6.842	6.837	-0.005
55	116	6.799	6.808	0.009
55	126	6.925	6.914	-0.011
55	128	6.864	6.800	-0.064
100	129	6.857	6.908	0.051
100	131	6.741	6.686	-0.055
100	132	6.865	6.847	-0.018
100	133	6.864	6.902	0.038
100	134	6.753	6.792	0.039
100	135	6.786	6.768	-0.018
100	138	6.801	6.860	0.059
100	139	6.662	6.717	0.055
100	140	6.771	6.810	0.039
100	141	6.685	6.698	0.013
100	142	6.840	6.887	0.047
100	143	6.803	6.861	0.058
100	144	6.747	6.773	0.026
100	220	6.847	6.848	0.001
100	221	6.720	6.738	0.018
100	224	6.757	6.800	0.043
100	225	6.649	6.668	0.019
100	226	6.923	6.942	0.019
100	228	6.730	6.816	0.086
100	232	6.791	6.870	0.079
100	233	6.795	6.815	0.020
100	236	6.646	6.672	0.026
100	238	6.851	6.899	0.048
100	243	6.700	6.704	0.004
105	244	6.779	6.782	0.003
105	246	6.681	6.768	0.087
105	248	6.831	6.860	0.029
105	374	6.789	6.844	0.055
105	376	6.720	6.754	0.034
	Max	6.985	7.008	0.027
	Average	6.795	6.808	0.012
	Min	6.636	6.633	-0.064
	Std Dev	0.091	0.091	0.030



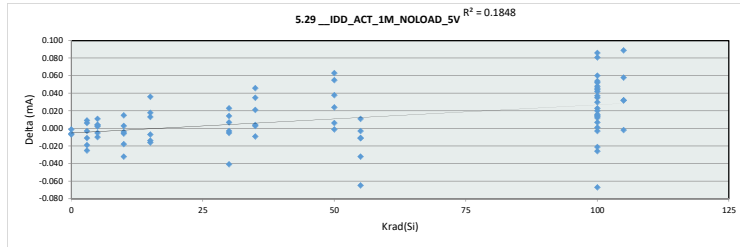
5.28 IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.706	6.633	6.666	6.645	6.648	6.649	6.667	6.692	6.772	6.668	6.754
Average	6.788	6.772	6.846	6.770	6.852	6.776	6.864	6.786	6.828	6.803	6.802
Max	6.848	6.879	6.994	6.863	7.008	6.887	6.980	6.867	6.914	6.942	6.860
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



LDR TID Report  
TPS7H5001-SP

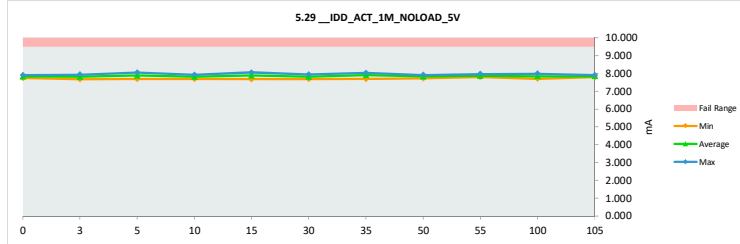
5.29 IDD_ACT_1M_NOLOAD_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.759	7.752	-0.007
0	118	7.857	7.856	-0.001
0	122	7.910	7.904	-0.006
3	61	7.901	7.876	-0.025
3	62	7.943	7.940	-0.003
3	63	7.821	7.827	0.006
3	64	7.894	7.883	-0.011
3	65	7.698	7.679	-0.019
3	66	7.724	7.733	0.009
5	67	7.783	7.787	0.004
5	68	7.945	7.940	-0.005
5	69	7.699	7.703	0.004
5	70	8.045	8.056	0.011
5	71	7.908	7.898	-0.010
5	72	7.986	7.898	-0.088
10	73	7.901	7.869	-0.032
10	74	7.943	7.925	-0.018
10	75	7.821	7.836	0.015
10	76	7.894	7.888	-0.006
10	77	7.698	7.694	-0.004
10	78	7.724	7.727	0.003
15	79	7.783	7.819	0.036
15	80	7.945	7.938	-0.007
15	81	7.699	7.683	-0.016
15	82	8.045	8.063	0.018
15	83	7.908	7.894	-0.014
15	84	7.986	7.999	0.013
30	86	7.901	7.915	0.014
30	89	7.943	7.950	0.007
30	91	7.821	7.844	0.023
30	92	7.894	7.853	-0.041
30	94	7.698	7.695	-0.003
30	95	7.724	7.719	-0.005
35	100	7.783	7.829	0.046
35	101	7.945	7.980	0.035
35	102	7.699	7.702	0.003
35	103	8.045	8.036	-0.009
35	104	7.908	7.912	0.004
35	105	7.986	8.007	0.021
50	106	7.899	7.923	0.024
50	107	7.804	7.842	0.038
50	108	7.681	7.744	0.063
50	109	7.846	7.901	0.055
50	110	7.865	7.871	0.006
50	111	7.739	7.738	-0.001
55	113	7.910	7.878	-0.032
55	114	7.822	7.811	-0.011
55	115	7.887	7.884	-0.003
55	116	7.845	7.856	0.011
55	126	7.986	7.975	-0.011
55	128	7.922	7.857	-0.065
100	129	7.906	7.954	0.048
100	131	7.791	7.724	-0.067
100	132	7.915	7.894	-0.021
100	133	7.915	7.945	0.030
100	134	7.806	7.843	0.037
100	135	7.839	7.813	-0.026
100	138	7.849	7.909	0.060
100	139	7.701	7.753	0.052
100	140	7.813	7.848	0.035
100	141	7.736	7.743	0.007
100	142	7.890	7.936	0.046
100	143	7.848	7.902	0.054
100	144	7.797	7.820	0.023
100	220	7.895	7.892	-0.003
100	221	7.772	7.784	0.012
100	224	7.796	7.838	0.042
100	225	7.699	7.713	0.014
100	226	7.981	7.995	0.014
100	228	7.775	7.861	0.086
100	232	7.840	7.921	0.081
100	233	7.864	7.880	0.016
100	236	7.688	7.708	0.020
100	238	7.906	7.950	0.044
100	243	7.744	7.745	0.001
105	244	7.824	7.822	-0.002
105	246	7.724	7.813	0.089
105	248	7.882	7.914	0.032
105	374	7.831	7.889	0.058
105	376	7.772	7.894	0.032
105	Max	8.045	8.063	0.089
105	Average	7.845	7.856	0.011
105	Min	7.681	7.679	-0.067
105	Std Dev	0.095	0.094	0.030



5.29 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	9.5 mA
Min Limit	9.5 mA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.752	7.679	7.703	7.694	7.683	7.695	7.702	7.738	7.811	7.708	7.804
Average	7.837	7.823	7.895	7.823	7.899	7.829	7.911	7.837	7.877	7.849	7.848
Max	7.904	7.940	8.056	7.925	8.063	7.950	8.036	7.923	7.975	7.995	7.914
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500

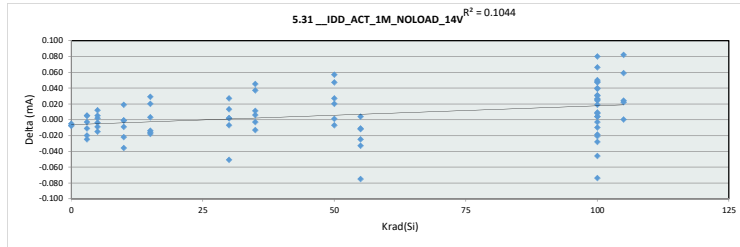




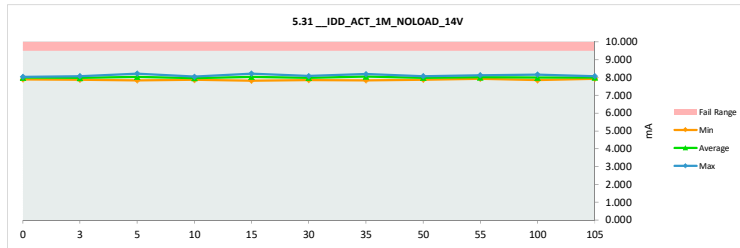
LDR TID Report  
TPS7H5001-SP

5.31 IDD_ACT_1M_NOLOAD_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.899	7.892	-0.007
0	118	8.031	8.026	-0.005
0	122	8.050	8.042	-0.008
3	61	8.064	8.039	-0.025
3	62	8.068	8.065	-0.003
3	63	7.933	7.938	0.005
3	64	8.048	8.037	-0.011
3	65	7.892	7.872	-0.020
3	66	7.867	7.872	0.005
5	67	7.922	7.918	-0.004
5	68	8.084	8.075	-0.009
5	69	7.833	7.838	0.005
5	70	8.198	8.210	0.012
5	71	8.054	8.039	-0.015
5	72	8.130	8.132	0.002
10	73	8.064	8.028	-0.036
10	74	8.068	8.046	-0.022
10	75	7.933	7.952	0.019
10	76	8.048	8.039	-0.009
10	77	7.892	7.891	-0.001
10	78	7.867	7.866	-0.001
15	79	7.922	7.951	0.029
15	80	8.084	8.070	-0.014
15	81	7.833	7.817	-0.016
15	82	8.198	8.218	0.020
15	83	8.054	8.036	-0.018
15	84	8.130	8.133	0.003
30	86	8.064	8.077	0.013
30	89	8.068	8.070	0.002
30	91	7.933	7.960	0.027
30	92	8.048	7.997	-0.051
30	94	7.892	7.894	0.002
30	95	7.867	7.860	-0.007
35	100	7.922	7.967	0.045
35	101	8.084	8.121	0.037
35	102	7.833	7.839	0.006
35	103	8.198	8.185	-0.013
35	104	8.054	8.051	-0.003
35	105	8.130	8.141	0.011
50	106	8.052	8.072	0.020
50	107	7.968	7.995	0.027
50	108	7.846	7.903	0.057
50	109	7.959	8.016	0.047
50	110	8.044	8.045	0.001
50	111	7.879	7.872	-0.007
55	113	8.085	8.052	-0.033
55	114	7.950	7.925	-0.025
55	115	8.008	7.996	-0.012
55	116	7.979	7.983	0.004
55	126	8.134	8.123	-0.011
55	128	8.031	7.956	-0.075
100	129	8.085	8.124	0.039
100	131	7.921	7.847	-0.074
100	132	8.050	8.022	-0.028
100	133	8.103	8.129	0.026
100	134	7.951	7.977	0.026
100	135	7.971	7.925	-0.046
100	138	7.993	8.043	0.050
100	139	7.853	7.900	0.047
100	140	7.966	7.996	0.030
100	141	7.870	7.860	-0.010
100	142	8.030	8.070	0.040
100	143	7.990	8.038	0.048
100	144	7.934	7.953	0.019
100	220	8.033	8.014	-0.019
100	221	7.901	7.898	-0.003
100	224	7.949	7.980	0.031
100	225	7.900	7.908	0.008
100	226	8.159	8.163	0.004
100	228	7.933	8.013	0.080
100	232	7.996	8.062	0.066
100	233	8.030	8.039	0.009
100	236	7.852	7.856	0.004
100	238	8.036	8.060	0.024
100	243	7.908	7.887	-0.021
105	244	7.978	7.978	0.000
105	246	7.848	7.930	0.082
105	248	8.050	8.072	0.022
105	374	8.006	8.065	0.059
105	376	7.921	7.945	0.024
	Max	8.198	8.218	0.020
	Average	7.993	7.999	0.006
	Min	7.833	7.817	-0.016
	Std Dev	0.095	0.095	0.030



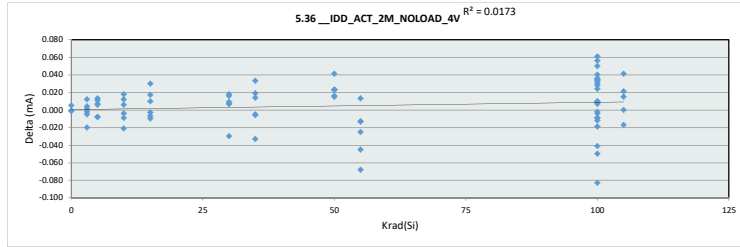
5.31 IDD_ACT											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	9.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.892	7.872	7.838	7.866	7.817	7.860	7.839	7.872	7.925	7.847	7.930
Average	7.987	7.971	8.035	7.970	8.038	7.976	8.051	7.984	8.006	7.990	7.998
Max	8.042	8.065	8.210	8.046	8.218	8.077	8.185	8.072	8.123	8.163	8.072
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



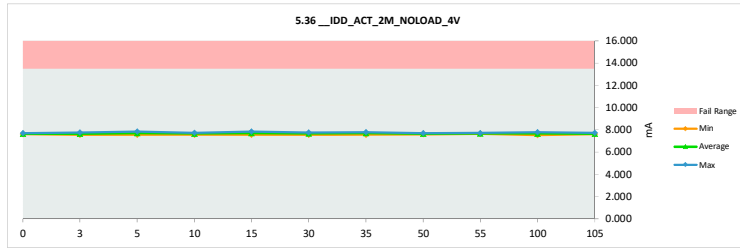
LDR TID Report  
TPS7H5001-SP

5.36 IDD_ACT_2M_NOLOAD_4V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	13.5	13.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.611	7.610	-0.001
0	118	7.639	7.638	-0.001
0	122	7.699	7.704	0.005
3	61	7.675	7.655	-0.020
3	62	7.741	7.745	0.004
3	63	7.672	7.684	0.012
3	64	7.717	7.715	-0.002
3	65	7.547	7.542	-0.005
3	66	7.570	7.571	0.001
5	67	7.560	7.566	0.006
5	68	7.738	7.730	-0.008
5	69	7.561	7.568	0.007
5	70	7.820	7.833	0.013
5	71	7.711	7.703	-0.008
5	72	7.761	7.772	0.011
10	73	7.675	7.654	-0.021
10	74	7.741	7.732	-0.009
10	75	7.672	7.690	0.018
10	76	7.717	7.713	-0.004
10	77	7.547	7.553	0.006
10	78	7.570	7.582	0.012
15	79	7.560	7.590	0.030
15	80	7.738	7.735	-0.003
15	81	7.561	7.551	-0.010
15	82	7.820	7.837	0.017
15	83	7.711	7.704	-0.007
15	84	7.761	7.771	0.010
30	86	7.675	7.691	0.016
30	89	7.741	7.748	0.007
30	91	7.672	7.690	0.018
30	92	7.717	7.687	-0.030
30	94	7.547	7.556	0.009
30	95	7.570	7.577	0.007
35	100	7.560	7.593	0.033
35	101	7.738	7.757	0.019
35	102	7.561	7.556	-0.005
35	103	7.820	7.787	-0.033
35	104	7.711	7.705	-0.006
35	105	7.761	7.775	0.014
50	106	7.674	7.697	0.023
50	107	7.574	7.597	0.023
50	108	7.599	7.614	0.015
50	109	7.612	7.653	0.041
50	110	7.628	7.651	0.023
50	111	7.608	7.624	0.016
55	113	7.748	7.703	-0.045
55	114	7.650	7.637	-0.013
55	115	7.663	7.650	-0.013
55	116	7.620	7.633	0.013
55	126	7.735	7.710	-0.025
55	128	7.701	7.633	-0.068
100	129	7.675	7.715	0.040
100	131	7.618	7.568	-0.050
100	132	7.725	7.684	-0.041
100	133	7.688	7.718	0.030
100	134	7.594	7.628	0.034
100	135	7.779	7.696	-0.083
100	138	7.630	7.665	0.035
100	139	7.573	7.605	0.032
100	140	7.622	7.672	0.050
100	141	7.571	7.562	-0.009
100	142	7.680	7.704	0.024
100	143	7.644	7.680	0.036
100	144	7.602	7.609	0.007
100	220	7.705	7.701	-0.004
100	221	7.583	7.581	-0.002
100	224	7.609	7.637	0.028
100	225	7.594	7.585	-0.009
100	226	7.764	7.773	0.009
100	228	7.589	7.650	0.061
100	232	7.657	7.713	0.056
100	233	7.687	7.697	0.010
100	236	7.581	7.569	-0.012
100	238	7.749	7.758	0.009
100	243	7.549	7.530	-0.019
105	244	7.622	7.605	-0.017
105	246	7.567	7.608	0.041
105	248	7.712	7.712	0.000
105	374	7.640	7.655	0.015
105	376	7.606	7.627	0.021
105	Max	7.820	7.837	0.061
105	Average	7.657	7.662	0.005
105	Min	7.547	7.530	-0.083
105	Std Dev	0.075	0.072	0.025



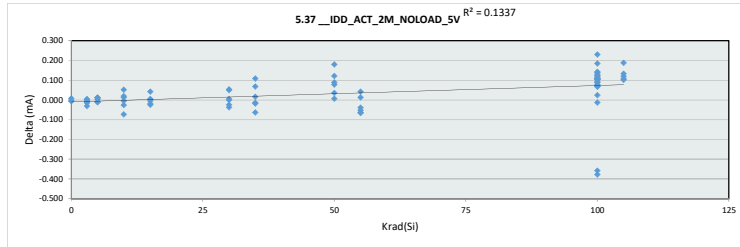
5.36 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.610	7.542	7.566	7.553	7.551	7.556	7.556	7.597	7.633	7.530	7.605
Average	7.651	7.652	7.695	7.654	7.698	7.658	7.696	7.639	7.661	7.654	7.641
Max	7.704	7.745	7.833	7.732	7.837	7.748	7.787	7.697	7.710	7.773	7.712
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



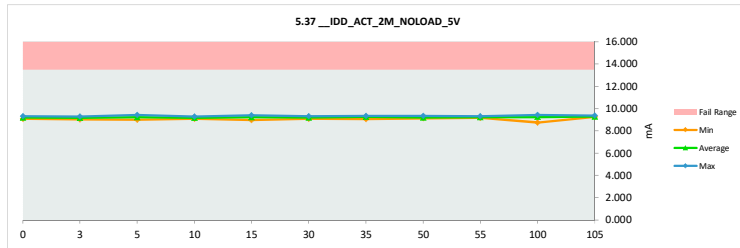
LDR TID Report  
TPS7H5001-SP

5.37 IDD_ACT_2M_NOLOAD_5V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	13.5	13.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.103	9.096	-0.007
0	118	9.228	9.226	-0.002
0	122	9.320	9.328	0.008
3	61	9.283	9.251	-0.032
3	62	9.313	9.303	-0.010
3	63	9.154	9.159	0.005
3	64	9.265	9.260	-0.005
3	65	9.044	9.029	-0.015
3	66	9.104	9.104	0.000
5	67	9.101	9.112	0.011
5	68	9.314	9.303	-0.011
5	69	8.992	9.003	0.011
5	70	9.426	9.428	0.002
5	71	9.278	9.266	-0.012
5	72	9.372	9.383	0.011
10	73	9.283	9.209	-0.074
10	74	9.313	9.286	-0.027
10	75	9.154	9.173	0.019
10	76	9.265	9.260	-0.005
10	77	9.044	9.096	0.052
10	78	9.104	9.116	0.012
15	79	9.101	9.143	0.042
15	80	9.314	9.316	0.002
15	81	8.992	8.987	-0.005
15	82	9.426	9.406	-0.020
15	83	9.278	9.254	-0.024
15	84	9.372	9.377	0.005
30	86	9.283	9.258	-0.025
30	89	9.313	9.313	0.000
30	91	9.154	9.204	0.050
30	92	9.265	9.228	-0.037
30	94	9.044	9.097	0.053
30	95	9.104	9.109	0.005
35	100	9.101	9.209	0.108
35	101	9.314	9.329	0.015
35	102	8.992	9.060	0.068
35	103	9.426	9.363	-0.063
35	104	9.278	9.263	-0.015
35	105	9.372	9.353	-0.019
50	106	9.228	9.349	0.121
50	107	9.108	9.143	0.035
50	108	8.983	9.163	0.180
50	109	9.204	9.210	0.006
50	110	9.218	9.296	0.078
50	111	9.049	9.137	0.088
55	113	9.302	9.315	0.013
55	114	9.183	9.224	0.041
55	115	9.229	9.177	-0.052
55	116	9.227	9.188	-0.039
55	126	9.318	9.252	-0.066
55	128	9.292	9.227	-0.065
100	129	9.300	9.405	0.105
100	131	9.204	9.191	-0.013
100	132	9.325	9.349	0.024
100	133	9.296	9.419	0.123
100	134	9.216	9.323	0.107
100	135	9.137	8.759	-0.378
100	138	9.220	9.362	0.142
100	139	9.058	9.179	0.121
100	140	9.188	9.292	0.104
100	141	9.084	9.154	0.070
100	142	9.282	8.924	-0.358
100	143	9.187	9.325	0.138
100	144	9.160	9.272	0.112
100	220	9.268	9.335	0.067
100	221	9.121	9.212	0.091
100	224	9.143	9.230	0.087
100	225	9.060	9.162	0.102
100	226	9.310	9.441	0.131
100	228	9.128	9.358	0.230
100	232	9.228	9.412	0.184
100	233	9.251	9.318	0.067
100	236	8.978	9.083	0.105
100	238	9.312	9.423	0.111
100	243	9.090	9.169	0.079
105	244	9.160	9.261	0.101
105	246	9.090	9.277	0.187
105	248	9.259	9.378	0.119
105	374	9.192	9.325	0.133
105	376	9.151	9.258	0.107
105	Max	9.426	9.441	0.015
105	Average	9.204	9.237	0.033
105	Min	8.978	8.759	-0.378
105	Std Dev	0.113	0.124	0.093



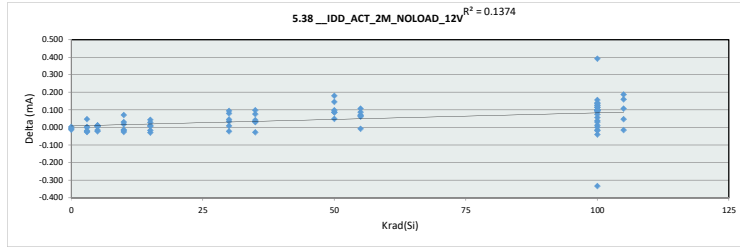
5.37 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5 mA										
Min Limit	mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.096	9.029	9.003	9.096	8.987	9.097	9.060	9.137	9.177	8.759	9.258
Average	9.217	9.184	9.249	9.190	9.247	9.202	9.263	9.216	9.231	9.254	9.300
Max	9.328	9.303	9.428	9.286	9.406	9.313	9.363	9.349	9.315	9.441	9.378
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



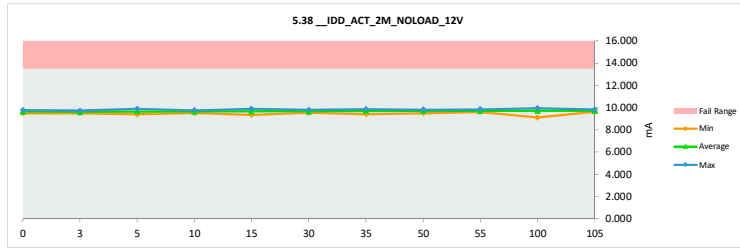
LDR TID Report  
TPS7H5001-SP

5.38 IDD_ACT_2M_NOLOAD_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	13.5	13.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.515	9.503	-0.012
0	118	9.709	9.704	-0.005
0	122	9.765	9.769	0.004
3	61	9.753	9.728	-0.025
3	62	9.727	9.727	0.000
3	63	9.504	9.508	0.004
3	64	9.692	9.674	-0.018
3	65	9.573	9.549	-0.024
3	66	9.489	9.536	0.047
5	67	9.553	9.559	0.006
5	68	9.746	9.724	-0.022
5	69	9.388	9.394	0.006
5	70	9.885	9.897	0.012
5	71	9.701	9.686	-0.015
5	72	9.781	9.791	0.010
10	73	9.753	9.728	-0.025
10	74	9.727	9.709	-0.018
10	75	9.504	9.522	0.018
10	76	9.692	9.763	0.071
10	77	9.573	9.560	-0.013
10	78	9.489	9.519	0.030
15	79	9.553	9.596	0.043
15	80	9.746	9.729	-0.017
15	81	9.388	9.359	-0.029
15	82	9.885	9.893	0.008
15	83	9.701	9.730	0.029
15	84	9.781	9.792	0.011
30	86	9.753	9.798	0.045
30	89	9.727	9.736	0.009
30	91	9.504	9.598	0.094
30	92	9.692	9.727	0.035
30	94	9.573	9.551	-0.022
30	95	9.489	9.569	0.080
35	100	9.553	9.652	0.099
35	101	9.746	9.777	0.031
35	102	9.388	9.420	0.032
35	103	9.885	9.857	-0.028
35	104	9.701	9.743	0.042
35	105	9.781	9.857	0.076
50	106	9.731	9.819	0.088
50	107	9.633	9.731	0.098
50	108	9.450	9.631	0.181
50	109	9.565	9.708	0.145
50	110	9.731	9.816	0.085
50	111	9.456	9.505	0.049
55	113	9.775	9.837	0.062
55	114	9.544	9.611	0.067
55	115	9.600	9.671	0.071
55	116	9.617	9.724	0.107
55	126	9.760	9.848	0.088
55	128	9.632	9.624	-0.008
100	129	9.794	9.908	0.114
100	131	9.603	9.588	-0.015
100	132	9.745	9.758	0.013
100	133	9.836	9.937	0.101
100	134	9.644	9.743	0.099
100	135	9.521	9.913	0.392
100	138	9.626	9.782	0.156
100	139	9.493	9.620	0.127
100	140	9.631	9.746	0.115
100	141	9.469	9.508	0.039
100	142	9.691	9.828	0.137
100	143	9.655	9.730	0.075
100	144	9.620	9.677	0.057
100	220	9.752	9.711	-0.041
100	221	9.561	9.563	0.002
100	224	9.580	9.677	0.097
100	225	9.610	9.699	0.089
100	226	9.873	9.903	0.030
100	228	9.655	9.796	0.141
100	232	9.744	9.842	0.098
100	233	9.676	9.777	0.101
100	236	9.431	9.098	-0.333
100	238	9.677	9.801	0.124
100	243	9.604	9.585	-0.019
105	244	9.654	9.702	0.048
105	246	9.456	9.643	0.187
105	248	9.723	9.831	0.108
105	374	9.665	9.826	0.161
105	376	9.638	9.624	-0.014
105	Max	9.885	9.937	0.392
105	Average	9.644	9.691	0.047
105	Min	9.388	9.098	-0.333
105	Std Dev	0.123	0.144	0.081



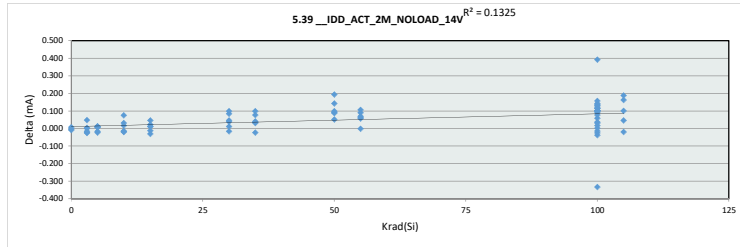
5.38 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.503	9.508	9.394	9.519	9.359	9.551	9.420	9.505	9.611	9.098	9.624
Average	9.659	9.620	9.675	9.634	9.683	9.663	9.718	9.702	9.719	9.716	9.725
Max	9.769	9.728	9.897	9.763	9.893	9.798	9.857	9.819	9.848	9.937	9.831
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500



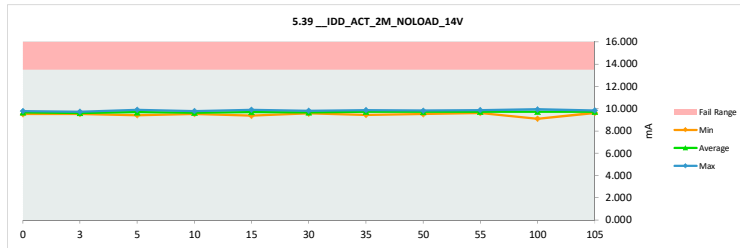
LDR TID Report  
TPS7H5001-SP

5.39 IDD_ACT_2M_NOLOAD_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	13.5
Min Limit	13.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.518	9.509	-0.009
0	118	9.710	9.706	-0.004
0	122	9.766	9.774	0.008
3	61	9.754	9.731	-0.023
3	62	9.728	9.728	0.000
3	63	9.508	9.511	0.003
3	64	9.696	9.680	-0.016
3	65	9.578	9.552	-0.026
3	66	9.496	9.543	0.047
5	67	9.557	9.566	0.009
5	68	9.747	9.724	-0.023
5	69	9.393	9.399	0.006
5	70	9.885	9.897	0.012
5	71	9.708	9.691	-0.017
5	72	9.785	9.794	0.009
10	73	9.754	9.733	-0.021
10	74	9.728	9.710	-0.018
10	75	9.508	9.524	0.016
10	76	9.696	9.771	0.075
10	77	9.578	9.564	-0.014
10	78	9.496	9.526	0.030
15	79	9.557	9.602	0.045
15	80	9.747	9.734	-0.013
15	81	9.393	9.362	-0.031
15	82	9.885	9.895	0.010
15	83	9.708	9.730	0.022
15	84	9.785	9.795	0.010
30	86	9.754	9.799	0.045
30	89	9.728	9.738	0.010
30	91	9.508	9.607	0.099
30	92	9.696	9.735	0.039
30	94	9.578	9.561	-0.017
30	95	9.496	9.579	0.083
35	100	9.557	9.656	0.099
35	101	9.747	9.782	0.035
35	102	9.393	9.424	0.031
35	103	9.885	9.861	-0.024
35	104	9.708	9.748	0.040
35	105	9.785	9.861	0.076
50	106	9.732	9.823	0.091
50	107	9.638	9.737	0.099
50	108	9.451	9.644	0.193
50	109	9.569	9.711	0.142
50	110	9.734	9.821	0.087
50	111	9.456	9.507	0.051
55	113	9.783	9.840	0.057
55	114	9.550	9.612	0.062
55	115	9.606	9.675	0.069
55	116	9.624	9.729	0.105
55	126	9.766	9.855	0.089
55	128	9.635	9.633	-0.002
100	129	9.797	9.911	0.114
100	131	9.604	9.590	-0.014
100	132	9.747	9.764	0.017
100	133	9.838	9.939	0.101
100	134	9.645	9.746	0.101
100	135	9.528	9.919	0.391
100	138	9.629	9.786	0.157
100	139	9.493	9.624	0.131
100	140	9.634	9.748	0.114
100	141	9.476	9.512	0.036
100	142	9.693	9.827	0.134
100	143	9.658	9.735	0.077
100	144	9.621	9.680	0.059
100	220	9.751	9.713	-0.038
100	221	9.564	9.565	0.001
100	224	9.583	9.682	0.099
100	225	9.612	9.702	0.090
100	226	9.875	9.905	0.030
100	228	9.658	9.800	0.142
100	232	9.745	9.845	0.100
100	233	9.680	9.780	0.100
100	236	9.434	9.100	-0.334
100	238	9.681	9.803	0.122
100	243	9.614	9.589	-0.025
105	244	9.659	9.705	0.046
105	246	9.461	9.648	0.187
105	248	9.732	9.832	0.100
105	374	9.668	9.830	0.162
105	376	9.643	9.623	-0.020
105		9.885	9.939	0.391
	Max	9.647	9.695	0.048
	Average	9.647	9.695	0.048
	Min	9.393	9.100	-0.334
	Std Dev	0.122	0.143	0.081



5.39 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	13.5 mA										
Min Limit	3 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.509	9.511	9.399	9.524	9.362	9.561	9.424	9.507	9.612	9.100	9.623
Average	9.663	9.624	9.679	9.638	9.686	9.670	9.722	9.707	9.724	9.719	9.728
Max	9.774	9.731	9.897	9.771	9.895	9.799	9.861	9.823	9.855	9.939	9.832
UL	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500	13.500

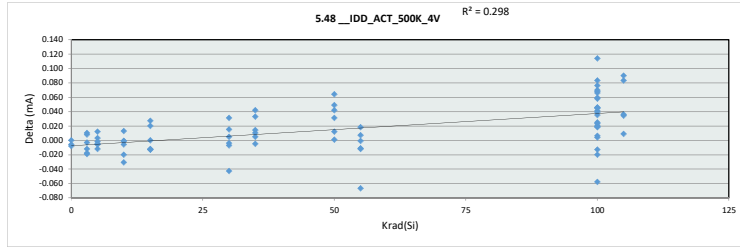




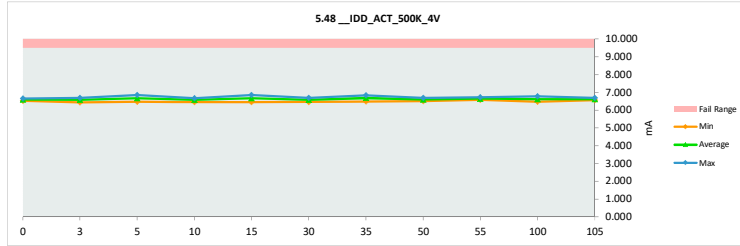
LDR TID Report  
TPS7H5001-SP

5.48 IDD_ACT_500K_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.519	6.513	-0.006
0	118	6.617	6.617	0.000
0	122	6.649	6.641	-0.008
3	61	6.643	6.624	-0.019
3	62	6.681	6.678	-0.003
3	63	6.584	6.592	0.008
3	64	6.646	6.634	-0.012
3	65	6.455	6.437	-0.018
3	66	6.472	6.482	0.010
5	67	6.550	6.548	-0.002
5	68	6.696	6.691	-0.005
5	69	6.467	6.470	0.003
5	70	6.831	6.843	0.012
5	71	6.666	6.654	-0.012
5	72	6.739	6.733	-0.006
10	73	6.643	6.612	-0.031
10	74	6.681	6.661	-0.020
10	75	6.584	6.597	0.013
10	76	6.646	6.640	-0.006
10	77	6.455	6.454	-0.001
10	78	6.472	6.471	-0.001
15	79	6.550	6.577	0.027
15	80	6.696	6.683	-0.013
15	81	6.467	6.455	-0.012
15	82	6.831	6.851	0.020
15	83	6.666	6.653	-0.013
15	84	6.739	6.739	0.000
30	86	6.643	6.658	0.015
30	89	6.681	6.686	0.005
30	91	6.584	6.615	0.031
30	92	6.646	6.603	-0.043
30	94	6.455	6.451	-0.004
30	95	6.472	6.465	-0.007
35	100	6.550	6.592	0.042
35	101	6.696	6.729	0.033
35	102	6.467	6.477	0.010
35	103	6.831	6.826	-0.005
35	104	6.666	6.671	0.005
35	105	6.739	6.753	0.014
50	106	6.650	6.681	0.031
50	107	6.550	6.592	0.042
50	108	6.447	6.511	0.064
50	109	6.606	6.655	0.049
50	110	6.614	6.626	0.012
50	111	6.495	6.496	0.001
55	113	6.656	6.655	-0.001
55	114	6.590	6.579	-0.011
55	115	6.647	6.665	0.018
55	116	6.594	6.601	0.007
55	126	6.737	6.725	-0.012
55	128	6.662	6.595	-0.067
100	129	6.653	6.729	0.076
100	131	6.543	6.485	-0.058
100	132	6.670	6.657	-0.013
100	133	6.654	6.695	0.041
100	134	6.549	6.594	0.045
100	135	6.588	6.568	-0.020
100	138	6.598	6.664	0.066
100	139	6.464	6.522	0.058
100	140	6.577	6.616	0.039
100	141	6.491	6.509	0.018
100	142	6.639	6.709	0.070
100	143	6.605	6.673	0.068
100	144	6.547	6.582	0.035
100	220	6.654	6.658	0.004
100	221	6.530	6.549	0.019
100	224	6.567	6.613	0.046
100	225	6.449	6.469	0.020
100	226	6.731	6.776	0.045
100	228	6.529	6.643	0.114
100	232	6.589	6.672	0.083
100	233	6.595	6.618	0.023
100	236	6.455	6.480	0.025
100	238	6.651	6.710	0.059
100	243	6.505	6.511	0.006
105	244	6.574	6.583	0.009
105	246	6.479	6.569	0.090
105	248	6.634	6.668	0.034
105	374	6.593	6.676	0.083
105	376	6.527	6.558	0.036
105	Max	6.831	6.851	0.020
105	Average	6.599	6.615	0.016
105	Min	6.447	6.437	-0.010
105	Std Dev	0.094	0.095	0.033



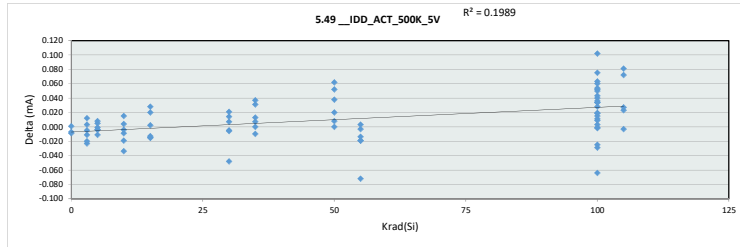
5.48 IDD_ACT_5											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.513	6.437	6.470	6.454	6.455	6.451	6.477	6.496	6.579	6.469	6.558
Average	6.590	6.575	6.657	6.573	6.660	6.580	6.675	6.594	6.637	6.613	6.611
Max	6.641	6.678	6.843	6.661	6.851	6.686	6.826	6.681	6.725	6.776	6.676
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



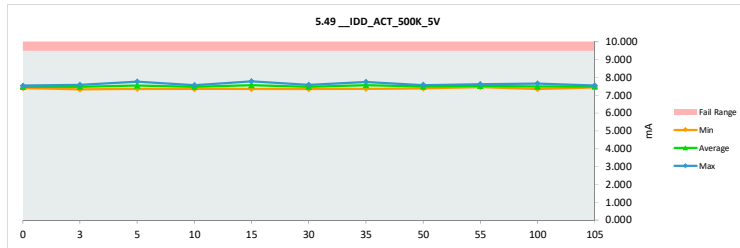
LDR TID Report  
TPS7H5001-SP

5.49 IDD_ACT_500K_5V	
Test Site	
Tester	
Test Number	
Unit	mA      mA
Max Limit	9.5      9.5
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.420	7.411	-0.009
0	118	7.507	7.508	0.001
0	122	7.548	7.541	-0.007
3	61	7.548	7.525	-0.023
3	62	7.586	7.581	-0.005
3	63	7.479	7.482	0.003
3	64	7.553	7.542	-0.011
3	65	7.359	7.339	-0.020
3	66	7.368	7.380	0.012
5	67	7.450	7.449	-0.001
5	68	7.586	7.582	-0.004
5	69	7.366	7.371	0.005
5	70	7.760	7.768	0.008
5	71	7.568	7.557	-0.011
5	72	7.640	7.636	-0.004
10	73	7.548	7.514	-0.034
10	74	7.586	7.567	-0.019
10	75	7.479	7.494	0.015
10	76	7.553	7.544	-0.009
10	77	7.359	7.355	-0.004
10	78	7.368	7.372	0.004
15	79	7.450	7.478	0.028
15	80	7.586	7.573	-0.013
15	81	7.366	7.351	-0.015
15	82	7.760	7.780	0.020
15	83	7.568	7.553	-0.015
15	84	7.640	7.642	0.002
30	86	7.548	7.562	0.014
30	89	7.586	7.593	0.007
30	91	7.479	7.500	0.021
30	92	7.553	7.505	-0.048
30	94	7.359	7.354	-0.005
30	95	7.368	7.362	-0.006
35	100	7.450	7.487	0.037
35	101	7.586	7.617	0.031
35	102	7.366	7.373	0.007
35	103	7.760	7.750	-0.010
35	104	7.568	7.568	0.000
35	105	7.640	7.653	0.013
50	106	7.559	7.579	0.020
50	107	7.461	7.499	0.038
50	108	7.345	7.407	0.062
50	109	7.500	7.552	0.052
50	110	7.518	7.526	0.008
50	111	7.395	7.395	0.000
55	113	7.559	7.540	-0.019
55	114	7.486	7.467	-0.019
55	115	7.552	7.549	-0.003
55	116	7.484	7.487	0.003
55	126	7.636	7.622	-0.014
55	128	7.567	7.495	-0.072
100	129	7.556	7.619	0.063
100	131	7.437	7.373	-0.064
100	132	7.578	7.553	-0.025
100	133	7.552	7.586	0.034
100	134	7.462	7.498	0.036
100	135	7.491	7.462	-0.029
100	138	7.502	7.562	0.060
100	139	7.352	7.402	0.050
100	140	7.472	7.506	0.034
100	141	7.396	7.399	0.003
100	142	7.537	7.589	0.052
100	143	7.497	7.551	0.054
100	144	7.445	7.464	0.019
100	220	7.549	7.548	-0.001
100	221	7.428	7.437	0.009
100	224	7.462	7.502	0.040
100	225	7.355	7.366	0.011
100	226	7.633	7.661	0.028
100	228	7.420	7.522	0.102
100	232	7.497	7.572	0.075
100	233	7.510	7.525	0.015
100	236	7.342	7.361	0.019
100	238	7.551	7.594	0.043
100	243	7.397	7.395	-0.002
105	244	7.468	7.465	-0.003
105	246	7.372	7.453	0.081
105	248	7.534	7.557	0.023
105	374	7.477	7.549	0.072
105	376	7.428	7.455	0.027
105	Max	7.760	7.780	0.102
105	Average	7.500	7.511	0.010
105	Min	7.342	7.339	-0.072
105	Std Dev	0.098	0.097	0.031



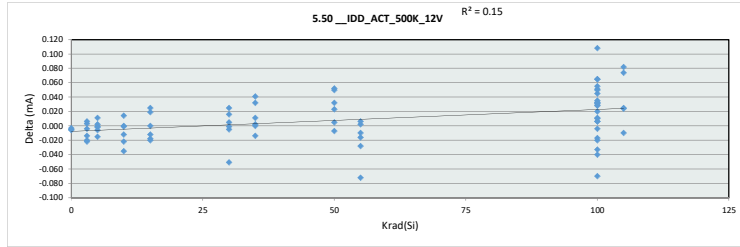
5.49 IDD_ACT_5											
Test Site											
Tester											
Test Number											
Max Limit	9.5      mA										
Min Limit											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.411	7.339	7.371	7.355	7.351	7.354	7.373	7.395	7.467	7.361	7.453
Average	7.487	7.475	7.561	7.474	7.563	7.479	7.575	7.493	7.527	7.502	7.496
Max	7.541	7.581	7.768	7.567	7.780	7.593	7.750	7.579	7.622	7.661	7.557
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



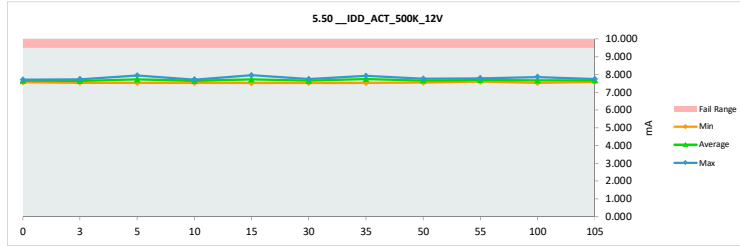
LDR TID Report  
TPS7H5001-SP

5.50 IDD_ACT_500K_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	9.5	9.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.579	7.574	-0.005
0	118	7.698	7.695	-0.003
0	122	7.709	7.703	-0.006
3	61	7.730	7.708	-0.022
3	62	7.742	7.738	-0.004
3	63	7.620	7.623	0.003
3	64	7.732	7.718	-0.014
3	65	7.572	7.552	-0.020
3	66	7.535	7.541	0.006
5	67	7.611	7.610	-0.001
5	68	7.745	7.739	-0.006
5	69	7.535	7.537	0.002
5	70	7.941	7.952	0.011
5	71	7.739	7.724	-0.015
5	72	7.802	7.800	-0.002
10	73	7.730	7.695	-0.035
10	74	7.742	7.720	-0.022
10	75	7.620	7.634	0.014
10	76	7.732	7.720	-0.012
10	77	7.572	7.571	-0.001
10	78	7.535	7.535	0.000
15	79	7.611	7.636	0.025
15	80	7.745	7.733	-0.012
15	81	7.535	7.515	-0.020
15	82	7.941	7.960	0.019
15	83	7.739	7.721	-0.018
15	84	7.802	7.802	0.000
30	86	7.730	7.746	0.016
30	89	7.742	7.747	0.005
30	91	7.620	7.645	0.025
30	92	7.732	7.681	-0.051
30	94	7.572	7.571	-0.001
30	95	7.535	7.530	-0.005
35	100	7.611	7.652	0.041
35	101	7.745	7.777	0.032
35	102	7.535	7.537	0.002
35	103	7.941	7.927	-0.014
35	104	7.739	7.739	0.000
35	105	7.802	7.813	0.011
50	106	7.742	7.765	0.023
50	107	7.645	7.677	0.032
50	108	7.532	7.584	0.052
50	109	7.643	7.693	0.050
50	110	7.722	7.727	0.005
50	111	7.561	7.554	-0.007
55	113	7.757	7.747	-0.010
55	114	7.638	7.610	-0.028
55	115	7.695	7.701	0.006
55	116	7.639	7.641	0.002
55	126	7.809	7.793	-0.016
55	128	7.709	7.637	-0.072
100	129	7.764	7.829	0.065
100	131	7.599	7.529	-0.070
100	132	7.743	7.710	-0.033
100	133	7.765	7.785	0.020
100	134	7.629	7.661	0.032
100	135	7.643	7.603	-0.040
100	138	7.668	7.723	0.055
100	139	7.520	7.565	0.045
100	140	7.648	7.683	0.035
100	141	7.557	7.553	-0.004
100	142	7.704	7.754	0.050
100	143	7.661	7.712	0.051
100	144	7.610	7.638	0.028
100	220	7.722	7.705	-0.017
100	221	7.577	7.583	0.006
100	224	7.636	7.667	0.031
100	225	7.569	7.575	0.006
100	226	7.835	7.864	0.029
100	228	7.601	7.709	0.108
100	232	7.674	7.739	0.065
100	233	7.694	7.705	0.011
100	236	7.516	7.526	0.010
100	238	7.706	7.739	0.033
100	243	7.580	7.560	-0.020
105	244	7.646	7.636	-0.010
105	246	7.519	7.593	0.074
105	248	7.722	7.746	0.024
105	374	7.670	7.752	0.082
105	376	7.607	7.632	0.025
	Max	7.941	7.960	0.108
	Average	7.672	7.680	0.008
	Min	7.516	7.515	-0.072
	Std Dev	0.099	0.100	0.032



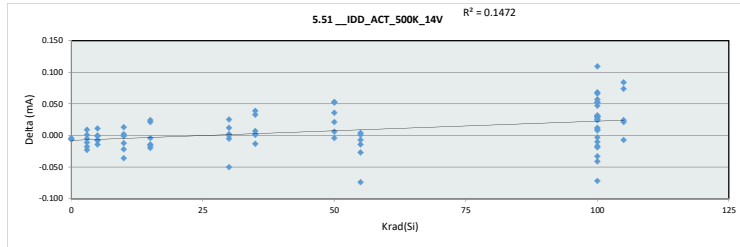
5.50 IDD_ACT_5		
Test Site		
Tester		
Test Number		
Min Limit	9.5	mA
Max Limit		
Krad(Si)	0	3
LL		
Min	7.574	7.541
Average	7.657	7.647
Max	7.703	7.738
UL	9.500	9.500



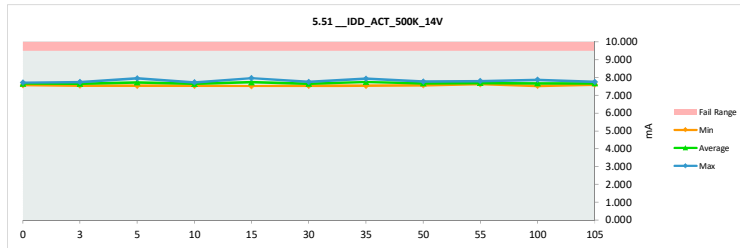
LDR TID Report  
TPS7H5001-SP

5.51 IDD_ACT_500K_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	9.5
Min Limit	9.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.582	7.576	-0.006
0	118	7.703	7.699	-0.004
0	122	7.714	7.708	-0.006
3	61	7.737	7.714	-0.023
3	62	7.748	7.743	-0.005
3	63	7.625	7.626	0.001
3	64	7.734	7.723	-0.011
3	65	7.576	7.558	-0.018
3	66	7.540	7.549	0.009
5	67	7.614	7.614	0.000
5	68	7.749	7.742	-0.007
5	69	7.540	7.539	-0.001
5	70	7.944	7.955	0.011
5	71	7.742	7.728	-0.014
5	72	7.810	7.802	-0.008
10	73	7.737	7.701	-0.036
10	74	7.748	7.726	-0.022
10	75	7.625	7.638	0.013
10	76	7.734	7.722	-0.012
10	77	7.576	7.578	0.002
10	78	7.540	7.539	-0.001
15	79	7.614	7.638	0.024
15	80	7.749	7.735	-0.014
15	81	7.540	7.520	-0.020
15	82	7.944	7.965	0.021
15	83	7.742	7.726	-0.016
15	84	7.810	7.806	-0.004
30	86	7.737	7.749	0.012
30	89	7.748	7.750	0.002
30	91	7.625	7.650	0.025
30	92	7.734	7.684	-0.050
30	94	7.576	7.576	0.000
30	95	7.540	7.535	-0.005
35	100	7.614	7.653	0.039
35	101	7.749	7.782	0.033
35	102	7.540	7.542	0.002
35	103	7.944	7.931	-0.013
35	104	7.742	7.743	0.001
35	105	7.810	7.817	0.007
50	106	7.747	7.768	0.021
50	107	7.648	7.684	0.036
50	108	7.536	7.588	0.052
50	109	7.647	7.700	0.053
50	110	7.726	7.732	0.006
50	111	7.566	7.562	-0.004
55	113	7.762	7.755	-0.007
55	114	7.645	7.618	-0.027
55	115	7.698	7.702	0.004
55	116	7.646	7.647	0.001
55	126	7.812	7.798	-0.014
55	128	7.715	7.641	-0.074
100	129	7.766	7.834	0.068
100	131	7.606	7.534	-0.072
100	132	7.748	7.715	-0.033
100	133	7.768	7.792	0.024
100	134	7.636	7.666	0.030
100	135	7.647	7.606	-0.041
100	138	7.673	7.730	0.057
100	139	7.526	7.573	0.047
100	140	7.654	7.684	0.030
100	141	7.565	7.555	-0.010
100	142	7.708	7.760	0.052
100	143	7.665	7.718	0.053
100	144	7.614	7.639	0.025
100	220	7.725	7.708	-0.017
100	221	7.586	7.583	-0.003
100	224	7.641	7.671	0.030
100	225	7.573	7.583	0.010
100	226	7.840	7.868	0.028
100	228	7.607	7.716	0.109
100	232	7.677	7.743	0.066
100	233	7.698	7.710	0.012
100	236	7.520	7.528	0.008
100	238	7.713	7.745	0.032
100	243	7.583	7.564	-0.019
105	244	7.649	7.642	-0.007
105	246	7.523	7.597	0.074
105	248	7.727	7.751	0.024
105	374	7.674	7.758	0.084
105	376	7.611	7.632	0.021
105	Max	7.944	7.965	0.109
105	Average	7.677	7.685	0.008
105	Min	7.520	7.520	-0.074
105	Std Dev	0.098	0.100	0.032



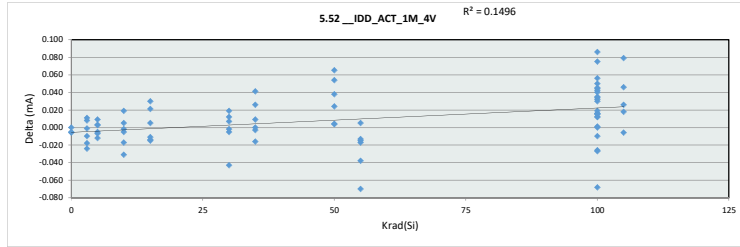
5.51 IDD_ACT_5											
Test Site											
Tester											
Test Number											
Max Limit	9.5 mA										
Min Limit	9.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.576	7.549	7.539	7.539	7.520	7.535	7.542	7.562	7.618	7.528	7.597
Average	7.661	7.652	7.730	7.651	7.732	7.657	7.745	7.672	7.694	7.676	7.676
Max	7.708	7.743	7.955	7.726	7.965	7.750	7.931	7.768	7.798	7.868	7.758
UL	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500	9.500



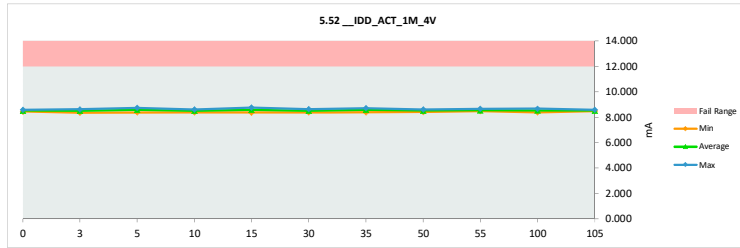
LDR TID Report  
TPS7H5001-SP

5.52 IDD_ACT_1M_4V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	12
Min Limit	12

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.436	8.431	-0.005
0	118	8.532	8.532	0.000
0	122	8.576	8.570	-0.006
3	61	8.566	8.542	-0.024
3	62	8.614	8.613	-0.001
3	63	8.486	8.494	0.008
3	64	8.566	8.556	-0.010
3	65	8.374	8.356	-0.018
3	66	8.399	8.410	0.011
5	67	8.455	8.458	0.003
5	68	8.617	8.610	-0.007
5	69	8.370	8.373	0.003
5	70	8.720	8.729	0.009
5	71	8.579	8.567	-0.012
5	72	8.654	8.649	-0.005
10	73	8.566	8.535	-0.031
10	74	8.614	8.597	-0.017
10	75	8.486	8.505	0.019
10	76	8.566	8.561	-0.005
10	77	8.374	8.372	-0.002
10	78	8.399	8.404	0.005
15	79	8.455	8.485	0.030
15	80	8.617	8.606	-0.011
15	81	8.370	8.356	-0.014
15	82	8.720	8.741	0.021
15	83	8.579	8.564	-0.015
15	84	8.654	8.659	0.005
30	86	8.566	8.578	0.012
30	89	8.614	8.621	0.007
30	91	8.486	8.505	0.019
30	92	8.566	8.523	-0.043
30	94	8.374	8.372	-0.002
30	95	8.399	8.394	-0.005
35	100	8.455	8.496	0.041
35	101	8.617	8.643	0.026
35	102	8.370	8.370	0.000
35	103	8.720	8.704	-0.016
35	104	8.579	8.576	-0.003
35	105	8.654	8.663	0.009
50	106	8.567	8.591	0.024
50	107	8.467	8.505	0.038
50	108	8.359	8.424	0.065
50	109	8.525	8.579	0.054
50	110	8.539	8.543	0.004
50	111	8.405	8.409	0.004
55	113	8.572	8.534	-0.038
55	114	8.488	8.471	-0.017
55	115	8.561	8.548	-0.013
55	116	8.502	8.507	0.005
55	126	8.655	8.640	-0.015
55	128	8.588	8.518	-0.070
100	129	8.575	8.619	0.044
100	131	8.470	8.402	-0.068
100	132	8.597	8.571	-0.026
100	133	8.579	8.609	0.030
100	134	8.476	8.510	0.034
100	135	8.504	8.477	-0.027
100	138	8.522	8.578	0.056
100	139	8.381	8.426	0.045
100	140	8.489	8.521	0.032
100	141	8.408	8.409	0.001
100	142	8.562	8.604	0.042
100	143	8.512	8.562	0.050
100	144	8.475	8.487	0.012
100	220	8.569	8.559	-0.010
100	221	8.442	8.457	0.015
100	224	8.473	8.508	0.035
100	225	8.376	8.388	0.012
100	226	8.643	8.659	0.016
100	228	8.447	8.533	0.086
100	232	8.508	8.583	0.075
100	233	8.512	8.531	0.019
100	236	8.355	8.371	0.016
100	238	8.576	8.616	0.040
100	243	8.411	8.411	0.000
105	244	8.484	8.478	-0.006
105	246	8.398	8.477	0.079
105	248	8.558	8.576	0.018
105	374	8.493	8.539	0.046
105	376	8.447	8.473	0.026
105	Max	8.720	8.741	0.086
105	Average	8.515	8.524	0.009
105	Min	8.355	8.356	-0.070
105	Std Dev	0.094	0.092	0.029



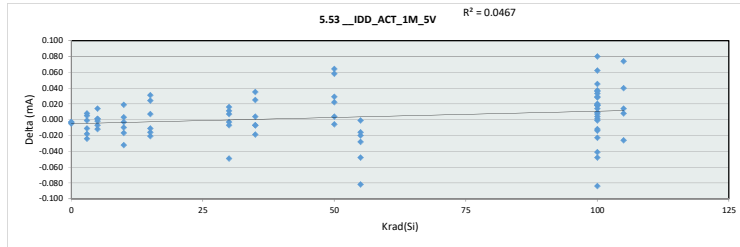
5.52 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	12 mA
Min Limit	12 mA
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	8.431 8.356 8.373 8.372 8.356 8.372 8.370 8.409 8.471 8.371 8.473
Average	8.511 8.495 8.564 8.496 8.569 8.499 8.575 8.509 8.536 8.516 8.509
Max	8.570 8.613 8.729 8.597 8.741 8.621 8.704 8.591 8.640 8.659 8.576
UL	12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000 12.000



LDR TID Report  
TPS7H5001-SP

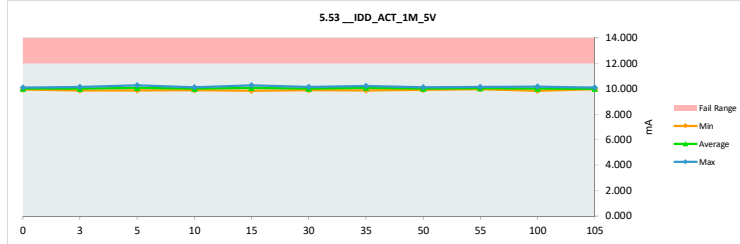
5.53 IDD_ACT_1M_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	12
Min Limit	12

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.941	9.936	-0.005
0	118	10.029	10.026	-0.003
0	122	10.090	10.087	-0.003
3	61	10.084	10.060	-0.024
3	62	10.134	10.133	-0.001
3	63	9.981	9.986	0.005
3	64	10.084	10.073	-0.011
3	65	9.885	9.867	-0.018
3	66	9.908	9.916	0.008
5	67	9.967	9.968	0.001
5	68	10.116	10.109	-0.007
5	69	9.875	9.876	0.001
5	70	10.248	10.262	0.014
5	71	10.089	10.077	-0.012
5	72	10.170	10.168	-0.002
10	73	10.084	10.052	-0.032
10	74	10.134	10.117	-0.017
10	75	9.981	10.000	0.019
10	76	10.084	10.074	-0.010
10	77	9.885	9.882	-0.003
10	78	9.908	9.911	0.003
15	79	9.967	9.998	0.031
15	80	10.116	10.105	-0.011
15	81	9.875	9.854	-0.021
15	82	10.248	10.272	0.024
15	83	10.089	10.073	-0.016
15	84	10.170	10.177	0.007
30	86	10.084	10.095	0.011
30	89	10.134	10.141	0.007
30	91	9.981	9.997	0.016
30	92	10.084	10.035	-0.049
30	94	9.885	9.882	-0.003
30	95	9.908	9.901	-0.007
35	100	9.967	10.002	0.035
35	101	10.116	10.141	0.025
35	102	9.875	9.868	-0.007
35	103	10.248	10.229	-0.019
35	104	10.089	10.082	-0.007
35	105	10.170	10.174	0.004
50	106	10.090	10.112	0.022
50	107	10.006	10.035	0.029
50	108	9.861	9.925	0.064
50	109	10.032	10.090	0.058
50	110	10.059	10.053	-0.006
50	111	9.915	9.919	0.004
55	113	10.083	10.035	-0.048
55	114	9.988	9.960	-0.028
55	115	10.074	10.054	-0.020
55	116	9.998	9.997	-0.001
55	126	10.169	10.153	-0.016
55	128	10.111	10.029	-0.082
100	129	10.091	10.120	0.029
100	131	9.976	9.892	-0.084
100	132	10.126	10.085	-0.041
100	133	10.087	10.107	0.020
100	134	10.017	10.035	0.018
100	135	10.016	9.968	-0.048
100	138	10.036	10.081	0.045
100	139	9.875	9.911	0.036
100	140	9.997	10.016	0.019
100	141	9.923	9.909	-0.014
100	142	10.076	10.113	0.037
100	143	10.012	10.045	0.033
100	144	9.983	9.987	0.004
100	220	10.072	10.049	-0.023
100	221	9.950	9.951	0.001
100	224	9.980	9.998	0.018
100	225	9.891	9.890	-0.001
100	226	10.154	10.168	0.014
100	228	9.948	10.028	0.080
100	232	10.031	10.093	0.062
100	233	10.035	10.042	0.007
100	236	9.838	9.847	0.009
100	238	10.086	10.114	0.028
100	243	9.907	9.895	-0.012
105	244	9.986	9.960	-0.026
105	246	9.894	9.968	0.074
105	248	10.071	10.085	0.014
105	374	9.980	10.020	0.040
105	376	9.967	9.975	0.008
105	Max	10.248	10.272	0.024
105	Average	10.026	10.029	0.003
105	Min	9.838	9.847	-0.009
105	Std Dev	0.099	0.098	0.030



5.53 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	12 mA
Min Limit	12 mA

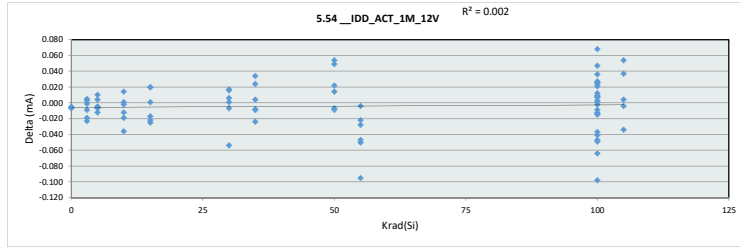
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.936	9.867	9.876	9.882	9.854	9.882	9.868	9.919	9.960	9.847	9.960
Average	10.016	10.006	10.077	10.006	10.080	10.009	10.083	10.022	10.038	10.014	10.002
Max	10.087	10.133	10.262	10.117	10.272	10.141	10.229	10.112	10.153	10.168	10.085
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000



LDR TID Report  
TPS7H5001-SP

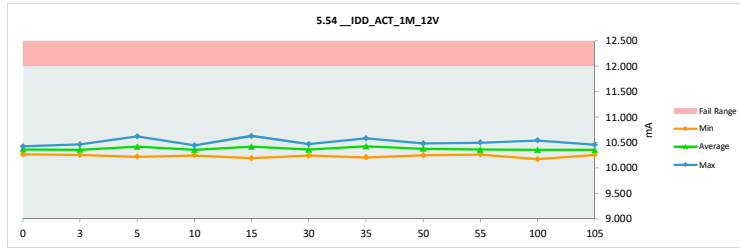
5.54 IDD_ACT_1M_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	12	12
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.275	10.268	-0.007
0	118	10.413	10.407	-0.006
0	122	10.428	10.423	-0.005
3	61	10.454	10.431	-0.023
3	62	10.463	10.462	-0.001
3	63	10.279	10.282	0.003
3	64	10.449	10.440	-0.009
3	65	10.311	10.292	-0.019
3	66	10.249	10.254	0.005
5	67	10.305	10.300	-0.005
5	68	10.447	10.440	-0.007
5	69	10.217	10.221	0.004
5	70	10.610	10.620	0.010
5	71	10.439	10.427	-0.012
5	72	10.509	10.504	-0.005
10	73	10.454	10.418	-0.036
10	74	10.463	10.444	-0.019
10	75	10.279	10.293	0.014
10	76	10.449	10.437	-0.012
10	77	10.311	10.312	0.001
10	78	10.249	10.247	-0.002
15	79	10.305	10.325	0.020
15	80	10.447	10.430	-0.017
15	81	10.217	10.195	-0.022
15	82	10.610	10.630	0.020
15	83	10.439	10.414	-0.025
15	84	10.509	10.510	0.001
30	86	10.454	10.471	0.017
30	89	10.463	10.469	0.006
30	91	10.279	10.295	0.016
30	92	10.449	10.395	-0.054
30	94	10.311	10.312	0.001
30	95	10.249	10.242	-0.007
35	100	10.305	10.339	0.034
35	101	10.447	10.471	0.024
35	102	10.217	10.209	-0.008
35	103	10.610	10.586	-0.024
35	104	10.439	10.430	-0.009
35	105	10.509	10.513	0.004
50	106	10.470	10.484	0.014
50	107	10.380	10.402	0.022
50	108	10.235	10.289	0.054
50	109	10.339	10.388	0.049
50	110	10.472	10.463	-0.009
50	111	10.256	10.249	-0.007
55	113	10.484	10.434	-0.050
55	114	10.312	10.265	-0.047
55	115	10.381	10.353	-0.028
55	116	10.322	10.318	-0.004
55	126	10.521	10.499	-0.022
55	128	10.414	10.319	-0.095
100	129	10.505	10.529	0.024
100	131	10.316	10.218	-0.098
100	132	10.461	10.412	-0.049
100	133	10.513	10.514	0.001
100	134	10.369	10.378	0.009
100	135	10.327	10.263	-0.064
100	138	10.383	10.419	0.036
100	139	10.223	10.244	0.021
100	140	10.363	10.375	0.012
100	141	10.262	10.225	-0.037
100	142	10.422	10.448	0.026
100	143	10.349	10.376	0.027
100	144	10.334	10.337	0.003
100	220	10.421	10.374	-0.047
100	221	10.265	10.252	-0.013
100	224	10.337	10.345	0.008
100	225	10.323	10.310	-0.013
100	226	10.552	10.543	-0.009
100	228	10.314	10.382	0.068
100	232	10.394	10.441	0.047
100	233	10.410	10.408	-0.002
100	236	10.190	10.175	-0.015
100	238	10.410	10.417	0.007
100	243	10.276	10.235	-0.041
105	244	10.347	10.313	-0.034
105	246	10.205	10.259	0.054
105	248	10.455	10.459	0.004
105	374	10.365	10.402	0.037
105	376	10.338	10.334	-0.004
105	Max	10.610	10.630	0.068
105	Average	10.379	10.375	-0.004
105	Min	10.190	10.175	-0.098
105	Std Dev	0.102	0.103	0.030



5.54 IDD_ACT		
Test Site		
Tester		
Test Number		
Max Limit	12	mA
Min Limit		mA
Krad(Si)	0	3

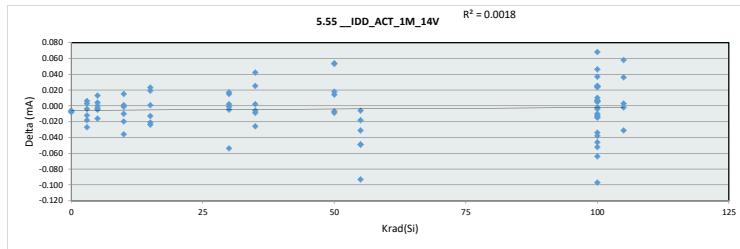
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.268	10.254	10.221	10.247	10.195	10.242	10.209	10.249	10.265	10.175	10.259
Average	10.366	10.360	10.419	10.359	10.417	10.364	10.425	10.379	10.365	10.359	10.353
Max	10.423	10.462	10.620	10.444	10.630	10.471	10.586	10.484	10.499	10.543	10.459
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000



LDR TID Report  
TPS7H5001-SP

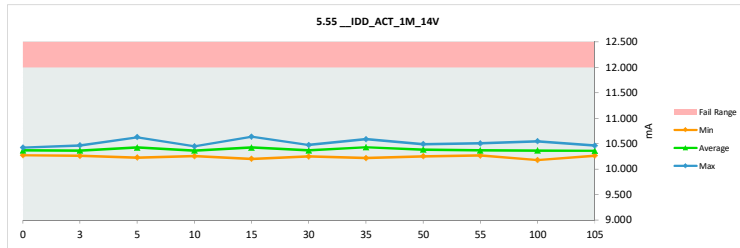
5.55 IDD_ACT_1M_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	12
Min Limit	12

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.279	10.272	-0.007
0	118	10.417	10.411	-0.006
0	122	10.433	10.425	-0.008
3	61	10.460	10.433	-0.027
3	62	10.469	10.465	-0.004
3	63	10.283	10.286	0.003
3	64	10.454	10.442	-0.012
3	65	10.316	10.298	-0.018
3	66	10.254	10.260	0.006
5	67	10.308	10.305	-0.003
5	68	10.451	10.446	-0.005
5	69	10.223	10.227	0.004
5	70	10.615	10.628	0.013
5	71	10.442	10.426	-0.016
5	72	10.513	10.513	0.000
10	73	10.460	10.424	-0.036
10	74	10.469	10.449	-0.020
10	75	10.283	10.298	0.015
10	76	10.454	10.444	-0.010
10	77	10.316	10.317	0.001
10	78	10.254	10.253	-0.001
15	79	10.308	10.331	0.023
15	80	10.451	10.438	-0.013
15	81	10.223	10.202	-0.021
15	82	10.615	10.634	0.019
15	83	10.442	10.418	-0.024
15	84	10.513	10.514	0.001
30	86	10.460	10.475	0.015
30	89	10.469	10.471	0.002
30	91	10.283	10.300	0.017
30	92	10.454	10.400	-0.054
30	94	10.316	10.315	-0.001
30	95	10.254	10.249	-0.005
35	100	10.308	10.350	0.042
35	101	10.451	10.476	0.025
35	102	10.223	10.217	-0.006
35	103	10.615	10.589	-0.026
35	104	10.442	10.433	-0.009
35	105	10.513	10.515	0.002
50	106	10.476	10.490	0.014
50	107	10.385	10.403	0.018
50	108	10.239	10.293	0.054
50	109	10.342	10.395	0.053
50	110	10.476	10.467	-0.009
50	111	10.259	10.252	-0.007
55	113	10.488	10.439	-0.049
55	114	10.317	10.268	-0.049
55	115	10.387	10.356	-0.031
55	116	10.327	10.321	-0.006
55	126	10.523	10.505	-0.018
55	128	10.419	10.326	-0.093
100	129	10.509	10.533	0.024
100	131	10.321	10.224	-0.097
100	132	10.468	10.416	-0.052
100	133	10.519	10.517	-0.002
100	134	10.375	10.381	0.006
100	135	10.332	10.268	-0.064
100	138	10.386	10.423	0.037
100	139	10.227	10.251	0.024
100	140	10.367	10.377	0.010
100	141	10.266	10.232	-0.034
100	142	10.427	10.452	0.025
100	143	10.353	10.378	0.025
100	144	10.338	10.343	0.005
100	220	10.425	10.379	-0.046
100	221	10.269	10.257	-0.012
100	224	10.342	10.349	0.007
100	225	10.327	10.313	-0.014
100	226	10.556	10.546	-0.010
100	228	10.318	10.386	0.068
100	232	10.398	10.444	0.046
100	233	10.413	10.409	-0.004
100	236	10.195	10.180	-0.015
100	238	10.414	10.420	0.006
100	243	10.278	10.240	-0.038
105	244	10.349	10.318	-0.031
105	246	10.207	10.265	0.058
105	248	10.459	10.462	0.003
105	374	10.368	10.404	0.036
105	376	10.341	10.339	-0.002
105	Max	10.615	10.634	0.068
105	Average	10.383	10.380	-0.004
105	Min	10.195	10.180	-0.097
105	Std Dev	0.102	0.102	0.030



5.55 IDD_ACT	
Test Site	
Tester	
Test Number	
Max Limit	12 mA
Min Limit	12 mA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.272	10.260	10.227	10.253	10.202	10.249	10.217	10.252	10.268	10.180	10.265
Average	10.369	10.364	10.424	10.364	10.423	10.368	10.430	10.383	10.369	10.363	10.358
Max	10.425	10.465	10.628	10.449	10.634	10.475	10.589	10.490	10.505	10.546	10.462
UL	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000	12.000

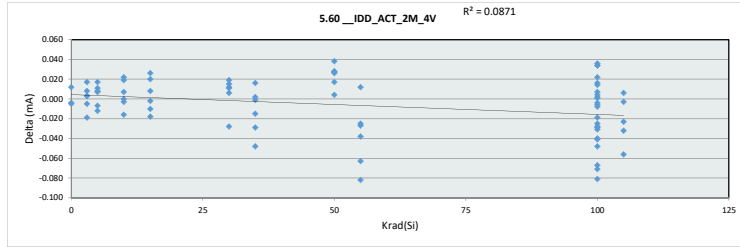




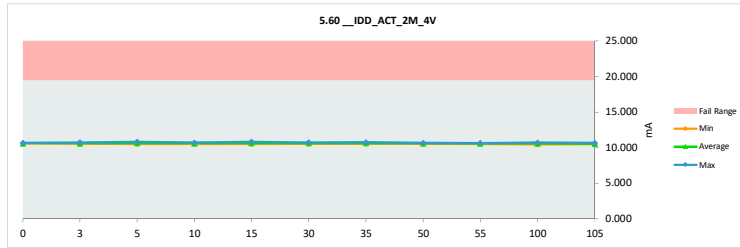
LDR TID Report  
TPS7H5001-SP

5.60 IDD_ACT_2M_4V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	19.5	19.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.578	10.574	-0.004
0	118	10.578	10.573	-0.005
0	122	10.638	10.650	0.012
3	61	10.601	10.582	-0.019
3	62	10.692	10.700	0.008
3	63	10.614	10.631	0.017
3	64	10.679	10.682	0.003
3	65	10.512	10.507	-0.005
3	66	10.530	10.533	0.003
5	67	10.495	10.503	0.008
5	68	10.673	10.661	-0.012
5	69	10.515	10.522	0.007
5	70	10.777	10.794	0.017
5	71	10.649	10.642	-0.007
5	72	10.690	10.701	0.011
10	73	10.691	10.585	-0.106
10	74	10.692	10.689	-0.003
10	75	10.614	10.636	0.022
10	76	10.679	10.679	0.000
10	77	10.512	10.519	0.007
10	78	10.530	10.549	0.019
15	79	10.495	10.521	0.026
15	80	10.673	10.671	-0.002
15	81	10.515	10.497	-0.018
15	82	10.777	10.797	0.020
15	83	10.649	10.639	-0.010
15	84	10.690	10.698	0.008
30	86	10.601	10.620	0.019
30	89	10.692	10.703	0.011
30	91	10.614	10.626	0.012
30	92	10.679	10.651	-0.028
30	94	10.512	10.518	0.006
30	95	10.530	10.545	0.015
35	100	10.495	10.511	0.016
35	101	10.673	10.675	0.002
35	102	10.515	10.486	-0.029
35	103	10.777	10.729	-0.048
35	104	10.649	10.634	-0.015
35	105	10.690	10.689	-0.001
50	106	10.615	10.641	0.026
50	107	10.504	10.521	0.017
50	108	10.583	10.587	0.004
50	109	10.561	10.599	0.038
50	110	10.561	10.589	0.028
50	111	10.558	10.585	0.027
55	113	10.688	10.625	-0.063
55	114	10.576	10.549	-0.027
55	115	10.587	10.562	-0.025
55	116	10.523	10.535	0.012
55	126	10.673	10.635	-0.038
55	128	10.639	10.557	-0.082
100	129	10.605	10.627	0.022
100	131	10.576	10.509	-0.067
100	132	10.703	10.632	-0.071
100	133	10.617	10.624	0.007
100	134	10.543	10.559	0.016
100	135	10.750	10.669	-0.081
100	138	10.560	10.574	0.014
100	139	10.548	10.540	-0.008
100	140	10.569	10.603	0.034
100	141	10.521	10.481	-0.040
100	142	10.621	10.622	0.001
100	143	10.565	10.567	0.002
100	144	10.562	10.531	-0.031
100	220	10.657	10.629	-0.028
100	221	10.531	10.512	-0.019
100	224	10.355	10.359	0.004
100	225	10.571	10.542	-0.029
100	226	10.696	10.690	-0.006
100	228	10.529	10.565	0.036
100	232	10.606	10.640	0.034
100	233	10.646	10.642	-0.004
100	236	10.535	10.494	-0.041
100	238	10.711	10.686	-0.025
100	243	10.481	10.433	-0.048
105	244	10.538	10.482	-0.056
105	246	10.515	10.521	0.006
105	248	10.673	10.641	-0.032
105	374	10.556	10.533	-0.023
105	375	10.564	10.561	-0.003
105	Max	10.777	10.797	0.038
105	Average	10.603	10.597	-0.006
105	Min	10.481	10.433	-0.082
105	Std Dev	0.075	0.075	0.027



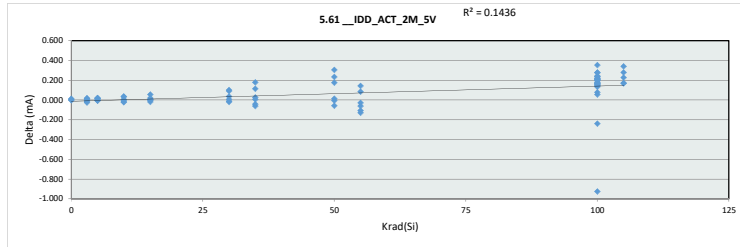
5.60 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5 mA										
Min Limit	mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.573	10.507	10.503	10.519	10.497	10.518	10.486	10.521	10.535	10.433	10.482
Average	10.599	10.606	10.637	10.610	10.637	10.611	10.621	10.587	10.577	10.580	10.548
Max	10.650	10.700	10.794	10.689	10.797	10.703	10.729	10.641	10.635	10.690	10.641
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500



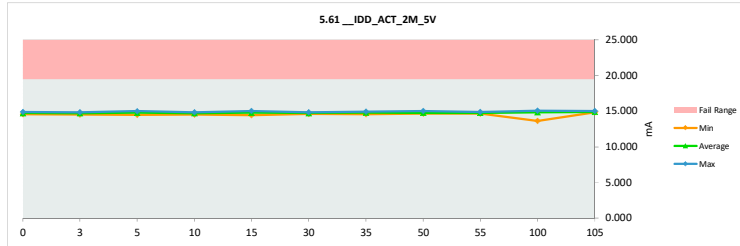
LDR TID Report  
TPS7H5001-SP

5.61 IDD_ACT_2M_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	19.5
Min Limit	19.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	14.561	14.557	-0.004
0	118	14.725	14.727	0.002
0	122	14.863	14.875	0.012
3	61	14.778	14.750	-0.028
3	62	14.838	14.845	0.007
3	63	14.592	14.606	0.014
3	64	14.787	14.783	-0.004
3	65	14.525	14.509	-0.016
3	66	14.591	14.605	0.014
5	67	14.628	14.648	0.020
5	68	14.824	14.818	-0.006
5	69	14.447	14.463	0.016
5	70	14.975	14.992	0.017
5	71	14.768	14.759	-0.009
5	72	14.905	14.909	0.004
10	73	14.778	14.753	-0.025
10	74	14.838	14.819	-0.019
10	75	14.592	14.626	0.034
10	76	14.787	14.792	0.005
10	77	14.525	14.522	-0.003
10	78	14.591	14.621	0.030
15	79	14.628	14.683	0.055
15	80	14.824	14.825	0.001
15	81	14.447	14.425	-0.022
15	82	14.975	14.995	0.020
15	83	14.768	14.770	0.002
15	84	14.905	14.900	-0.005
30	86	14.778	14.782	0.004
30	89	14.838	14.824	-0.014
30	91	14.592	14.691	0.099
30	92	14.787	14.766	-0.021
30	94	14.525	14.616	0.091
30	95	14.591	14.627	0.036
35	100	14.628	14.805	0.177
35	101	14.824	14.830	0.006
35	102	14.447	14.560	0.113
35	103	14.975	14.911	-0.064
35	104	14.768	14.794	0.026
35	105	14.905	14.862	-0.043
50	106	14.766	14.999	0.233
50	107	14.657	14.670	0.013
50	108	14.447	14.749	0.302
50	109	14.712	14.706	-0.006
50	110	14.750	14.691	-0.059
50	111	14.509	14.683	0.174
55	113	14.798	14.882	0.084
55	114	14.633	14.775	0.142
55	115	14.749	14.688	-0.061
55	116	14.703	14.674	-0.029
55	126	14.839	14.731	-0.108
55	128	14.799	14.667	-0.132
100	129	14.832	14.886	0.054
100	131	14.721	14.798	0.077
100	132	14.904	15.037	0.133
100	133	14.818	15.051	0.233
100	134	14.816	14.993	0.177
100	135	14.569	13.641	-0.928
100	138	14.725	14.886	0.161
100	139	14.525	14.740	0.215
100	140	14.699	14.909	0.210
100	141	14.583	14.730	0.147
100	142	14.795	14.556	-0.239
100	143	14.678	14.912	0.234
100	144	14.707	14.921	0.214
100	220	14.794	14.934	0.140
100	221	14.623	14.819	0.196
100	224	14.641	14.834	0.193
100	225	14.534	14.740	0.206
100	226	14.819	15.060	0.241
100	228	14.637	14.988	0.351
100	232	14.793	15.069	0.276
100	233	14.749	14.906	0.157
100	236	14.381	14.548	0.167
100	238	14.779	15.053	0.274
100	243	14.574	14.746	0.172
105	244	14.662	14.833	0.171
105	246	14.539	14.878	0.339
105	248	14.776	15.001	0.225
105	374	14.618	14.896	0.278
105	376	14.678	14.846	0.168
105	375	14.975	15.069	0.351
Max		14.706	14.772	0.066
Min		14.381	13.641	-0.928
Std Dev		0.137	0.196	0.163



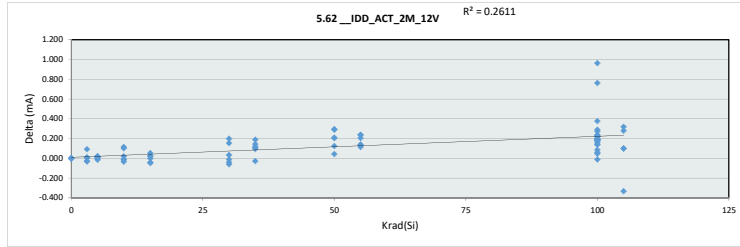
5.61 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5 mA										
Min Limit	19.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	14.557	14.509	14.463	14.522	14.425	14.616	14.560	14.670	14.667	13.641	14.833
Average	14.720	14.683	14.765	14.689	14.766	14.718	14.794	14.750	14.736	14.823	14.891
Max	14.875	14.845	14.992	14.819	14.995	14.824	14.911	14.999	14.882	15.069	15.001
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500



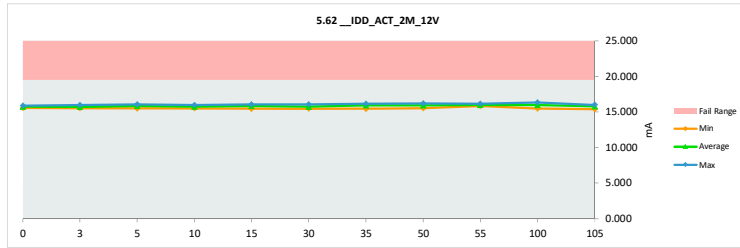
LDR TID Report  
TPS7H5001-SP

5.62 IDD_ACT_2M_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	19.5	19.5
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	15.575	15.568	-0.007
0	118	15.846	15.845	-0.001
0	122	15.906	15.909	0.003
3	61	15.887	15.979	0.092
3	62	15.851	15.858	0.007
3	63	15.516	15.520	0.004
3	64	15.837	15.805	-0.032
3	65	15.735	15.705	-0.030
3	66	15.553	15.561	0.008
5	67	15.829	15.841	0.012
5	68	15.844	15.837	-0.007
5	69	15.487	15.493	0.006
5	70	16.069	16.087	0.018
5	71	15.834	15.819	-0.015
5	72	16.042	16.063	0.021
10	73	15.887	16.000	0.113
10	74	15.851	15.952	0.101
10	75	15.516	15.537	0.021
10	76	15.837	15.800	-0.037
10	77	15.735	15.718	-0.017
10	78	15.553	15.543	-0.010
15	79	15.829	15.881	0.052
15	80	15.844	15.840	-0.004
15	81	15.487	15.437	-0.050
15	82	16.069	16.091	0.022
15	83	15.834	15.791	-0.043
15	84	16.042	16.063	0.021
30	86	15.887	16.085	0.198
30	89	15.851	16.004	0.153
30	91	15.516	15.503	-0.013
30	92	15.837	15.777	-0.060
30	94	15.735	15.694	-0.041
30	95	15.553	15.585	0.032
35	100	15.829	15.940	0.111
35	101	15.844	15.987	0.143
35	102	15.487	15.459	-0.028
35	103	16.069	16.185	0.116
35	104	15.834	16.022	0.188
35	105	16.042	16.135	0.093
50	106	15.903	16.192	0.289
50	107	15.772	16.067	0.295
50	108	15.517	15.725	0.208
50	109	15.778	15.982	0.204
50	110	16.086	16.210	0.124
50	111	15.515	15.557	0.042
55	113	15.920	16.155	0.235
55	114	15.612	15.850	0.238
55	115	15.711	15.914	0.203
55	116	15.808	15.942	0.134
55	126	15.984	16.123	0.139
55	128	15.742	15.854	0.112
100	129	16.064	16.255	0.191
100	131	15.770	15.829	0.059
100	132	15.962	16.103	0.141
100	133	16.079	16.240	0.161
100	134	15.909	16.096	0.187
100	135	15.512	16.274	0.762
100	138	15.762	16.049	0.287
100	139	15.571	15.750	0.179
100	140	15.812	16.035	0.223
100	141	15.505	15.493	-0.012
100	142	15.850	16.123	0.273
100	143	15.674	15.893	0.219
100	144	15.763	15.956	0.193
100	220	15.839	15.883	0.044
100	221	15.512	15.691	0.179
100	224	15.716	15.936	0.220
100	225	15.771	15.856	0.085
100	226	15.939	16.139	0.200
100	228	15.663	16.040	0.377
100	232	15.928	16.165	0.237
100	233	15.770	15.952	0.182
100	236	15.384	16.347	0.963
100	238	15.774	16.000	0.226
100	243	15.593	15.730	0.137
105	244	15.704	15.800	0.096
105	246	15.493	15.810	0.317
105	248	15.861	15.962	0.101
105	374	15.737	16.014	0.277
105	376	15.728	15.395	-0.333
105	Max	16.086	16.347	0.963
105	Average	15.772	15.891	0.119
105	Min	15.384	15.395	-0.333
105	Std Dev	0.176	0.223	0.168



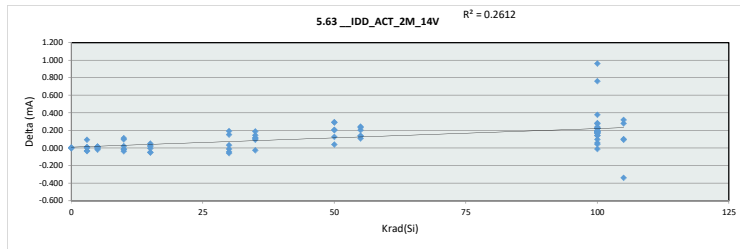
5.62 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5	mA									
Min Limit		mA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	15.568	15.520	15.493	15.537	15.437	15.503	15.459	15.557	15.850	15.493	15.395
Average	15.774	15.738	15.857	15.758	15.851	15.775	15.955	15.956	15.973	15.993	15.796
Max	15.909	15.979	16.087	16.000	16.091	16.085	16.185	16.210	16.155	16.347	16.014
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500



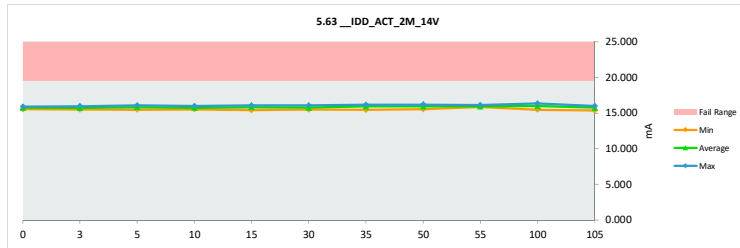
LDR TID Report  
TPS7H5001-SP

5.63 IDD_ACT_2M_14V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	19.5
Min Limit	19.5

Krad(Si)	Serial #	PRE	POST	Delta
0	117	15.580	15.574	-0.006
0	118	15.854	15.856	0.002
0	122	15.908	15.916	0.008
3	61	15.893	15.986	0.093
3	62	15.857	15.861	0.004
3	63	15.521	15.527	0.006
3	64	15.843	15.809	-0.034
3	65	15.742	15.710	-0.032
3	66	15.560	15.570	0.010
5	67	15.836	15.845	0.009
5	68	15.848	15.842	-0.006
5	69	15.491	15.498	0.007
5	70	16.076	16.090	0.014
5	71	15.841	15.822	-0.019
5	72	16.047	16.065	0.018
10	73	15.893	16.007	0.114
10	74	15.857	15.955	0.098
10	75	15.521	15.542	0.021
10	76	15.843	15.806	-0.037
10	77	15.742	15.725	-0.017
10	78	15.560	15.549	-0.011
15	79	15.836	15.887	0.051
15	80	15.848	15.844	-0.004
15	81	15.491	15.441	-0.050
15	82	16.076	16.102	0.026
15	83	15.841	15.794	-0.047
15	84	16.047	16.069	0.022
30	86	15.893	16.086	0.193
30	89	15.857	16.010	0.153
30	91	15.521	15.510	-0.011
30	92	15.843	15.784	-0.059
30	94	15.742	15.699	-0.043
30	95	15.560	15.592	0.032
35	100	15.836	15.947	0.111
35	101	15.848	15.992	0.144
35	102	15.491	15.465	-0.026
35	103	16.076	16.191	0.115
35	104	15.841	16.030	0.189
35	105	16.047	16.141	0.094
50	106	15.909	16.200	0.291
50	107	15.778	16.072	0.294
50	108	15.535	15.729	0.204
50	109	15.783	15.990	0.207
50	110	16.092	16.219	0.127
50	111	15.521	15.559	0.038
55	113	15.926	16.160	0.234
55	114	15.616	15.858	0.242
55	115	15.717	15.920	0.203
55	116	15.815	15.951	0.136
55	126	15.991	16.128	0.137
55	128	15.748	15.858	0.110
100	129	16.072	16.259	0.187
100	131	15.776	15.834	0.058
100	132	15.968	16.110	0.142
100	133	16.082	16.246	0.164
100	134	15.915	16.105	0.190
100	135	15.520	16.282	0.762
100	138	15.768	16.052	0.284
100	139	15.577	15.752	0.175
100	140	15.818	16.042	0.224
100	141	15.512	15.500	-0.012
100	142	15.855	16.131	0.276
100	143	15.677	15.898	0.221
100	144	15.772	15.965	0.193
100	220	15.845	15.889	0.044
100	221	15.517	15.692	0.175
100	224	15.722	15.942	0.220
100	225	15.777	15.874	0.097
100	226	15.945	16.144	0.199
100	228	15.671	16.048	0.377
100	232	15.930	16.171	0.241
100	233	15.779	15.948	0.169
100	236	15.391	16.352	0.961
100	238	15.776	16.005	0.229
100	243	15.597	15.740	0.143
105	244	15.712	15.807	0.095
105	246	15.496	15.815	0.319
105	248	15.868	15.971	0.103
105	374	15.739	16.020	0.281
105	376	15.793	15.392	-0.341
105	Max	16.092	16.352	0.961
105	Average	15.778	15.897	0.119
105	Min	15.391	15.392	-0.341
105	Std Dev	0.176	0.224	0.169

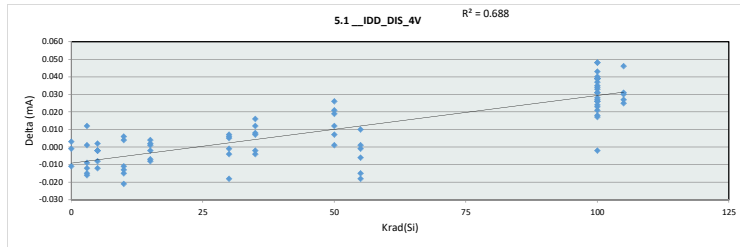


5.63 IDD_ACT_2											
Test Site											
Tester											
Test Number											
Max Limit	19.5 mA										
Min Limit	19.5 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	15.574	15.527	15.498	15.542	15.441	15.510	15.465	15.559	15.858	15.500	15.392
Average	15.782	15.744	15.860	15.764	15.856	15.780	15.961	15.962	15.979	15.999	15.801
Max	15.916	15.986	16.090	16.007	16.102	16.086	16.191	16.219	16.160	16.352	16.020
UL	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500	19.500

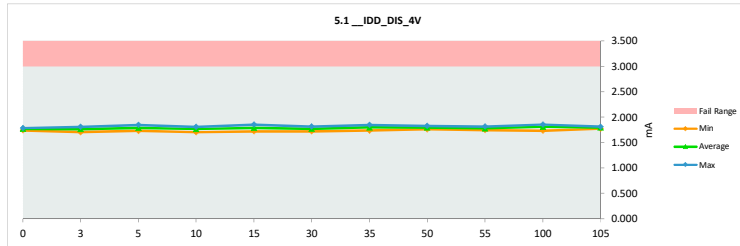


LDR TID Report  
TPS7H5001-SP

5.1 IDD_DIS_4V				
Test Site	Tester	Test Number	Unit	Max Limit
			mA	mA
			3	3
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.750	1.739	-0.011
0	118	1.782	1.781	-0.001
0	122	1.784	1.787	0.003
3	61	1.776	1.761	-0.015
3	62	1.819	1.807	-0.012
3	63	1.769	1.770	0.001
3	64	1.818	1.802	-0.016
3	65	1.714	1.705	-0.009
3	66	1.733	1.745	0.012
5	67	1.763	1.761	-0.002
5	68	1.822	1.820	-0.002
5	69	1.728	1.730	0.002
5	70	1.849	1.847	-0.002
5	71	1.791	1.783	-0.008
5	72	1.802	1.790	-0.012
10	73	1.776	1.755	-0.021
10	74	1.819	1.804	-0.015
10	75	1.769	1.773	0.004
10	76	1.818	1.807	-0.011
10	77	1.714	1.701	-0.013
10	78	1.733	1.739	0.006
15	79	1.763	1.767	0.004
15	80	1.822	1.820	-0.002
15	81	1.728	1.720	-0.008
15	82	1.849	1.851	0.002
15	83	1.791	1.792	0.001
15	84	1.802	1.795	-0.007
30	86	1.776	1.775	-0.001
30	89	1.819	1.815	-0.004
30	91	1.769	1.776	0.007
30	92	1.818	1.800	-0.018
30	94	1.714	1.720	0.006
30	95	1.733	1.738	0.005
35	100	1.763	1.775	0.012
35	101	1.822	1.838	0.016
35	102	1.728	1.736	0.008
35	103	1.849	1.847	-0.002
35	104	1.791	1.798	0.007
35	105	1.802	1.798	-0.004
50	106	1.816	1.823	0.007
50	107	1.800	1.826	0.026
50	108	1.750	1.771	0.021
50	109	1.796	1.815	0.019
50	110	1.784	1.785	0.001
50	111	1.748	1.760	0.012
55	113	1.788	1.773	-0.015
55	114	1.773	1.772	-0.001
55	115	1.765	1.775	0.010
55	116	1.790	1.791	0.001
55	126	1.822	1.816	-0.006
55	128	1.764	1.746	-0.018
100	129	1.795	1.834	0.039
100	131	1.762	1.760	-0.002
100	132	1.807	1.831	0.024
100	133	1.794	1.825	0.031
100	134	1.761	1.796	0.035
100	135	1.777	1.794	0.017
100	138	1.781	1.829	0.048
100	139	1.758	1.791	0.033
100	140	1.786	1.823	0.037
100	141	1.746	1.767	0.021
100	142	1.782	1.810	0.028
100	143	1.792	1.840	0.048
100	144	1.777	1.811	0.034
100	220	1.772	1.815	0.043
100	221	1.782	1.805	0.023
100	224	1.807	1.846	0.039
100	225	1.708	1.734	0.026
100	226	1.790	1.829	0.039
100	228	1.767	1.806	0.039
100	232	1.812	1.852	0.040
100	233	1.816	1.843	0.027
100	236	1.737	1.763	0.026
100	238	1.788	1.819	0.031
100	243	1.737	1.755	0.018
105	244	1.746	1.776	0.030
105	246	1.728	1.774	0.046
105	248	1.785	1.810	0.025
105	374	1.774	1.805	0.031
105	376	1.784	1.811	0.027
	Max	1.849	1.852	0.048
	Average	1.779	1.790	0.011
	Min	1.708	1.701	-0.021
	Std Dev	0.033	0.036	0.019



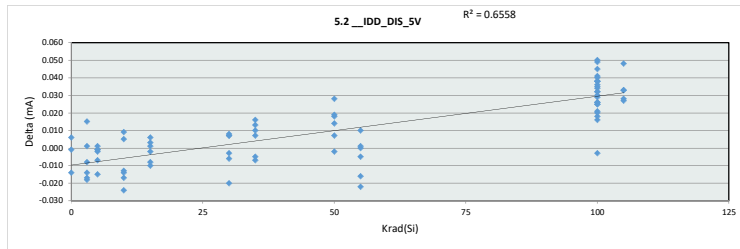
5.1 IDD DIS 4V											
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit						
			mA	3	3						
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.739	1.705	1.730	1.701	1.720	1.720	1.736	1.760	1.746	1.734	1.774
Average	1.769	1.765	1.789	1.763	1.791	1.771	1.799	1.797	1.779	1.807	1.795
Max	1.787	1.807	1.847	1.807	1.851	1.815	1.847	1.826	1.816	1.852	1.811
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000



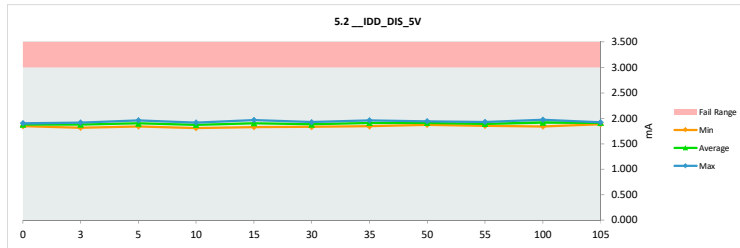
LDR TID Report  
TPS7H5001-SP

5.2 IDD_DIS_5V	
Test Site	
Tester	
Test Number	
Unit	mA
Max Limit	3
Min Limit	3

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.858	1.844	-0.014
0	118	1.889	1.888	-0.001
0	122	1.892	1.898	0.006
3	61	1.886	1.869	-0.017
3	62	1.928	1.914	-0.014
3	63	1.876	1.877	0.001
3	64	1.928	1.910	-0.018
3	65	1.821	1.813	-0.008
3	66	1.838	1.853	0.015
5	67	1.870	1.869	-0.001
5	68	1.929	1.928	-0.001
5	69	1.834	1.835	0.001
5	70	1.961	1.959	-0.002
5	71	1.898	1.891	-0.007
5	72	1.912	1.897	-0.015
10	73	1.886	1.862	-0.024
10	74	1.928	1.911	-0.017
10	75	1.876	1.881	0.005
10	76	1.928	1.915	-0.013
10	77	1.821	1.807	-0.014
10	78	1.838	1.847	0.009
15	79	1.870	1.876	0.006
15	80	1.929	1.927	-0.002
15	81	1.834	1.826	-0.008
15	82	1.961	1.962	0.001
15	83	1.898	1.901	0.003
15	84	1.912	1.902	-0.010
30	86	1.886	1.883	-0.003
30	89	1.928	1.922	-0.006
30	91	1.876	1.883	0.007
30	92	1.928	1.908	-0.020
30	94	1.821	1.828	0.007
30	95	1.838	1.846	0.008
35	100	1.870	1.883	0.013
35	101	1.929	1.945	0.016
35	102	1.834	1.841	0.007
35	103	1.961	1.956	-0.005
35	104	1.898	1.908	0.010
35	105	1.912	1.905	-0.007
50	106	1.924	1.931	0.007
50	107	1.909	1.937	0.028
50	108	1.859	1.878	0.019
50	109	1.907	1.925	0.018
50	110	1.894	1.892	-0.002
50	111	1.854	1.868	0.014
55	113	1.897	1.881	-0.016
55	114	1.879	1.879	0.000
55	115	1.871	1.881	0.010
55	116	1.898	1.899	0.001
55	126	1.931	1.926	-0.005
55	128	1.873	1.851	-0.022
100	129	1.902	1.940	0.038
100	131	1.871	1.868	-0.003
100	132	1.915	1.941	0.026
100	133	1.901	1.933	0.032
100	134	1.868	1.902	0.034
100	135	1.886	1.902	0.016
100	138	1.887	1.936	0.049
100	139	1.866	1.896	0.030
100	140	1.896	1.932	0.036
100	141	1.852	1.872	0.020
100	142	1.889	1.918	0.029
100	143	1.900	1.950	0.050
100	144	1.885	1.920	0.035
100	220	1.879	1.924	0.045
100	221	1.892	1.913	0.021
100	224	1.916	1.954	0.038
100	225	1.813	1.838	0.025
100	226	1.899	1.939	0.040
100	228	1.873	1.914	0.041
100	232	1.923	1.961	0.038
100	233	1.945	1.970	0.025
100	236	1.846	1.872	0.026
100	238	1.896	1.928	0.032
100	243	1.844	1.862	0.018
105	244	1.854	1.887	0.033
105	246	1.834	1.882	0.048
105	248	1.892	1.920	0.028
105	374	1.882	1.915	0.033
105	376	1.894	1.921	0.027
105	Max	1.961	1.970	0.050
105	Average	1.887	1.898	0.011
105	Min	1.813	1.807	-0.024
105	Std Dev	0.035	0.037	0.019



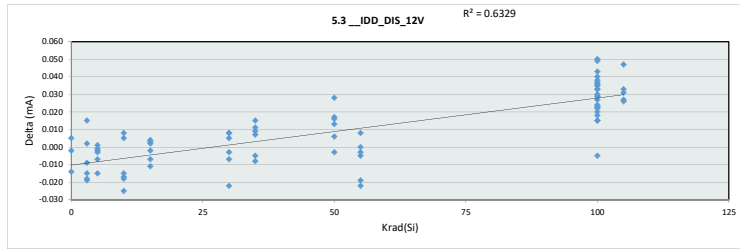
5.2 IDD DIS 5V											
Test Site											
Tester											
Test Number											
Max Limit	3 mA										
Min Limit	3 mA										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.844	1.813	1.835	1.807	1.826	1.828	1.841	1.868	1.851	1.838	1.882
Average	1.877	1.873	1.897	1.871	1.899	1.878	1.906	1.905	1.886	1.916	1.905
Max	1.898	1.914	1.959	1.915	1.962	1.922	1.956	1.937	1.926	1.970	1.921
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000



LDR TID Report  
TPS7H5001-SP

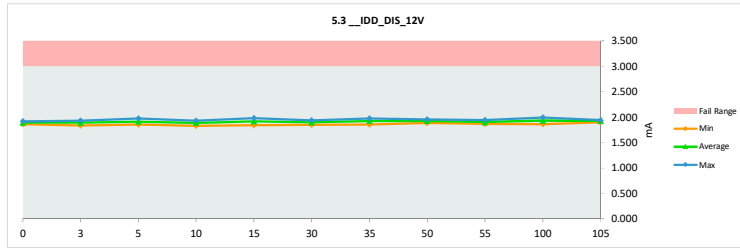
5.3 IDD_DIS_12V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	3	3
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.874	1.860	-0.014
0	118	1.910	1.908	-0.002
0	122	1.909	1.914	0.005
3	61	1.906	1.888	-0.018
3	62	1.943	1.928	-0.015
3	63	1.889	1.891	0.002
3	64	1.947	1.928	-0.019
3	65	1.845	1.836	-0.009
3	66	1.855	1.870	0.015
5	67	1.888	1.885	-0.003
5	68	1.945	1.944	-0.001
5	69	1.850	1.851	0.001
5	70	1.978	1.976	-0.002
5	71	1.915	1.908	-0.007
5	72	1.928	1.913	-0.015
10	73	1.906	1.881	-0.025
10	74	1.943	1.925	-0.018
10	75	1.889	1.894	0.005
10	76	1.947	1.932	-0.015
10	77	1.845	1.828	-0.017
10	78	1.855	1.863	0.008
15	79	1.888	1.892	0.004
15	80	1.945	1.943	-0.002
15	81	1.850	1.843	-0.007
15	82	1.978	1.980	0.002
15	83	1.915	1.918	0.003
15	84	1.928	1.917	-0.011
30	86	1.906	1.903	-0.003
30	89	1.943	1.936	-0.007
30	91	1.889	1.897	0.008
30	92	1.947	1.925	-0.022
30	94	1.845	1.850	0.005
30	95	1.855	1.863	0.008
35	100	1.888	1.899	0.011
35	101	1.945	1.960	0.015
35	102	1.850	1.857	0.007
35	103	1.978	1.973	-0.005
35	104	1.915	1.924	0.009
35	105	1.928	1.920	-0.008
50	106	1.943	1.949	0.006
50	107	1.928	1.956	0.028
50	108	1.879	1.896	0.017
50	109	1.922	1.938	0.016
50	110	1.914	1.911	-0.003
50	111	1.871	1.884	0.013
55	113	1.919	1.900	-0.019
55	114	1.895	1.892	-0.003
55	115	1.886	1.894	0.008
55	116	1.914	1.914	0.000
55	126	1.948	1.943	-0.005
55	128	1.886	1.864	-0.022
100	129	1.923	1.959	0.036
100	131	1.887	1.882	-0.005
100	132	1.932	1.955	0.023
100	133	1.923	1.952	0.029
100	134	1.885	1.918	0.033
100	135	1.901	1.916	0.015
100	138	1.904	1.953	0.049
100	139	1.883	1.913	0.030
100	140	1.915	1.950	0.035
100	141	1.869	1.887	0.018
100	142	1.907	1.934	0.027
100	143	1.917	1.967	0.050
100	144	1.902	1.935	0.033
100	220	1.896	1.939	0.043
100	221	1.907	1.927	0.020
100	224	1.935	1.972	0.037
100	225	1.837	1.859	0.022
100	226	1.920	1.958	0.038
100	228	1.892	1.932	0.040
100	232	1.942	1.977	0.035
100	233	1.967	1.990	0.023
100	236	1.866	1.890	0.024
100	238	1.912	1.941	0.029
100	243	1.863	1.878	0.015
105	244	1.873	1.904	0.031
105	246	1.849	1.896	0.047
105	248	1.912	1.938	0.026
105	374	1.902	1.935	0.033
105	376	1.912	1.939	0.027
	Max	1.978	1.990	0.050
	Average	1.905	1.914	0.010
	Min	1.837	1.828	-0.025
	Std Dev	0.034	0.037	0.019



5.3 IDD DIS 12V		
Test Site		
Tester		
Test Number		
Max Limit	3	mA
Min Limit		mA
Krad(Si)	0	3

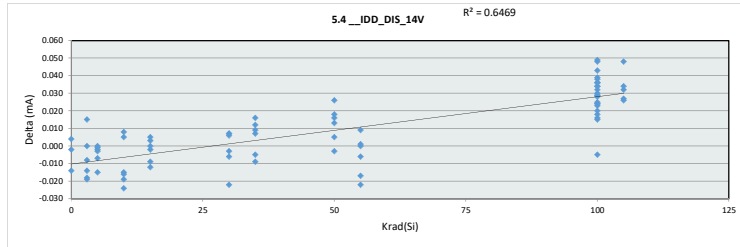
LL													
Min	1.860	1.836	1.851	1.828	1.843	1.850	1.857	1.884	1.864	1.859	1.896		
Average	1.894	1.890	1.913	1.887	1.916	1.896	1.922	1.922	1.901	1.933	1.922		
Max	1.914	1.928	1.976	1.932	1.980	1.936	1.973	1.956	1.943	1.990	1.939		
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000		



LDR TID Report  
TPS7H5001-SP

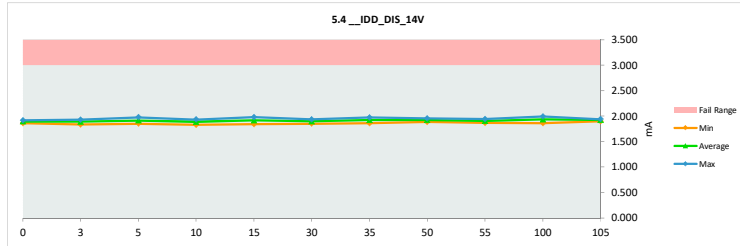
5.4 IDD_DIS_14V		
Test Site		
Tester		
Test Number		
Unit	mA	mA
Max Limit	3	3
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.879	1.865	-0.014
0	118	1.914	1.912	-0.002
0	122	1.914	1.918	0.004
3	61	1.910	1.892	-0.018
3	62	1.947	1.933	-0.014
3	63	1.894	1.894	0.000
3	64	1.951	1.932	-0.019
3	65	1.848	1.840	-0.008
3	66	1.859	1.874	0.015
5	67	1.891	1.889	-0.002
5	68	1.949	1.948	-0.001
5	69	1.855	1.855	0.000
5	70	1.983	1.980	-0.003
5	71	1.919	1.912	-0.007
5	72	1.933	1.918	-0.015
10	73	1.910	1.886	-0.024
10	74	1.947	1.928	-0.019
10	75	1.894	1.899	0.005
10	76	1.951	1.936	-0.015
10	77	1.848	1.832	-0.016
10	78	1.859	1.867	0.008
15	79	1.891	1.896	0.005
15	80	1.949	1.947	-0.002
15	81	1.855	1.846	-0.009
15	82	1.983	1.983	0.000
15	83	1.919	1.922	0.003
15	84	1.933	1.921	-0.012
30	86	1.910	1.907	-0.003
30	89	1.947	1.941	-0.006
30	91	1.894	1.901	0.007
30	92	1.951	1.929	-0.022
30	94	1.848	1.854	0.006
30	95	1.859	1.866	0.007
35	100	1.891	1.903	0.012
35	101	1.949	1.965	0.016
35	102	1.855	1.862	0.007
35	103	1.983	1.978	-0.005
35	104	1.919	1.928	0.009
35	105	1.933	1.924	-0.009
50	106	1.948	1.953	0.005
50	107	1.933	1.959	0.026
50	108	1.883	1.901	0.018
50	109	1.926	1.942	0.016
50	110	1.919	1.916	-0.003
50	111	1.875	1.888	0.013
55	113	1.922	1.905	-0.017
55	114	1.898	1.898	0.000
55	115	1.889	1.898	0.009
55	116	1.918	1.919	0.001
55	126	1.953	1.947	-0.006
55	128	1.890	1.868	-0.022
100	129	1.927	1.963	0.036
100	131	1.891	1.886	-0.005
100	132	1.936	1.961	0.025
100	133	1.927	1.957	0.030
100	134	1.889	1.921	0.032
100	135	1.905	1.920	0.015
100	138	1.909	1.957	0.048
100	139	1.888	1.917	0.029
100	140	1.919	1.953	0.034
100	141	1.872	1.890	0.018
100	142	1.910	1.938	0.028
100	143	1.921	1.970	0.049
100	144	1.906	1.940	0.034
100	220	1.900	1.943	0.043
100	221	1.912	1.932	0.020
100	224	1.940	1.976	0.036
100	225	1.841	1.864	0.023
100	226	1.924	1.962	0.038
100	228	1.897	1.936	0.039
100	232	1.946	1.982	0.036
100	233	1.971	1.995	0.024
100	236	1.870	1.894	0.024
100	238	1.916	1.945	0.029
100	243	1.867	1.883	0.016
105	244	1.877	1.909	0.032
105	246	1.853	1.901	0.048
105	248	1.916	1.943	0.027
105	374	1.907	1.941	0.034
105	376	1.916	1.942	0.026
105	Max	1.983	1.995	0.049
105	Average	1.909	1.919	0.010
105	Min	1.841	1.832	-0.024
105	Std Dev	0.034	0.037	0.019



5.4 IDD DIS 14		
Test Site		
Tester		
Test Number		
Max Limit	3	mA
Min Limit		mA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.865	1.840	1.855	1.832	1.846	1.854	1.862	1.888	1.868	1.864	1.901
Average	1.898	1.894	1.917	1.891	1.919	1.900	1.927	1.927	1.906	1.937	1.927
Max	1.918	1.933	1.980	1.936	1.983	1.941	1.978	1.959	1.947	1.995	1.943
UL	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000	3.000

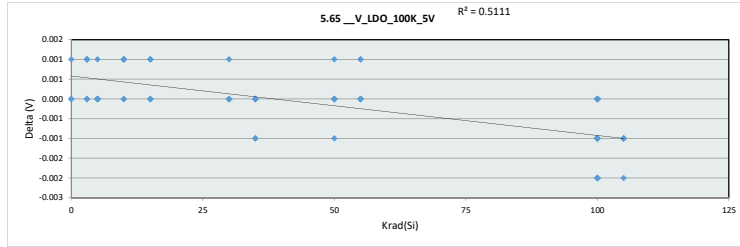




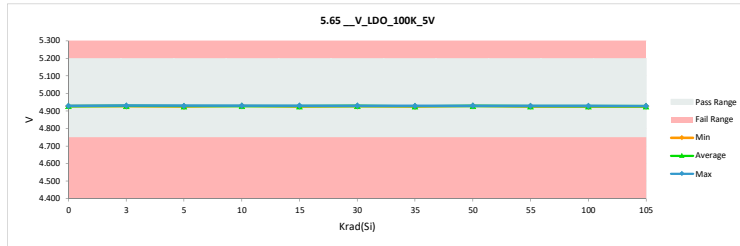
LDR TID Report  
TPS7H5001-SP

		5.65 V LDO 100K 5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	5.2	5.2	
Min Limit	4.75	4.75	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.929	4.929	0.000
0	118	4.927	4.927	0.000
0	122	4.927	4.928	0.001
3	61	4.927	4.927	0.000
3	62	4.927	4.928	0.001
3	63	4.927	4.928	0.001
3	64	4.928	4.928	0.000
3	65	4.930	4.931	0.001
3	66	4.930	4.930	0.000
5	67	4.929	4.929	0.000
5	68	4.926	4.926	0.000
5	69	4.929	4.929	0.000
5	70	4.925	4.925	0.000
5	71	4.926	4.927	0.001
5	72	4.926	4.926	0.000
10	73	4.927	4.928	0.001
10	74	4.927	4.928	0.001
10	75	4.927	4.928	0.001
10	76	4.928	4.928	0.000
10	77	4.930	4.930	0.000
10	78	4.930	4.931	0.001
15	79	4.929	4.929	0.000
15	80	4.926	4.926	0.000
15	81	4.929	4.930	0.001
15	82	4.925	4.926	0.001
15	83	4.926	4.927	0.001
15	84	4.926	4.926	0.000
30	86	4.927	4.927	0.000
30	89	4.927	4.927	0.000
30	91	4.927	4.927	0.000
30	92	4.928	4.928	0.000
30	94	4.930	4.930	0.000
30	95	4.930	4.931	0.001
35	100	4.929	4.928	-0.001
35	101	4.926	4.925	-0.001
35	102	4.929	4.929	0.000
35	103	4.925	4.925	0.000
35	104	4.926	4.926	0.000
35	105	4.926	4.926	0.000
50	106	4.927	4.928	0.001
50	107	4.929	4.929	0.000
50	108	4.930	4.929	-0.001
50	109	4.928	4.928	0.000
50	110	4.928	4.928	0.000
50	111	4.930	4.930	0.000
55	113	4.927	4.927	0.000
55	114	4.928	4.928	0.000
55	115	4.927	4.927	0.000
55	116	4.927	4.928	0.001
55	126	4.926	4.926	0.000
55	128	4.927	4.928	0.001
100	129	4.927	4.926	-0.001
100	131	4.929	4.929	0.000
100	132	4.927	4.927	0.000
100	133	4.927	4.926	-0.001
100	134	4.929	4.928	-0.001
100	135	4.929	4.929	0.000
100	138	4.928	4.927	-0.001
100	139	4.930	4.929	-0.001
100	140	4.928	4.927	-0.001
100	141	4.930	4.929	-0.001
100	142	4.927	4.926	-0.001
100	143	4.928	4.926	-0.002
100	144	4.929	4.928	-0.001
100	220	4.927	4.927	0.000
100	221	4.929	4.929	0.000
100	224	4.929	4.927	-0.002
100	225	4.931	4.929	-0.002
100	226	4.926	4.925	-0.001
100	228	4.929	4.927	-0.002
100	232	4.929	4.927	-0.002
100	233	4.928	4.927	-0.001
100	236	4.930	4.929	-0.001
100	238	4.927	4.926	-0.001
100	243	4.929	4.929	0.000
105	244	4.928	4.927	-0.001
105	246	4.930	4.928	-0.002
105	248	4.928	4.927	-0.001
105	374	4.927	4.926	-0.001
105	376	4.929	4.928	-0.001
105	Max	4.931	4.931	0.001
105	Average	4.928	4.928	0.000
105	Min	4.925	4.925	-0.002
105	Std Dev	0.001	0.001	0.001



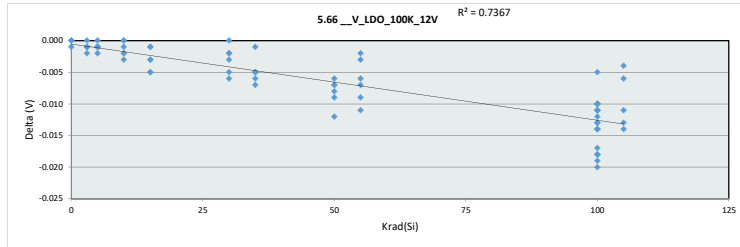
		5.65 V LDO 100								
Test Site										
Tester										
Test Number										
Max Limit	5.2	V								
Min Limit	4.75	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.927	4.927	4.925	4.928	4.926	4.927	4.925	4.928	4.926	4.925
Average	4.928	4.929	4.927	4.929	4.927	4.928	4.927	4.929	4.927	4.927
Max	4.929	4.931	4.929	4.931	4.930	4.931	4.929	4.930	4.928	4.928
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



LDR TID Report  
TPS7H5001-SP

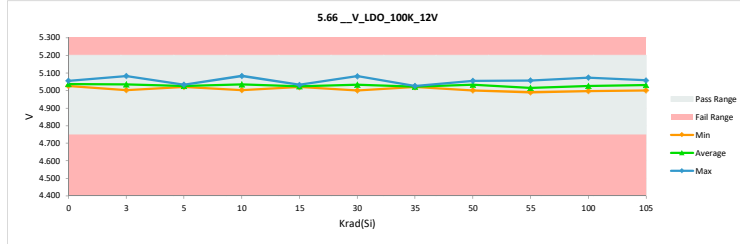
5.66 V_LDO_100K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.026	5.026	0.000
3	61	5.047	5.046	-0.001
3	62	5.010	5.009	-0.001
3	63	5.003	5.002	-0.001
3	64	5.040	5.040	0.000
3	65	5.083	5.082	-0.001
3	66	5.029	5.027	-0.002
5	67	5.024	5.022	-0.002
5	68	5.021	5.020	-0.001
5	69	5.026	5.025	-0.001
5	70	5.033	5.033	0.000
5	71	5.029	5.028	-0.001
5	72	5.026	5.025	-0.001
10	73	5.047	5.045	-0.002
10	74	5.010	5.007	-0.003
10	75	5.003	5.002	-0.001
10	76	5.040	5.040	0.000
10	77	5.083	5.083	0.000
10	78	5.029	5.027	-0.002
15	79	5.024	5.019	-0.005
15	80	5.021	5.020	-0.001
15	81	5.026	5.023	-0.003
15	82	5.033	5.032	-0.001
15	83	5.029	5.026	-0.003
15	84	5.026	5.021	-0.005
30	86	5.047	5.047	0.000
30	89	5.010	5.005	-0.005
30	91	5.003	5.000	-0.003
30	92	5.040	5.034	-0.006
30	94	5.083	5.081	-0.002
30	95	5.029	5.027	-0.002
35	100	5.024	5.019	-0.005
35	101	5.021	5.020	-0.001
35	102	5.026	5.021	-0.005
35	103	5.033	5.026	-0.007
35	104	5.029	5.024	-0.005
35	105	5.026	5.020	-0.006
50	106	5.038	5.031	-0.007
50	107	5.053	5.041	-0.012
50	108	5.056	5.047	-0.009
50	109	5.008	5.000	-0.008
50	110	5.061	5.055	-0.006
50	111	5.029	5.022	-0.007
55	113	5.058	5.056	-0.002
55	114	5.013	5.002	-0.011
55	115	5.005	4.998	-0.007
55	116	5.019	5.013	-0.006
55	126	5.033	5.030	-0.003
55	128	4.999	4.990	-0.009
100	129	5.062	5.051	-0.011
100	131	5.021	5.011	-0.010
100	132	5.025	5.012	-0.013
100	133	5.074	5.061	-0.013
100	134	5.032	5.022	-0.010
100	135	5.019	5.000	-0.019
100	138	5.032	5.019	-0.013
100	139	5.040	5.030	-0.010
100	140	5.041	5.031	-0.010
100	141	5.025	5.007	-0.018
100	142	5.028	5.018	-0.010
100	143	5.029	5.018	-0.011
100	144	5.023	5.018	-0.005
100	220	5.025	5.008	-0.017
100	221	5.016	5.002	-0.014
100	224	5.039	5.027	-0.012
100	225	5.087	5.073	-0.014
100	226	5.059	5.045	-0.014
100	228	5.047	5.036	-0.011
100	232	5.043	5.025	-0.018
100	233	5.051	5.040	-0.011
100	236	5.051	5.033	-0.018
100	238	5.014	4.996	-0.018
100	243	5.048	5.028	-0.020
105	244	5.042	5.036	-0.006
105	246	5.014	5.000	-0.014
105	248	5.051	5.040	-0.011
105	374	5.061	5.057	-0.004
105	376	5.036	5.023	-0.013
	Max	5.087	5.083	0.000
	Average	5.034	5.027	-0.007
	Min	4.999	4.990	-0.020
	Std Dev	0.019	0.020	0.006



5.66 V_LDO_100	
Test Site	
Tester	
Test Number	
Max Limit	5.2
Min Limit	4.75

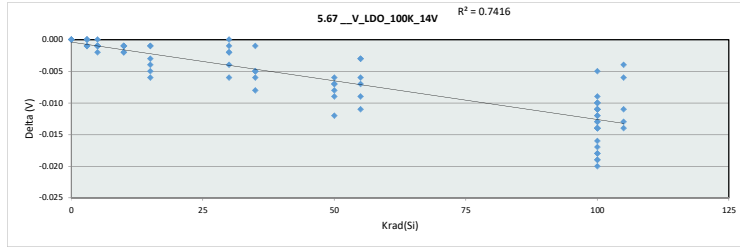
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.026	5.002	5.020	5.002	5.019	5.000	5.019	5.000	4.990	4.996	5.000
Average	5.036	5.034	5.026	5.034	5.024	5.032	5.022	5.033	5.015	5.025	5.031
Max	5.055	5.082	5.033	5.083	5.032	5.081	5.026	5.055	5.056	5.073	5.057
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



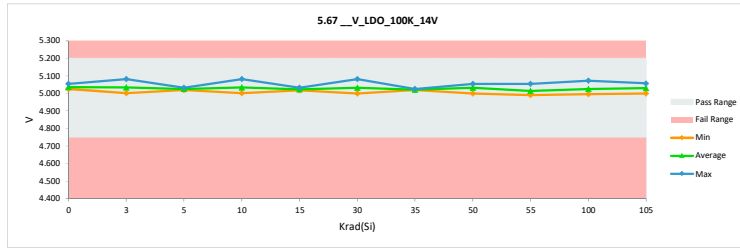
LDR TID Report  
TPS7H5001-SP

5.67 V_LDO_100K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.028	5.028	0.000
0	118	5.055	5.055	0.000
0	122	5.026	5.026	0.000
3	61	5.047	5.046	-0.001
3	62	5.009	5.009	0.000
3	63	5.002	5.002	0.000
3	64	5.040	5.040	0.000
3	65	5.083	5.082	-0.001
3	66	5.028	5.027	-0.001
5	67	5.024	5.022	-0.002
5	68	5.021	5.020	-0.001
5	69	5.026	5.025	-0.001
5	70	5.033	5.033	0.000
5	71	5.029	5.028	-0.001
5	72	5.026	5.025	-0.001
10	73	5.047	5.045	-0.002
10	74	5.009	5.007	-0.002
10	75	5.002	5.001	-0.001
10	76	5.040	5.039	-0.001
10	77	5.083	5.082	-0.001
10	78	5.028	5.027	-0.001
15	79	5.024	5.018	-0.006
15	80	5.021	5.020	-0.001
15	81	5.026	5.023	-0.003
15	82	5.033	5.032	-0.001
15	83	5.029	5.025	-0.004
15	84	5.026	5.021	-0.005
30	86	5.047	5.046	-0.001
30	89	5.009	5.005	-0.004
30	91	5.026	5.020	-0.006
30	92	5.040	5.034	-0.006
30	94	5.083	5.081	-0.002
30	95	5.028	5.028	0.000
35	100	5.024	5.019	-0.005
35	101	5.021	5.020	-0.001
35	102	5.026	5.021	-0.005
35	103	5.033	5.025	-0.008
35	104	5.029	5.024	-0.005
35	105	5.026	5.020	-0.006
50	106	5.038	5.031	-0.007
50	107	5.053	5.041	-0.012
50	108	5.056	5.047	-0.009
50	109	5.008	5.000	-0.008
50	110	5.061	5.055	-0.006
50	111	5.029	5.022	-0.007
55	113	5.058	5.055	-0.003
55	114	5.013	5.002	-0.011
55	115	5.005	4.998	-0.007
55	116	5.019	5.013	-0.006
55	126	5.033	5.030	-0.003
55	128	4.999	4.990	-0.009
100	129	5.062	5.051	-0.011
100	131	5.021	5.010	-0.011
100	132	5.024	5.012	-0.012
100	133	5.075	5.061	-0.014
100	134	5.032	5.022	-0.010
100	135	5.019	5.000	-0.019
100	138	5.032	5.019	-0.013
100	139	5.040	5.030	-0.010
100	140	5.041	5.032	-0.009
100	141	5.025	5.007	-0.018
100	142	5.028	5.018	-0.010
100	143	5.029	5.018	-0.011
100	144	5.023	5.018	-0.005
100	220	5.024	5.008	-0.016
100	221	5.016	5.002	-0.014
100	224	5.040	5.027	-0.013
100	225	5.087	5.073	-0.014
100	226	5.059	5.045	-0.014
100	228	5.047	5.036	-0.011
100	232	5.042	5.025	-0.017
100	233	5.051	5.039	-0.012
100	236	5.051	5.032	-0.019
100	238	5.014	4.996	-0.018
100	243	5.048	5.028	-0.020
105	244	5.042	5.036	-0.006
105	246	5.014	5.000	-0.014
105	248	5.051	5.040	-0.011
105	374	5.061	5.057	-0.004
105	376	5.036	5.023	-0.013
105	Max	5.087	5.082	0.000
105	Average	5.034	5.027	-0.007
105	Min	4.999	4.990	-0.020
105	Std Dev	0.020	0.020	0.006

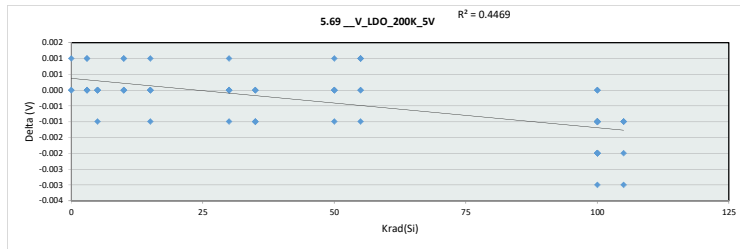


5.67 V_LDO_100										
Test Site										
Tester										
Test Number										
Max Limit	5.2 V									
Min Limit	4.75 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.026	5.002	5.020	5.001	5.018	5.000	5.019	5.000	4.990	4.996
Average	5.036	5.034	5.026	5.034	5.023	5.032	5.022	5.033	5.015	5.025
Max	5.055	5.082	5.033	5.082	5.032	5.081	5.025	5.055	5.055	5.073
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

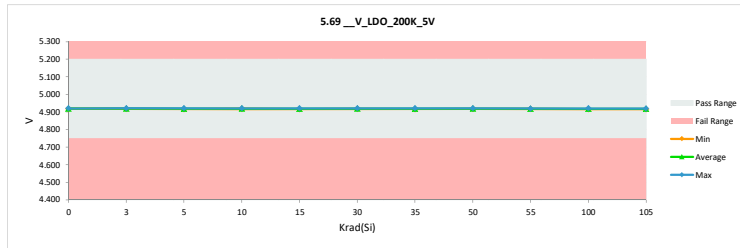


LDR TID Report  
TPS7H5001-SP

5.69 V_LDO_200K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.75	4.75		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.921	4.921	0.000
0	118	4.919	4.919	0.000
0	122	4.918	4.919	0.001
3	61	4.918	4.919	0.001
3	62	4.918	4.919	0.001
3	63	4.919	4.919	0.000
3	64	4.920	4.920	0.000
3	65	4.922	4.922	0.000
3	66	4.922	4.922	0.000
5	67	4.921	4.920	-0.001
5	68	4.917	4.917	0.000
5	69	4.921	4.921	0.000
5	70	4.916	4.916	0.000
5	71	4.918	4.918	0.000
5	72	4.917	4.917	0.000
10	73	4.918	4.919	0.001
10	74	4.918	4.919	0.001
10	75	4.919	4.919	0.000
10	76	4.920	4.920	0.000
10	77	4.922	4.922	0.000
10	78	4.922	4.922	0.000
15	79	4.921	4.920	-0.001
15	80	4.917	4.918	0.001
15	81	4.921	4.921	0.000
15	82	4.916	4.916	0.000
15	83	4.918	4.918	0.000
15	84	4.917	4.917	0.000
30	86	4.918	4.918	0.000
30	89	4.918	4.919	0.001
30	91	4.919	4.918	-0.001
30	92	4.920	4.920	0.000
30	94	4.922	4.922	0.000
30	95	4.922	4.922	0.000
35	100	4.921	4.920	-0.001
35	101	4.917	4.916	-0.001
35	102	4.921	4.921	0.000
35	103	4.916	4.916	0.000
35	104	4.918	4.917	-0.001
35	105	4.917	4.917	0.000
50	106	4.918	4.918	0.000
50	107	4.921	4.920	-0.001
50	108	4.921	4.921	0.000
50	109	4.920	4.920	0.000
50	110	4.919	4.919	0.000
50	111	4.921	4.922	0.001
55	113	4.918	4.919	0.001
55	114	4.919	4.920	0.001
55	115	4.918	4.918	0.000
55	116	4.919	4.918	-0.001
55	126	4.918	4.918	0.000
55	128	4.918	4.919	0.001
100	129	4.919	4.917	-0.002
100	131	4.921	4.921	0.000
100	132	4.919	4.918	-0.001
100	133	4.919	4.918	-0.001
100	134	4.921	4.920	-0.001
100	135	4.920	4.920	0.000
100	138	4.920	4.918	-0.002
100	139	4.922	4.920	-0.002
100	140	4.920	4.918	-0.002
100	141	4.921	4.921	0.000
100	142	4.918	4.917	-0.001
100	143	4.919	4.917	-0.002
100	144	4.921	4.920	-0.001
100	220	4.919	4.918	-0.001
100	221	4.921	4.920	-0.001
100	224	4.921	4.919	-0.002
100	225	4.922	4.921	-0.001
100	226	4.917	4.916	-0.001
100	228	4.920	4.918	-0.002
100	232	4.921	4.918	-0.003
100	233	4.920	4.919	-0.001
100	236	4.922	4.921	-0.001
100	238	4.918	4.917	-0.001
100	243	4.921	4.920	-0.001
105	244	4.919	4.918	-0.001
105	246	4.922	4.919	-0.003
105	248	4.919	4.918	-0.001
105	374	4.919	4.917	-0.002
105	376	4.921	4.920	-0.001
	Max	4.922	4.922	0.001
	Average	4.919	4.919	0.000
	Min	4.916	4.916	-0.003
	Std Dev	0.002	0.002	0.001



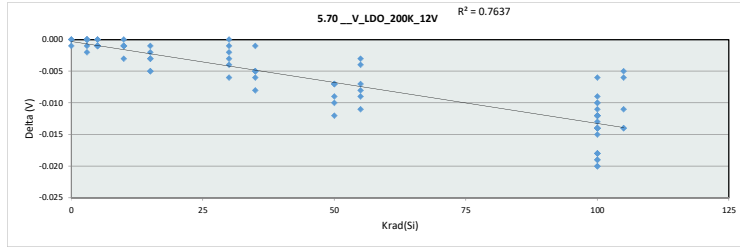
5.69 V_LDO_200											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.75	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.919	4.919	4.916	4.919	4.916	4.918	4.916	4.918	4.918	4.916	4.917
Average	4.920	4.920	4.918	4.920	4.918	4.920	4.918	4.920	4.919	4.919	4.918
Max	4.921	4.922	4.921	4.922	4.921	4.922	4.921	4.922	4.920	4.921	4.920
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



LDR TID Report  
TPS7H5001-SP

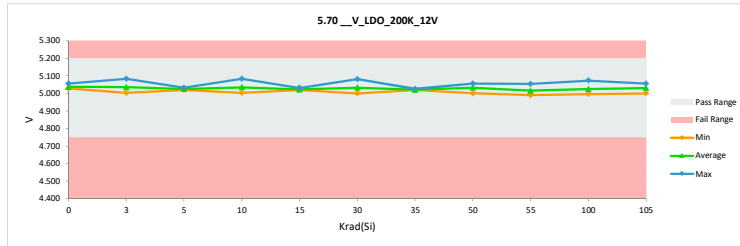
5.70_V_LDO_200K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.030	5.030	0.000
0	118	5.056	5.056	0.000
0	122	5.029	5.028	-0.001
3	61	5.048	5.047	-0.001
3	62	5.011	5.011	0.000
3	63	5.004	5.004	0.000
3	64	5.042	5.042	0.000
3	65	5.084	5.084	0.000
3	66	5.030	5.028	-0.002
5	67	5.025	5.024	-0.001
5	68	5.022	5.021	-0.001
5	69	5.027	5.026	-0.001
5	70	5.034	5.033	-0.001
5	71	5.030	5.030	0.000
5	72	5.026	5.026	0.000
10	73	5.048	5.047	-0.001
10	74	5.011	5.008	-0.003
10	75	5.004	5.003	-0.001
10	76	5.042	5.041	-0.001
10	77	5.084	5.084	0.000
10	78	5.030	5.029	-0.001
15	79	5.025	5.020	-0.005
15	80	5.022	5.021	-0.001
15	81	5.027	5.024	-0.003
15	82	5.034	5.032	-0.002
15	83	5.030	5.027	-0.003
15	84	5.026	5.021	-0.005
30	86	5.048	5.048	0.000
30	89	5.011	5.007	-0.004
30	91	5.001	5.001	-0.003
30	92	5.042	5.036	-0.006
30	94	5.084	5.082	-0.002
30	95	5.030	5.029	-0.001
35	100	5.025	5.020	-0.005
35	101	5.022	5.021	-0.001
35	102	5.027	5.022	-0.005
35	103	5.034	5.026	-0.008
35	104	5.030	5.025	-0.005
35	105	5.026	5.020	-0.006
50	106	5.040	5.031	-0.009
50	107	5.055	5.043	-0.012
50	108	5.057	5.047	-0.010
50	109	5.009	5.002	-0.007
50	110	5.063	5.056	-0.007
50	111	5.030	5.023	-0.007
55	113	5.059	5.055	-0.004
55	114	5.014	5.003	-0.011
55	115	5.006	4.998	-0.008
55	116	5.021	5.014	-0.007
55	126	5.034	5.031	-0.003
55	128	5.000	4.991	-0.009
100	129	5.063	5.051	-0.012
100	131	5.023	5.011	-0.012
100	132	5.026	5.012	-0.014
100	133	5.076	5.062	-0.014
100	134	5.033	5.023	-0.010
100	135	5.019	5.001	-0.018
100	138	5.033	5.019	-0.014
100	139	5.041	5.031	-0.010
100	140	5.042	5.033	-0.009
100	141	5.027	5.007	-0.020
100	142	5.029	5.018	-0.011
100	143	5.031	5.019	-0.012
100	144	5.024	5.018	-0.006
100	220	5.026	5.008	-0.018
100	221	5.017	5.002	-0.015
100	224	5.041	5.028	-0.013
100	225	5.088	5.074	-0.014
100	226	5.059	5.045	-0.014
100	228	5.048	5.036	-0.012
100	232	5.044	5.026	-0.018
100	233	5.052	5.040	-0.012
100	236	5.052	5.033	-0.019
100	238	5.016	4.997	-0.019
100	243	5.049	5.029	-0.020
105	244	5.043	5.037	-0.006
105	246	5.015	5.001	-0.014
105	248	5.052	5.041	-0.011
105	374	5.062	5.057	-0.005
105	376	5.038	5.024	-0.014
	Max	5.088	5.084	0.000
	Average	5.035	5.028	-0.007
	Min	5.000	4.991	-0.020
	Std Dev	0.019	0.020	0.006



5.70_V_LDO_200	
Test Site	
Tester	
Test Number	
Max Limit	5.2 V
Min Limit	4.75 V

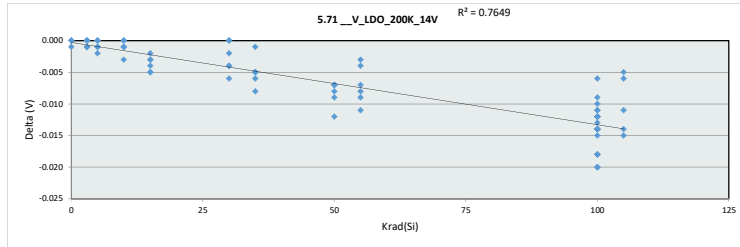
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.028	5.004	5.021	5.003	5.020	5.001	5.020	5.002	4.991	4.997	5.001
Average	5.038	5.036	5.027	5.035	5.024	5.034	5.022	5.034	5.015	5.026	5.032
Max	5.056	5.084	5.033	5.084	5.032	5.082	5.026	5.056	5.055	5.074	5.057
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



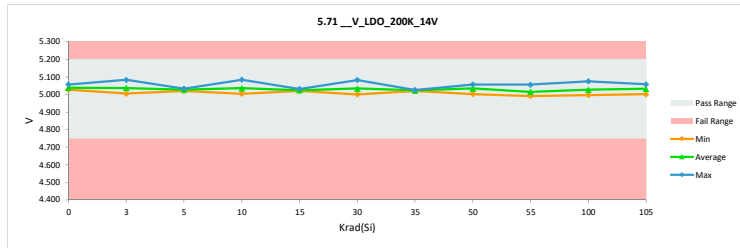
LDR TID Report  
TPS7H5001-SP

5.71 V_LDO_200K_14V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	5.2 5.2
Min Limit	4.75 4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.030	5.029	-0.001
0	118	5.056	5.056	0.000
0	122	5.028	5.028	0.000
3	61	5.048	5.047	-0.001
3	62	5.011	5.011	0.000
3	63	5.004	5.004	0.000
3	64	5.042	5.042	0.000
3	65	5.084	5.083	-0.001
3	66	5.029	5.028	-0.001
5	67	5.025	5.024	-0.001
5	68	5.022	5.020	-0.002
5	69	5.027	5.027	0.000
5	70	5.034	5.033	-0.001
5	71	5.030	5.030	0.000
5	72	5.026	5.026	0.000
10	73	5.048	5.047	-0.001
10	74	5.011	5.008	-0.003
10	75	5.004	5.003	-0.001
10	76	5.042	5.041	-0.001
10	77	5.084	5.084	0.000
10	78	5.029	5.029	0.000
15	79	5.025	5.020	-0.005
15	80	5.022	5.020	-0.002
15	81	5.027	5.024	-0.003
15	82	5.034	5.031	-0.003
15	83	5.030	5.026	-0.004
15	84	5.026	5.021	-0.005
30	86	5.048	5.048	0.000
30	89	5.011	5.007	-0.004
30	91	5.004	5.000	-0.004
30	92	5.042	5.036	-0.006
30	94	5.084	5.082	-0.002
30	95	5.029	5.029	0.000
35	100	5.025	5.020	-0.005
35	101	5.022	5.021	-0.001
35	102	5.027	5.022	-0.005
35	103	5.034	5.026	-0.008
35	104	5.030	5.025	-0.005
35	105	5.026	5.020	-0.006
50	106	5.039	5.031	-0.008
50	107	5.055	5.043	-0.012
50	108	5.057	5.048	-0.009
50	109	5.009	5.002	-0.007
50	110	5.063	5.056	-0.007
50	111	5.030	5.023	-0.007
55	113	5.059	5.055	-0.004
55	114	5.014	5.003	-0.011
55	115	5.006	4.998	-0.008
55	116	5.021	5.014	-0.007
55	126	5.034	5.031	-0.003
55	128	5.000	4.991	-0.009
100	129	5.063	5.051	-0.012
100	131	5.023	5.011	-0.012
100	132	5.026	5.012	-0.014
100	133	5.076	5.062	-0.014
100	134	5.033	5.023	-0.010
100	135	5.019	5.001	-0.018
100	138	5.033	5.019	-0.014
100	139	5.041	5.030	-0.011
100	140	5.042	5.033	-0.009
100	141	5.027	5.007	-0.020
100	142	5.029	5.018	-0.011
100	143	5.030	5.018	-0.012
100	144	5.024	5.018	-0.006
100	220	5.026	5.008	-0.018
100	221	5.017	5.002	-0.015
100	224	5.041	5.028	-0.013
100	225	5.088	5.074	-0.014
100	226	5.059	5.045	-0.014
100	228	5.048	5.036	-0.012
100	232	5.044	5.026	-0.018
100	233	5.052	5.040	-0.012
100	236	5.051	5.033	-0.018
100	238	5.016	4.996	-0.020
100	243	5.049	5.029	-0.020
105	244	5.043	5.037	-0.006
105	246	5.016	5.001	-0.015
105	248	5.052	5.041	-0.011
105	374	5.062	5.057	-0.005
105	376	5.038	5.024	-0.014
105	Max	5.088	5.084	0.000
105	Average	5.035	5.028	-0.007
105	Min	5.000	4.991	-0.010
105	Std Dev	0.019	0.020	0.006



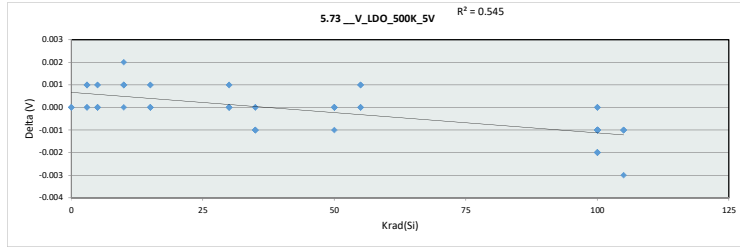
5.71 V_LDO_200										
Test Site										
Tester										
Test Number										
Max Limit	5.2 V									
Min Limit	4.75 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.028	5.004	5.020	5.003	5.020	5.000	5.020	5.002	4.991	4.996
Average	5.038	5.036	5.027	5.035	5.024	5.034	5.022	5.034	5.015	5.026
Max	5.056	5.083	5.033	5.084	5.031	5.082	5.026	5.056	5.055	5.074
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



LDR TID Report  
TPS7H5001-SP

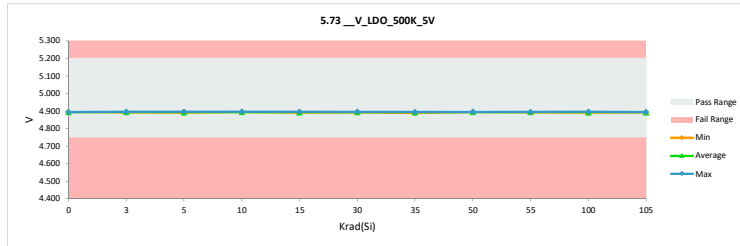
5.73 V_LDO_500K_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.894	4.894	0.000
0	118	4.893	4.893	0.000
0	122	4.893	4.893	0.000
3	61	4.891	4.892	0.001
3	62	4.892	4.893	0.001
3	63	4.893	4.893	0.000
3	64	4.893	4.894	0.001
3	65	4.895	4.896	0.001
3	66	4.895	4.895	0.000
5	67	4.894	4.895	0.001
5	68	4.891	4.891	0.000
5	69	4.895	4.895	0.000
5	70	4.890	4.890	0.000
5	71	4.891	4.892	0.001
5	72	4.892	4.892	0.000
10	73	4.891	4.893	0.002
10	74	4.892	4.893	0.001
10	75	4.893	4.893	0.000
10	76	4.893	4.894	0.001
10	77	4.895	4.896	0.001
10	78	4.895	4.896	0.001
15	79	4.894	4.894	0.000
15	80	4.891	4.892	0.001
15	81	4.895	4.895	0.000
15	82	4.890	4.890	0.000
15	83	4.891	4.892	0.001
15	84	4.892	4.892	0.000
30	86	4.891	4.891	0.000
30	89	4.892	4.893	0.001
30	91	4.893	4.893	0.000
30	92	4.893	4.894	0.001
30	94	4.895	4.895	0.000
30	95	4.895	4.895	0.000
35	100	4.894	4.894	0.000
35	101	4.891	4.890	-0.001
35	102	4.895	4.894	-0.001
35	103	4.890	4.889	-0.001
35	104	4.891	4.891	0.000
35	105	4.892	4.891	-0.001
50	106	4.893	4.893	0.000
50	107	4.895	4.894	-0.001
50	108	4.895	4.895	0.000
50	109	4.894	4.894	0.000
50	110	4.893	4.893	0.000
50	111	4.895	4.895	0.000
55	113	4.893	4.893	0.000
55	114	4.893	4.894	0.001
55	115	4.892	4.893	0.001
55	116	4.893	4.893	0.000
55	126	4.892	4.892	0.000
55	128	4.892	4.893	0.001
100	129	4.893	4.891	-0.002
100	131	4.894	4.894	0.000
100	132	4.893	4.892	-0.001
100	133	4.893	4.892	-0.001
100	134	4.894	4.893	-0.001
100	135	4.894	4.894	0.000
100	138	4.894	4.892	-0.002
100	139	4.895	4.894	-0.001
100	140	4.893	4.892	-0.001
100	141	4.895	4.894	-0.001
100	142	4.892	4.891	-0.001
100	143	4.893	4.891	-0.002
100	144	4.895	4.894	-0.001
100	220	4.893	4.892	-0.001
100	221	4.895	4.894	-0.001
100	224	4.895	4.893	-0.002
100	225	4.895	4.894	-0.001
100	226	4.891	4.890	-0.001
100	228	4.894	4.892	-0.002
100	232	4.894	4.892	-0.002
100	233	4.894	4.893	-0.001
100	236	4.895	4.895	0.000
100	238	4.893	4.891	-0.002
100	243	4.895	4.894	-0.001
105	244	4.893	4.892	-0.001
105	246	4.896	4.893	-0.003
105	248	4.893	4.892	-0.001
105	374	4.893	4.892	-0.001
105	376	4.895	4.894	-0.001
	Max	4.896	4.896	0.002
	Average	4.893	4.893	0.000
	Min	4.890	4.889	-0.003
	Std Dev	0.002	0.002	0.001



5.73 V_LDO_500	
Test Site	
Tester	
Test Number	
Max Limit	5.2 V
Min Limit	4.75 V

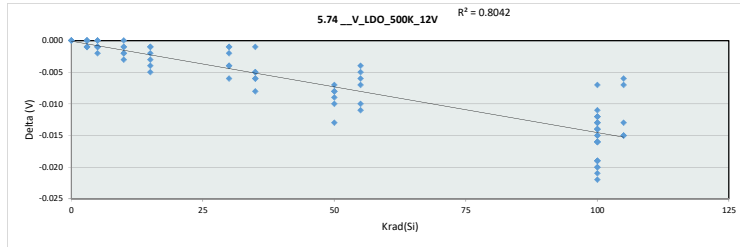
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.893	4.892	4.890	4.893	4.890	4.891	4.889	4.893	4.892	4.890	4.892
Average	4.893	4.894	4.893	4.894	4.894	4.894	4.892	4.894	4.893	4.893	4.893
Max	4.894	4.896	4.895	4.896	4.895	4.895	4.894	4.895	4.894	4.895	4.894
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



LDR TID Report  
TPS7H5001-SP

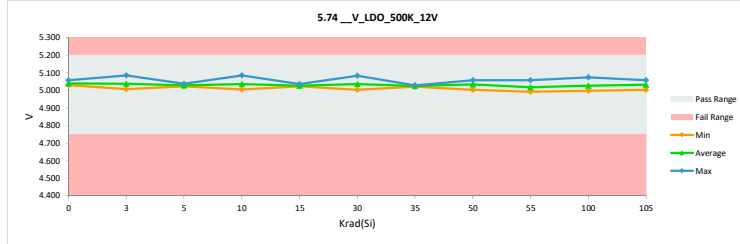
5.74 V_LDO_500K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.032	5.032	0.000
0	118	5.058	5.058	0.000
0	122	5.031	5.031	0.000
3	61	5.051	5.050	-0.001
3	62	5.013	5.013	0.000
3	63	5.007	5.007	0.000
3	64	5.044	5.044	0.000
3	65	5.086	5.085	-0.001
3	66	5.032	5.031	-0.001
5	67	5.028	5.026	-0.002
5	68	5.025	5.024	-0.001
5	69	5.029	5.029	0.000
5	70	5.038	5.038	0.000
5	71	5.033	5.032	-0.001
5	72	5.030	5.029	-0.001
10	73	5.051	5.049	-0.002
10	74	5.013	5.010	-0.003
10	75	5.007	5.005	-0.002
10	76	5.044	5.044	0.000
10	77	5.086	5.085	-0.001
10	78	5.032	5.031	-0.001
15	79	5.028	5.023	-0.005
15	80	5.025	5.024	-0.001
15	81	5.029	5.027	-0.002
15	82	5.038	5.037	-0.001
15	83	5.033	5.030	-0.003
15	84	5.030	5.026	-0.004
30	86	5.051	5.050	-0.001
30	89	5.013	5.009	-0.004
30	91	5.007	5.003	-0.004
30	92	5.044	5.038	-0.006
30	94	5.086	5.084	-0.002
30	95	5.032	5.031	-0.001
35	100	5.028	5.022	-0.006
35	101	5.025	5.024	-0.001
35	102	5.029	5.024	-0.005
35	103	5.038	5.030	-0.008
35	104	5.033	5.028	-0.005
35	105	5.030	5.024	-0.006
50	106	5.043	5.035	-0.008
50	107	5.058	5.045	-0.013
50	108	5.059	5.049	-0.010
50	109	5.011	5.003	-0.008
50	110	5.065	5.058	-0.007
50	111	5.032	5.023	-0.009
55	113	5.064	5.059	-0.005
55	114	5.016	5.005	-0.011
55	115	5.008	5.001	-0.007
55	116	5.023	5.017	-0.006
55	126	5.037	5.033	-0.004
55	128	5.003	4.993	-0.010
100	129	5.068	5.054	-0.014
100	131	5.025	5.012	-0.013
100	132	5.029	5.013	-0.016
100	133	5.079	5.063	-0.016
100	134	5.036	5.024	-0.012
100	135	5.021	5.001	-0.020
100	138	5.034	5.020	-0.014
100	139	5.044	5.032	-0.012
100	140	5.045	5.034	-0.011
100	141	5.030	5.009	-0.021
100	142	5.032	5.020	-0.012
100	143	5.033	5.020	-0.013
100	144	5.026	5.019	-0.007
100	220	5.029	5.010	-0.019
100	221	5.019	5.003	-0.016
100	224	5.044	5.029	-0.015
100	225	5.090	5.075	-0.015
100	226	5.063	5.047	-0.016
100	228	5.051	5.038	-0.013
100	232	5.047	5.028	-0.019
100	233	5.055	5.041	-0.014
100	236	5.053	5.034	-0.019
100	238	5.018	4.998	-0.020
100	243	5.052	5.030	-0.022
105	244	5.045	5.038	-0.007
105	246	5.018	5.003	-0.015
105	248	5.055	5.042	-0.013
105	374	5.065	5.059	-0.006
105	376	5.040	5.025	-0.015
105	Max	5.090	5.085	0.000
105	Average	5.038	5.030	-0.008
105	Min	5.003	4.993	-0.022
105	Std Dev	0.020	0.020	0.006



5.74 V_LDO_50	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.031	5.007	5.024	5.005	5.023	5.003	5.022	5.003	4.993	5.003
Average	5.040	5.038	5.030	5.037	5.028	5.036	5.025	5.036	5.018	5.027
Max	5.058	5.085	5.038	5.085	5.037	5.084	5.030	5.058	5.059	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

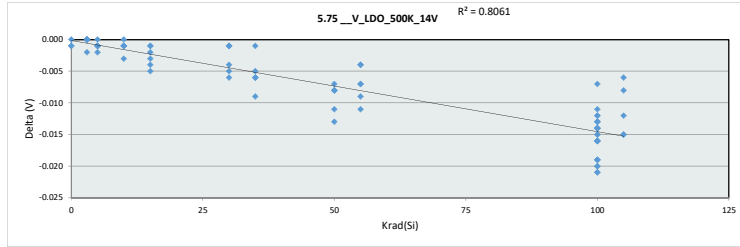




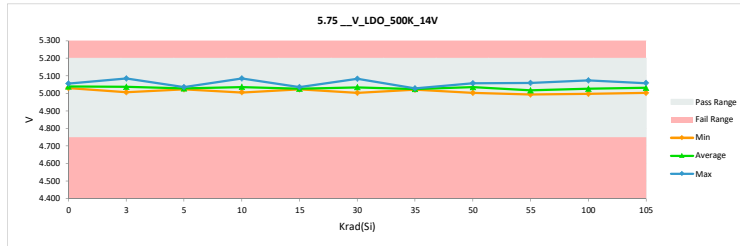
LDR TID Report  
TPS7H5001-SP

5.75 V_LDO_500K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.032	5.031	-0.001
0	118	5.058	5.057	-0.001
0	122	5.031	5.031	0.000
3	61	5.050	5.050	0.000
3	62	5.013	5.013	0.000
3	63	5.007	5.007	0.000
3	64	5.044	5.044	0.000
3	65	5.085	5.085	0.000
3	66	5.032	5.030	-0.002
5	67	5.028	5.026	-0.002
5	68	5.025	5.024	-0.001
5	69	5.029	5.029	0.000
5	70	5.038	5.037	-0.001
5	71	5.033	5.032	-0.001
5	72	5.030	5.029	-0.001
10	73	5.050	5.049	-0.001
10	74	5.013	5.010	-0.003
10	75	5.007	5.006	-0.001
10	76	5.044	5.043	-0.001
10	77	5.085	5.085	0.000
10	78	5.032	5.031	-0.001
15	79	5.028	5.023	-0.005
15	80	5.025	5.024	-0.001
15	81	5.029	5.027	-0.002
15	82	5.038	5.037	-0.001
15	83	5.033	5.030	-0.003
15	84	5.030	5.026	-0.004
30	86	5.050	5.049	-0.001
30	89	5.013	5.008	-0.005
30	91	5.007	5.003	-0.004
30	92	5.044	5.038	-0.006
30	94	5.085	5.084	-0.001
30	95	5.032	5.031	-0.001
35	100	5.028	5.022	-0.006
35	101	5.025	5.024	-0.001
35	102	5.029	5.023	-0.006
35	103	5.038	5.029	-0.009
35	104	5.033	5.028	-0.005
35	105	5.030	5.024	-0.006
50	106	5.043	5.035	-0.008
50	107	5.058	5.045	-0.013
50	108	5.060	5.049	-0.011
50	109	5.011	5.003	-0.008
50	110	5.065	5.058	-0.007
50	111	5.032	5.024	-0.008
55	113	5.064	5.060	-0.004
55	114	5.016	5.005	-0.011
55	115	5.008	5.001	-0.007
55	116	5.023	5.016	-0.007
55	126	5.037	5.033	-0.004
55	128	5.003	4.994	-0.009
100	129	5.068	5.053	-0.015
100	131	5.025	5.012	-0.013
100	132	5.029	5.013	-0.016
100	133	5.079	5.063	-0.016
100	134	5.036	5.024	-0.012
100	135	5.021	5.001	-0.020
100	138	5.034	5.020	-0.014
100	139	5.044	5.032	-0.012
100	140	5.045	5.034	-0.011
100	141	5.030	5.009	-0.021
100	142	5.032	5.019	-0.013
100	143	5.033	5.020	-0.013
100	144	5.026	5.019	-0.007
100	220	5.029	5.010	-0.019
100	221	5.019	5.003	-0.016
100	224	5.043	5.029	-0.014
100	225	5.090	5.075	-0.015
100	226	5.063	5.047	-0.016
100	228	5.051	5.037	-0.014
100	232	5.047	5.028	-0.019
100	233	5.055	5.041	-0.014
100	236	5.053	5.034	-0.019
100	238	5.018	4.998	-0.020
100	243	5.051	5.030	-0.021
105	244	5.045	5.037	-0.008
105	246	5.018	5.003	-0.015
105	248	5.054	5.042	-0.012
105	374	5.065	5.059	-0.006
105	376	5.040	5.025	-0.015
105	Max	5.090	5.085	0.000
105	Average	5.038	5.030	-0.008
105	Min	5.003	4.994	-0.021
105	Std Dev	0.019	0.020	0.006



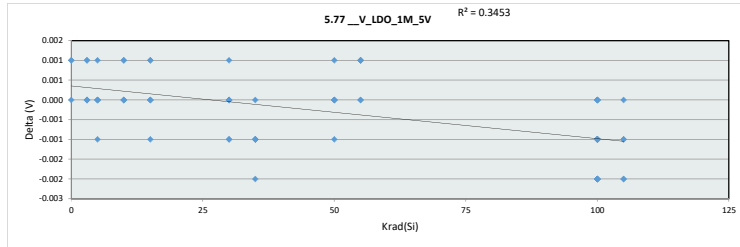
5.75 V_LDO_500										
Test Site										
Tester										
Test Number										
Max Limit	5.2 V									
Min Limit	4.75 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.031	5.007	5.024	5.006	5.023	5.003	5.022	5.003	4.994	4.998
Average	5.040	5.038	5.030	5.037	5.028	5.036	5.025	5.036	5.018	5.027
Max	5.057	5.085	5.037	5.085	5.037	5.084	5.029	5.058	5.060	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



LDR TID Report  
TPS7H5001-SP

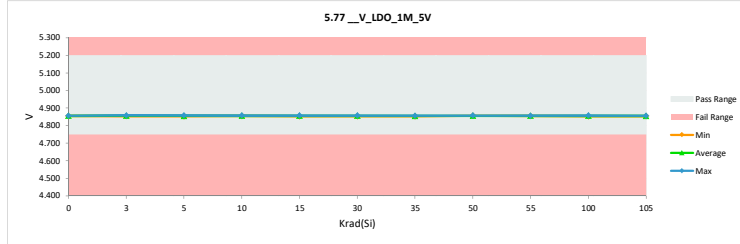
5.77_V_LDO_1M_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.856	4.857	0.001
0	118	4.855	4.856	0.001
0	122	4.855	4.856	0.001
3	61	4.854	4.854	0.000
3	62	4.855	4.855	0.000
3	63	4.856	4.856	0.000
3	64	4.856	4.857	0.001
3	65	4.858	4.859	0.001
3	66	4.858	4.858	0.000
5	67	4.858	4.857	-0.001
5	68	4.854	4.854	0.000
5	69	4.858	4.858	0.000
5	70	4.853	4.853	0.000
5	71	4.854	4.855	0.001
5	72	4.855	4.855	0.000
10	73	4.854	4.855	0.001
10	74	4.855	4.855	0.000
10	75	4.856	4.856	0.000
10	76	4.856	4.857	0.001
10	77	4.858	4.858	0.000
10	78	4.858	4.858	0.000
15	79	4.858	4.857	-0.001
15	80	4.854	4.855	0.001
15	81	4.858	4.858	0.000
15	82	4.853	4.853	0.000
15	83	4.854	4.855	0.001
15	84	4.855	4.855	0.000
30	86	4.854	4.853	-0.001
30	89	4.855	4.855	0.000
30	91	4.856	4.856	0.000
30	92	4.856	4.857	0.001
30	94	4.858	4.858	0.000
30	95	4.858	4.858	0.000
35	100	4.858	4.856	-0.002
35	101	4.854	4.853	-0.001
35	102	4.858	4.857	-0.001
35	103	4.853	4.852	-0.001
35	104	4.854	4.854	0.000
35	105	4.855	4.854	-0.001
50	106	4.856	4.856	0.000
50	107	4.857	4.857	0.000
50	108	4.858	4.857	-0.001
50	109	4.857	4.857	0.000
50	110	4.855	4.856	0.001
50	111	4.858	4.858	0.000
55	113	4.856	4.856	0.000
55	114	4.856	4.857	0.001
55	115	4.855	4.856	0.001
55	116	4.856	4.856	0.000
55	126	4.854	4.855	0.001
55	128	4.855	4.856	0.001
100	129	4.856	4.855	-0.001
100	131	4.857	4.857	0.000
100	132	4.855	4.855	0.000
100	133	4.855	4.854	-0.001
100	134	4.857	4.856	-0.001
100	135	4.857	4.857	0.000
100	138	4.857	4.855	-0.002
100	139	4.858	4.857	-0.001
100	140	4.856	4.855	-0.001
100	141	4.858	4.857	-0.001
100	142	4.855	4.854	-0.001
100	143	4.856	4.854	-0.002
100	144	4.858	4.856	-0.002
100	220	4.855	4.855	0.000
100	221	4.857	4.857	0.000
100	224	4.857	4.856	-0.001
100	225	4.858	4.857	-0.001
100	226	4.854	4.853	-0.001
100	228	4.857	4.855	-0.002
100	232	4.857	4.855	-0.002
100	233	4.856	4.855	-0.001
100	236	4.859	4.858	-0.001
100	238	4.855	4.853	-0.002
100	243	4.858	4.857	-0.001
105	244	4.855	4.855	0.000
105	246	4.858	4.856	-0.002
105	248	4.856	4.854	-0.002
105	374	4.856	4.855	-0.001
105	376	4.858	4.857	-0.001
	Max	4.859	4.859	0.001
	Average	4.856	4.856	0.000
	Min	4.853	4.852	-0.002
	Std Dev	0.002	0.002	0.001



5.77_V_LDO_1M	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.75

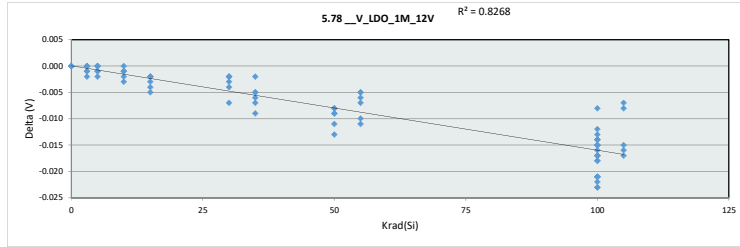
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	4.855	4.854	4.853	4.855	4.853	4.853	4.852	4.856	4.855	4.853	4.854
Average	4.856	4.857	4.855	4.857	4.856	4.856	4.854	4.857	4.856	4.856	4.855
Max	4.857	4.859	4.858	4.858	4.858	4.858	4.857	4.858	4.857	4.858	4.857
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



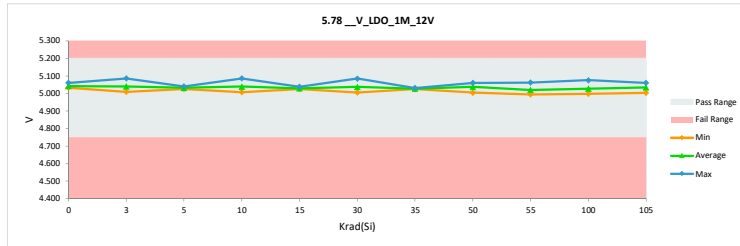
LDR TID Report  
TPS7H5001-SP

		5.78 V_LDO_1M_12V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	5.2	5.2	
Min Limit	4.75	4.75	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.033	5.033	0.000
0	118	5.059	5.059	0.000
0	122	5.033	5.033	0.000
3	61	5.052	5.051	-0.001
3	62	5.014	5.014	0.000
3	63	5.008	5.008	0.000
3	64	5.046	5.046	0.000
3	65	5.086	5.085	-0.001
3	66	5.034	5.032	-0.002
5	67	5.030	5.029	-0.001
5	68	5.027	5.025	-0.002
5	69	5.030	5.030	0.000
5	70	5.040	5.040	0.000
5	71	5.034	5.034	0.000
5	72	5.031	5.030	-0.001
10	73	5.052	5.050	-0.002
10	74	5.014	5.011	-0.003
10	75	5.008	5.007	-0.001
10	76	5.046	5.045	-0.001
10	77	5.086	5.086	0.000
10	78	5.034	5.033	-0.001
15	79	5.030	5.025	-0.005
15	80	5.027	5.025	-0.002
15	81	5.030	5.028	-0.002
15	82	5.040	5.038	-0.002
15	83	5.034	5.031	-0.003
15	84	5.031	5.027	-0.004
30	86	5.052	5.050	-0.002
30	89	5.014	5.010	-0.004
30	91	5.008	5.005	-0.003
30	92	5.046	5.039	-0.007
30	94	5.086	5.084	-0.002
30	95	5.034	5.032	-0.002
35	100	5.030	5.024	-0.006
35	101	5.027	5.025	-0.002
35	102	5.030	5.025	-0.005
35	103	5.040	5.031	-0.009
35	104	5.034	5.029	-0.005
35	105	5.031	5.024	-0.007
50	106	5.045	5.037	-0.008
50	107	5.060	5.047	-0.013
50	108	5.061	5.050	-0.011
50	109	5.013	5.004	-0.009
50	110	5.067	5.059	-0.008
50	111	5.032	5.023	-0.009
55	113	5.066	5.061	-0.005
55	114	5.018	5.007	-0.011
55	115	5.009	5.002	-0.007
55	116	5.024	5.018	-0.006
55	126	5.040	5.035	-0.005
55	128	5.004	4.994	-0.010
100	129	5.070	5.054	-0.016
100	131	5.027	5.012	-0.015
100	132	5.030	5.013	-0.017
100	133	5.082	5.064	-0.018
100	134	5.038	5.024	-0.014
100	135	5.022	5.001	-0.021
100	138	5.035	5.018	-0.017
100	139	5.046	5.032	-0.014
100	140	5.047	5.035	-0.012
100	141	5.032	5.009	-0.023
100	142	5.032	5.019	-0.013
100	143	5.034	5.020	-0.014
100	144	5.027	5.019	-0.008
100	220	5.031	5.010	-0.021
100	221	5.021	5.003	-0.018
100	224	5.045	5.030	-0.015
100	225	5.092	5.075	-0.017
100	226	5.064	5.047	-0.017
100	228	5.052	5.037	-0.015
100	232	5.050	5.029	-0.021
100	233	5.057	5.042	-0.015
100	236	5.054	5.033	-0.021
100	238	5.020	4.998	-0.022
100	243	5.053	5.030	-0.023
105	244	5.046	5.038	-0.008
105	246	5.020	5.003	-0.017
105	248	5.057	5.042	-0.015
105	374	5.066	5.059	-0.007
105	376	5.042	5.026	-0.016
105	Max	5.092	5.086	0.000
105	Average	5.039	5.031	-0.008
105	Min	5.004	4.994	-0.023
105	Std Dev	0.020	0.020	0.007



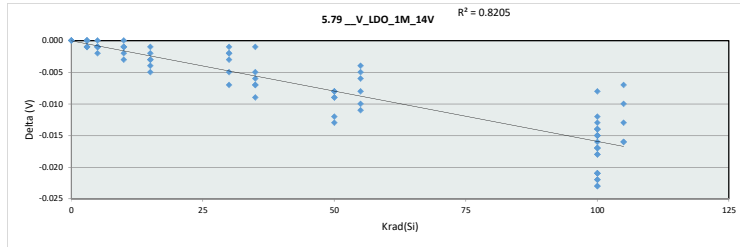
		5.78 V_LDO_1M								
Test Site										
Tester										
Test Number										
Max Limit	5.2	V								
Min Limit	4.75	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.033	5.008	5.025	5.007	5.025	5.005	5.024	5.004	4.994	5.003
Average	5.042	5.039	5.031	5.039	5.029	5.037	5.026	5.037	5.020	5.027
Max	5.059	5.085	5.040	5.086	5.038	5.084	5.031	5.059	5.061	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



LDR TID Report  
TPS7H5001-SP

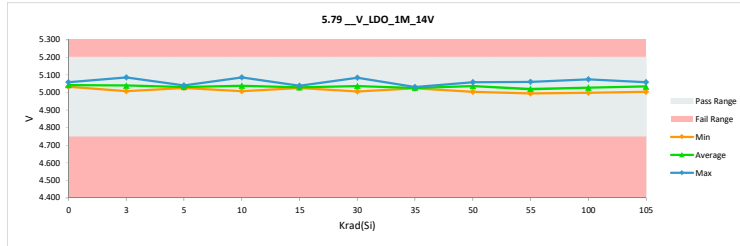
5.79_V_LDO_1M_14V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	5.2 5.2
Min Limit	4.75 4.75

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.033	5.033	0.000
0	118	5.059	5.059	0.000
0	122	5.033	5.033	0.000
3	61	5.052	5.051	-0.001
3	62	5.014	5.014	0.000
3	63	5.008	5.008	0.000
3	64	5.046	5.046	0.000
3	65	5.086	5.085	-0.001
3	66	5.033	5.032	-0.001
5	67	5.030	5.028	-0.002
5	68	5.026	5.025	-0.001
5	69	5.031	5.030	-0.001
5	70	5.040	5.040	0.000
5	71	5.034	5.033	-0.001
5	72	5.031	5.030	-0.001
10	73	5.052	5.050	-0.002
10	74	5.014	5.011	-0.003
10	75	5.008	5.007	-0.001
10	76	5.046	5.045	-0.001
10	77	5.086	5.086	0.000
10	78	5.033	5.032	-0.001
15	79	5.030	5.025	-0.005
15	80	5.026	5.025	-0.001
15	81	5.031	5.028	-0.003
15	82	5.040	5.038	-0.002
15	83	5.034	5.031	-0.003
15	84	5.031	5.027	-0.004
30	86	5.052	5.050	-0.002
30	89	5.014	5.009	-0.005
30	91	5.008	5.005	-0.003
30	92	5.046	5.039	-0.007
30	94	5.086	5.084	-0.002
30	95	5.033	5.032	-0.001
35	100	5.030	5.024	-0.006
35	101	5.026	5.025	-0.001
35	102	5.031	5.024	-0.007
35	103	5.040	5.031	-0.009
35	104	5.034	5.029	-0.005
35	105	5.031	5.024	-0.007
50	106	5.045	5.037	-0.008
50	107	5.060	5.047	-0.013
50	108	5.061	5.049	-0.012
50	109	5.013	5.004	-0.009
50	110	5.067	5.059	-0.008
50	111	5.032	5.023	-0.009
55	113	5.066	5.061	-0.005
55	114	5.018	5.007	-0.011
55	115	5.009	5.001	-0.008
55	116	5.024	5.018	-0.006
55	126	5.039	5.035	-0.004
55	128	5.004	4.994	-0.010
100	129	5.070	5.054	-0.016
100	131	5.027	5.012	-0.015
100	132	5.030	5.013	-0.017
100	133	5.081	5.064	-0.017
100	134	5.038	5.024	-0.014
100	135	5.022	5.000	-0.022
100	138	5.034	5.019	-0.015
100	139	5.046	5.033	-0.013
100	140	5.047	5.035	-0.012
100	141	5.032	5.009	-0.023
100	142	5.032	5.018	-0.014
100	143	5.034	5.020	-0.014
100	144	5.027	5.019	-0.008
100	220	5.031	5.010	-0.021
100	221	5.021	5.003	-0.018
100	224	5.045	5.030	-0.015
100	225	5.092	5.075	-0.017
100	226	5.064	5.046	-0.018
100	228	5.052	5.037	-0.015
100	232	5.050	5.029	-0.021
100	233	5.057	5.042	-0.015
100	236	5.054	5.033	-0.021
100	238	5.020	4.998	-0.022
100	243	5.053	5.030	-0.023
105	244	5.047	5.037	-0.010
105	246	5.020	5.004	-0.016
105	248	5.056	5.043	-0.013
105	374	5.066	5.059	-0.007
105	376	5.042	5.026	-0.016
105	Max	5.092	5.086	0.000
105	Average	5.039	5.031	-0.008
105	Min	5.004	4.994	-0.023
105	Std Dev	0.020	0.020	0.007



5.79_V_LDO_1M	
Test Site	
Tester	
Test Number	
Max Limit	5.2 V
Min Limit	4.75 V

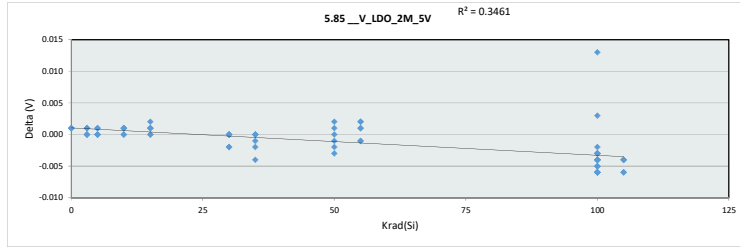
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750	4.750
Min	5.033	5.008	5.025	5.007	5.025	5.005	5.024	5.004	4.994	4.998	5.004
Average	5.042	5.039	5.031	5.039	5.029	5.037	5.026	5.037	5.019	5.027	5.034
Max	5.059	5.085	5.040	5.086	5.038	5.084	5.031	5.059	5.061	5.075	5.059
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



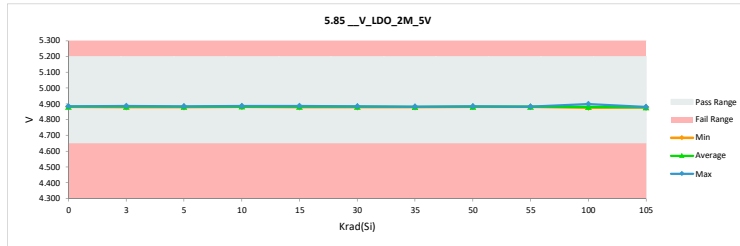
LDR TID Report  
TPS7H5001-SP

		5.85 V_LDO_2M_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	5.2	5.2	
Min Limit	4.65	4.65	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	4.884	4.885	0.001
0	118	4.882	4.883	0.001
0	122	4.880	4.881	0.001
3	61	4.880	4.880	0.000
3	62	4.882	4.883	0.001
3	63	4.884	4.884	0.000
3	64	4.884	4.885	0.001
3	65	4.886	4.887	0.001
3	66	4.885	4.885	0.000
5	67	4.885	4.885	0.000
5	68	4.880	4.880	0.000
5	69	4.886	4.886	0.000
5	70	4.880	4.880	0.000
5	71	4.880	4.881	0.001
5	72	4.881	4.882	0.001
10	73	4.880	4.881	0.001
10	74	4.882	4.883	0.001
10	75	4.884	4.884	0.000
10	76	4.884	4.885	0.001
10	77	4.886	4.887	0.001
10	78	4.885	4.885	0.000
15	79	4.885	4.885	0.000
15	80	4.880	4.881	0.001
15	81	4.886	4.887	0.001
15	82	4.880	4.880	0.000
15	83	4.880	4.881	0.001
15	84	4.881	4.883	0.002
30	86	4.880	4.880	0.000
30	89	4.882	4.882	0.000
30	91	4.884	4.884	0.000
30	92	4.884	4.884	0.000
30	94	4.886	4.884	-0.002
30	95	4.885	4.885	0.000
35	100	4.885	4.881	-0.004
35	101	4.880	4.880	0.000
35	102	4.886	4.884	-0.002
35	103	4.880	4.879	-0.001
35	104	4.880	4.880	0.000
35	105	4.881	4.881	0.000
50	106	4.883	4.881	-0.002
50	107	4.885	4.886	0.001
50	108	4.886	4.883	-0.003
50	109	4.885	4.885	0.000
50	110	4.882	4.884	0.002
50	111	4.886	4.885	-0.001
55	113	4.882	4.881	-0.001
55	114	4.883	4.882	-0.001
55	115	4.882	4.884	0.002
55	116	4.882	4.883	0.001
55	126	4.882	4.883	0.001
55	128	4.882	4.884	0.002
100	129	4.883	4.881	-0.002
100	131	4.883	4.880	-0.003
100	132	4.881	4.878	-0.003
100	133	4.882	4.877	-0.005
100	134	4.883	4.879	-0.004
100	135	4.886	4.899	0.013
100	138	4.884	4.880	-0.004
100	139	4.886	4.881	-0.005
100	140	4.882	4.878	-0.004
100	141	4.885	4.882	-0.003
100	142	4.881	4.884	0.003
100	143	4.883	4.877	-0.006
100	144	4.885	4.879	-0.006
100	220	4.882	4.879	-0.003
100	221	4.885	4.881	-0.004
100	224	4.886	4.880	-0.006
100	225	4.886	4.881	-0.005
100	226	4.881	4.877	-0.004
100	228	4.883	4.877	-0.006
100	232	4.884	4.878	-0.006
100	233	4.883	4.879	-0.004
100	236	4.888	4.885	-0.003
100	238	4.882	4.876	-0.006
100	243	4.885	4.882	-0.003
105	244	4.881	4.877	-0.004
105	246	4.886	4.880	-0.006
105	248	4.882	4.878	-0.004
105	374	4.884	4.878	-0.006
105	376	4.885	4.881	-0.004
	Max	4.888	4.899	0.013
	Average	4.883	4.882	-0.001
	Min	4.880	4.876	-0.006
	Std Dev	0.002	0.003	0.003



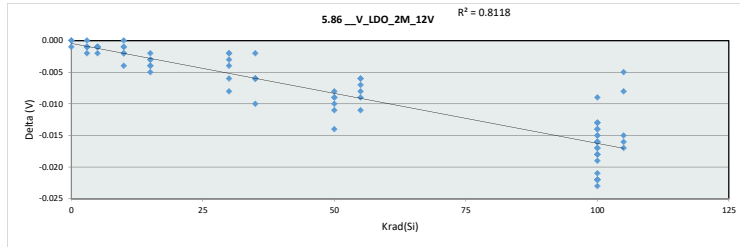
		5.85 V_LDO_2M								
Test Site										
Tester										
Test Number										
Max Limit	5.2	V								
Min Limit	4.65	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	4.881	4.880	4.880	4.881	4.880	4.880	4.879	4.881	4.881	4.876
Average	4.883	4.884	4.882	4.884	4.883	4.883	4.881	4.884	4.883	4.880
Max	4.885	4.887	4.886	4.887	4.887	4.885	4.884	4.886	4.884	4.899
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



LDR TID Report  
TPS7H5001-SP

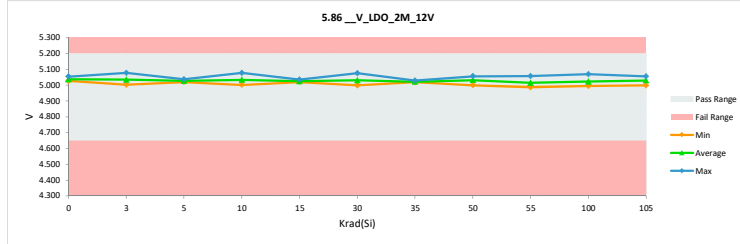
5.86_V_LDO_2M_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	5.2
Min Limit	4.65

Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.030	5.030	0.000
3	61	5.048	5.047	-0.001
3	62	5.013	5.012	-0.001
3	63	5.004	5.004	0.000
3	64	5.043	5.042	-0.001
3	65	5.079	5.078	-0.001
3	66	5.032	5.030	-0.002
5	67	5.028	5.027	-0.001
5	68	5.023	5.021	-0.002
5	69	5.026	5.025	-0.001
5	70	5.039	5.038	-0.001
5	71	5.031	5.030	-0.001
5	72	5.027	5.026	-0.001
10	73	5.048	5.046	-0.002
10	74	5.013	5.009	-0.004
10	75	5.004	5.003	-0.001
10	76	5.043	5.042	-0.001
10	77	5.079	5.079	0.000
10	78	5.032	5.030	-0.002
15	79	5.028	5.023	-0.005
15	80	5.023	5.021	-0.002
15	81	5.026	5.023	-0.003
15	82	5.039	5.036	-0.003
15	83	5.031	5.027	-0.004
15	84	5.027	5.023	-0.004
30	86	5.048	5.046	-0.002
30	89	5.013	5.007	-0.006
30	91	5.004	5.000	-0.004
30	92	5.043	5.035	-0.008
30	94	5.079	5.077	-0.002
30	95	5.032	5.029	-0.003
35	100	5.028	5.022	-0.006
35	101	5.023	5.021	-0.002
35	102	5.026	5.020	-0.006
35	103	5.039	5.029	-0.010
35	104	5.031	5.025	-0.006
35	105	5.027	5.021	-0.006
50	106	5.044	5.035	-0.009
50	107	5.059	5.045	-0.014
50	108	5.025	5.043	-0.011
50	109	5.010	5.001	-0.009
50	110	5.065	5.057	-0.008
50	111	5.028	5.018	-0.010
55	113	5.064	5.058	-0.006
55	114	5.014	5.003	-0.011
55	115	5.006	4.998	-0.008
55	116	5.021	5.014	-0.007
55	126	5.037	5.031	-0.006
55	128	4.997	4.988	-0.009
100	129	5.069	5.051	-0.018
100	131	5.023	5.008	-0.015
100	132	5.025	5.008	-0.017
100	133	5.080	5.063	-0.017
100	134	5.036	5.021	-0.015
100	135	5.017	4.995	-0.022
100	138	5.030	5.014	-0.016
100	139	5.042	5.028	-0.014
100	140	5.045	5.032	-0.013
100	141	5.029	5.006	-0.023
100	142	5.026	5.013	-0.013
100	143	5.030	5.017	-0.013
100	144	5.024	5.015	-0.009
100	220	5.030	5.008	-0.022
100	221	5.018	4.999	-0.019
100	224	5.043	5.027	-0.016
100	225	5.088	5.070	-0.018
100	226	5.062	5.044	-0.018
100	228	5.046	5.032	-0.014
100	232	5.050	5.028	-0.022
100	233	5.055	5.039	-0.016
100	236	5.047	5.026	-0.021
100	238	5.017	4.995	-0.022
100	243	5.048	5.026	-0.022
105	244	5.042	5.034	-0.008
105	246	5.016	5.000	-0.016
105	248	5.055	5.040	-0.015
105	374	5.061	5.056	-0.005
105	376	5.041	5.024	-0.017
	Max	5.088	5.079	0.000
	Average	5.036	5.027	-0.009
	Min	4.997	4.988	-0.023
	Std Dev	0.019	0.020	0.007



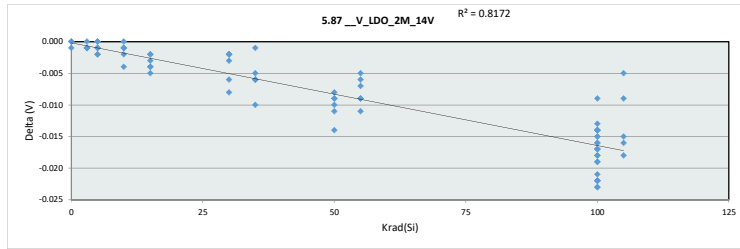
5.86_V_LDO_2M	
Test Site	
Tester	
Test Number	
Max Limit	5.2 V
Min Limit	4.65 V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.028	5.004	5.021	5.003	5.021	5.000	5.020	5.001	4.988	4.995	5.000
Average	5.038	5.036	5.028	5.035	5.026	5.032	5.023	5.033	5.015	5.024	5.031
Max	5.055	5.078	5.038	5.079	5.036	5.077	5.029	5.057	5.058	5.070	5.056
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200

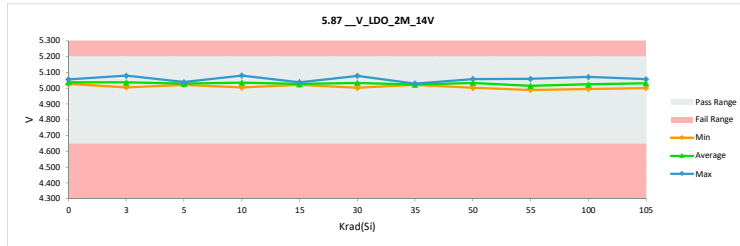


LDR TID Report  
TPS7H5001-SP

5.87_V_LDO_2M_14V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	5.2	5.2		
Min Limit	4.65	4.65		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	5.029	5.028	-0.001
0	118	5.055	5.055	0.000
0	122	5.030	5.030	0.000
3	61	5.048	5.047	-0.001
3	62	5.013	5.012	-0.001
3	63	5.004	5.004	0.000
3	64	5.043	5.042	-0.001
3	65	5.079	5.078	-0.001
3	66	5.031	5.030	-0.001
5	67	5.028	5.026	-0.002
5	68	5.023	5.021	-0.002
5	69	5.026	5.026	0.000
5	70	5.039	5.038	-0.001
5	71	5.030	5.030	0.000
5	72	5.027	5.026	-0.001
10	73	5.048	5.046	-0.002
10	74	5.013	5.009	-0.004
10	75	5.004	5.003	-0.001
10	76	5.043	5.042	-0.001
10	77	5.079	5.079	0.000
10	78	5.031	5.030	-0.001
15	79	5.028	5.023	-0.005
15	80	5.023	5.021	-0.002
15	81	5.026	5.022	-0.004
15	82	5.039	5.037	-0.002
15	83	5.030	5.027	-0.003
15	84	5.027	5.023	-0.004
30	86	5.048	5.046	-0.002
30	89	5.013	5.007	-0.006
30	91	5.004	5.001	-0.003
30	92	5.043	5.035	-0.008
30	94	5.079	5.077	-0.002
30	95	5.031	5.029	-0.002
35	100	5.028	5.022	-0.006
35	101	5.023	5.022	-0.001
35	102	5.026	5.020	-0.006
35	103	5.039	5.029	-0.010
35	104	5.030	5.025	-0.005
35	105	5.027	5.021	-0.006
50	106	5.044	5.035	-0.009
50	107	5.059	5.045	-0.014
50	108	5.054	5.043	-0.011
50	109	5.010	5.001	-0.009
50	110	5.065	5.057	-0.008
50	111	5.028	5.018	-0.010
55	113	5.063	5.058	-0.005
55	114	5.014	5.003	-0.011
55	115	5.007	4.998	-0.009
55	116	5.021	5.014	-0.007
55	126	5.037	5.031	-0.006
55	128	4.997	4.988	-0.009
100	129	5.069	5.052	-0.017
100	131	5.023	5.008	-0.015
100	132	5.025	5.007	-0.018
100	133	5.080	5.063	-0.017
100	134	5.036	5.020	-0.016
100	135	5.016	4.994	-0.022
100	138	5.030	5.013	-0.017
100	139	5.042	5.028	-0.014
100	140	5.045	5.032	-0.013
100	141	5.029	5.006	-0.023
100	142	5.027	5.013	-0.014
100	143	5.030	5.016	-0.014
100	144	5.024	5.015	-0.009
100	220	5.030	5.008	-0.022
100	221	5.018	4.999	-0.019
100	224	5.042	5.027	-0.015
100	225	5.088	5.070	-0.018
100	226	5.062	5.043	-0.019
100	228	5.046	5.032	-0.014
100	232	5.050	5.028	-0.022
100	233	5.055	5.039	-0.016
100	236	5.047	5.026	-0.021
100	238	5.017	4.995	-0.022
100	243	5.048	5.025	-0.023
105	244	5.042	5.033	-0.009
105	246	5.016	5.000	-0.016
105	248	5.055	5.040	-0.015
105	374	5.061	5.056	-0.005
105	376	5.041	5.023	-0.018
105	Max	5.088	5.079	0.000
105	Average	5.036	5.027	-0.009
105	Min	4.997	4.988	-0.023
105	Std Dev	0.019	0.020	0.007



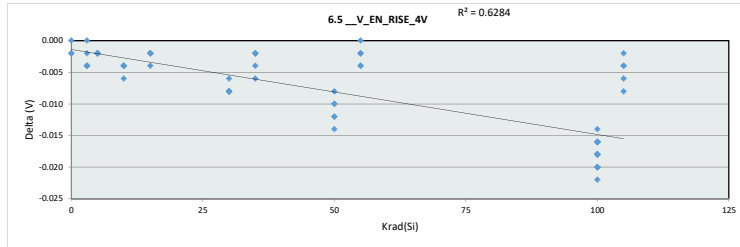
5.87_V_LDO_2M											
Test Site											
Tester											
Test Number											
Max Limit	5.2	V									
Min Limit	4.65	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650	4.650
Min	5.028	5.004	5.021	5.003	5.021	5.001	5.020	5.001	4.988	4.994	5.000
Average	5.038	5.036	5.028	5.035	5.026	5.033	5.023	5.033	5.015	5.023	5.030
Max	5.055	5.078	5.038	5.079	5.037	5.077	5.029	5.057	5.058	5.070	5.056
UL	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200	5.200



LDR TID Report  
TPS7H5001-SP

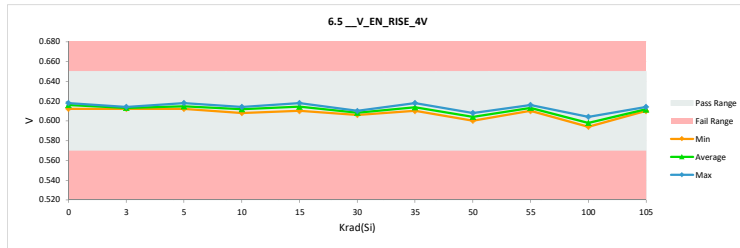
6.5_V_EN_RISE_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.618	0.618	0.000
0	118	0.614	0.612	-0.002
0	122	0.620	0.618	-0.002
3	61	0.618	0.614	-0.004
3	62	0.616	0.612	-0.004
3	63	0.612	0.612	0.000
3	64	0.618	0.614	-0.004
3	65	0.616	0.614	-0.002
3	66	0.616	0.612	-0.004
5	67	0.620	0.618	-0.002
5	68	0.616	0.614	-0.002
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.612	-0.006
10	77	0.616	0.612	-0.004
10	78	0.616	0.612	-0.004
15	79	0.620	0.618	-0.002
15	80	0.616	0.614	-0.002
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.610	-0.004
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.608	-0.008
30	95	0.616	0.608	-0.008
35	100	0.620	0.618	-0.002
35	101	0.616	0.614	-0.002
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.612	-0.002
35	105	0.614	0.610	-0.004
50	106	0.614	0.606	-0.008
50	107	0.614	0.602	-0.012
50	108	0.612	0.600	-0.012
50	109	0.616	0.606	-0.010
50	110	0.618	0.608	-0.010
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.612	0.612	0.000
55	115	0.618	0.616	-0.002
55	116	0.616	0.612	-0.004
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.620	0.600	-0.020
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.604	-0.014
100	139	0.616	0.600	-0.016
100	140	0.612	0.594	-0.018
100	141	0.618	0.602	-0.016
100	142	0.612	0.594	-0.018
100	143	0.616	0.598	-0.018
100	144	0.622	0.600	-0.022
100	220	0.616	0.598	-0.018
100	221	0.616	0.598	-0.018
100	224	0.616	0.598	-0.018
100	225	0.614	0.596	-0.018
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.596	-0.020
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.620	0.612	-0.008
105	376	0.616	0.610	-0.006
	Max	0.622	0.618	0.000
	Average	0.616	0.608	-0.008
	Min	0.612	0.594	-0.022
	Std Dev	0.002	0.007	0.007



6.5_V_EN_RISE	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.612	0.612	0.608	0.610	0.606	0.610	0.600	0.610	0.594	0.610
Average	0.616	0.613	0.615	0.612	0.614	0.608	0.614	0.604	0.613	0.598	0.612
Max	0.618	0.614	0.618	0.614	0.618	0.610	0.618	0.608	0.616	0.604	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

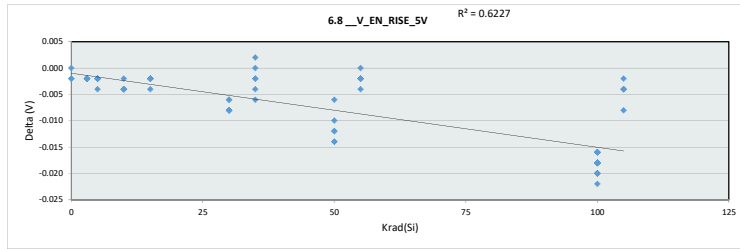




LDR TID Report  
TPS7H5001-SP

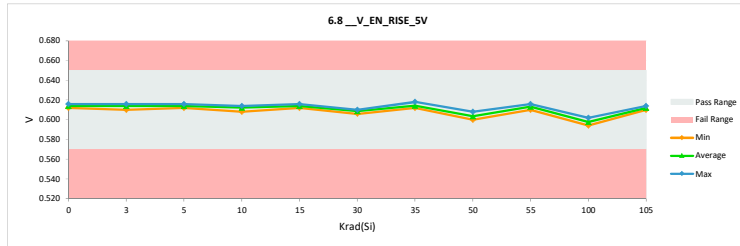
6.8_V_EN_RISE_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.614	-0.004
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.614	-0.002
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.614	0.600	-0.012
50	109	0.618	0.608	-0.010
50	110	0.614	0.608	-0.006
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.616	0.000
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.594	-0.022
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.622	0.602	-0.020
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.620	0.612	-0.008
105	376	0.616	0.612	-0.004
105	375	0.622	0.618	0.002
Max		0.616	0.608	-0.008
Average		0.612	0.594	-0.022
Min		0.612	0.594	-0.022
Std Dev		0.002	0.007	0.007



6.8_V_EN_RISE	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

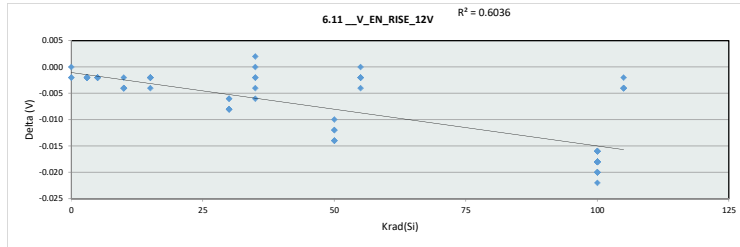
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594	0.610
Average	0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.604	0.613	0.598	0.612
Max	0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



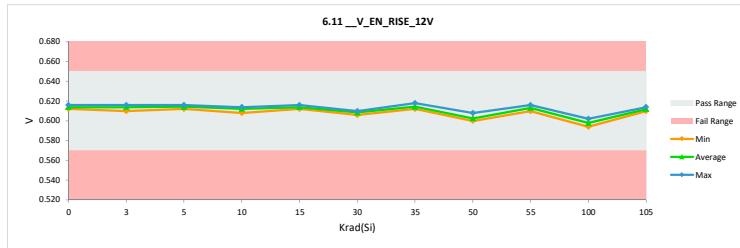
LDR TID Report  
TPS7H5001-SP

6.11 V_EN_RISE_12V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	0.65	0.65
Min Limit	0.57	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.616	-0.002
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.614	-0.002
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.612	0.600	-0.012
50	109	0.618	0.608	-0.010
50	110	0.616	0.602	-0.014
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.616	0.000
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.596	-0.020
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.624	0.602	-0.022
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.616	0.612	-0.004
105	376	0.616	0.612	-0.004
	Max	0.624	0.618	0.002
	Average	0.616	0.608	-0.008
	Min	0.612	0.594	-0.022
	Std Dev	0.002	0.007	0.007



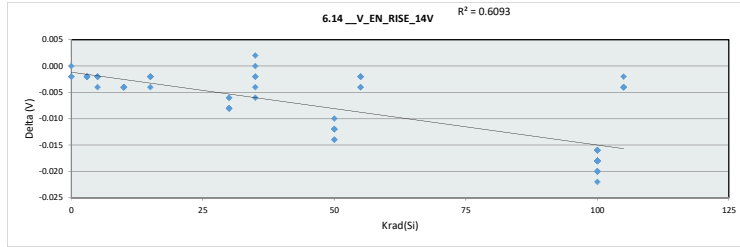
6.11 V_EN_RISE										
Test Site										
Tester										
Test Number										
Max Limit	0.65	V								
Min Limit	0.57	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594
Average	0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.603	0.613	0.598
Max	0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



LDR TID Report  
TPS7H5001-SP

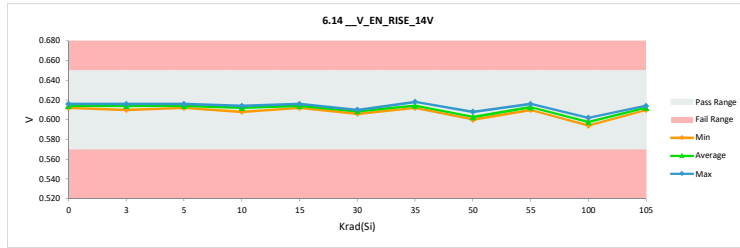
6.14 V_EN_RISE_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.616	0.616	0.000
0	118	0.614	0.612	-0.002
0	122	0.616	0.614	-0.002
3	61	0.618	0.616	-0.002
3	62	0.616	0.614	-0.002
3	63	0.612	0.610	-0.002
3	64	0.618	0.616	-0.002
3	65	0.616	0.614	-0.002
3	66	0.616	0.614	-0.002
5	67	0.616	0.614	-0.002
5	68	0.618	0.614	-0.004
5	69	0.618	0.616	-0.002
5	70	0.618	0.616	-0.002
5	71	0.614	0.612	-0.002
5	72	0.614	0.612	-0.002
10	73	0.618	0.614	-0.004
10	74	0.616	0.612	-0.004
10	75	0.612	0.608	-0.004
10	76	0.618	0.614	-0.004
10	77	0.616	0.612	-0.004
10	78	0.616	0.612	-0.004
15	79	0.616	0.614	-0.002
15	80	0.618	0.614	-0.004
15	81	0.618	0.616	-0.002
15	82	0.618	0.616	-0.002
15	83	0.614	0.612	-0.002
15	84	0.614	0.612	-0.002
30	86	0.618	0.610	-0.008
30	89	0.616	0.608	-0.008
30	91	0.612	0.606	-0.006
30	92	0.618	0.610	-0.008
30	94	0.616	0.610	-0.006
30	95	0.616	0.608	-0.008
35	100	0.616	0.618	0.002
35	101	0.618	0.614	-0.004
35	102	0.618	0.616	-0.002
35	103	0.618	0.612	-0.006
35	104	0.614	0.614	0.000
35	105	0.614	0.612	-0.002
50	106	0.614	0.602	-0.012
50	107	0.616	0.602	-0.014
50	108	0.614	0.600	-0.012
50	109	0.618	0.608	-0.010
50	110	0.614	0.602	-0.012
50	111	0.616	0.602	-0.014
55	113	0.614	0.612	-0.002
55	114	0.614	0.612	-0.002
55	115	0.616	0.612	-0.004
55	116	0.616	0.614	-0.002
55	126	0.618	0.616	-0.002
55	128	0.614	0.610	-0.004
100	129	0.616	0.596	-0.020
100	131	0.616	0.596	-0.020
100	132	0.618	0.600	-0.018
100	133	0.616	0.598	-0.018
100	134	0.612	0.596	-0.016
100	135	0.614	0.596	-0.018
100	138	0.618	0.600	-0.018
100	139	0.616	0.594	-0.022
100	140	0.612	0.594	-0.018
100	141	0.618	0.600	-0.018
100	142	0.614	0.594	-0.020
100	143	0.616	0.598	-0.018
100	144	0.622	0.602	-0.020
100	220	0.616	0.600	-0.016
100	221	0.618	0.600	-0.018
100	224	0.614	0.596	-0.018
100	225	0.616	0.596	-0.020
100	226	0.618	0.600	-0.018
100	228	0.618	0.602	-0.016
100	232	0.616	0.598	-0.018
100	233	0.616	0.598	-0.018
100	236	0.614	0.596	-0.018
100	238	0.614	0.598	-0.016
100	243	0.614	0.598	-0.016
105	244	0.616	0.612	-0.004
105	246	0.614	0.610	-0.004
105	248	0.616	0.614	-0.002
105	374	0.616	0.612	-0.004
105	376	0.616	0.612	-0.004
105	Max	0.622	0.618	0.002
105	Average	0.616	0.608	-0.008
105	Min	0.612	0.594	-0.022
105	Std Dev	0.002	0.007	0.007



6.14 V_EN_RISE	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

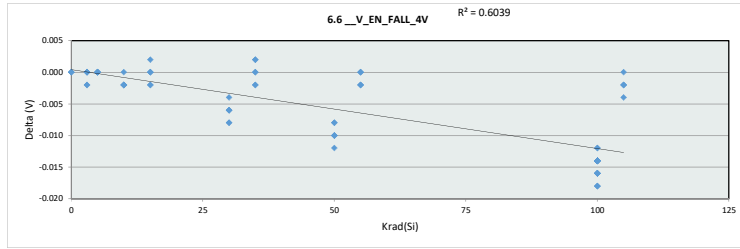
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.612	0.610	0.612	0.608	0.612	0.606	0.612	0.600	0.610	0.594	0.610
Average	0.614	0.614	0.614	0.612	0.614	0.609	0.614	0.603	0.613	0.598	0.612
Max	0.616	0.616	0.616	0.614	0.616	0.610	0.618	0.608	0.616	0.602	0.614
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



LDR TID Report  
TPS7H5001-SP

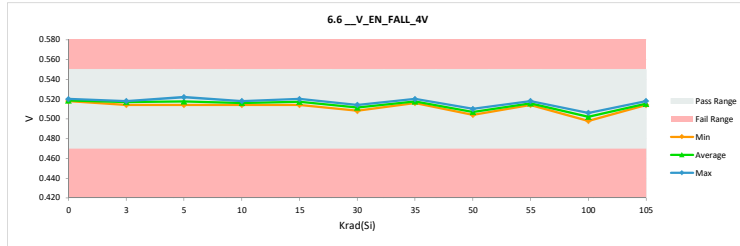
6.6_V_EN_FALL_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.518	0.518	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.516	-0.002
3	63	0.516	0.514	-0.002
3	64	0.518	0.518	0.000
3	65	0.518	0.518	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.516	0.516	0.000
5	69	0.520	0.520	0.000
5	70	0.518	0.518	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.002
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.518	0.516	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.516	0.516	0.000
15	81	0.520	0.518	-0.002
15	82	0.518	0.520	0.002
15	83	0.516	0.516	0.000
15	84	0.514	0.514	0.000
30	86	0.518	0.512	-0.006
30	89	0.518	0.510	-0.008
30	91	0.518	0.508	-0.008
30	92	0.518	0.512	-0.006
30	94	0.518	0.514	-0.004
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.516	0.518	0.002
35	102	0.520	0.520	0.000
35	103	0.518	0.516	-0.002
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.516	0.508	-0.008
50	107	0.516	0.504	-0.012
50	108	0.514	0.504	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.510	-0.008
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.518	0.000
55	128	0.516	0.514	-0.002
100	129	0.516	0.498	-0.018
100	131	0.516	0.500	-0.016
100	132	0.520	0.506	-0.014
100	133	0.516	0.500	-0.016
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.516	0.502	-0.014
100	140	0.512	0.498	-0.014
100	141	0.520	0.506	-0.014
100	142	0.512	0.498	-0.014
100	143	0.516	0.502	-0.014
100	144	0.524	0.506	-0.018
100	220	0.518	0.502	-0.016
100	221	0.516	0.502	-0.014
100	224	0.516	0.500	-0.016
100	225	0.516	0.502	-0.014
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.516	0.500	-0.016
100	243	0.516	0.504	-0.012
105	244	0.516	0.514	-0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.520	0.514	-0.002
	Max	0.524	0.522	0.002
	Average	0.517	0.511	-0.006
	Min	0.512	0.498	-0.018
	Std Dev	0.002	0.007	0.006



6.6_V_EN_FALL	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

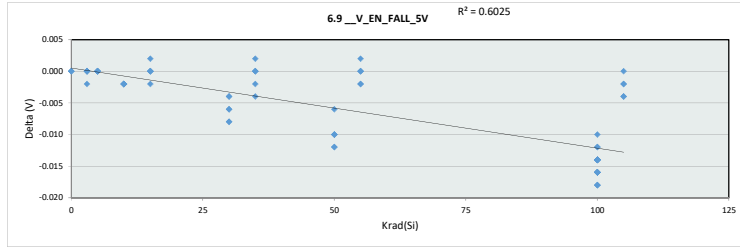
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.514	0.514	0.514	0.514	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.517	0.518	0.518	0.516	0.517	0.511	0.518	0.507	0.516	0.502
Max	0.520	0.518	0.522	0.518	0.520	0.514	0.520	0.510	0.518	0.506	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



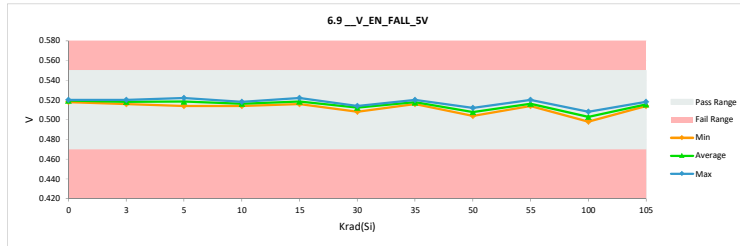
LDR TID Report  
TPS7H5001-SP

6.9_V_EN_FALL_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.520	0.518	-0.002
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.518	0.000
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.002
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.520	0.518	-0.002
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.522	0.000
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.514	-0.004
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.520	0.512	-0.008
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.518	0.506	-0.012
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.514	0.498	-0.016
100	141	0.520	0.506	-0.014
100	142	0.516	0.500	-0.016
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.516	0.502	-0.014
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	-0.002
105	246	0.518	0.514	-0.004
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
105	Max	0.524	0.522	0.002
105	Average	0.518	0.512	-0.006
105	Min	0.514	0.498	-0.018
105	Std Dev	0.002	0.007	0.007



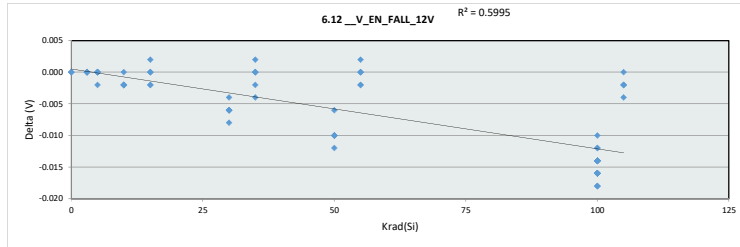
6.9_V_EN_FALL		0	5	10	15	30	35	50	55	100	105
LL		0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min		0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498
Average		0.519	0.518	0.518	0.516	0.518	0.512	0.518	0.508	0.516	0.503
Max		0.520	0.520	0.522	0.518	0.522	0.514	0.520	0.512	0.520	0.508
UL		0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



LDR TID Report  
TPS7H5001-SP

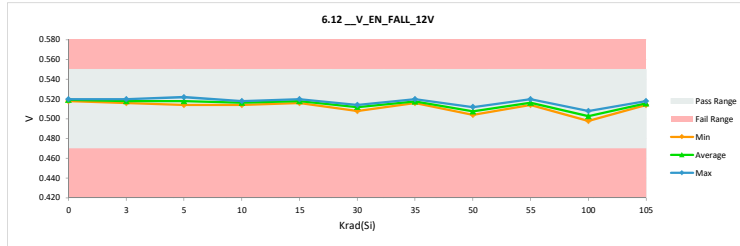
6.12 V_EN_FALL_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.518	0.518	0.000
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.516	-0.002
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.002
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.512	-0.006
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.518	0.512	-0.006
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.514	0.498	-0.016
100	141	0.520	0.506	-0.014
100	142	0.516	0.500	-0.016
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.518	0.502	-0.016
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	-0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
	Max	0.524	0.522	0.002
	Average	0.518	0.512	-0.006
	Min	0.514	0.498	-0.018
	Std Dev	0.002	0.007	0.007



6.12 V_EN_FALL	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

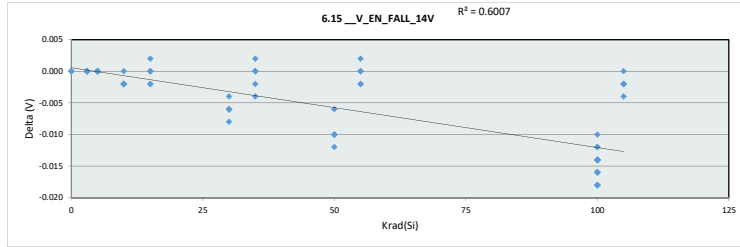
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.518	0.518	0.518	0.518	0.512	0.518	0.508	0.516	0.503	0.516
Max	0.520	0.520	0.522	0.518	0.520	0.514	0.520	0.512	0.520	0.508	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



LDR TID Report  
TPS7H5001-SP

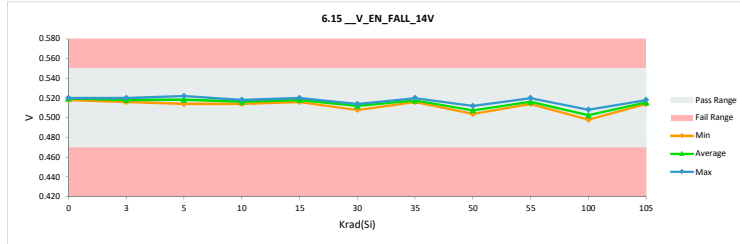
6.15_V_EN_FALL_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.520	0.520	0.000
0	118	0.518	0.518	0.000
0	122	0.520	0.520	0.000
3	61	0.518	0.518	0.000
3	62	0.518	0.518	0.000
3	63	0.516	0.516	0.000
3	64	0.518	0.518	0.000
3	65	0.520	0.520	0.000
3	66	0.518	0.518	0.000
5	67	0.522	0.522	0.000
5	68	0.518	0.518	0.000
5	69	0.520	0.520	0.000
5	70	0.520	0.520	0.000
5	71	0.516	0.516	0.000
5	72	0.514	0.514	-0.000
10	73	0.518	0.516	-0.002
10	74	0.518	0.516	-0.002
10	75	0.516	0.514	-0.002
10	76	0.518	0.518	0.000
10	77	0.520	0.518	-0.002
10	78	0.518	0.516	-0.002
15	79	0.522	0.520	-0.002
15	80	0.518	0.516	-0.002
15	81	0.520	0.520	0.000
15	82	0.520	0.520	0.000
15	83	0.516	0.516	0.000
15	84	0.514	0.516	0.002
30	86	0.518	0.512	-0.006
30	89	0.518	0.514	-0.004
30	91	0.516	0.508	-0.008
30	92	0.518	0.512	-0.006
30	94	0.520	0.514	-0.006
30	95	0.518	0.512	-0.006
35	100	0.522	0.520	-0.002
35	101	0.518	0.518	0.000
35	102	0.520	0.520	0.000
35	103	0.520	0.516	-0.004
35	104	0.516	0.516	0.000
35	105	0.514	0.516	0.002
50	106	0.518	0.508	-0.010
50	107	0.516	0.504	-0.012
50	108	0.516	0.506	-0.010
50	109	0.520	0.510	-0.010
50	110	0.518	0.512	-0.006
50	111	0.516	0.506	-0.010
55	113	0.516	0.514	-0.002
55	114	0.514	0.514	0.000
55	115	0.520	0.518	-0.002
55	116	0.516	0.516	0.000
55	126	0.518	0.520	0.002
55	128	0.516	0.516	0.000
100	129	0.516	0.498	-0.018
100	131	0.516	0.502	-0.014
100	132	0.522	0.506	-0.016
100	133	0.516	0.504	-0.012
100	134	0.514	0.502	-0.012
100	135	0.516	0.498	-0.018
100	138	0.520	0.506	-0.014
100	139	0.518	0.504	-0.014
100	140	0.512	0.498	-0.014
100	141	0.520	0.506	-0.014
100	142	0.516	0.498	-0.018
100	143	0.518	0.502	-0.016
100	144	0.524	0.508	-0.016
100	220	0.518	0.504	-0.014
100	221	0.518	0.502	-0.016
100	224	0.516	0.502	-0.014
100	225	0.518	0.502	-0.016
100	226	0.520	0.506	-0.014
100	228	0.518	0.504	-0.014
100	232	0.518	0.502	-0.016
100	233	0.518	0.504	-0.014
100	236	0.514	0.500	-0.014
100	238	0.518	0.500	-0.018
100	243	0.516	0.506	-0.010
105	244	0.518	0.516	-0.002
105	246	0.516	0.514	-0.002
105	248	0.516	0.516	0.000
105	374	0.522	0.518	-0.004
105	376	0.516	0.514	-0.002
105	Max	0.524	0.522	0.002
105	Average	0.518	0.512	-0.006
105	Min	0.512	0.498	-0.018
105	Std Dev	0.002	0.007	0.007



6.15_V_EN_FALL	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

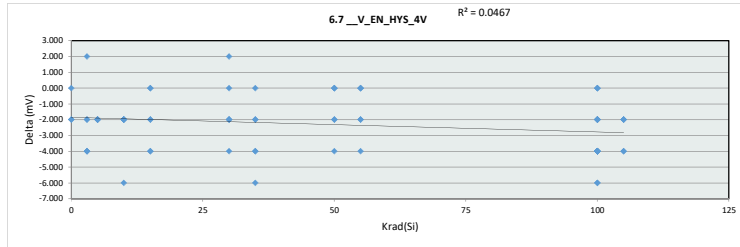
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.518	0.516	0.514	0.514	0.516	0.508	0.516	0.504	0.514	0.498	0.514
Average	0.519	0.518	0.518	0.518	0.518	0.512	0.518	0.508	0.516	0.503	0.516
Max	0.520	0.520	0.522	0.518	0.520	0.514	0.520	0.512	0.520	0.508	0.518
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



LDR TID Report  
TPS7H5001-SP

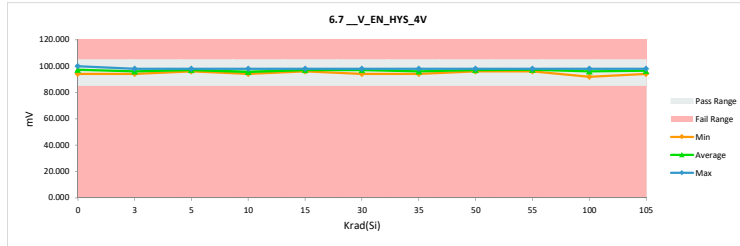
6.7_V_EN_HYS_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	100.000	100.000	0.000
0	118	96.000	94.000	-2.000
0	122	100.000	98.000	-2.000
3	61	100.000	96.000	-4.000
3	62	98.000	96.000	-2.000
3	63	96.000	98.000	2.000
3	64	100.000	96.000	-4.000
3	65	98.000	96.000	-2.000
3	66	98.000	94.000	-4.000
5	67	98.000	96.000	-2.000
5	68	100.000	98.000	-2.000
5	69	98.000	96.000	-2.000
5	70	100.000	98.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	100.000	94.000	-6.000
10	77	98.000	96.000	-2.000
10	78	98.000	96.000	-2.000
15	79	98.000	98.000	0.000
15	80	100.000	98.000	-2.000
15	81	98.000	98.000	0.000
15	82	100.000	96.000	-4.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	98.000	-2.000
30	89	98.000	98.000	0.000
30	91	96.000	98.000	2.000
30	92	100.000	98.000	-2.000
30	94	98.000	94.000	-4.000
30	95	98.000	96.000	-2.000
35	100	98.000	98.000	0.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	100.000	96.000	-4.000
35	104	98.000	96.000	-2.000
35	105	100.000	94.000	-6.000
50	106	98.000	98.000	0.000
50	107	98.000	98.000	0.000
50	108	98.000	96.000	-2.000
50	109	96.000	96.000	0.000
50	110	100.000	98.000	-2.000
50	111	100.000	96.000	-4.000
55	113	98.000	98.000	0.000
55	114	98.000	98.000	0.000
55	115	98.000	98.000	0.000
55	116	100.000	96.000	-4.000
55	126	100.000	98.000	-2.000
55	128	98.000	96.000	-2.000
100	129	100.000	98.000	-2.000
100	131	100.000	96.000	-4.000
100	132	100.000	94.000	-6.000
100	133	100.000	98.000	-2.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	98.000	0.000
100	139	100.000	98.000	-2.000
100	140	100.000	96.000	-4.000
100	141	98.000	96.000	-2.000
100	142	100.000	96.000	-4.000
100	143	100.000	96.000	-4.000
100	144	98.000	94.000	-4.000
100	220	98.000	96.000	-2.000
100	221	100.000	96.000	-4.000
100	224	100.000	98.000	-2.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	92.000	-6.000
100	236	100.000	96.000	-4.000
100	238	98.000	98.000	0.000
100	243	98.000	94.000	-4.000
105	244	100.000	98.000	-2.000
105	246	98.000	96.000	-2.000
105	248	100.000	98.000	-2.000
105	374	98.000	94.000	-4.000
105	375	100.000	96.000	-4.000
105	Max	100.000	100.000	2.000
105	Average	98.775	96.450	-2.325
105	Min	96.000	92.000	-6.000
105	Std Dev	1.211	1.558	1.727



6.7_V_EN_HYS	
Test Site	
Tester	
Test Number	
Max Limit	105 mV
Min Limit	85 mV

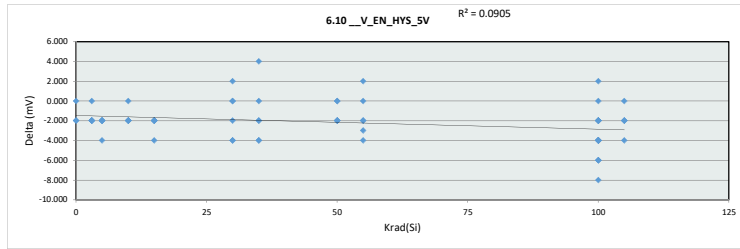
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	96.000	94.000	96.000	94.000	94.000	96.000	96.000	92.000	94.000
Average	97.333	96.000	97.000	95.667	97.000	97.000	96.000	97.000	97.333	96.000	96.400
Max	100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000



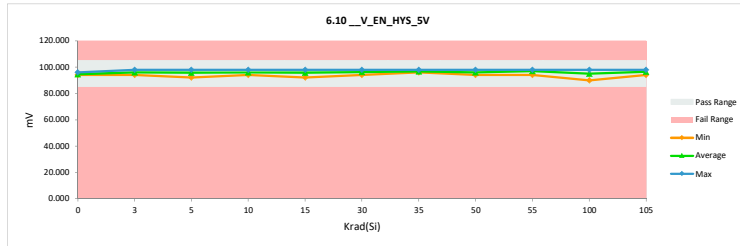


LDR TID Report  
TPS7H5001-SP

6.10_V_EN_HYS_SV					
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit
			mV	105	85
			mV	105	85
Krad(Si)	Serial #	PRE	POST	Delta	
0	117	96.000	96.000	0.000	
0	118	96.000	94.000	-2.000	
0	122	96.000	94.000	-2.000	
3	61	100.000	98.000	-2.000	
3	62	98.000	96.000	-2.000	
3	63	96.000	94.000	-2.000	
3	64	98.000	98.000	0.000	
3	65	96.000	94.000	-2.000	
3	66	98.000	96.000	-2.000	
5	67	94.000	92.000	-2.000	
5	68	100.000	96.000	-4.000	
5	69	98.000	96.000	-2.000	
5	70	98.000	96.000	-2.000	
5	71	98.000	96.000	-2.000	
5	72	100.000	98.000	-2.000	
10	73	100.000	98.000	-2.000	
10	74	98.000	96.000	-2.000	
10	75	96.000	94.000	-2.000	
10	76	98.000	96.000	-2.000	
10	77	96.000	96.000	0.000	
10	78	98.000	96.000	-2.000	
15	79	94.000	92.000	-2.000	
15	80	100.000	98.000	-2.000	
15	81	98.000	96.000	-2.000	
15	82	98.000	96.000	-2.000	
15	83	98.000	96.000	-2.000	
15	84	100.000	96.000	-4.000	
30	86	100.000	96.000	-4.000	
30	89	98.000	94.000	-4.000	
30	91	96.000	94.000	-2.000	
30	92	98.000	98.000	0.000	
30	94	96.000	96.000	0.000	
30	95	98.000	96.000	-2.000	
35	100	94.000	98.000	4.000	
35	101	100.000	96.000	-4.000	
35	102	98.000	96.000	-2.000	
35	103	98.000	96.000	-2.000	
35	104	98.000	98.000	0.000	
35	105	100.000	96.000	-4.000	
50	106	96.000	94.000	-2.000	
50	107	100.000	98.000	-2.000	
50	108	96.000	94.000	-2.000	
50	109	98.000	98.000	0.000	
50	110	96.000	96.000	0.000	
50	111	98.000	96.000	-2.000	
55	113	98.000	98.000	0.000	
55	114	100.000	98.000	-2.000	
55	115	96.000	98.000	2.000	
55	116	100.000	98.000	-2.000	
55	126	100.000	96.000	-4.000	
55	128	97.000	94.000	-3.000	
100	129	100.000	98.000	-2.000	
100	131	100.000	94.000	-6.000	
100	132	96.000	94.000	-2.000	
100	133	100.000	94.000	-6.000	
100	134	98.000	94.000	-4.000	
100	135	98.000	98.000	0.000	
100	138	98.000	94.000	-4.000	
100	139	98.000	90.000	-8.000	
100	140	98.000	96.000	-2.000	
100	141	98.000	94.000	-4.000	
100	142	98.000	94.000	-4.000	
100	143	98.000	96.000	-2.000	
100	144	98.000	94.000	-4.000	
100	220	98.000	96.000	-2.000	
100	221	100.000	98.000	-2.000	
100	224	98.000	94.000	-4.000	
100	225	98.000	94.000	-4.000	
100	226	98.000	94.000	-4.000	
100	228	100.000	98.000	-2.000	
100	232	98.000	96.000	-2.000	
100	233	98.000	94.000	-4.000	
100	236	100.000	96.000	-4.000	
100	238	96.000	98.000	2.000	
100	243	98.000	92.000	-6.000	
105	244	98.000	96.000	-2.000	
105	246	96.000	96.000	0.000	
105	248	100.000	98.000	-2.000	
105	374	98.000	94.000	-4.000	
105	376	100.000	98.000	-2.000	
105	Max	100.000	98.000	4.000	
105	Average	97.963	95.775	-2.188	
105	Min	94.000	90.000	-8.000	
105	Std Dev	1.579	1.800	1.890	



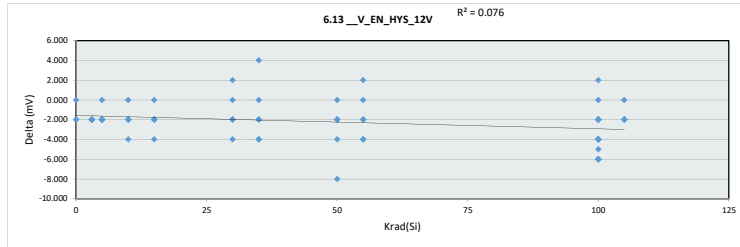
6.10_V_EN_HYS										
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit					
			mV	105	85					
			mV	105	85					
Krad(Si)	LL	Min	Average	Max	UL					
0	85.000	94.667	96.000	96.000	105.000					
3	85.000	94.000	95.667	98.000	105.000					
5	85.000	92.000	96.000	98.000	105.000					
10	85.000	94.000	96.000	98.000	105.000					
15	85.000	92.000	95.667	98.000	105.000					
30	85.000	94.000	96.333	98.000	105.000					
35	85.000	96.000	96.667	98.000	105.000					
50	85.000	94.000	96.000	98.000	105.000					
55	85.000	94.000	97.000	98.000	105.000					
100	85.000	90.000	95.000	98.000	105.000					
105	85.000	94.000	96.400	98.000	105.000					



LDR TID Report  
TPS7H5001-SP

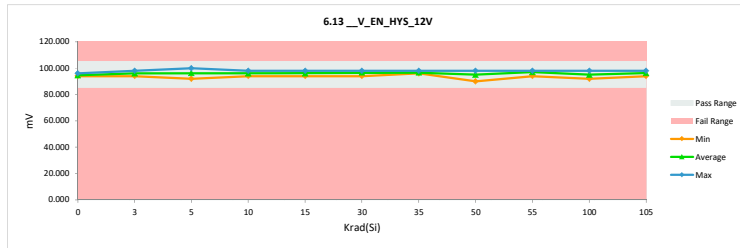
6.13 V_EN_HYS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	96.000	96.000	0.000
0	118	96.000	94.000	-2.000
0	122	96.000	94.000	-2.000
3	61	100.000	98.000	-2.000
3	62	98.000	96.000	-2.000
3	63	96.000	94.000	-2.000
3	64	100.000	98.000	-2.000
3	65	96.000	94.000	-2.000
3	66	98.000	96.000	-2.000
5	67	94.000	92.000	-2.000
5	68	100.000	100.000	0.000
5	69	98.000	96.000	-2.000
5	70	98.000	96.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	100.000	96.000	-4.000
10	77	96.000	96.000	0.000
10	78	98.000	96.000	-2.000
15	79	94.000	94.000	0.000
15	80	100.000	98.000	-2.000
15	81	98.000	96.000	-2.000
15	82	98.000	96.000	-2.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	98.000	-2.000
30	89	98.000	94.000	-4.000
30	91	96.000	94.000	-2.000
30	92	100.000	98.000	-2.000
30	94	96.000	96.000	0.000
30	95	98.000	96.000	-2.000
35	100	94.000	98.000	4.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	98.000	96.000	-2.000
35	104	98.000	98.000	0.000
35	105	100.000	96.000	-4.000
50	106	96.000	94.000	-2.000
50	107	100.000	98.000	-2.000
50	108	96.000	94.000	-2.000
50	109	98.000	98.000	0.000
50	110	98.000	90.000	-8.000
50	111	100.000	96.000	-4.000
55	113	98.000	98.000	0.000
55	114	100.000	98.000	-2.000
55	115	96.000	98.000	2.000
55	116	100.000	98.000	-2.000
55	126	100.000	96.000	-4.000
55	128	98.000	94.000	-4.000
100	129	100.000	98.000	-2.000
100	131	100.000	94.000	-6.000
100	132	96.000	94.000	-2.000
100	133	100.000	94.000	-6.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	94.000	-4.000
100	139	98.000	92.000	-6.000
100	140	98.000	96.000	-2.000
100	141	98.000	93.000	-5.000
100	142	98.000	94.000	-4.000
100	143	98.000	96.000	-2.000
100	144	100.000	94.000	-6.000
100	220	98.000	96.000	-2.000
100	221	100.000	98.000	-2.000
100	224	96.000	94.000	-2.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	94.000	-4.000
100	236	100.000	96.000	-4.000
100	238	96.000	98.000	2.000
100	243	98.000	92.000	-6.000
105	244	98.000	96.000	-2.000
105	246	98.000	96.000	-2.000
105	248	100.000	98.000	-2.000
105	374	94.000	94.000	0.000
105	375	100.000	98.000	-2.000
105	Max	100.000	100.000	4.000
105	Average	98.075	95.813	-2.263
105	Min	94.000	90.000	-8.000
105	Std Dev	1.697	1.903	1.960



6.13 V_EN_HYS	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

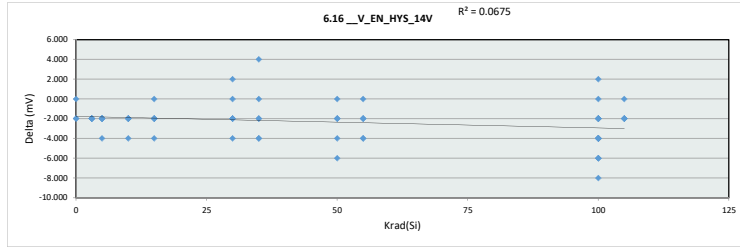
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	92.000	94.000	94.000	94.000	96.000	90.000	94.000	92.000	94.000
Average	94.667	96.000	96.333	96.000	96.000	96.667	96.667	95.000	97.000	95.042	96.400
Max	96.000	98.000	100.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000



LDR TID Report  
TPS7H5001-SP

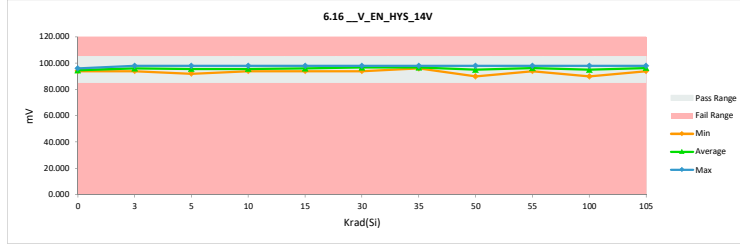
6.16_V_EN_HYS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

Krad(Si)	Serial #	PRE	POST	Delta
0	117	96.000	96.000	0.000
0	118	96.000	94.000	-2.000
0	122	96.000	94.000	-2.000
3	61	100.000	98.000	-2.000
3	62	98.000	96.000	-2.000
3	63	96.000	94.000	-2.000
3	64	100.000	98.000	-2.000
3	65	96.000	94.000	-2.000
3	66	98.000	96.000	-2.000
5	67	94.000	92.000	-2.000
5	68	100.000	96.000	-4.000
5	69	98.000	96.000	-2.000
5	70	98.000	96.000	-2.000
5	71	98.000	96.000	-2.000
5	72	100.000	98.000	-2.000
10	73	100.000	98.000	-2.000
10	74	98.000	96.000	-2.000
10	75	96.000	94.000	-2.000
10	76	100.000	96.000	-4.000
10	77	96.000	94.000	-2.000
10	78	98.000	96.000	-2.000
15	79	94.000	94.000	0.000
15	80	100.000	98.000	-2.000
15	81	98.000	96.000	-2.000
15	82	98.000	96.000	-2.000
15	83	98.000	96.000	-2.000
15	84	100.000	96.000	-4.000
30	86	100.000	98.000	-2.000
30	89	98.000	94.000	-4.000
30	91	96.000	94.000	-2.000
30	92	100.000	98.000	-2.000
30	94	96.000	96.000	0.000
30	95	98.000	96.000	-2.000
35	100	94.000	98.000	4.000
35	101	100.000	96.000	-4.000
35	102	98.000	96.000	-2.000
35	103	98.000	96.000	-2.000
35	104	98.000	98.000	0.000
35	105	100.000	96.000	-4.000
50	106	96.000	94.000	-2.000
50	107	100.000	98.000	-2.000
50	108	96.000	94.000	-2.000
50	109	98.000	98.000	0.000
50	110	96.000	90.000	-6.000
50	111	100.000	96.000	-4.000
55	113	98.000	98.000	0.000
55	114	100.000	98.000	-2.000
55	115	96.000	94.000	-2.000
55	116	100.000	98.000	-2.000
55	126	100.000	96.000	-4.000
55	128	98.000	94.000	-4.000
100	129	100.000	98.000	-2.000
100	131	100.000	94.000	-6.000
100	132	96.000	94.000	-2.000
100	133	100.000	94.000	-6.000
100	134	98.000	94.000	-4.000
100	135	98.000	98.000	0.000
100	138	98.000	94.000	-4.000
100	139	98.000	90.000	-8.000
100	140	100.000	96.000	-4.000
100	141	98.000	94.000	-4.000
100	142	98.000	96.000	-2.000
100	143	98.000	96.000	-2.000
100	144	98.000	94.000	-4.000
100	220	98.000	96.000	-2.000
100	221	100.000	98.000	-2.000
100	224	98.000	94.000	-4.000
100	225	98.000	94.000	-4.000
100	226	98.000	94.000	-4.000
100	228	100.000	98.000	-2.000
100	232	98.000	96.000	-2.000
100	233	98.000	94.000	-4.000
100	236	100.000	96.000	-4.000
100	238	96.000	98.000	2.000
100	243	98.000	92.000	-6.000
105	244	98.000	96.000	-2.000
105	246	98.000	96.000	-2.000
105	248	100.000	98.000	-2.000
105	374	94.000	94.000	0.000
105	376	100.000	98.000	-2.000
Max		100.000	98.000	4.000
Average		98.075	95.700	-2.375
Min		94.000	90.000	-8.000
Std Dev		1.697	1.885	1.831



6.16_V_EN_HYS	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	105
Min Limit	85

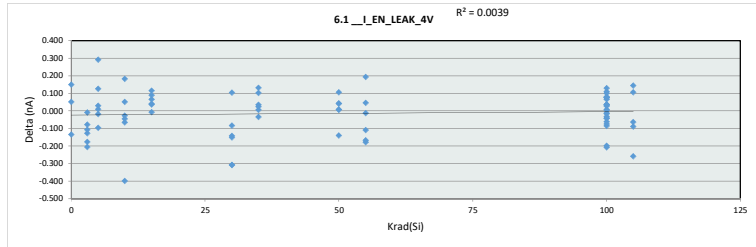
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000
Min	94.000	94.000	92.000	94.000	94.000	94.000	96.000	90.000	94.000	90.000	94.000
Average	94.667	96.000	95.667	95.667	96.000	96.667	96.667	95.000	96.333	95.083	96.400
Max	96.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000	98.000
UL	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000	105.000



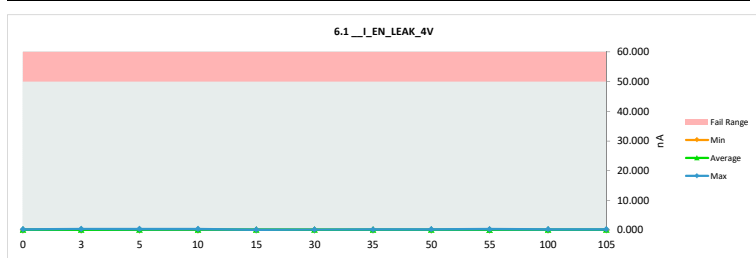
LDR TID Report  
TPS7H5001-SP

6.1_I_EN_LEAK_4V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.095	0.244	0.149
0	118	0.017	0.068	0.051
0	122	0.203	0.068	-0.135
3	61	0.029	0.020	-0.009
3	62	0.156	0.029	-0.127
3	63	0.400	0.322	-0.078
3	64	0.361	0.156	-0.205
3	65	0.166	0.059	-0.107
3	66	0.264	0.088	-0.176
5	67	0.039	0.020	-0.019
5	68	0.020	0.312	0.292
5	69	0.039	0.049	0.010
5	70	0.039	0.068	0.029
5	71	0.117	0.020	-0.097
5	72	0.059	0.185	0.126
10	73	0.029	0.212	0.183
10	74	0.156	0.207	0.051
10	75	0.400	0.002	-0.398
10	76	0.361	0.295	-0.066
10	77	0.166	0.120	-0.046
10	78	0.264	0.237	-0.027
15	79	0.039	0.076	0.037
15	80	0.020	0.134	0.114
15	81	0.039	0.105	0.066
15	82	0.039	0.129	0.090
15	83	0.117	0.110	-0.007
15	84	0.059	0.100	0.041
30	86	0.029	0.132	0.103
30	89	0.156	0.073	-0.083
30	91	0.400	0.093	-0.307
30	92	0.361	0.054	-0.307
30	94	0.166	0.024	-0.142
30	95	0.264	0.112	-0.152
35	100	0.039	0.171	0.132
35	101	0.020	0.122	0.102
35	102	0.039	0.063	0.024
35	103	0.039	0.073	0.034
35	104	0.117	0.122	0.005
35	105	0.059	0.024	-0.035
50	106	0.146	0.005	-0.141
50	107	0.095	0.102	0.007
50	108	0.041	0.083	0.042
50	109	0.041	0.083	0.042
50	110	0.027	0.034	0.007
50	111	0.134	0.239	0.105
55	113	0.203	0.024	-0.179
55	114	0.222	0.054	-0.168
55	115	0.066	0.112	0.046
55	116	0.124	0.015	-0.109
55	126	0.066	0.259	0.193
55	128	0.017	0.005	-0.012
100	129	0.105	0.142	0.037
100	131	0.124	0.083	-0.041
100	132	0.183	0.210	0.027
100	133	0.154	0.112	-0.042
100	134	0.129	0.054	-0.075
100	135	0.002	0.132	0.130
100	138	0.056	0.024	-0.032
100	139	0.173	0.112	-0.061
100	140	0.027	0.102	0.075
100	141	0.203	0.005	-0.198
100	142	0.022	0.122	0.100
100	143	0.115	0.102	-0.013
100	144	0.066	0.073	0.007
100	220	0.037	0.073	0.036
100	221	0.271	0.063	-0.208
100	224	0.090	0.073	-0.017
100	225	0.090	0.093	0.003
100	226	0.105	0.181	0.076
100	228	0.012	0.122	0.110
100	232	0.149	0.063	-0.086
100	233	0.115	0.181	0.066
100	236	0.061	0.093	0.032
100	238	0.056	0.132	0.076
100	243	0.051	0.132	0.081
105	244	0.085	0.190	0.105
105	246	0.134	0.044	-0.090
105	248	0.027	0.171	0.144
105	374	0.176	0.112	-0.064
105	376	0.264	0.005	-0.259
105	Max	0.400	0.322	0.292
105	Average	0.121	0.106	-0.014
105	Min	0.002	0.002	-0.398
105	Std Dev	0.102	0.074	0.123



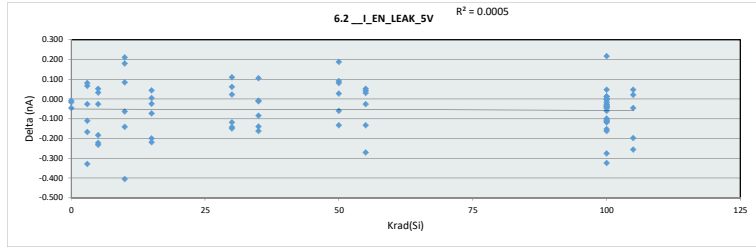
6.1_I_EN_LEAK	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105



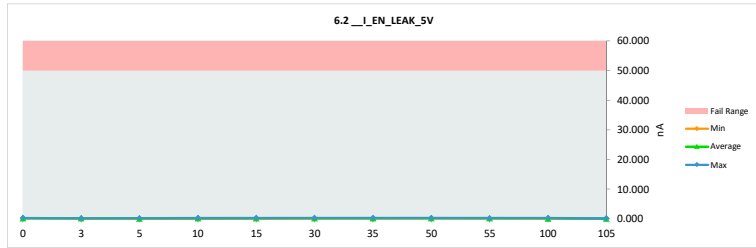
LDR TID Report  
TPS7H5001-SP

6.2_I_EN_LEAK_5V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.098	0.090	-0.008
0	118	0.127	0.110	-0.017
0	122	0.234	0.188	-0.046
3	61	0.049	0.115	0.066
3	62	0.088	0.061	-0.027
3	63	0.185	0.017	-0.168
3	64	0.039	0.120	0.081
3	65	0.195	0.085	-0.110
3	66	0.429	0.100	-0.329
5	67	0.020	0.071	0.051
5	68	0.156	0.129	-0.027
5	69	0.224	0.002	-0.222
5	70	0.127	0.159	0.032
5	71	0.273	0.090	-0.183
5	72	0.234	0.002	-0.232
10	73	0.049	0.229	0.180
10	74	0.088	0.024	-0.064
10	75	0.185	0.268	0.083
10	76	0.039	0.249	0.210
10	77	0.195	0.054	-0.141
10	78	0.429	0.024	-0.405
15	79	0.020	0.063	0.043
15	80	0.156	0.083	-0.073
15	81	0.224	0.229	0.005
15	82	0.127	0.102	-0.025
15	83	0.273	0.073	-0.200
15	84	0.234	0.015	-0.219
30	86	0.049	0.159	0.110
30	89	0.088	0.110	0.022
30	91	0.185	0.066	-0.119
30	92	0.039	0.100	0.061
30	94	0.195	0.046	-0.149
30	95	0.429	0.286	-0.143
35	100	0.020	0.012	-0.008
35	101	0.156	0.144	-0.012
35	102	0.224	0.061	-0.163
35	103	0.127	0.232	0.105
35	104	0.273	0.134	-0.139
35	105	0.234	0.149	-0.085
50	106	0.312	0.178	-0.134
50	107	0.059	0.149	0.090
50	108	0.020	0.207	0.187
50	109	0.117	0.198	0.081
50	110	0.068	0.095	0.027
50	111	0.127	0.066	-0.061
55	113	0.020	0.071	0.051
55	114	0.273	0.139	-0.134
55	115	0.137	0.168	0.031
55	116	0.156	0.129	-0.027
55	126	0.381	0.110	-0.271
55	128	0.078	0.120	0.042
100	129	0.176	0.139	-0.037
100	131	0.224	0.115	-0.109
100	132	0.049	0.051	0.002
100	133	0.322	0.046	-0.276
100	134	0.088	0.027	-0.061
100	135	0.224	0.105	-0.119
100	138	0.137	0.100	-0.037
100	139	0.117	0.129	0.012
100	140	0.185	0.032	-0.153
100	141	0.049	0.017	-0.032
100	142	0.224	0.061	-0.163
100	143	0.010	0.056	0.046
100	144	0.068	0.022	-0.046
100	220	0.029	0.002	-0.027
100	221	0.195	0.090	-0.105
100	224	0.020	0.237	0.217
100	225	0.078	0.032	-0.046
100	226	0.078	0.061	-0.017
100	228	0.215	0.212	-0.003
100	232	0.098	0.110	0.012
100	233	0.117	0.017	-0.100
100	236	0.146	0.032	-0.114
100	238	0.049	0.046	-0.003
100	243	0.400	0.076	-0.324
105	244	0.127	0.081	-0.046
105	246	0.322	0.066	-0.256
105	248	0.029	0.076	0.047
105	374	0.020	0.041	0.021
105	376	0.254	0.056	-0.198
105	Max	0.429	0.286	0.217
105	Average	0.154	0.100	-0.054
105	Min	0.010	0.002	-0.405
105	Std Dev	0.108	0.068	0.124



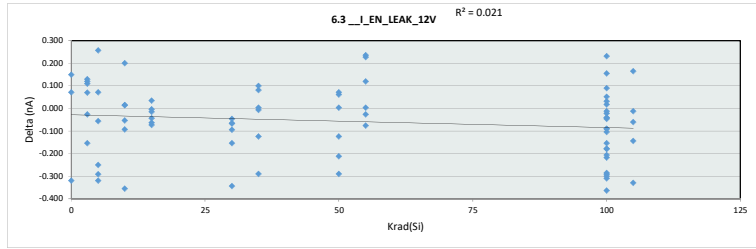
6.2_I_EN_LEAK	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	0.090 0.017 0.002 0.024 0.015 0.046 0.012 0.066 0.071 0.002 0.041
Average	0.129 0.083 0.076 0.141 0.094 0.128 0.122 0.149 0.123 0.076 0.064
Max	0.188 0.120 0.159 0.268 0.229 0.286 0.232 0.207 0.168 0.237 0.081
UL	50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000 50.000



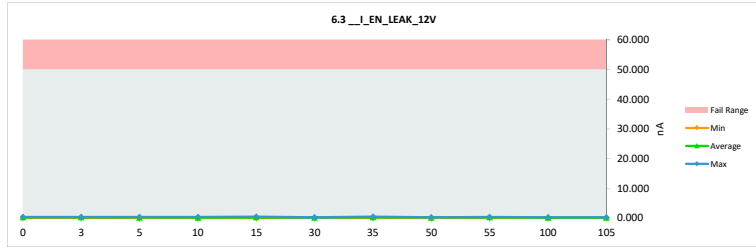
LDR TID Report  
TPS7H5001-SP

6.3 I_EN_LEAK_12V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.205	0.276	0.071
0	118	0.664	0.344	-0.320
0	122	0.039	0.188	0.149
3	61	0.371	0.344	-0.027
3	62	0.166	0.295	0.129
3	63	0.127	0.246	0.119
3	64	0.137	0.207	0.070
3	65	0.342	0.188	-0.154
3	66	0.176	0.286	0.110
5	67	0.117	0.373	0.256
5	68	0.107	0.178	0.071
5	69	0.117	0.061	-0.056
5	70	0.429	0.178	-0.251
5	71	0.332	0.041	-0.291
5	72	0.449	0.129	-0.320
10	73	0.371	0.015	-0.356
10	74	0.166	0.366	0.200
10	75	0.127	0.142	0.015
10	76	0.137	0.151	0.014
10	77	0.342	0.249	-0.093
10	78	0.176	0.122	-0.054
15	79	0.117	0.044	-0.073
15	80	0.107	0.044	-0.063
15	81	0.117	0.073	-0.044
15	82	0.429	0.425	-0.004
15	83	0.332	0.366	0.034
15	84	0.449	0.434	-0.015
30	86	0.371	0.027	-0.344
30	89	0.166	0.100	-0.066
30	91	0.127	0.081	-0.046
30	92	0.137	0.071	-0.066
30	94	0.342	0.188	-0.154
30	95	0.176	0.081	-0.095
35	100	0.117	0.110	-0.007
35	101	0.107	0.207	0.100
35	102	0.117	0.198	0.081
35	103	0.429	0.432	0.003
35	104	0.332	0.207	-0.125
35	105	0.449	0.159	-0.290
50	106	0.273	0.061	-0.212
50	107	0.166	0.237	0.071
50	108	0.410	0.120	-0.290
50	109	0.234	0.110	-0.124
50	110	0.107	0.110	0.003
50	111	0.088	0.149	0.061
55	113	0.059	0.178	0.119
55	114	0.254	0.178	-0.076
55	115	0.254	0.227	-0.027
55	116	0.351	0.354	0.003
55	126	0.107	0.334	0.227
55	128	0.059	0.295	0.236
100	129	0.303	0.012	-0.291
100	131	0.303	0.124	-0.179
100	132	0.390	0.183	-0.207
100	133	0.244	0.232	-0.012
100	134	0.244	0.222	-0.022
100	135	0.166	0.076	-0.090
100	138	0.010	0.164	0.154
100	139	0.273	0.095	-0.178
100	140	0.107	0.124	0.017
100	141	0.215	0.173	-0.042
100	142	0.351	0.051	-0.300
100	143	0.469	0.251	-0.218
100	144	0.361	0.051	-0.310
100	220	0.459	0.095	-0.364
100	221	0.156	0.115	-0.041
100	224	0.117	0.027	-0.090
100	225	0.205	0.051	-0.154
100	226	0.224	0.120	-0.104
100	228	0.059	0.290	0.231
100	232	0.176	0.129	-0.047
100	233	0.371	0.085	-0.286
100	236	0.000	0.090	0.090
100	238	0.137	0.188	0.051
100	243	0.098	0.129	0.031
105	244	0.273	0.129	-0.144
105	246	0.127	0.066	-0.061
105	248	0.254	0.242	-0.012
105	374	0.342	0.012	-0.330
105	376	0.078	0.242	0.164
105	375	0.664	0.434	0.256
Max		0.230	0.172	-0.058
Average		0.230	0.172	-0.058
Min		0.000	0.012	-0.364
Std Dev		0.134	0.107	0.157



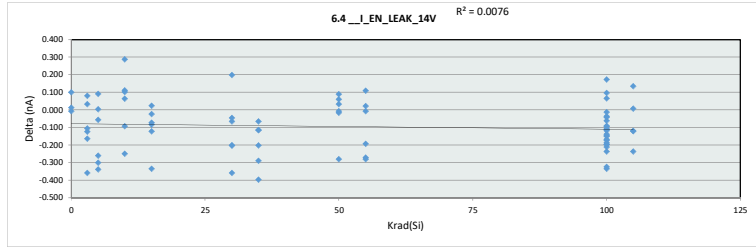
6.3 I_EN_LEAK											
Test Site											
Tester											
Test Number											
Max Limit	50	nA									
Min Limit		nA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	0.188	0.188	0.041	0.015	0.044	0.027	0.110	0.061	0.178	0.012	0.012
Average	0.269	0.261	0.160	0.174	0.231	0.091	0.219	0.131	0.261	0.128	0.138
Max	0.344	0.344	0.373	0.366	0.434	0.188	0.432	0.237	0.354	0.290	0.242
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



LDR TID Report  
TPS7H5001-SP

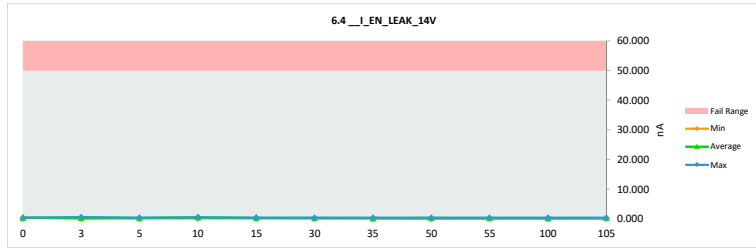
6.4 I_EN_LEAK_14V	
Test Site	
Tester	
Test Number	
Unit	nA nA
Max Limit	50 50
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.371	0.383	0.012
0	118	0.234	0.334	0.100
0	122	0.381	0.373	-0.008
3	61	0.127	0.159	0.032
3	62	0.400	0.237	-0.163
3	63	0.449	0.530	0.081
3	64	0.459	0.100	-0.359
3	65	0.361	0.237	-0.124
3	66	0.420	0.315	-0.105
5	67	0.429	0.168	-0.261
5	68	0.595	0.256	-0.339
5	69	0.381	0.081	-0.300
5	70	0.234	0.237	0.003
5	71	0.390	0.334	-0.056
5	72	0.224	0.091	-0.133
10	73	0.127	0.415	0.288
10	74	0.400	0.151	-0.249
10	75	0.449	0.356	-0.093
10	76	0.459	0.522	0.063
10	77	0.361	0.473	0.112
10	78	0.420	0.522	0.102
15	79	0.429	0.405	-0.024
15	80	0.595	0.259	-0.336
15	81	0.381	0.405	0.024
15	82	0.234	0.151	-0.083
15	83	0.390	0.268	-0.122
15	84	0.224	0.151	-0.073
30	86	0.127	0.325	0.198
30	89	0.400	0.198	-0.202
30	91	0.400	0.449	-0.359
30	92	0.459	0.393	-0.066
30	94	0.361	0.315	-0.046
30	95	0.420	0.217	-0.203
35	100	0.429	0.139	-0.290
35	101	0.595	0.198	-0.397
35	102	0.381	0.315	-0.066
35	103	0.234	0.032	-0.202
35	104	0.390	0.276	-0.114
35	105	0.224	0.110	-0.114
50	106	0.215	0.276	0.061
50	107	0.322	0.354	0.032
50	108	0.185	0.168	-0.017
50	109	0.244	0.334	0.090
50	110	0.351	0.071	-0.280
50	111	0.342	0.334	-0.008
55	113	0.303	0.325	0.022
55	114	0.332	0.139	-0.193
55	115	0.361	0.090	-0.271
55	116	0.137	0.246	0.109
55	126	0.410	0.129	-0.281
55	128	0.303	0.295	-0.008
100	129	0.137	0.203	0.066
100	131	0.303	0.290	-0.013
100	132	0.371	0.203	-0.168
100	133	0.107	0.203	0.096
100	134	0.156	0.095	-0.061
100	135	0.351	0.203	-0.148
100	138	0.410	0.300	-0.110
100	139	0.322	0.212	-0.110
100	140	0.342	0.007	-0.335
100	141	0.517	0.320	-0.197
100	142	0.234	0.022	-0.212
100	143	0.117	0.290	0.173
100	144	0.185	0.012	-0.173
100	220	0.176	0.134	-0.042
100	221	0.322	0.183	-0.139
100	224	0.439	0.203	-0.236
100	225	0.361	0.164	-0.197
100	226	0.205	0.110	-0.095
100	228	0.410	0.222	-0.188
100	232	0.098	0.061	-0.037
100	233	0.185	0.066	-0.119
100	236	0.342	0.251	-0.091
100	238	0.439	0.290	-0.149
100	243	0.429	0.105	-0.324
105	244	0.254	0.134	-0.120
105	246	0.166	0.300	0.134
105	248	0.293	0.173	-0.120
105	374	0.254	0.017	-0.237
105	376	0.390	0.398	0.008
105	Max	0.595	0.530	0.288
105	Average	0.328	0.233	-0.094
105	Min	0.098	0.007	-0.397
105	Std Dev	0.119	0.124	0.148



6.4 I_EN_LEAK	
Test Site	
Tester	
Test Number	
Max Limit	50 nA
Min Limit	nA
Krad(Si)	0 3

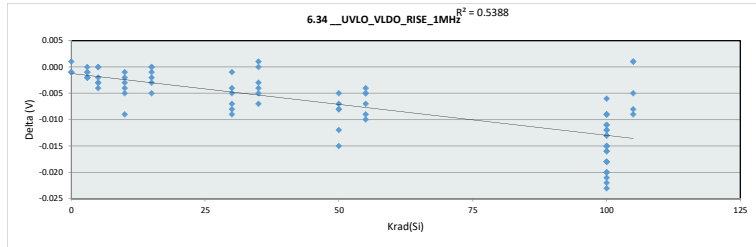
LL	0	3	5	10	15	30	35	50	55	100	105
Min	0.334	0.100	0.081	0.151	0.151	0.090	0.032	0.071	0.090	0.007	0.017
Average	0.363	0.263	0.232	0.407	0.273	0.256	0.178	0.256	0.204	0.173	0.204
Max	0.383	0.530	0.334	0.522	0.405	0.393	0.315	0.354	0.325	0.320	0.398
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



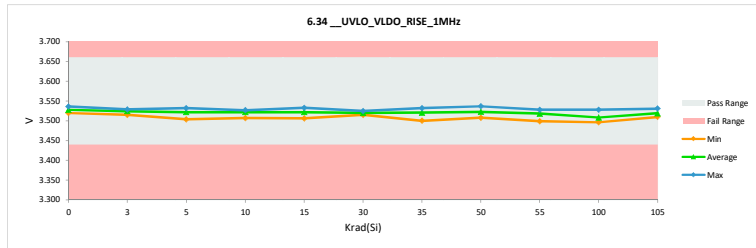
LDR TID Report  
TPS7H5001-SP

6.34 UVLO_VLDO_RISE_1MHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.520	0.001
0	118	3.528	3.527	-0.001
0	122	3.537	3.536	-0.001
3	61	3.525	3.523	-0.002
3	62	3.528	3.526	-0.002
3	63	3.516	3.515	-0.001
3	64	3.529	3.528	-0.001
3	65	3.528	3.526	-0.002
3	66	3.529	3.529	0.000
5	67	3.535	3.532	-0.003
5	68	3.507	3.504	-0.003
5	69	3.532	3.528	-0.004
5	70	3.524	3.524	0.000
5	71	3.523	3.523	0.000
5	72	3.523	3.521	-0.002
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.516	3.507	-0.009
10	76	3.529	3.525	-0.004
10	77	3.528	3.527	-0.001
10	78	3.529	3.527	-0.002
15	79	3.535	3.533	-0.002
15	80	3.507	3.506	-0.001
15	81	3.532	3.529	-0.003
15	82	3.524	3.524	0.000
15	83	3.523	3.518	-0.005
15	84	3.523	3.523	0.000
30	86	3.525	3.517	-0.008
30	89	3.528	3.521	-0.007
30	91	3.516	3.515	-0.001
30	92	3.529	3.520	-0.009
30	94	3.528	3.523	-0.005
30	95	3.529	3.525	-0.004
35	100	3.535	3.532	-0.003
35	101	3.507	3.500	-0.007
35	102	3.532	3.528	-0.004
35	103	3.524	3.525	0.001
35	104	3.523	3.518	-0.005
35	105	3.523	3.523	0.000
50	106	3.528	3.523	-0.005
50	107	3.534	3.519	-0.015
50	108	3.520	3.508	-0.012
50	109	3.539	3.531	-0.008
50	110	3.545	3.537	-0.008
50	111	3.525	3.518	-0.007
55	113	3.529	3.520	-0.009
55	114	3.504	3.499	-0.005
55	115	3.531	3.527	-0.004
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.524	3.519	-0.005
100	129	3.535	3.524	-0.011
100	131	3.518	3.509	-0.009
100	132	3.530	3.512	-0.018
100	133	3.521	3.509	-0.012
100	134	3.535	3.515	-0.020
100	135	3.520	3.502	-0.018
100	138	3.528	3.508	-0.020
100	139	3.527	3.512	-0.015
100	140	3.520	3.511	-0.009
100	141	3.528	3.506	-0.022
100	142	3.506	3.497	-0.009
100	143	3.526	3.511	-0.015
100	144	3.526	3.515	-0.011
100	220	3.529	3.506	-0.023
100	221	3.541	3.523	-0.018
100	224	3.513	3.497	-0.016
100	225	3.512	3.496	-0.016
100	226	3.541	3.528	-0.013
100	228	3.524	3.509	-0.015
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.504	-0.012
100	243	3.513	3.507	-0.006
105	244	3.509	3.510	0.001
105	246	3.530	3.525	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.531	0.001
105	376	3.520	3.511	-0.009
105	Max	3.545	3.537	0.001
105	Average	3.525	3.518	-0.007
105	Min	3.504	3.496	-0.023
105	Std Dev	0.008	0.010	0.006



6.34 UVLO_VLD		0	5	10	15	30	35	50	55	100	105
LL		3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min		3.520	3.515	3.504	3.507	3.506	3.515	3.500	3.508	3.499	3.496
Average		3.528	3.525	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509
Max		3.536	3.529	3.532	3.527	3.533	3.525	3.532	3.537	3.528	3.528
UL		3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660

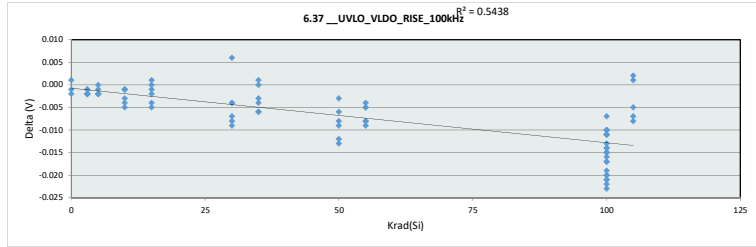




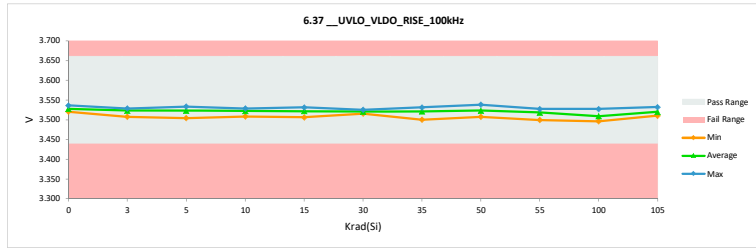
LDR TID Report  
TPS7H5001-SP

6.37 UVLO_VLDO_RISE_100kHz			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	3.66	3.66	
Min Limit	3.44	3.44	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.520	0.001
0	118	3.527	3.526	-0.001
0	122	3.538	3.536	-0.002
3	61	3.525	3.523	-0.002
3	62	3.528	3.526	-0.002
3	63	3.509	3.507	-0.002
3	64	3.529	3.528	-0.001
3	65	3.528	3.526	-0.002
3	66	3.529	3.528	-0.001
5	67	3.535	3.533	-0.002
5	68	3.506	3.504	-0.002
5	69	3.531	3.529	-0.002
5	70	3.525	3.525	0.000
5	71	3.524	3.522	-0.002
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.509	3.508	-0.001
10	76	3.529	3.525	-0.004
10	77	3.528	3.527	-0.001
10	78	3.529	3.528	-0.001
15	79	3.535	3.531	-0.004
15	80	3.506	3.506	0.000
15	81	3.531	3.529	-0.002
15	82	3.525	3.524	-0.001
15	83	3.524	3.519	-0.005
15	84	3.522	3.523	0.001
30	86	3.525	3.517	-0.008
30	89	3.528	3.521	-0.007
30	91	3.509	3.515	0.006
30	92	3.529	3.520	-0.009
30	94	3.528	3.524	-0.004
30	95	3.529	3.525	-0.004
35	100	3.535	3.531	-0.004
35	101	3.506	3.500	-0.006
35	102	3.531	3.528	-0.003
35	103	3.525	3.525	0.000
35	104	3.524	3.518	-0.006
35	105	3.522	3.523	0.001
50	106	3.527	3.524	-0.003
50	107	3.533	3.520	-0.013
50	108	3.519	3.507	-0.012
50	109	3.540	3.531	-0.009
50	110	3.546	3.538	-0.008
50	111	3.524	3.518	-0.006
55	113	3.528	3.520	-0.008
55	114	3.504	3.499	-0.005
55	115	3.531	3.526	-0.005
55	116	3.525	3.516	-0.009
55	126	3.535	3.527	-0.008
55	128	3.524	3.520	-0.004
100	129	3.536	3.525	-0.011
100	131	3.519	3.509	-0.010
100	132	3.531	3.511	-0.020
100	133	3.520	3.509	-0.011
100	134	3.534	3.515	-0.019
100	135	3.520	3.503	-0.017
100	138	3.528	3.507	-0.021
100	139	3.527	3.512	-0.015
100	140	3.521	3.511	-0.010
100	141	3.528	3.505	-0.023
100	142	3.507	3.496	-0.011
100	143	3.526	3.512	-0.014
100	144	3.526	3.516	-0.010
100	220	3.529	3.507	-0.022
100	221	3.541	3.524	-0.017
100	224	3.513	3.497	-0.016
100	225	3.512	3.497	-0.015
100	226	3.542	3.527	-0.015
100	228	3.524	3.510	-0.014
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.505	-0.011
100	243	3.514	3.507	-0.007
105	244	3.509	3.510	0.001
105	246	3.530	3.525	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.532	0.002
105	375	3.519	3.512	-0.007
105	Max	3.546	3.538	0.006
105	Average	3.525	3.518	-0.007
105	Min	3.504	3.496	-0.023
105	Std Dev	0.009	0.010	0.007



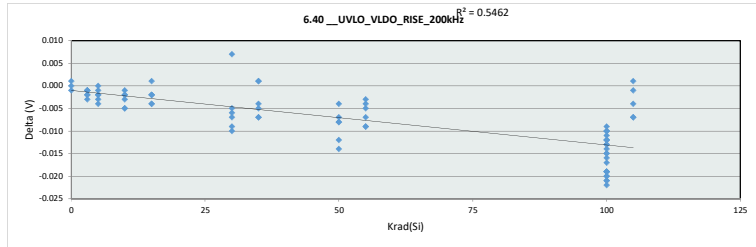
6.37 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.66	V									
Min Limit	3.44	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.507	3.504	3.508	3.506	3.515	3.500	3.507	3.499	3.496	3.510
Average	3.527	3.523	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509	3.520
Max	3.536	3.528	3.533	3.528	3.531	3.525	3.531	3.538	3.527	3.527	3.532
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



LDR TID Report  
TPS7H5001-SP

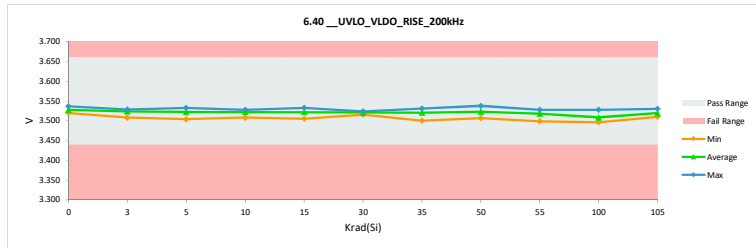
6.40 UVLO_VLDO_RISE_200kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.520	0.001
0	118	3.527	3.526	-0.001
0	122	3.537	3.537	0.000
3	61	3.525	3.524	-0.001
3	62	3.528	3.526	-0.002
3	63	3.509	3.508	-0.001
3	64	3.530	3.527	-0.003
3	65	3.529	3.527	-0.002
3	66	3.530	3.529	-0.001
5	67	3.535	3.533	-0.002
5	68	3.507	3.504	-0.003
5	69	3.533	3.529	-0.004
5	70	3.525	3.525	0.000
5	71	3.524	3.522	-0.002
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.509	3.508	-0.001
10	76	3.530	3.525	-0.005
10	77	3.529	3.527	-0.002
10	78	3.530	3.528	-0.002
15	79	3.535	3.533	-0.002
15	80	3.507	3.505	-0.002
15	81	3.533	3.529	-0.004
15	82	3.525	3.523	-0.002
15	83	3.524	3.520	-0.004
15	84	3.522	3.523	0.001
30	86	3.525	3.516	-0.009
30	89	3.528	3.521	-0.007
30	91	3.509	3.516	0.007
30	92	3.530	3.520	-0.010
30	94	3.529	3.524	-0.005
30	95	3.530	3.524	-0.006
35	100	3.535	3.531	-0.004
35	101	3.507	3.500	-0.007
35	102	3.533	3.528	-0.005
35	103	3.525	3.526	0.001
35	104	3.524	3.517	-0.007
35	105	3.522	3.523	0.001
50	106	3.528	3.524	-0.004
50	107	3.534	3.520	-0.014
50	108	3.519	3.507	-0.012
50	109	3.539	3.531	-0.008
50	110	3.546	3.538	-0.008
50	111	3.524	3.517	-0.007
55	113	3.529	3.520	-0.009
55	114	3.504	3.499	-0.005
55	115	3.531	3.527	-0.004
55	116	3.524	3.515	-0.009
55	126	3.535	3.528	-0.007
55	128	3.523	3.520	-0.003
100	129	3.536	3.524	-0.012
100	131	3.518	3.508	-0.010
100	132	3.530	3.511	-0.019
100	133	3.521	3.509	-0.012
100	134	3.534	3.515	-0.019
100	135	3.520	3.501	-0.019
100	138	3.528	3.508	-0.020
100	139	3.527	3.512	-0.015
100	140	3.521	3.512	-0.009
100	141	3.528	3.506	-0.022
100	142	3.506	3.496	-0.010
100	143	3.526	3.511	-0.015
100	144	3.526	3.516	-0.010
100	220	3.528	3.507	-0.021
100	221	3.542	3.523	-0.019
100	224	3.513	3.497	-0.016
100	225	3.513	3.496	-0.017
100	226	3.541	3.528	-0.013
100	228	3.524	3.510	-0.014
100	232	3.525	3.504	-0.021
100	233	3.523	3.503	-0.020
100	236	3.516	3.503	-0.013
100	238	3.516	3.504	-0.012
100	243	3.519	3.508	-0.011
105	244	3.509	3.510	0.001
105	246	3.530	3.526	-0.004
105	248	3.526	3.519	-0.007
105	374	3.531	3.530	-0.001
105	376	3.519	3.512	-0.007
105	Max	3.546	3.538	0.007
105	Average	3.525	3.518	-0.007
105	Min	3.504	3.496	-0.022
105	Std Dev	0.009	0.010	0.007



6.40 UVLO_VLD	
Test Site	
Tester	
Test Number	
Max Limit	3.66 V
Min Limit	3.44 V

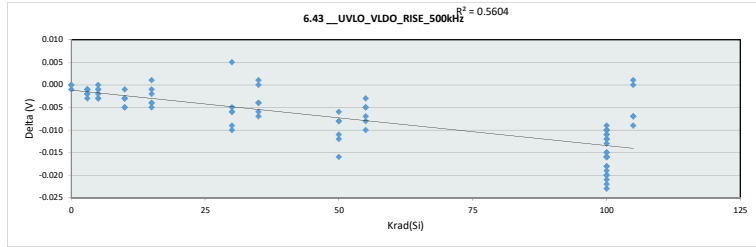
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.520	3.508	3.504	3.508	3.505	3.516	3.500	3.507	3.499	3.496	3.510
Average	3.528	3.524	3.522	3.522	3.522	3.520	3.521	3.523	3.518	3.509	3.519
Max	3.537	3.529	3.533	3.528	3.533	3.524	3.531	3.538	3.528	3.528	3.530
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



LDR TID Report  
TPS7H5001-SP

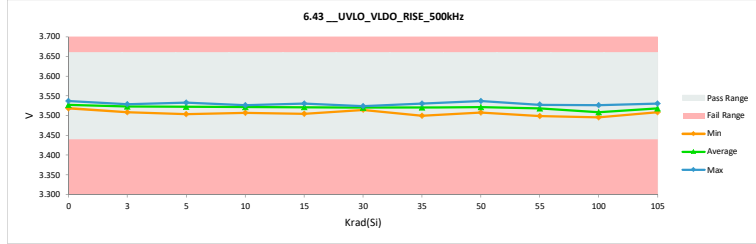
6.43 UVLO_VLDO_RISE_500kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.519	3.519	0.000
0	118	3.528	3.527	-0.001
0	122	3.537	3.537	0.000
3	61	3.525	3.523	-0.002
3	62	3.528	3.525	-0.003
3	63	3.510	3.509	-0.001
3	64	3.530	3.529	-0.001
3	65	3.528	3.526	-0.002
3	66	3.530	3.529	-0.001
5	67	3.535	3.533	-0.002
5	68	3.507	3.504	-0.003
5	69	3.532	3.529	-0.003
5	70	3.525	3.525	0.000
5	71	3.524	3.523	-0.001
5	72	3.522	3.521	-0.001
10	73	3.525	3.520	-0.005
10	74	3.528	3.525	-0.003
10	75	3.510	3.507	-0.003
10	76	3.530	3.525	-0.005
10	77	3.528	3.527	-0.001
10	78	3.530	3.527	-0.003
15	79	3.535	3.531	-0.004
15	80	3.507	3.505	-0.002
15	81	3.532	3.528	-0.004
15	82	3.525	3.524	-0.001
15	83	3.524	3.519	-0.005
15	84	3.522	3.523	0.001
30	86	3.525	3.516	-0.009
30	89	3.528	3.522	-0.006
30	91	3.510	3.515	0.005
30	92	3.530	3.520	-0.010
30	94	3.528	3.523	-0.005
30	95	3.530	3.524	-0.006
35	100	3.535	3.531	-0.004
35	101	3.507	3.500	-0.007
35	102	3.532	3.528	-0.004
35	103	3.525	3.525	0.000
35	104	3.524	3.518	-0.006
35	105	3.522	3.523	0.001
50	106	3.528	3.516	-0.012
50	107	3.535	3.519	-0.016
50	108	3.519	3.508	-0.011
50	109	3.539	3.531	-0.008
50	110	3.545	3.537	-0.008
50	111	3.525	3.519	-0.006
55	113	3.528	3.520	-0.008
55	114	3.504	3.499	-0.005
55	115	3.530	3.527	-0.003
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.525	3.520	-0.005
100	129	3.536	3.524	-0.012
100	131	3.518	3.508	-0.010
100	132	3.531	3.512	-0.019
100	133	3.520	3.509	-0.011
100	134	3.535	3.515	-0.020
100	135	3.520	3.502	-0.018
100	138	3.528	3.507	-0.021
100	139	3.527	3.511	-0.016
100	140	3.521	3.512	-0.009
100	141	3.528	3.505	-0.023
100	142	3.506	3.496	-0.010
100	143	3.526	3.511	-0.015
100	144	3.527	3.516	-0.011
100	220	3.529	3.507	-0.022
100	221	3.541	3.523	-0.018
100	224	3.513	3.497	-0.016
100	225	3.512	3.496	-0.016
100	226	3.542	3.527	-0.015
100	228	3.525	3.509	-0.016
100	232	3.524	3.504	-0.020
100	233	3.524	3.504	-0.020
100	236	3.516	3.503	-0.013
100	238	3.517	3.505	-0.012
100	243	3.518	3.508	-0.010
105	244	3.509	3.509	0.000
105	246	3.532	3.525	-0.007
105	248	3.526	3.519	-0.007
105	374	3.530	3.531	0.001
105	376	3.520	3.511	-0.009
105	Max	3.545	3.537	0.005
105	Average	3.525	3.518	-0.008
105	Min	3.504	3.496	-0.023
105	Std Dev	0.009	0.010	0.007



6.43 UVLO_VLD	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

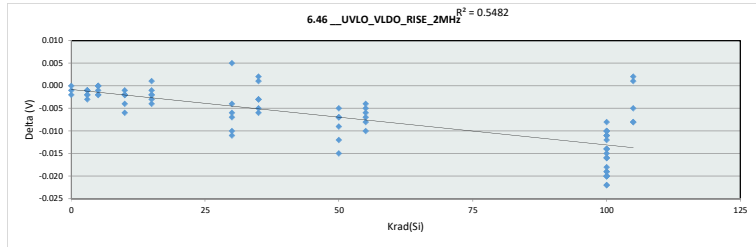
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.519	3.509	3.504	3.507	3.505	3.515	3.500	3.508	3.499	3.496	3.509
Average	3.528	3.524	3.523	3.522	3.522	3.520	3.521	3.522	3.518	3.509	3.519
Max	3.537	3.529	3.533	3.527	3.531	3.524	3.531	3.537	3.528	3.527	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



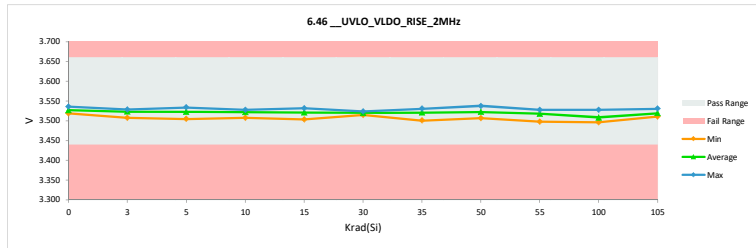
LDR TID Report  
TPS7H5001-SP

6.46 UVLO_VLDO_RISE_2MHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.66
Min Limit	3.44

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.520	3.519	-0.001
0	118	3.527	3.527	0.000
0	122	3.538	3.536	-0.002
3	61	3.526	3.523	-0.003
3	62	3.528	3.527	-0.001
3	63	3.510	3.508	-0.002
3	64	3.530	3.528	-0.002
3	65	3.528	3.527	-0.001
3	66	3.530	3.529	-0.001
5	67	3.534	3.534	0.000
5	68	3.507	3.505	-0.002
5	69	3.531	3.529	-0.002
5	70	3.524	3.524	0.000
5	71	3.523	3.522	-0.001
5	72	3.522	3.522	0.000
10	73	3.526	3.520	-0.006
10	74	3.528	3.526	-0.002
10	75	3.510	3.508	-0.002
10	76	3.530	3.526	-0.004
10	77	3.528	3.527	-0.001
10	78	3.530	3.528	-0.002
15	79	3.534	3.532	-0.002
15	80	3.507	3.504	-0.003
15	81	3.531	3.529	-0.002
15	82	3.524	3.523	-0.001
15	83	3.523	3.519	-0.004
15	84	3.522	3.523	0.001
30	86	3.526	3.516	-0.010
30	89	3.528	3.521	-0.007
30	91	3.510	3.515	0.005
30	92	3.530	3.519	-0.011
30	94	3.528	3.524	-0.004
30	95	3.530	3.524	-0.006
35	100	3.534	3.531	-0.003
35	101	3.507	3.501	-0.006
35	102	3.531	3.528	-0.003
35	103	3.524	3.525	0.001
35	104	3.523	3.518	-0.005
35	105	3.522	3.524	0.002
50	106	3.528	3.523	-0.005
50	107	3.534	3.519	-0.015
50	108	3.519	3.507	-0.012
50	109	3.540	3.531	-0.009
50	110	3.545	3.538	-0.007
50	111	3.526	3.519	-0.007
55	113	3.529	3.521	-0.008
55	114	3.504	3.498	-0.006
55	115	3.531	3.527	-0.004
55	116	3.526	3.516	-0.010
55	126	3.535	3.528	-0.007
55	128	3.524	3.519	-0.005
100	129	3.535	3.524	-0.011
100	131	3.517	3.509	-0.008
100	132	3.531	3.511	-0.020
100	133	3.521	3.510	-0.011
100	134	3.534	3.515	-0.019
100	135	3.520	3.502	-0.018
100	138	3.527	3.507	-0.020
100	139	3.526	3.512	-0.014
100	140	3.522	3.511	-0.011
100	141	3.528	3.506	-0.022
100	142	3.507	3.497	-0.010
100	143	3.526	3.510	-0.016
100	144	3.526	3.516	-0.010
100	220	3.529	3.507	-0.022
100	221	3.541	3.522	-0.019
100	224	3.514	3.498	-0.016
100	225	3.512	3.496	-0.016
100	226	3.542	3.528	-0.014
100	228	3.524	3.509	-0.015
100	232	3.525	3.505	-0.020
100	233	3.523	3.503	-0.020
100	236	3.516	3.502	-0.014
100	238	3.515	3.505	-0.010
100	243	3.520	3.508	-0.012
105	244	3.509	3.511	0.002
105	246	3.531	3.526	-0.005
105	248	3.527	3.519	-0.008
105	374	3.530	3.531	0.001
105	375	3.519	3.511	-0.008
105	Max	3.545	3.538	0.005
105	Average	3.525	3.518	-0.007
105	Min	3.504	3.496	-0.022
105	Std Dev	0.009	0.010	0.007



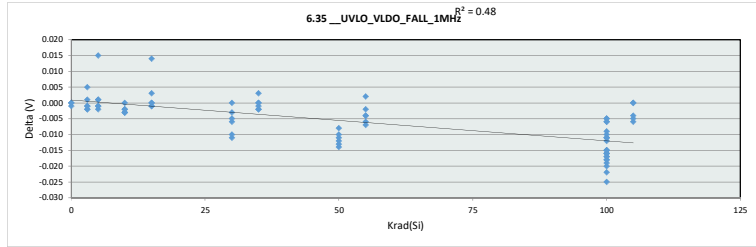
6.46 UVLO_VLD											
Test Site											
Tester											
Test Number											
Max Limit	3.66 V										
Min Limit	3.44 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440	3.440
Min	3.519	3.508	3.505	3.508	3.504	3.515	3.501	3.507	3.498	3.496	3.511
Average	3.527	3.524	3.523	3.523	3.522	3.520	3.521	3.523	3.518	3.509	3.520
Max	3.536	3.529	3.534	3.528	3.532	3.524	3.531	3.538	3.528	3.528	3.531
UL	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660	3.660



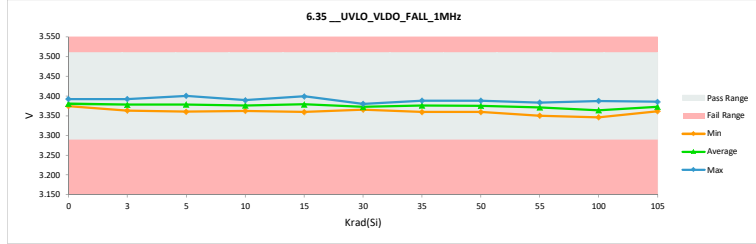
LDR TID Report  
TPS7H5001-SP

6.35 UVLO_VLDO_FALL_1MHz			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	3.51	3.51	
Min Limit	3.29	3.29	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.374	0.000
0	118	3.375	3.375	0.000
0	122	3.393	3.392	-0.001
3	61	3.373	3.378	0.005
3	62	3.377	3.376	-0.001
3	63	3.365	3.363	-0.002
3	64	3.381	3.379	-0.002
3	65	3.391	3.392	0.001
3	66	3.380	3.379	-0.001
5	67	3.385	3.400	0.015
5	68	3.359	3.360	0.001
5	69	3.390	3.388	-0.002
5	70	3.375	3.376	0.001
5	71	3.378	3.377	-0.001
5	72	3.370	3.369	-0.001
10	73	3.373	3.373	0.000
10	74	3.377	3.374	-0.003
10	75	3.365	3.362	-0.003
10	76	3.381	3.378	-0.003
10	77	3.391	3.389	-0.002
10	78	3.380	3.378	-0.002
15	79	3.385	3.399	0.014
15	80	3.359	3.359	0.000
15	81	3.390	3.389	-0.001
15	82	3.375	3.375	0.000
15	83	3.378	3.377	-0.001
15	84	3.370	3.373	0.003
30	86	3.373	3.367	-0.006
30	89	3.377	3.372	-0.005
30	91	3.365	3.365	0.000
30	92	3.381	3.371	-0.010
30	94	3.391	3.380	-0.011
30	95	3.380	3.377	-0.003
35	100	3.385	3.383	-0.002
35	101	3.359	3.359	0.000
35	102	3.390	3.388	-0.002
35	103	3.375	3.375	0.000
35	104	3.378	3.377	-0.001
35	105	3.370	3.373	0.003
50	106	3.391	3.377	-0.014
50	107	3.389	3.379	-0.010
50	108	3.371	3.359	-0.012
50	109	3.390	3.379	-0.011
50	110	3.396	3.388	-0.008
50	111	3.382	3.369	-0.013
55	113	3.377	3.375	-0.002
55	114	3.354	3.350	-0.004
55	115	3.379	3.381	0.002
55	116	3.373	3.367	-0.006
55	126	3.390	3.383	-0.007
55	128	3.372	3.368	-0.004
100	129	3.383	3.374	-0.009
100	131	3.381	3.359	-0.022
100	132	3.384	3.367	-0.017
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.372	-0.018
100	140	3.370	3.365	-0.005
100	141	3.377	3.357	-0.020
100	142	3.371	3.346	-0.025
100	143	3.375	3.360	-0.015
100	144	3.374	3.368	-0.006
100	220	3.379	3.369	-0.010
100	221	3.389	3.373	-0.016
100	224	3.367	3.348	-0.019
100	225	3.363	3.352	-0.011
100	226	3.392	3.387	-0.005
100	228	3.383	3.367	-0.016
100	232	3.378	3.360	-0.018
100	233	3.373	3.362	-0.011
100	236	3.385	3.369	-0.016
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.361	3.361	0.000
105	246	3.380	3.376	-0.004
105	248	3.375	3.370	-0.005
105	374	3.385	3.385	0.000
105	375	3.376	3.370	-0.006
105	Max	3.396	3.400	0.015
105	Average	3.378	3.372	-0.006
105	Min	3.354	3.346	-0.025
105	Std Dev	0.009	0.011	0.008



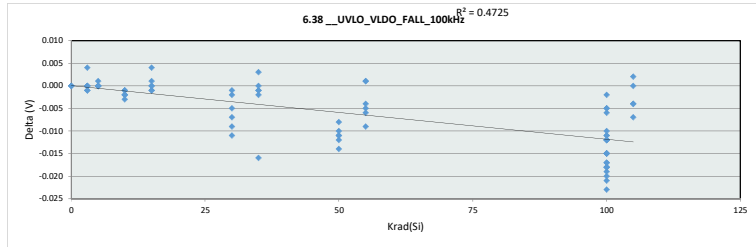
6.35 UVLO_VLD										
Test Site										
Tester										
Test Number										
Max Limit	3.51	V								
Min Limit	3.29	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.363	3.360	3.362	3.359	3.365	3.359	3.359	3.350	3.346
Average	3.380	3.378	3.378	3.376	3.379	3.372	3.376	3.375	3.371	3.364
Max	3.392	3.392	3.400	3.389	3.399	3.380	3.388	3.388	3.383	3.387
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



LDR TID Report  
TPS7H5001-SP

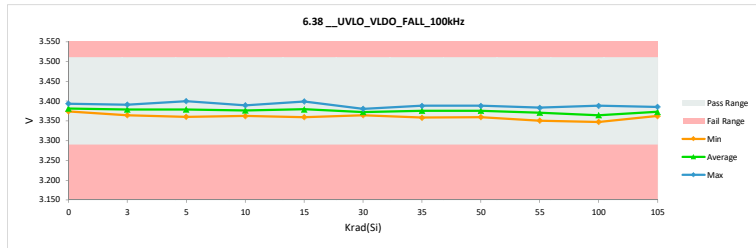
6.38 UVLO_VLDO_FALL_100kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.51
Min Limit	3.29

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.374	0.000
0	118	3.376	3.376	0.000
0	122	3.393	3.393	0.000
3	61	3.374	3.378	0.004
3	62	3.377	3.377	0.000
3	63	3.365	3.364	-0.001
3	64	3.381	3.380	-0.001
3	65	3.391	3.391	0.000
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.360	0.001
5	69	3.389	3.389	0.000
5	70	3.375	3.375	0.000
5	71	3.378	3.378	0.000
5	72	3.370	3.370	0.000
10	73	3.374	3.373	-0.001
10	74	3.377	3.375	-0.002
10	75	3.365	3.362	-0.003
10	76	3.381	3.379	-0.002
10	77	3.391	3.389	-0.002
10	78	3.380	3.379	-0.001
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.389	3.390	0.001
15	82	3.375	3.374	-0.001
15	83	3.378	3.378	0.000
15	84	3.370	3.374	0.004
30	86	3.374	3.367	-0.007
30	89	3.377	3.372	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.372	-0.009
30	94	3.391	3.380	-0.011
30	95	3.380	3.378	-0.002
35	100	3.400	3.384	-0.016
35	101	3.359	3.358	-0.001
35	102	3.389	3.388	-0.001
35	103	3.375	3.375	0.000
35	104	3.378	3.376	-0.002
35	105	3.370	3.373	0.003
50	106	3.391	3.377	-0.014
50	107	3.390	3.379	-0.011
50	108	3.370	3.359	-0.011
50	109	3.390	3.380	-0.010
50	110	3.396	3.388	-0.008
50	111	3.382	3.370	-0.012
55	113	3.376	3.377	0.001
55	114	3.355	3.350	-0.005
55	115	3.379	3.380	0.001
55	116	3.373	3.367	-0.006
55	126	3.392	3.383	-0.009
55	128	3.372	3.368	-0.004
100	129	3.384	3.373	-0.011
100	131	3.381	3.360	-0.021
100	132	3.385	3.368	-0.017
100	133	3.380	3.374	-0.006
100	134	3.382	3.364	-0.018
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.391	3.372	-0.019
100	140	3.369	3.367	-0.002
100	141	3.378	3.358	-0.020
100	142	3.370	3.347	-0.023
100	143	3.373	3.361	-0.012
100	144	3.373	3.368	-0.005
100	220	3.380	3.368	-0.012
100	221	3.389	3.374	-0.015
100	224	3.366	3.348	-0.018
100	225	3.364	3.352	-0.012
100	226	3.393	3.388	-0.005
100	228	3.384	3.367	-0.017
100	232	3.378	3.360	-0.018
100	233	3.373	3.363	-0.010
100	236	3.384	3.369	-0.015
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.362	0.002
105	246	3.380	3.376	-0.004
105	248	3.375	3.371	-0.004
105	374	3.385	3.385	0.000
105	375	3.376	3.369	-0.007
105	376	3.400	3.400	0.000
	Max	3.400	3.400	0.000
	Average	3.378	3.372	-0.006
	Min	3.355	3.347	-0.008
	Std Dev	0.010	0.011	0.007



6.38 UVLO_VLD	
Test Site	
Tester	
Test Number	
Max Limit	3.51
Min Limit	3.29
Unit	V

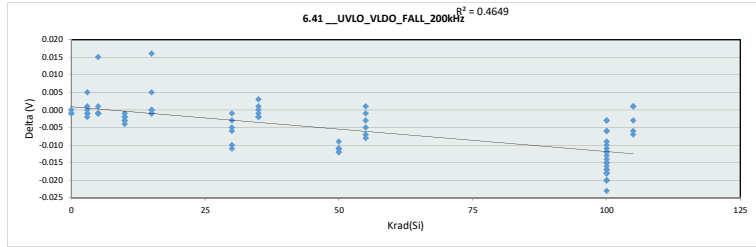
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.364	3.360	3.362	3.359	3.364	3.358	3.359	3.350	3.347	3.362
Average	3.381	3.378	3.379	3.376	3.379	3.372	3.376	3.376	3.371	3.364	3.373
Max	3.393	3.391	3.400	3.389	3.399	3.380	3.388	3.388	3.383	3.388	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



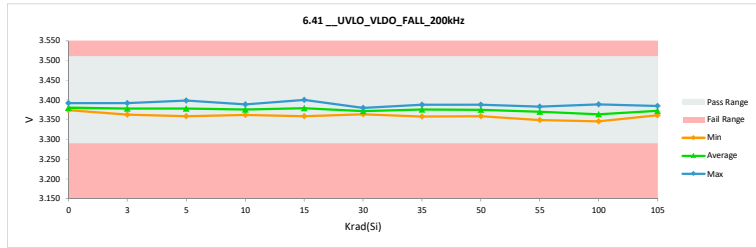
LDR TID Report  
TPS7H5001-SP

6.41 UVLO_VLDO_FALL_200kHz			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	3.51	3.51	
Min Limit	3.29	3.29	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.374	0.000
0	118	3.376	3.375	-0.001
0	122	3.393	3.392	-0.001
3	61	3.374	3.379	0.005
3	62	3.378	3.377	-0.001
3	63	3.365	3.363	-0.002
3	64	3.381	3.380	-0.001
3	65	3.391	3.392	0.001
3	66	3.380	3.380	0.000
5	67	3.384	3.399	0.015
5	68	3.360	3.359	-0.001
5	69	3.389	3.388	-0.001
5	70	3.375	3.374	-0.001
5	71	3.378	3.377	-0.001
5	72	3.370	3.371	0.001
10	73	3.374	3.372	-0.002
10	74	3.378	3.374	-0.004
10	75	3.365	3.362	-0.003
10	76	3.381	3.378	-0.003
10	77	3.391	3.389	-0.002
10	78	3.380	3.379	-0.001
15	79	3.384	3.400	0.016
15	80	3.360	3.359	-0.001
15	81	3.389	3.389	0.000
15	82	3.375	3.374	-0.001
15	83	3.378	3.378	0.000
15	84	3.370	3.375	0.005
30	86	3.374	3.368	-0.006
30	89	3.378	3.373	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.371	-0.010
30	94	3.391	3.380	-0.011
30	95	3.380	3.377	-0.003
35	100	3.384	3.384	0.000
35	101	3.360	3.358	-0.002
35	102	3.389	3.388	-0.001
35	103	3.375	3.376	0.001
35	104	3.378	3.376	-0.002
35	105	3.370	3.373	0.003
50	106	3.390	3.378	-0.012
50	107	3.390	3.379	-0.011
50	108	3.371	3.359	-0.012
50	109	3.390	3.379	-0.011
50	110	3.397	3.388	-0.009
50	111	3.381	3.370	-0.011
55	113	3.377	3.376	-0.001
55	114	3.354	3.349	-0.005
55	115	3.379	3.380	0.001
55	116	3.373	3.366	-0.007
55	126	3.391	3.383	-0.008
55	128	3.372	3.369	-0.003
100	129	3.384	3.375	-0.009
100	131	3.383	3.360	-0.023
100	132	3.385	3.367	-0.018
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.391	3.373	-0.018
100	140	3.369	3.366	-0.003
100	141	3.378	3.358	-0.020
100	142	3.366	3.346	-0.020
100	143	3.375	3.360	-0.015
100	144	3.374	3.368	-0.006
100	220	3.379	3.369	-0.010
100	221	3.387	3.373	-0.014
100	224	3.366	3.346	-0.020
100	225	3.364	3.351	-0.013
100	226	3.392	3.389	-0.003
100	228	3.384	3.367	-0.017
100	232	3.378	3.360	-0.018
100	233	3.374	3.362	-0.012
100	236	3.385	3.369	-0.016
100	238	3.369	3.357	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.361	0.001
105	246	3.380	3.377	-0.003
105	248	3.376	3.370	-0.006
105	374	3.384	3.385	0.001
105	376	3.376	3.369	-0.007
105	375	3.397	3.400	0.016
Average		3.378	3.372	-0.006
Min		3.354	3.346	-0.023
Std Dev		0.009	0.011	0.008



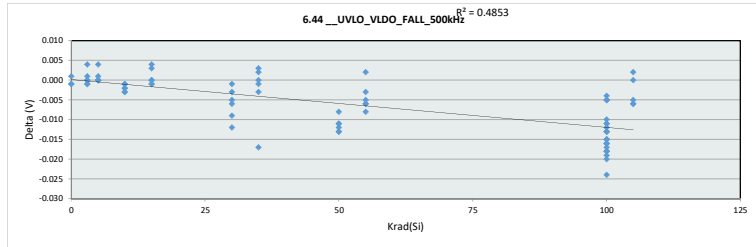
6.41 UVLO_VLD										
Test Site										
Tester										
Test Number										
Min Limit	3.51	V								
Max Limit	3.29	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.374	3.363	3.359	3.362	3.359	3.364	3.358	3.359	3.349	3.346
Average	3.380	3.379	3.378	3.376	3.379	3.372	3.376	3.376	3.371	3.364
Max	3.392	3.392	3.399	3.389	3.400	3.380	3.388	3.388	3.383	3.389
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



LDR TID Report  
TPS7H5001-SP

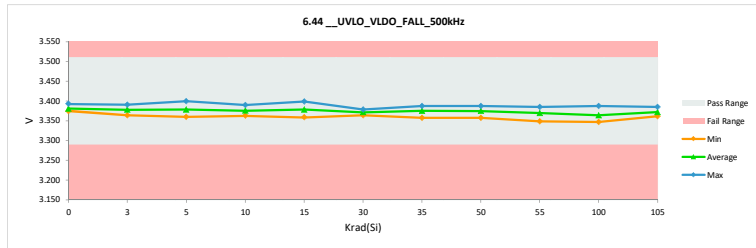
6.44 UVLO_VLDO_FALL_500kHz	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	3.51
Min Limit	3.29

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.375	0.001
0	118	3.376	3.375	-0.001
0	122	3.394	3.393	-0.001
3	61	3.374	3.378	0.004
3	62	3.377	3.378	0.001
3	63	3.365	3.364	-0.001
3	64	3.381	3.380	-0.001
3	65	3.391	3.391	0.000
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.360	0.001
5	69	3.385	3.389	0.004
5	70	3.375	3.375	0.000
5	71	3.378	3.378	0.000
5	72	3.371	3.371	0.000
10	73	3.374	3.373	-0.001
10	74	3.377	3.374	-0.003
10	75	3.365	3.363	-0.002
10	76	3.381	3.378	-0.003
10	77	3.391	3.390	-0.001
10	78	3.380	3.378	-0.002
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.385	3.388	0.003
15	82	3.375	3.375	0.000
15	83	3.378	3.377	-0.001
15	84	3.371	3.375	0.004
30	86	3.374	3.368	-0.006
30	89	3.377	3.372	-0.005
30	91	3.365	3.364	-0.001
30	92	3.381	3.372	-0.009
30	94	3.391	3.379	-0.012
30	95	3.380	3.377	-0.003
35	100	3.400	3.383	-0.017
35	101	3.359	3.358	-0.001
35	102	3.385	3.388	0.003
35	103	3.375	3.375	0.000
35	104	3.378	3.375	-0.003
35	105	3.371	3.373	0.002
50	106	3.391	3.378	-0.013
50	107	3.390	3.379	-0.011
50	108	3.371	3.358	-0.013
50	109	3.390	3.379	-0.011
50	110	3.396	3.388	-0.008
50	111	3.381	3.369	-0.012
55	113	3.376	3.371	-0.005
55	114	3.355	3.349	-0.006
55	115	3.379	3.381	0.002
55	116	3.374	3.366	-0.008
55	126	3.391	3.385	-0.006
55	128	3.372	3.369	-0.003
100	129	3.384	3.373	-0.011
100	131	3.383	3.359	-0.024
100	132	3.384	3.367	-0.017
100	133	3.379	3.374	-0.005
100	134	3.381	3.365	-0.016
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.372	-0.018
100	140	3.369	3.365	-0.004
100	141	3.377	3.357	-0.020
100	142	3.365	3.347	-0.018
100	143	3.374	3.361	-0.013
100	144	3.374	3.369	-0.005
100	220	3.380	3.369	-0.011
100	221	3.389	3.373	-0.016
100	224	3.367	3.348	-0.019
100	225	3.364	3.351	-0.013
100	226	3.393	3.388	-0.005
100	228	3.383	3.367	-0.016
100	232	3.378	3.360	-0.018
100	233	3.374	3.361	-0.013
100	236	3.384	3.369	-0.015
100	238	3.370	3.358	-0.012
100	243	3.367	3.357	-0.010
105	244	3.360	3.362	0.002
105	246	3.381	3.376	-0.005
105	248	3.376	3.370	-0.006
105	374	3.385	3.385	0.000
105	375	3.376	3.370	-0.006
105	Max	3.400	3.400	0.004
105	Average	3.378	3.372	-0.006
105	Min	3.355	3.347	-0.024
105	Std Dev	0.010	0.011	0.007



6.44 UVLO_VLD	
Test Site	
Tester	
Test Number	
Max Limit	3.51
Min Limit	3.29
Unit	V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.375	3.364	3.360	3.363	3.359	3.364	3.358	3.358	3.349	3.347	3.362
Average	3.381	3.379	3.379	3.376	3.379	3.372	3.375	3.375	3.370	3.364	3.373
Max	3.393	3.391	3.400	3.390	3.399	3.379	3.388	3.388	3.385	3.388	3.385
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510

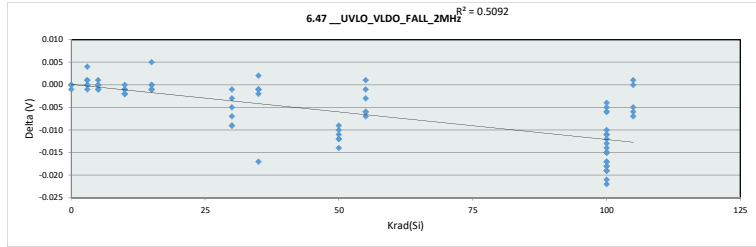




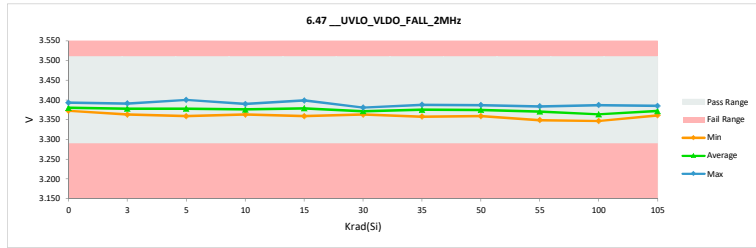
LDR TID Report  
TPS7H5001-SP

6.47 UVLO_VLDO_FALL_2MHz			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	3.51	3.51	
Min Limit	3.29	3.29	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	3.374	3.373	-0.001
0	118	3.375	3.375	0.000
0	122	3.393	3.393	0.000
3	61	3.374	3.378	0.004
3	62	3.376	3.377	0.001
3	63	3.364	3.363	-0.001
3	64	3.380	3.380	0.000
3	65	3.390	3.391	0.001
3	66	3.380	3.380	0.000
5	67	3.400	3.400	0.000
5	68	3.359	3.359	0.000
5	69	3.389	3.388	-0.001
5	70	3.376	3.375	-0.001
5	71	3.377	3.378	0.001
5	72	3.370	3.370	0.000
10	73	3.374	3.372	-0.002
10	74	3.376	3.375	-0.001
10	75	3.364	3.363	-0.001
10	76	3.380	3.378	-0.002
10	77	3.390	3.390	0.000
10	78	3.380	3.378	-0.002
15	79	3.400	3.399	-0.001
15	80	3.359	3.359	0.000
15	81	3.389	3.388	-0.001
15	82	3.376	3.375	-0.001
15	83	3.377	3.377	0.000
15	84	3.370	3.375	0.005
30	86	3.374	3.367	-0.007
30	89	3.376	3.371	-0.005
30	91	3.364	3.363	-0.001
30	92	3.380	3.371	-0.009
30	94	3.390	3.381	-0.009
30	95	3.380	3.377	-0.003
35	100	3.400	3.383	-0.017
35	101	3.359	3.358	-0.001
35	102	3.389	3.388	-0.001
35	103	3.376	3.374	-0.002
35	104	3.377	3.376	-0.001
35	105	3.370	3.372	0.002
50	106	3.391	3.377	-0.014
50	107	3.390	3.380	-0.010
50	108	3.371	3.359	-0.012
50	109	3.389	3.378	-0.011
50	110	3.396	3.387	-0.009
50	111	3.381	3.369	-0.012
55	113	3.377	3.376	-0.001
55	114	3.355	3.349	-0.006
55	115	3.379	3.380	0.001
55	116	3.373	3.367	-0.006
55	126	3.391	3.384	-0.007
55	128	3.371	3.368	-0.003
100	129	3.383	3.373	-0.010
100	131	3.382	3.360	-0.022
100	132	3.386	3.367	-0.019
100	133	3.380	3.374	-0.006
100	134	3.381	3.364	-0.017
100	135	3.375	3.360	-0.015
100	138	3.378	3.363	-0.015
100	139	3.390	3.371	-0.019
100	140	3.370	3.366	-0.004
100	141	3.378	3.357	-0.021
100	142	3.366	3.347	-0.019
100	143	3.374	3.360	-0.014
100	144	3.374	3.369	-0.005
100	220	3.379	3.368	-0.011
100	221	3.388	3.373	-0.015
100	224	3.365	3.347	-0.018
100	225	3.364	3.351	-0.013
100	226	3.393	3.387	-0.006
100	228	3.383	3.366	-0.017
100	232	3.378	3.360	-0.018
100	233	3.374	3.363	-0.011
100	236	3.385	3.370	-0.015
100	238	3.370	3.358	-0.012
100	243	3.368	3.357	-0.011
105	244	3.360	3.361	0.001
105	246	3.381	3.376	-0.005
105	248	3.376	3.370	-0.006
105	374	3.385	3.385	0.000
105	376	3.376	3.369	-0.007
105	Max	3.400	3.400	0.005
105	Average	3.378	3.372	-0.006
105	Min	3.355	3.347	-0.022
105	Std Dev	0.010	0.011	0.007



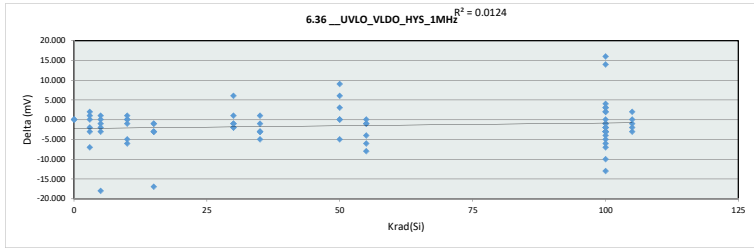
6.47 UVLO_VLD										
Test Site										
Tester										
Test Number										
Max Limit	3.51	V								
Min Limit	3.29	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290	3.290
Min	3.373	3.363	3.359	3.363	3.359	3.363	3.358	3.359	3.349	3.361
Average	3.380	3.378	3.378	3.376	3.379	3.372	3.375	3.375	3.371	3.364
Max	3.393	3.391	3.400	3.390	3.399	3.381	3.388	3.387	3.384	3.387
UL	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510	3.510



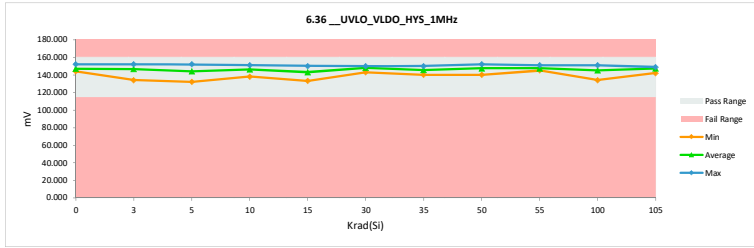
LDR TID Report  
TPS7H5001-SP

6.36 UVLO_VLDO_HYS_1MHz		
Test Site		
Tester		
Test Number		
Unit	mV	mV
Max Limit	160	160
Min Limit	115	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	145.000	145.000	0.000
0	118	152.000	152.000	0.000
0	122	144.000	144.000	0.000
3	61	152.000	145.000	-7.000
3	62	151.000	149.000	-2.000
3	63	151.000	152.000	1.000
3	64	148.000	150.000	2.000
3	65	137.000	134.000	-3.000
3	66	149.000	149.000	0.000
5	67	150.000	132.000	-18.000
5	68	147.000	145.000	-2.000
5	69	143.000	140.000	-3.000
5	70	149.000	149.000	0.000
5	71	145.000	146.000	1.000
5	72	153.000	152.000	-1.000
10	73	152.000	147.000	-5.000
10	74	151.000	151.000	0.000
10	75	151.000	145.000	-6.000
10	76	148.000	147.000	-1.000
10	77	137.000	138.000	1.000
10	78	149.000	149.000	0.000
15	79	150.000	133.000	-17.000
15	80	147.000	146.000	-1.000
15	81	143.000	140.000	-3.000
15	82	149.000	148.000	-1.000
15	83	145.000	142.000	-3.000
15	84	153.000	150.000	-3.000
30	86	152.000	150.000	-2.000
30	89	151.000	149.000	-2.000
30	91	151.000	150.000	-1.000
30	92	148.000	149.000	1.000
30	94	137.000	143.000	6.000
30	95	149.000	148.000	-1.000
35	100	150.000	149.000	-1.000
35	101	147.000	142.000	-5.000
35	102	143.000	140.000	-3.000
35	103	149.000	150.000	1.000
35	104	145.000	142.000	-3.000
35	105	153.000	150.000	-3.000
50	106	137.000	146.000	9.000
50	107	145.000	140.000	-5.000
50	108	149.000	149.000	0.000
50	109	149.000	152.000	3.000
50	110	149.000	149.000	0.000
50	111	143.000	149.000	6.000
55	113	153.000	145.000	-8.000
55	114	150.000	149.000	-1.000
55	115	152.000	146.000	-6.000
55	116	153.000	149.000	-4.000
55	126	145.000	145.000	0.000
55	128	152.000	151.000	-1.000
100	129	152.000	151.000	-1.000
100	131	136.000	150.000	14.000
100	132	147.000	145.000	-2.000
100	133	140.000	135.000	-5.000
100	134	154.000	151.000	-3.000
100	135	145.000	142.000	-3.000
100	138	149.000	145.000	-4.000
100	139	137.000	140.000	3.000
100	140	150.000	147.000	-3.000
100	141	151.000	149.000	-2.000
100	142	135.000	151.000	16.000
100	143	152.000	151.000	-1.000
100	144	153.000	147.000	-6.000
100	220	150.000	137.000	-13.000
100	221	152.000	150.000	-2.000
100	224	147.000	149.000	2.000
100	225	148.000	145.000	-3.000
100	226	149.000	142.000	-7.000
100	228	141.000	143.000	2.000
100	232	147.000	144.000	-3.000
100	233	150.000	140.000	-10.000
100	236	131.000	134.000	3.000
100	238	146.000	146.000	0.000
100	243	146.000	150.000	4.000
105	244	149.000	149.000	0.000
105	246	150.000	149.000	-1.000
105	248	152.000	149.000	-3.000
105	374	145.000	147.000	2.000
105	376	144.000	142.000	-2.000
105	Max	154.000	152.000	16.000
	Average	147.388	145.900	-1.488
	Min	131.000	132.000	-18.000
	Std Dev	4.982	4.864	4.963



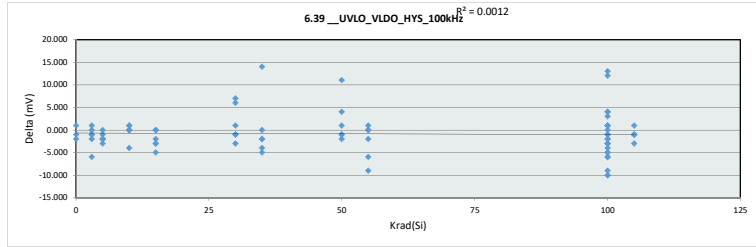
6.36 UVLO_VLD																		
Test Site																		
Tester																		
Test Number																		
Max Limit	160	mV																
Min Limit	115	mV																
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105							
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000							
Min	144.000	134.000	132.000	138.000	133.000	143.000	140.000	145.000	134.000	142.000								
Average	147.000	146.500	144.000	146.167	143.167	148.167	145.500	147.500	145.167	147.200								
Max	152.000	152.000	152.000	151.000	150.000	150.000	150.000	152.000	151.000	149.000								
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000								



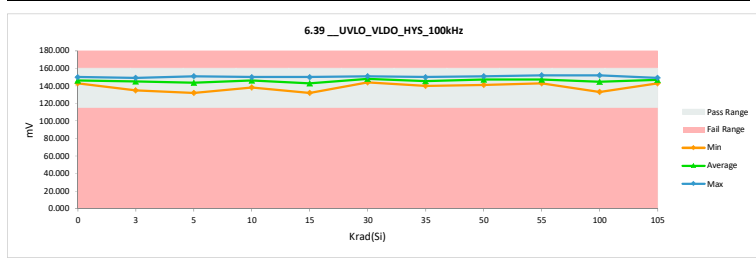
LDR TID Report  
TPS7H5001-SP

6.39 UVLO_VLDO_HYS_100kHz		
Test Site		
Tester		
Test Number		
Unit	mV	mV
Max Limit	160	160
Min Limit	115	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	145.000	146.000	1.000
0	118	151.000	150.000	-1.000
0	122	145.000	143.000	-2.000
3	61	151.000	145.000	-6.000
3	62	150.000	149.000	-1.000
3	63	145.000	144.000	-1.000
3	64	147.000	148.000	1.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	134.000	132.000	-2.000
5	68	147.000	144.000	-3.000
5	69	142.000	140.000	-2.000
5	70	150.000	150.000	0.000
5	71	146.000	144.000	-2.000
5	72	152.000	151.000	-1.000
10	73	151.000	147.000	-4.000
10	74	150.000	150.000	0.000
10	75	145.000	146.000	1.000
10	76	147.000	147.000	0.000
10	77	137.000	138.000	1.000
10	78	149.000	149.000	0.000
15	79	134.000	132.000	-2.000
15	80	147.000	147.000	0.000
15	81	142.000	139.000	-3.000
15	82	150.000	150.000	0.000
15	83	146.000	141.000	-5.000
15	84	152.000	149.000	-3.000
30	86	151.000	150.000	-1.000
30	89	150.000	149.000	-1.000
30	91	145.000	151.000	6.000
30	92	147.000	148.000	1.000
30	94	137.000	144.000	7.000
30	95	149.000	146.000	-3.000
35	100	134.000	148.000	14.000
35	101	147.000	142.000	-5.000
35	102	142.000	140.000	-2.000
35	103	150.000	150.000	0.000
35	104	146.000	142.000	-4.000
35	105	152.000	150.000	-2.000
50	106	136.000	147.000	11.000
50	107	143.000	141.000	-2.000
50	108	149.000	148.000	-1.000
50	109	150.000	151.000	1.000
50	110	150.000	149.000	-1.000
50	111	143.000	147.000	4.000
55	113	152.000	143.000	-9.000
55	114	149.000	149.000	0.000
55	115	152.000	146.000	-6.000
55	116	152.000	150.000	-2.000
55	126	143.000	144.000	1.000
55	128	152.000	152.000	0.000
100	129	152.000	152.000	0.000
100	131	137.000	149.000	12.000
100	132	146.000	144.000	-2.000
100	133	140.000	135.000	-5.000
100	134	152.000	151.000	-1.000
100	135	145.000	142.000	-3.000
100	138	150.000	144.000	-6.000
100	139	136.000	140.000	4.000
100	140	151.000	145.000	-6.000
100	141	150.000	148.000	-2.000
100	142	136.000	149.000	13.000
100	143	153.000	150.000	-3.000
100	144	153.000	148.000	-5.000
100	220	149.000	139.000	-10.000
100	221	151.000	150.000	-1.000
100	224	148.000	149.000	1.000
100	225	149.000	145.000	-4.000
100	226	149.000	140.000	-9.000
100	228	140.000	143.000	3.000
100	232	147.000	144.000	-3.000
100	233	150.000	140.000	-10.000
100	236	132.000	133.000	1.000
100	238	146.000	147.000	1.000
100	243	146.000	150.000	4.000
105	244	149.000	148.000	-1.000
105	246	150.000	149.000	-1.000
105	248	151.000	148.000	-3.000
105	374	146.000	147.000	1.000
105	376	144.000	143.000	-1.000
105	Max	153.000	152.000	14.000
105	Average	146.375	145.550	-0.825
105	Min	132.000	132.000	-10.000
105	Std Dev	5.371	4.752	4.380



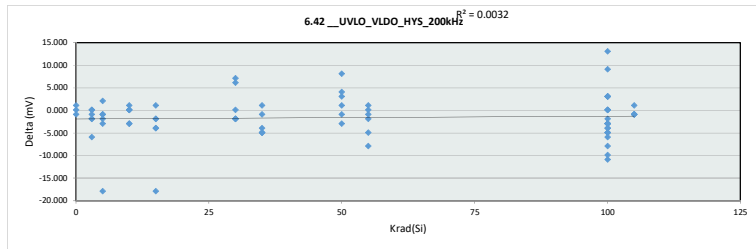
6.39 UVLO_VLD		
Test Site		
Tester		
Test Number		
Max Limit	160	mV
Min Limit	115	mV



LDR TID Report  
TPS7H5001-SP

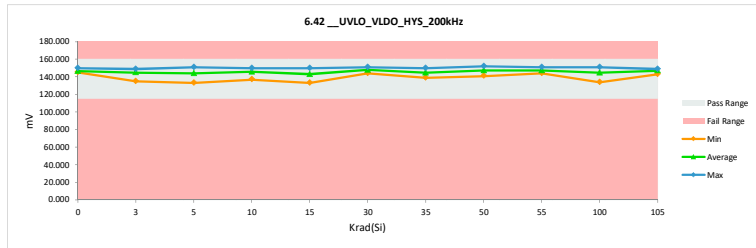
6.42 UVLO_VLDO_HYS_200kHz		
Test Site		
Tester		
Test Number		
Unit	mV	mV
Max Limit	160	160
Min Limit	115	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	145.000	145.000	0.000
0	118	151.000	150.000	-1.000
0	122	144.000	145.000	1.000
3	61	151.000	145.000	-6.000
3	62	150.000	149.000	-1.000
3	63	145.000	145.000	0.000
3	64	149.000	147.000	-2.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	151.000	133.000	-18.000
5	68	147.000	145.000	-2.000
5	69	144.000	141.000	-3.000
5	70	149.000	151.000	2.000
5	71	146.000	145.000	-1.000
5	72	151.000	150.000	-1.000
10	73	151.000	148.000	-3.000
10	74	150.000	150.000	0.000
10	75	145.000	146.000	1.000
10	76	149.000	146.000	-3.000
10	77	137.000	137.000	0.000
10	78	149.000	149.000	0.000
15	79	151.000	133.000	-18.000
15	80	147.000	145.000	-2.000
15	81	144.000	140.000	-4.000
15	82	149.000	150.000	1.000
15	83	146.000	142.000	-4.000
15	84	151.000	149.000	-2.000
30	86	151.000	149.000	-2.000
30	89	150.000	148.000	-2.000
30	91	145.000	151.000	6.000
30	92	149.000	149.000	0.000
30	94	137.000	144.000	7.000
30	95	149.000	147.000	-2.000
35	100	151.000	147.000	-4.000
35	101	147.000	142.000	-5.000
35	102	144.000	139.000	-5.000
35	103	149.000	150.000	1.000
35	104	146.000	141.000	-5.000
35	105	151.000	150.000	-1.000
50	106	138.000	146.000	8.000
50	107	144.000	141.000	-3.000
50	108	149.000	148.000	-1.000
50	109	149.000	152.000	3.000
50	110	149.000	150.000	1.000
50	111	143.000	147.000	4.000
55	113	152.000	144.000	-8.000
55	114	150.000	149.000	-1.000
55	115	152.000	147.000	-5.000
55	116	151.000	149.000	-2.000
55	126	144.000	145.000	1.000
55	128	151.000	151.000	0.000
100	129	153.000	150.000	-3.000
100	131	135.000	148.000	13.000
100	132	146.000	144.000	-2.000
100	133	140.000	135.000	-5.000
100	134	153.000	150.000	-3.000
100	135	145.000	142.000	-3.000
100	138	150.000	145.000	-5.000
100	139	136.000	139.000	3.000
100	140	152.000	146.000	-6.000
100	141	151.000	148.000	-3.000
100	142	140.000	149.000	9.000
100	143	151.000	151.000	0.000
100	144	152.000	148.000	-4.000
100	220	149.000	138.000	-11.000
100	221	155.000	150.000	-5.000
100	224	148.000	151.000	3.000
100	225	149.000	145.000	-4.000
100	226	149.000	139.000	-10.000
100	228	140.000	143.000	3.000
100	232	147.000	144.000	-3.000
100	233	149.000	141.000	-8.000
100	236	131.000	134.000	3.000
100	238	147.000	147.000	0.000
100	243	151.000	151.000	0.000
105	244	148.000	149.000	1.000
105	246	150.000	149.000	-1.000
105	248	150.000	149.000	-1.000
105	374	147.000	146.000	-1.000
105	376	144.000	143.000	-1.000
105		155.000	152.000	13.000
	Max	155.000	152.000	13.000
	Average	147.213	145.625	-1.588
	Min	131.000	133.000	-18.000
	Std Dev	4.711	4.662	4.673



6.42 UVLO_VLD		
Test Site		
Tester		
Test Number		
Max Limit	160	mV
Min Limit	115	mV

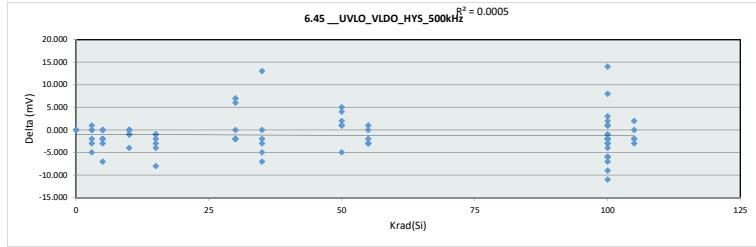
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	145.000	135.000	133.000	137.000	133.000	144.000	139.000	141.000	144.000	134.000	143.000
Average	146.667	145.000	144.167	146.000	143.167	148.000	144.833	147.333	147.500	144.917	147.200
Max	150.000	149.000	151.000	150.000	150.000	151.000	150.000	152.000	151.000	151.000	149.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



# LDR TID Report TPS7H5001-SP

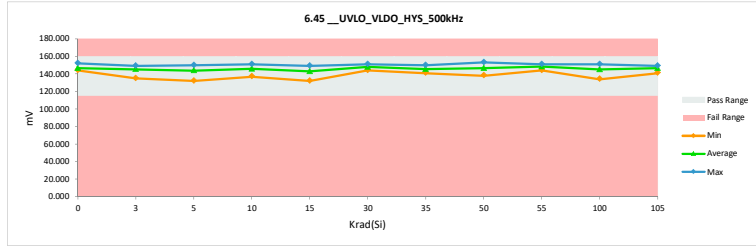
6.45 UVLO_VLDO_HYS_500kHz	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	160
Min Limit	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	144.000	144.000	0.000
0	118	152.000	152.000	0.000
0	122	144.000	144.000	0.000
3	61	151.000	146.000	-5.000
3	62	151.000	148.000	-3.000
3	63	145.000	145.000	0.000
3	64	148.000	149.000	1.000
3	65	137.000	135.000	-2.000
3	66	149.000	149.000	0.000
5	67	135.000	132.000	-3.000
5	68	147.000	145.000	-2.000
5	69	148.000	141.000	-7.000
5	70	150.000	150.000	0.000
5	71	145.000	145.000	0.000
5	72	152.000	150.000	-2.000
10	73	151.000	147.000	-4.000
10	74	151.000	151.000	0.000
10	75	145.000	144.000	-1.000
10	76	148.000	147.000	-1.000
10	77	137.000	137.000	0.000
10	78	149.000	149.000	0.000
15	79	135.000	132.000	-3.000
15	80	147.000	146.000	-1.000
15	81	148.000	140.000	-8.000
15	82	150.000	149.000	-1.000
15	83	145.000	143.000	-2.000
15	84	152.000	148.000	-4.000
30	86	151.000	149.000	-2.000
30	89	151.000	149.000	-2.000
30	91	145.000	151.000	6.000
30	92	148.000	148.000	0.000
30	94	137.000	144.000	7.000
30	95	149.000	147.000	-2.000
35	100	135.000	148.000	13.000
35	101	147.000	142.000	-5.000
35	102	148.000	141.000	-7.000
35	103	150.000	150.000	0.000
35	104	145.000	142.000	-3.000
35	105	152.000	150.000	-2.000
50	106	137.000	138.000	1.000
50	107	145.000	140.000	-5.000
50	108	148.000	150.000	2.000
50	109	149.000	153.000	4.000
50	110	149.000	150.000	1.000
50	111	144.000	149.000	5.000
55	113	152.000	149.000	-3.000
55	114	150.000	150.000	0.000
55	115	150.000	147.000	-3.000
55	116	153.000	150.000	-3.000
55	126	143.000	144.000	1.000
55	128	153.000	151.000	-2.000
100	129	152.000	151.000	-1.000
100	131	135.000	149.000	14.000
100	132	147.000	145.000	-2.000
100	133	141.000	135.000	-6.000
100	134	153.000	150.000	-3.000
100	135	145.000	142.000	-3.000
100	138	150.000	144.000	-6.000
100	139	138.000	139.000	1.000
100	140	152.000	148.000	-4.000
100	141	151.000	149.000	-2.000
100	142	141.000	149.000	8.000
100	143	152.000	150.000	-2.000
100	144	153.000	147.000	-6.000
100	220	149.000	138.000	-11.000
100	221	152.000	150.000	-2.000
100	224	147.000	150.000	3.000
100	225	148.000	145.000	-3.000
100	226	149.000	140.000	-9.000
100	228	142.000	143.000	1.000
100	232	146.000	144.000	-2.000
100	233	150.000	143.000	-7.000
100	236	132.000	134.000	2.000
100	238	147.000	148.000	1.000
100	243	151.000	150.000	-1.000
105	244	149.000	147.000	-2.000
105	246	151.000	149.000	-2.000
105	248	149.000	149.000	0.000
105	374	145.000	147.000	2.000
105	376	144.000	141.000	-3.000
105	Max	153.000	153.000	14.000
105	Average	146.850	145.700	-1.150
105	Min	132.000	132.000	-11.000
105	Std Dev	5.139	4.853	4.041



6.45 UVLO_VLD	
Test Site	
Tester	
Test Number	
Max Limit	160 mV
Min Limit	115 mV

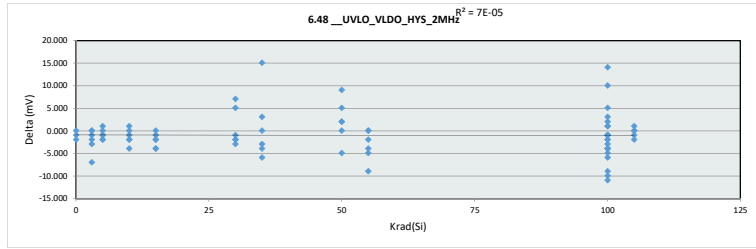
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	144.000	135.000	132.000	137.000	132.000	144.000	141.000	138.000	144.000	134.000	141.000
Average	146.667	145.333	143.833	145.833	143.000	148.000	145.500	146.667	148.500	145.125	146.600
Max	152.000	149.000	150.000	151.000	149.000	151.000	150.000	153.000	151.000	151.000	149.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



LDR TID Report  
TPS7H5001-SP

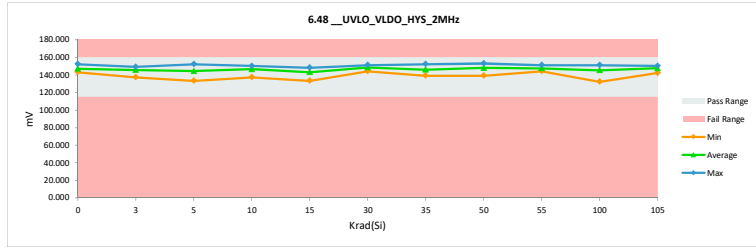
6.48 UVLO_VLDO_HYS_2MHz		
Test Site		
Tester		
Test Number		
Unit	mV	mV
Max Limit	160	160
Min Limit	115	115

Krad(Si)	Serial #	PRE	POST	Delta
0	117	146.000	146.000	0.000
0	118	153.000	152.000	-1.000
0	122	145.000	145.000	-2.000
3	61	152.000	145.000	-7.000
3	62	152.000	149.000	-3.000
3	63	146.000	145.000	-1.000
3	64	150.000	148.000	-2.000
3	65	137.000	137.000	0.000
3	66	149.000	149.000	0.000
5	67	134.000	133.000	-1.000
5	68	148.000	146.000	-2.000
5	69	142.000	141.000	-1.000
5	70	148.000	149.000	1.000
5	71	146.000	144.000	-2.000
5	72	152.000	152.000	0.000
10	73	152.000	148.000	-4.000
10	74	152.000	150.000	-2.000
10	75	146.000	145.000	-1.000
10	76	150.000	148.000	-2.000
10	77	137.000	137.000	0.000
10	78	149.000	150.000	1.000
15	79	134.000	133.000	-1.000
15	80	148.000	146.000	-2.000
15	81	142.000	140.000	-2.000
15	82	148.000	148.000	0.000
15	83	146.000	142.000	-4.000
15	84	152.000	148.000	-4.000
30	86	152.000	149.000	-3.000
30	89	152.000	150.000	-2.000
30	91	146.000	151.000	5.000
30	92	150.000	148.000	-2.000
30	94	137.000	144.000	7.000
30	95	149.000	148.000	-1.000
35	100	134.000	149.000	15.000
35	101	148.000	142.000	-6.000
35	102	142.000	139.000	-3.000
35	103	148.000	151.000	3.000
35	104	146.000	142.000	-4.000
35	105	152.000	152.000	0.000
50	106	137.000	146.000	9.000
50	107	144.000	139.000	-5.000
50	108	148.000	148.000	0.000
50	109	151.000	153.000	2.000
50	110	149.000	151.000	2.000
50	111	145.000	150.000	5.000
55	113	153.000	144.000	-9.000
55	114	149.000	149.000	0.000
55	115	152.000	147.000	-5.000
55	116	153.000	149.000	-4.000
55	126	144.000	144.000	0.000
55	128	153.000	151.000	-2.000
100	129	152.000	151.000	-1.000
100	131	135.000	149.000	14.000
100	132	145.000	144.000	-1.000
100	133	140.000	136.000	-4.000
100	134	152.000	151.000	-1.000
100	135	145.000	142.000	-3.000
100	138	148.000	144.000	-4.000
100	139	136.000	141.000	5.000
100	140	152.000	146.000	-6.000
100	141	150.000	149.000	-1.000
100	142	141.000	151.000	10.000
100	143	152.000	150.000	-2.000
100	144	152.000	147.000	-5.000
100	220	150.000	139.000	-11.000
100	221	153.000	149.000	-4.000
100	224	149.000	151.000	2.000
100	225	149.000	145.000	-4.000
100	226	149.000	140.000	-9.000
100	228	140.000	143.000	3.000
100	232	147.000	145.000	-2.000
100	233	150.000	140.000	-10.000
100	236	131.000	132.000	1.000
100	238	146.000	147.000	1.000
100	243	152.000	151.000	-1.000
105	244	149.000	150.000	1.000
105	246	150.000	150.000	0.000
105	248	151.000	149.000	-2.000
105	374	146.000	146.000	0.000
105	376	143.000	142.000	-1.000
105	Max	153.000	153.000	15.000
105	Average	146.813	145.875	-0.938
105	Min	131.000	132.000	-11.000
105	Std Dev	5.526	4.845	4.385



6.48 UVLO_VLD		
Test Site		
Tester		
Test Number		
Max Limit	160	mV
Min Limit	115	mV

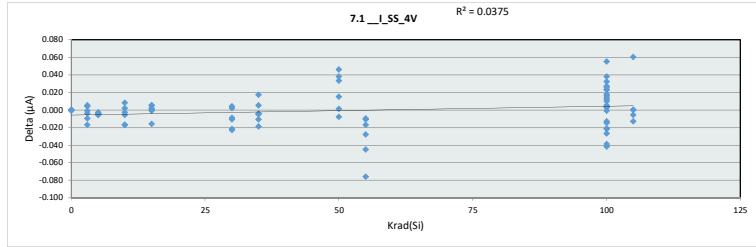
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000
Min	143.000	137.000	133.000	137.000	133.000	144.000	139.000	139.000	144.000	132.000	142.000
Average	147.000	145.500	144.167	146.333	142.833	148.333	145.833	147.833	147.333	145.125	147.400
Max	152.000	149.000	152.000	150.000	148.000	151.000	152.000	153.000	151.000	151.000	150.000
UL	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000	160.000



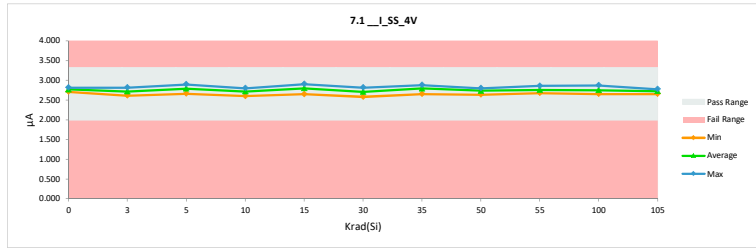
LDR TID Report  
TPS7H5001-SP

7.1_I_SS_4V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	3.32
Min Limit	1.98

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.702	2.702	0.000
0	118	2.786	2.785	-0.001
0	122	2.808	2.808	0.000
3	61	2.747	2.730	-0.017
3	62	2.811	2.810	-0.001
3	63	2.697	2.702	0.005
3	64	2.784	2.780	-0.004
3	65	2.661	2.651	-0.010
3	66	2.601	2.605	0.004
5	67	2.692	2.687	-0.005
5	68	2.837	2.833	-0.004
5	69	2.660	2.657	-0.003
5	70	2.897	2.893	-0.004
5	71	2.834	2.829	-0.005
5	72	2.832	2.826	-0.006
10	73	2.747	2.730	-0.017
10	74	2.811	2.794	-0.017
10	75	2.697	2.699	0.002
10	76	2.784	2.792	0.008
10	77	2.661	2.658	-0.003
10	78	2.601	2.595	-0.006
15	79	2.692	2.697	0.005
15	80	2.837	2.836	-0.001
15	81	2.660	2.644	-0.016
15	82	2.897	2.902	0.005
15	83	2.834	2.834	0.000
15	84	2.832	2.834	0.002
30	86	2.747	2.751	0.004
30	89	2.811	2.813	0.002
30	91	2.697	2.688	-0.009
30	92	2.784	2.761	-0.023
30	94	2.661	2.650	-0.011
30	95	2.601	2.579	-0.022
35	100	2.692	2.687	-0.005
35	101	2.837	2.854	0.017
35	102	2.660	2.649	-0.011
35	103	2.897	2.878	-0.019
35	104	2.834	2.839	0.005
35	105	2.832	2.828	-0.004
50	106	2.705	2.720	0.015
50	107	2.755	2.793	0.038
50	108	2.671	2.717	0.046
50	109	2.721	2.754	0.033
50	110	2.779	2.780	0.001
50	111	2.643	2.635	-0.008
55	113	2.790	2.745	-0.045
55	114	2.758	2.741	-0.017
55	115	2.694	2.683	-0.011
55	116	2.851	2.823	-0.028
55	126	2.864	2.854	-0.010
55	128	2.749	2.673	-0.076
100	129	2.794	2.821	0.027
100	131	2.745	2.703	-0.042
100	132	2.761	2.748	-0.013
100	133	2.843	2.857	0.014
100	134	2.741	2.753	0.012
100	135	2.699	2.660	-0.039
100	138	2.739	2.762	0.023
100	139	2.652	2.670	0.018
100	140	2.832	2.835	0.003
100	141	2.698	2.677	-0.021
100	142	2.821	2.837	0.016
100	143	2.760	2.765	0.005
100	144	2.712	2.716	0.004
100	220	2.772	2.757	-0.015
100	221	2.691	2.690	-0.001
100	224	2.734	2.760	0.026
100	225	2.636	2.646	0.010
100	226	2.829	2.867	0.038
100	228	2.721	2.776	0.055
100	232	2.751	2.783	0.032
100	233	2.780	2.753	-0.027
100	236	2.661	2.664	0.003
100	238	2.702	2.725	0.023
100	243	2.713	2.691	-0.022
105	244	2.767	2.761	-0.006
105	246	2.680	2.740	0.060
105	248	2.645	2.645	0.000
105	374	2.767	2.767	0.000
105	376	2.701	2.688	-0.013
105	Max	2.897	2.902	0.006
105	Average	2.747	2.747	-0.001
105	Min	2.601	2.579	-0.076
105	Std Dev	0.074	0.076	0.021



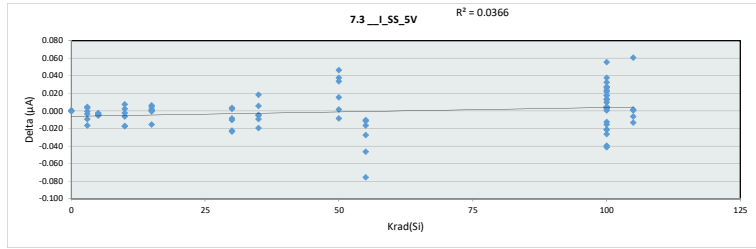
7.1_I_SS_4V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.702	2.605	2.657	2.595	2.644	2.579	2.649	2.635	2.673	2.646	2.645
Average	2.765	2.713	2.788	2.711	2.791	2.707	2.789	2.733	2.753	2.747	2.720
Max	2.808	2.810	2.893	2.794	2.902	2.813	2.878	2.793	2.854	2.867	2.767
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320



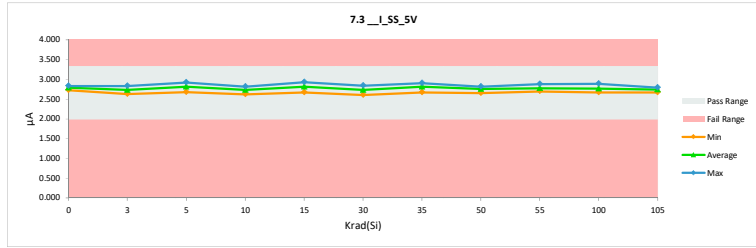
LDR TID Report  
TPS7H5001-SP

7.3_I_SS_5V		
Test Site		
Tester		
Test Number		
Unit	µA	µA
Max Limit	3.32	3.32
Min Limit	1.98	1.98

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.724	2.724	0.000
0	118	2.808	2.808	0.000
0	122	2.831	2.830	-0.001
3	61	2.770	2.753	-0.017
3	62	2.834	2.833	-0.001
3	63	2.719	2.723	0.004
3	64	2.807	2.803	-0.004
3	65	2.682	2.672	-0.010
3	66	2.623	2.626	0.003
5	67	2.715	2.710	-0.005
5	68	2.859	2.855	-0.004
5	69	2.681	2.678	-0.003
5	70	2.920	2.916	-0.004
5	71	2.857	2.852	-0.005
5	72	2.856	2.850	-0.006
10	73	2.770	2.752	-0.018
10	74	2.834	2.816	-0.018
10	75	2.980	2.721	-0.002
10	76	2.807	2.814	0.007
10	77	2.682	2.679	-0.003
10	78	2.623	2.616	-0.007
15	79	2.715	2.721	0.006
15	80	2.859	2.858	-0.001
15	81	2.681	2.665	-0.016
15	82	2.920	2.924	0.004
15	83	2.857	2.858	0.001
15	84	2.856	2.856	0.000
30	86	2.770	2.773	0.003
30	89	2.834	2.836	0.002
30	91	2.719	2.710	-0.009
30	92	2.807	2.783	-0.024
30	94	2.682	2.671	-0.011
30	95	2.623	2.600	-0.023
35	100	2.715	2.710	-0.005
35	101	2.859	2.877	0.018
35	102	2.681	2.671	-0.010
35	103	2.920	2.900	-0.020
35	104	2.857	2.862	0.005
35	105	2.856	2.850	-0.006
50	106	2.728	2.743	0.015
50	107	2.778	2.815	0.037
50	108	2.693	2.739	0.046
50	109	2.743	2.776	0.033
50	110	2.802	2.803	0.001
50	111	2.664	2.655	-0.009
55	113	2.813	2.766	-0.047
55	114	2.779	2.762	-0.017
55	115	2.716	2.704	-0.012
55	116	2.873	2.845	-0.028
55	126	2.887	2.876	-0.011
55	128	2.772	2.696	-0.076
100	129	2.816	2.843	0.027
100	131	2.768	2.726	-0.042
100	132	2.782	2.769	-0.013
100	133	2.866	2.879	0.013
100	134	2.763	2.775	0.012
100	135	2.722	2.682	-0.040
100	138	2.762	2.783	0.021
100	139	2.673	2.690	0.017
100	140	2.854	2.856	0.002
100	141	2.720	2.698	-0.022
100	142	2.844	2.861	0.017
100	143	2.781	2.785	0.004
100	144	2.734	2.737	0.003
100	220	2.794	2.778	-0.016
100	221	2.711	2.711	0.000
100	224	2.756	2.782	0.026
100	225	2.656	2.665	0.009
100	226	2.852	2.889	0.037
100	228	2.742	2.797	0.055
100	232	2.774	2.806	0.032
100	233	2.802	2.775	-0.027
100	236	2.681	2.685	0.004
100	238	2.723	2.746	0.023
100	243	2.734	2.712	-0.022
105	244	2.790	2.783	-0.007
105	246	2.701	2.761	0.060
105	248	2.665	2.665	0.000
105	374	2.788	2.789	0.001
105	376	2.725	2.711	-0.014
105	Max	2.920	2.924	0.060
105	Average	2.769	2.768	-0.001
105	Min	2.623	2.600	-0.076
105	Std Dev	0.074	0.076	0.021



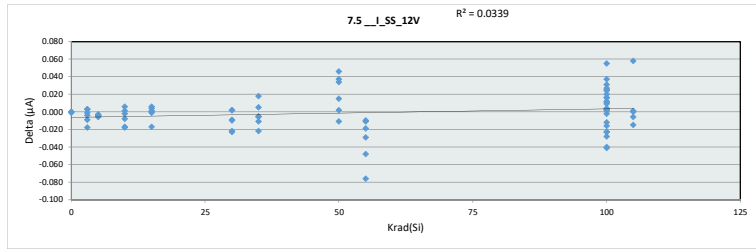
7.3_I_SS_5V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.724	2.626	2.678	2.616	2.665	2.600	2.671	2.655	2.696	2.665	2.665
Average	2.787	2.735	2.810	2.733	2.814	2.729	2.812	2.755	2.775	2.768	2.742
Max	2.830	2.833	2.916	2.816	2.924	2.836	2.900	2.815	2.876	2.889	2.789
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320



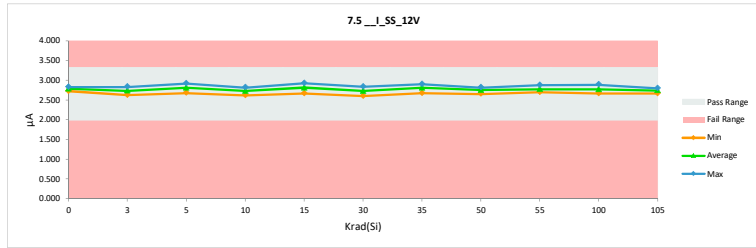


LDR TID Report  
TPS7H5001-SP

7.5_I_SS_12V				
Test Site				
Tester				
Test Number				
Unit		µA	µA	
Max Limit		3.32	3.32	
Min Limit		1.98	1.98	
Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.725	2.725	0.000
0	118	2.811	2.810	-0.001
0	122	2.831	2.831	0.000
3	61	2.772	2.754	-0.018
3	62	2.834	2.833	-0.001
3	63	2.720	2.723	0.003
3	64	2.808	2.804	-0.004
3	65	2.683	2.674	-0.009
3	66	2.624	2.627	0.003
5	67	2.716	2.711	-0.005
5	68	2.859	2.855	-0.004
5	69	2.683	2.679	-0.004
5	70	2.921	2.918	-0.003
5	71	2.858	2.853	-0.005
5	72	2.856	2.850	-0.006
10	73	2.772	2.754	-0.018
10	74	2.834	2.817	-0.017
10	75	2.720	2.721	0.001
10	76	2.808	2.814	0.006
10	77	2.683	2.681	-0.002
10	78	2.624	2.616	-0.008
15	79	2.716	2.722	0.006
15	80	2.859	2.858	-0.001
15	81	2.683	2.666	-0.017
15	82	2.921	2.925	0.004
15	83	2.858	2.859	0.001
15	84	2.856	2.856	0.000
30	86	2.772	2.774	0.002
30	89	2.834	2.836	0.002
30	91	2.720	2.711	-0.009
30	92	2.808	2.785	-0.023
30	94	2.683	2.673	-0.010
30	95	2.624	2.602	-0.022
35	100	2.716	2.710	-0.006
35	101	2.859	2.877	0.018
35	102	2.683	2.672	-0.011
35	103	2.921	2.899	-0.022
35	104	2.858	2.863	0.005
35	105	2.856	2.851	-0.005
50	106	2.729	2.744	0.015
50	107	2.780	2.817	0.037
50	108	2.695	2.741	0.046
50	109	2.743	2.777	0.034
50	110	2.802	2.804	0.002
50	111	2.665	2.654	-0.011
55	113	2.815	2.767	-0.048
55	114	2.780	2.761	-0.019
55	115	2.716	2.705	-0.011
55	116	2.874	2.845	-0.029
55	126	2.888	2.878	-0.010
55	128	2.772	2.696	-0.076
100	129	2.818	2.845	0.027
100	131	2.769	2.728	-0.041
100	132	2.782	2.770	-0.012
100	133	2.868	2.880	0.012
100	134	2.764	2.775	0.011
100	135	2.722	2.682	-0.040
100	138	2.763	2.783	0.020
100	139	2.674	2.690	0.016
100	140	2.856	2.857	0.001
100	141	2.721	2.698	-0.023
100	142	2.845	2.861	0.016
100	143	2.781	2.785	0.004
100	144	2.734	2.737	0.003
100	220	2.795	2.779	-0.016
100	221	2.712	2.710	-0.002
100	224	2.757	2.782	0.025
100	225	2.658	2.667	0.009
100	226	2.853	2.890	0.037
100	228	2.743	2.798	0.055
100	232	2.775	2.806	0.031
100	233	2.803	2.775	-0.028
100	236	2.683	2.686	0.003
100	238	2.723	2.747	0.024
100	243	2.735	2.712	-0.023
105	244	2.791	2.785	-0.006
105	246	2.702	2.760	0.058
105	248	2.666	2.666	0.000
105	374	2.790	2.791	0.001
105	376	2.726	2.711	-0.015
	Max	2.921	2.925	0.058
	Average	2.770	2.769	-0.001
	Min	2.624	2.602	-0.076
	Std Dev	0.074	0.076	0.021



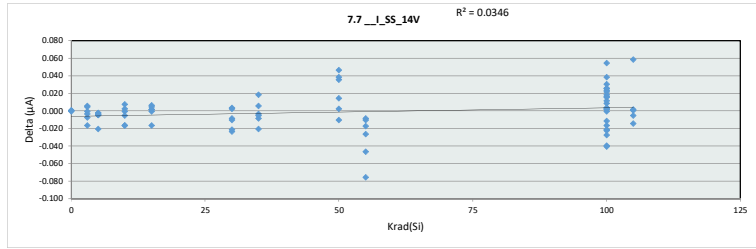
7.5_I_SS_12V											
Test Site											
Tester											
Test Number											
Max Limit		3.32	µA								
Min Limit		1.98	µA								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.725	2.627	2.679	2.616	2.666	2.602	2.672	2.654	2.696	2.667	2.666
Average	2.789	2.736	2.811	2.734	2.814	2.730	2.812	2.756	2.775	2.768	2.743
Max	2.831	2.833	2.918	2.817	2.925	2.836	2.899	2.817	2.878	2.890	2.791
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320



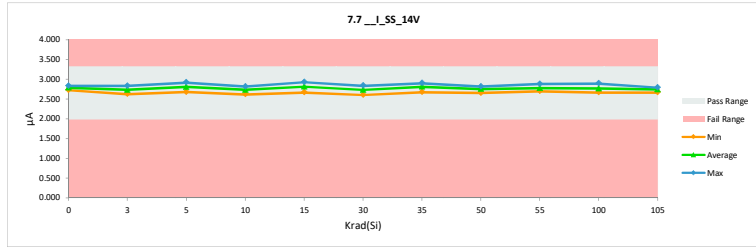
LDR TID Report  
TPS7H5001-SP

7.7_I_SS_14V		
Test Site		
Tester		
Test Number		
Unit	µA	µA
Max Limit	3.32	3.32
Min Limit	1.98	1.98

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.725	2.725	0.000
0	118	2.810	2.810	0.000
0	122	2.831	2.830	-0.001
3	61	2.771	2.754	-0.017
3	62	2.834	2.833	-0.001
3	63	2.719	2.724	0.005
3	64	2.808	2.804	-0.004
3	65	2.683	2.675	-0.008
3	66	2.623	2.627	0.004
5	67	2.716	2.711	-0.005
5	68	2.860	2.855	-0.005
5	69	2.682	2.679	-0.003
5	70	2.921	2.918	-0.003
5	71	2.858	2.853	-0.005
5	72	2.856	2.835	-0.021
10	73	2.771	2.754	-0.017
10	74	2.834	2.817	-0.017
10	75	2.719	2.721	0.002
10	76	2.808	2.815	0.007
10	77	2.683	2.682	-0.001
10	78	2.623	2.617	-0.006
15	79	2.716	2.722	0.006
15	80	2.860	2.859	-0.001
15	81	2.682	2.665	-0.017
15	82	2.921	2.925	0.004
15	83	2.858	2.859	0.001
15	84	2.856	2.857	0.001
30	86	2.771	2.774	0.003
30	89	2.834	2.836	0.002
30	91	2.719	2.710	-0.009
30	92	2.808	2.784	-0.024
30	94	2.683	2.672	-0.011
30	95	2.623	2.601	-0.022
35	100	2.716	2.710	-0.006
35	101	2.860	2.878	0.018
35	102	2.682	2.673	-0.009
35	103	2.921	2.900	-0.021
35	104	2.858	2.863	0.005
35	105	2.856	2.852	-0.004
50	106	2.729	2.743	0.014
50	107	2.779	2.817	0.038
50	108	2.695	2.741	0.046
50	109	2.743	2.778	0.035
50	110	2.803	2.805	0.002
50	111	2.665	2.654	-0.011
55	113	2.815	2.768	-0.047
55	114	2.781	2.763	-0.018
55	115	2.716	2.705	-0.011
55	116	2.874	2.847	-0.027
55	126	2.888	2.879	-0.009
55	128	2.772	2.696	-0.076
100	129	2.819	2.844	0.025
100	131	2.769	2.728	-0.041
100	132	2.782	2.770	-0.012
100	133	2.867	2.882	0.015
100	134	2.765	2.776	0.011
100	135	2.722	2.682	-0.040
100	138	2.762	2.783	0.021
100	139	2.674	2.690	0.016
100	140	2.856	2.857	0.001
100	141	2.721	2.699	-0.022
100	142	2.844	2.862	0.018
100	143	2.781	2.784	0.003
100	144	2.735	2.737	0.002
100	220	2.795	2.778	-0.017
100	221	2.712	2.711	-0.001
100	224	2.757	2.782	0.025
100	225	2.658	2.666	0.008
100	226	2.853	2.891	0.038
100	228	2.744	2.798	0.054
100	232	2.776	2.806	0.030
100	233	2.804	2.776	-0.028
100	236	2.682	2.685	0.003
100	238	2.723	2.746	0.023
100	243	2.735	2.712	-0.023
105	244	2.791	2.785	-0.006
105	246	2.702	2.760	0.058
105	248	2.666	2.667	0.001
105	374	2.790	2.790	0.000
105	376	2.726	2.711	-0.015
105	375	2.921	2.925	0.004
105	Average	2.770	2.769	-0.001
105	Min	2.623	2.601	-0.022
105	Std Dev	0.074	0.076	0.021



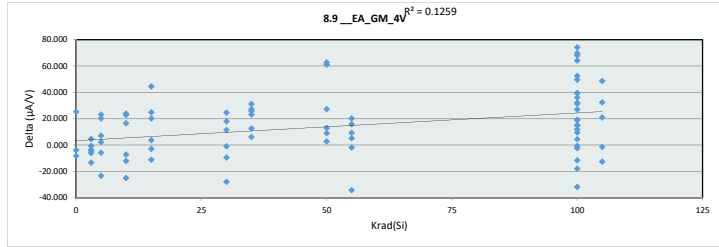
7.7_I_SS_14V											
Test Site											
Tester											
Test Number											
Max Limit	3.32	µA									
Min Limit	1.98	µA									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980	1.980
Min	2.725	2.627	2.679	2.617	2.665	2.601	2.673	2.654	2.696	2.666	2.667
Average	2.788	2.736	2.809	2.734	2.815	2.730	2.813	2.756	2.776	2.769	2.743
Max	2.830	2.833	2.918	2.817	2.925	2.836	2.900	2.817	2.879	2.891	2.790
UL	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320	3.320



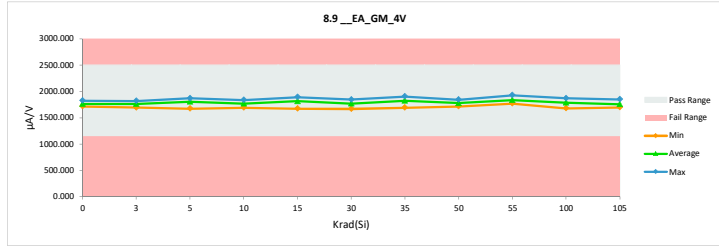
LDR TID Report  
TPS7H5001-SP

8.9_EA_GM_4V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1720.652	1712.302	-8.350
0	118	1747.872	1743.857	-4.015
0	122	1795.843	1821.097	25.254
3	61	1831.434	1818.008	-13.426
3	62	1812.910	1817.506	4.596
3	63	1728.389	1722.321	-6.068
3	64	1810.738	1806.294	-4.444
3	65	1695.071	1691.414	-3.657
3	66	1706.354	1705.678	-0.676
5	67	1781.172	1801.319	20.147
5	68	1787.111	1794.019	6.908
5	69	1666.754	1668.835	2.081
5	70	1872.927	1866.998	-5.929
5	71	1864.032	1840.727	-23.305
5	72	1820.267	1843.205	22.938
10	73	1831.434	1819.142	-12.292
10	74	1812.910	1787.958	-24.952
10	75	1728.389	1752.217	23.828
10	76	1810.738	1833.555	22.817
10	77	1695.071	1687.772	-7.299
10	78	1706.354	1722.772	16.418
15	79	1781.172	1801.329	20.157
15	80	1787.111	1775.978	-11.133
15	81	1666.754	1670.422	3.668
15	82	1872.927	1869.953	-2.974
15	83	1864.032	1888.805	24.773
15	84	1820.267	1864.570	44.303
30	86	1831.434	1842.891	11.457
30	89	1812.910	1811.922	-0.988
30	91	1728.389	1746.399	18.010
30	92	1810.738	1801.329	-9.409
30	94	1695.071	1667.077	-27.994
30	95	1706.354	1730.743	24.389
35	100	1781.172	1806.858	25.686
35	101	1787.111	1799.667	12.556
35	102	1666.754	1689.719	22.965
35	103	1872.927	1900.111	27.184
35	104	1864.032	1869.942	5.910
35	105	1820.267	1851.340	31.073
50	106	1707.344	1734.500	27.156
50	107	1768.294	1781.172	12.878
50	108	1751.906	1812.910	61.004
50	109	1778.020	1840.737	62.717
50	110	1759.900	1768.872	8.972
50	111	1710.809	1713.508	2.699
55	113	1770.860	1768.872	-1.988
55	114	1848.240	1864.021	15.781
55	115	1823.630	1832.714	9.084
55	116	1790.381	1810.553	20.172
55	126	1919.788	1924.942	5.154
55	128	1852.301	1818.008	-34.293
100	129	1774.203	1823.630	49.427
100	131	1748.392	1779.566	31.174
100	132	1746.390	1728.398	-17.992
100	133	1814.273	1833.085	18.812
100	134	1784.934	1852.833	67.899
100	135	1842.456	1810.553	-31.903
100	138	1763.572	1802.829	39.257
100	139	1722.321	1758.305	35.984
100	140	1768.872	1778.378	9.506
100	141	1795.286	1792.811	-2.475
100	142	1823.630	1822.927	-0.703
100	143	1792.562	1866.562	74.000
100	144	1770.197	1784.953	14.756
100	220	1825.525	1844.745	19.220
100	221	1702.415	1729.209	26.794
100	224	1704.595	1719.532	14.937
100	225	1774.193	1762.609	-11.584
100	226	1730.600	1782.989	52.389
100	228	1765.344	1834.999	69.655
100	232	1699.466	1763.591	64.125
100	233	1763.572	1802.974	39.402
100	236	1661.481	1673.388	11.907
100	238	1774.794	1806.848	32.054
100	243	1702.406	1706.684	4.278
105	244	1731.422	1763.572	32.150
105	246	1706.354	1754.976	48.622
105	248	1695.645	1716.462	20.817
105	374	1844.988	1843.599	-1.389
105	376	1705.218	1692.625	-12.593
105	Max	1919.788	1924.942	74.000
105	Average	1772.655	1786.881	14.226
105	Min	1661.481	1667.077	-34.293
105	Std Dev	59.035	60.061	24.002



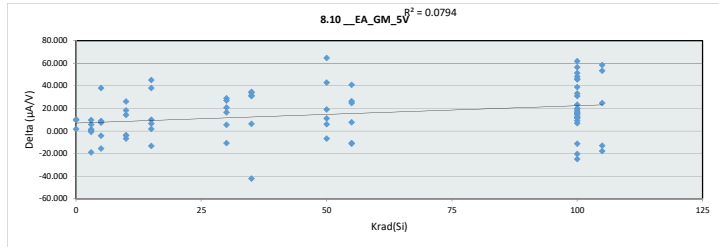
8.9_EA_GM_4V											
Test Site											
Tester											
Test Number											
Max Limit	2500 µA/V										
Min Limit	1150 µA/V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1712.302	1691.414	1668.835	1687.772	1670.422	1667.077	1689.719	1713.508	1768.872	1673.388	1692.625
Average	1759.085	1760.204	1802.517	1767.236	1811.843	1766.727	1819.606	1775.283	1836.518	1785.933	1754.247
Max	1821.097	1818.008	1866.998	1833.555	1888.805	1842.891	1900.111	1840.737	1924.942	1866.562	1843.599
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



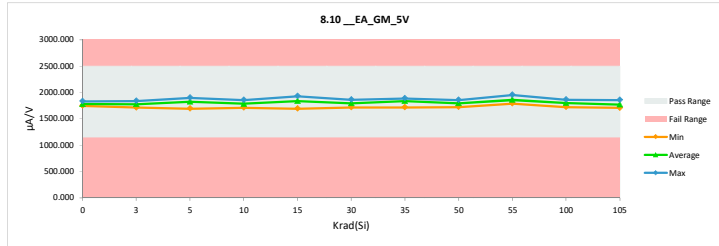
LDR TID Report  
TPS7H5001-SP

		8.10 EA GM 5V	
Test Site			
Tester			
Test Number			
Unit		µA/V	µA/V
Max Limit		2500	2500
Min Limit		1150	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1733.380	1742.698	9.318
0	118	1755.058	1765.019	9.961
0	122	1828.041	1829.297	1.256
3	61	1834.999	1815.765	-19.234
3	62	1820.514	1819.270	-1.244
3	63	1747.882	1753.078	5.196
3	64	1825.903	1835.311	9.408
3	65	1712.310	1712.258	-0.052
3	66	1726.744	1727.713	0.969
5	67	1795.065	1790.372	-4.693
5	68	1799.947	1807.773	7.826
5	69	1682.956	1691.490	8.534
5	70	1916.181	1900.111	-16.070
5	71	1881.898	1889.039	7.141
5	72	1845.425	1882.912	37.487
10	73	1834.999	1852.833	17.834
10	74	1820.514	1813.224	-7.290
10	75	1747.882	1761.600	13.718
10	76	1825.903	1851.729	25.826
10	77	1712.310	1708.039	-4.271
10	78	1726.744	1722.321	-4.423
15	79	1795.065	1801.329	6.264
15	80	1799.947	1801.329	1.382
15	81	1682.956	1692.625	9.669
15	82	1916.181	1902.556	-13.625
15	83	1881.898	1926.522	44.624
15	84	1845.425	1883.102	37.677
30	86	1834.999	1861.508	26.509
30	89	1820.514	1849.075	28.561
30	91	1747.882	1768.284	20.402
30	92	1825.903	1814.803	-11.100
30	94	1712.310	1717.297	4.987
30	95	1726.744	1742.707	15.963
35	100	1795.065	1825.913	30.848
35	101	1799.947	1834.287	34.340
35	102	1682.956	1716.304	33.348
35	103	1916.181	1873.513	-42.668
35	104	1881.898	1887.923	6.025
35	105	1845.425	1875.901	30.476
50	106	1729.984	1748.747	18.763
50	107	1790.630	1801.329	10.699
50	108	1767.632	1831.834	64.202
50	109	1813.537	1855.876	42.339
50	110	1786.749	1792.351	5.602
50	111	1728.897	1721.755	-7.142
55	113	1761.813	1787.939	26.126
55	114	1863.700	1887.934	24.234
55	115	1829.297	1869.755	40.458
55	116	1805.186	1812.411	7.225
55	126	1961.582	1950.442	-11.140
55	128	1852.311	1840.737	-11.574
100	129	1806.848	1852.311	45.463
100	131	1762.701	1777.935	15.234
100	132	1781.087	1755.692	-25.395
100	133	1834.999	1846.501	11.512
100	134	1801.329	1852.291	50.962
100	135	1828.061	1807.228	-20.833
100	138	1788.902	1777.106	-11.796
100	139	1737.563	1782.781	45.218
100	140	1806.848	1822.610	15.762
100	141	1807.968	1818.935	10.967
100	142	1804.691	1823.819	19.128
100	143	1834.999	1852.311	17.312
100	144	1791.041	1799.677	8.636
100	220	1846.867	1861.085	14.218
100	221	1704.196	1760.269	56.073
100	224	1726.104	1732.450	6.346
100	225	1779.557	1812.411	32.854
100	226	1760.787	1799.185	38.398
100	228	1773.612	1834.999	61.387
100	232	1733.022	1755.986	22.964
100	233	1808.708	1828.051	19.343
100	236	1674.557	1722.321	47.764
100	238	1777.775	1808.240	30.465
100	243	1716.190	1728.300	12.110
105	244	1722.321	1780.454	58.133
105	246	1717.305	1770.206	52.901
105	248	1709.998	1734.357	24.359
105	374	1869.671	1856.195	-13.476
105	376	1724.339	1706.094	-18.245
105	Max	1961.582	1950.442	64.202
105	Average	1790.116	1805.097	14.980
105	Min	1674.557	1691.490	-42.668
105	Std Dev	61.263	59.207	22.015



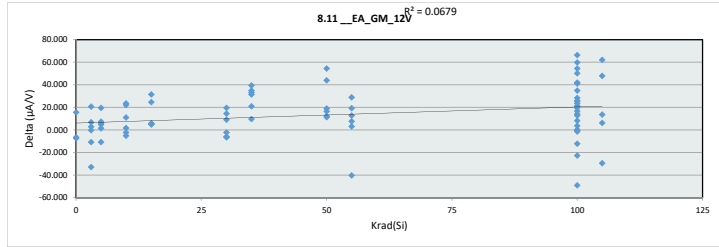
		8.10 EA GM 5V									
Test Site											
Tester											
Test Number											
Max Limit	2500	µA/V									
Min Limit	1150	µA/V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1742.698	1712.258	1691.490	1708.039	1692.625	1717.297	1716.304	1721.755	1787.939	1722.321	1706.094
Average	1779.005	1777.233	1826.950	1784.958	1834.577	1792.279	1835.640	1791.982	1858.203	1800.521	1769.461
Max	1829.297	1835.311	1900.111	1852.833	1926.522	1861.508	1887.923	1855.876	1950.442	1861.085	1856.195
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



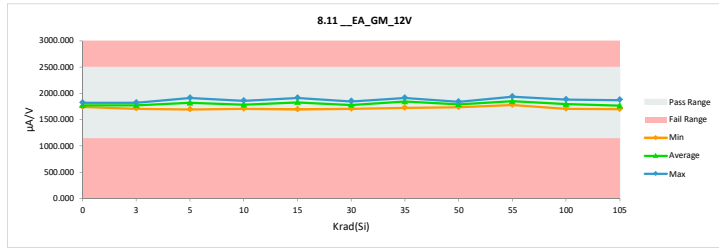
LDR TID Report  
TPS7H5001-SP

8.11 EA GM 12V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1749.951	1743.232	-6.719
0	118	1748.155	1763.572	15.417
0	122	1828.320	1821.008	-7.312
3	61	1856.482	1823.620	-32.862
3	62	1816.601	1823.244	6.643
3	63	1737.563	1758.305	20.742
3	64	1823.125	1812.196	-10.929
3	65	1707.344	1710.242	2.898
3	66	1728.389	1728.007	-0.382
5	67	1801.329	1790.372	-10.957
5	68	1807.101	1808.513	1.412
5	69	1692.625	1697.506	4.881
5	70	1906.881	1914.213	7.332
5	71	1862.552	1881.898	19.346
5	72	1842.931	1848.434	5.503
10	73	1856.482	1858.158	1.676
10	74	1816.601	1827.593	10.992
10	75	1737.563	1759.632	22.069
10	76	1823.125	1846.501	23.376
10	77	1707.344	1705.106	-2.238
10	78	1728.389	1723.330	-5.059
15	79	1801.329	1806.848	5.519
15	80	1807.101	1812.411	5.310
15	81	1692.625	1697.497	4.872
15	82	1906.881	1912.446	5.565
15	83	1862.552	1894.008	31.456
15	84	1842.931	1867.632	24.701
30	86	1856.482	1850.370	-6.112
30	89	1816.601	1830.924	14.323
30	91	1737.563	1746.462	8.899
30	92	1823.125	1816.483	-6.642
30	94	1707.344	1704.872	-2.472
30	95	1728.389	1747.872	19.483
35	100	1801.329	1822.214	20.885
35	101	1807.101	1846.501	39.400
35	102	1692.625	1727.366	34.741
35	103	1906.881	1916.411	9.530
35	104	1862.552	1893.997	31.445
35	105	1842.931	1875.901	32.970
50	106	1740.411	1752.601	12.190
50	107	1790.391	1806.858	16.467
50	108	1767.753	1822.214	54.461
50	109	1795.834	1839.587	43.753
50	110	1789.598	1800.836	11.238
50	111	1717.297	1736.089	18.792
55	113	1774.193	1781.863	7.670
55	114	1872.927	1875.912	2.985
55	115	1837.864	1866.707	28.843
55	116	1803.158	1822.234	19.076
55	126	1924.942	1937.614	12.672
55	128	1864.021	1823.630	-40.391
100	129	1806.848	1834.989	28.141
100	131	1764.750	1784.174	19.424
100	132	1794.739	1745.572	-49.167
100	133	1829.297	1850.370	21.073
100	134	1823.006	1846.501	23.495
100	135	1832.144	1809.450	-22.694
100	138	1801.329	1801.329	0.000
100	139	1729.637	1795.834	66.197
100	140	1801.319	1818.008	16.689
100	141	1808.084	1816.129	8.045
100	142	1813.998	1828.041	14.043
100	143	1825.515	1885.239	59.724
100	144	1806.858	1806.848	-0.010
100	220	1858.158	1861.798	3.640
100	221	1705.236	1740.131	34.895
100	224	1722.321	1720.811	-1.510
100	225	1767.548	1780.161	12.613
100	226	1755.471	1797.209	41.738
100	228	1780.756	1834.989	54.233
100	232	1727.873	1768.872	40.999
100	233	1800.642	1826.459	25.817
100	236	1681.876	1707.344	25.468
100	238	1784.943	1834.989	50.046
100	243	1729.556	1717.297	-12.259
105	244	1737.051	1784.953	47.902
105	246	1706.884	1768.872	61.988
105	248	1708.700	1714.804	6.104
105	374	1860.910	1874.560	13.650
105	376	1732.450	1703.029	-29.421
105	Max	1924.942	1937.614	66.197
105	Average	1790.644	1804.248	13.604
105	Min	1681.876	1697.497	-49.167
105	Std Dev	58.886	59.591	21.992



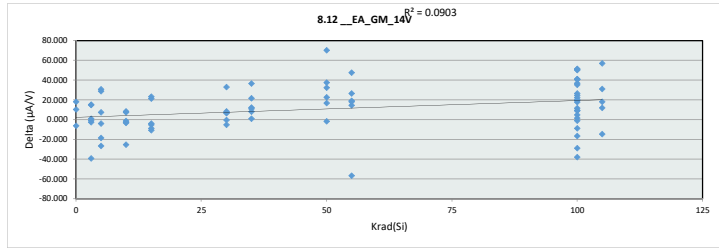
8.11 EA GM 12											
Test Site											
Tester											
Test Number											
Max Limit	2500 µA/V										
Min Limit	1150 µA/V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1743.232	1710.242	1697.506	1705.106	1697.497	1704.872	1727.366	1736.089	1781.863	1707.344	1703.029
Average	1775.937	1775.936	1823.489	1786.720	1831.807	1782.831	1847.065	1793.031	1851.327	1800.523	1769.244
Max	1821.008	1823.620	1914.213	1858.158	1912.446	1850.370	1916.411	1839.587	1937.614	1885.239	1874.560
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



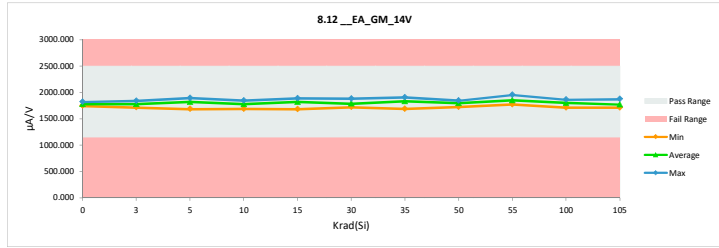
LDR TID Report  
TPS7H5001-SP

8.12 EA GM 14V	
Test Site	
Tester	
Test Number	
Unit	µA/V
Max Limit	2500
Min Limit	1150

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1727.366	1745.045	17.679
0	118	1758.959	1768.872	9.913
0	122	1824.711	1818.008	-6.703
3	61	1846.501	1806.848	-39.653
3	62	1829.297	1827.872	-1.425
3	63	1742.698	1757.549	14.851
3	64	1824.463	1838.821	14.358
3	65	1712.302	1712.870	0.568
3	66	1720.493	1717.297	-3.196
5	67	1812.411	1793.472	-18.939
5	68	1805.613	1812.411	6.798
5	69	1687.280	1682.956	-4.324
5	70	1897.049	1869.953	-27.096
5	71	1864.032	1892.342	28.310
5	72	1837.693	1867.964	30.271
10	73	1846.501	1842.648	-3.853
10	74	1829.297	1827.414	-1.883
10	75	1742.698	1750.590	7.892
10	76	1824.463	1831.424	6.961
10	77	1712.302	1686.576	-25.726
10	78	1720.493	1717.297	-3.196
15	79	1812.411	1801.319	-11.092
15	80	1805.613	1801.329	-4.284
15	81	1687.280	1681.522	-5.758
15	82	1897.049	1887.923	-9.126
15	83	1864.032	1886.723	22.691
15	84	1837.693	1858.426	20.733
30	86	1846.501	1878.895	32.394
30	89	1829.297	1828.579	-0.718
30	91	1742.698	1749.030	6.332
30	92	1824.463	1818.935	-5.528
30	94	1712.302	1718.669	6.367
30	95	1720.493	1728.389	7.896
35	100	1812.411	1822.966	10.555
35	101	1805.613	1826.857	21.244
35	102	1687.280	1687.781	0.501
35	103	1897.049	1904.586	7.537
35	104	1864.032	1875.912	11.880
35	105	1837.693	1873.921	36.228
50	106	1724.676	1740.989	16.313
50	107	1784.953	1822.026	37.073
50	108	1759.642	1829.297	69.655
50	109	1812.411	1844.218	31.807
50	110	1786.578	1808.708	22.130
50	111	1725.350	1723.321	-2.029
55	113	1758.305	1772.422	14.117
55	114	1868.464	1886.055	17.591
55	115	1816.139	1863.390	47.251
55	116	1810.563	1829.297	18.734
55	126	1924.257	1950.442	26.185
55	128	1864.032	1806.858	-57.174
100	129	1822.917	1834.047	11.130
100	131	1759.051	1778.020	18.969
100	132	1783.956	1745.527	-38.429
100	133	1836.717	1860.672	23.955
100	134	1827.175	1847.060	19.885
100	135	1823.620	1806.760	-16.860
100	138	1801.329	1802.325	0.996
100	139	1747.872	1788.559	40.687
100	140	1817.998	1816.611	-1.387
100	141	1783.871	1818.806	34.935
100	142	1801.319	1852.301	50.982
100	143	1818.008	1844.187	26.179
100	144	1819.615	1790.381	-29.234
100	220	1852.311	1861.085	8.774
100	221	1707.692	1747.872	40.180
100	224	1727.375	1731.824	4.449
100	225	1761.461	1778.887	17.426
100	226	1751.760	1801.329	49.569
100	228	1790.381	1840.727	50.346
100	232	1763.572	1785.551	21.979
100	233	1806.858	1815.441	8.583
100	236	1675.769	1712.310	36.541
100	238	1777.775	1817.998	40.223
100	243	1732.065	1722.825	-9.240
105	244	1739.284	1795.834	56.550
105	246	1717.297	1747.872	30.575
105	248	1706.441	1723.994	17.553
105	374	1859.002	1870.547	11.545
105	376	1727.375	1712.302	-15.073
105	Max	1924.257	1950.442	69.655
105	Average	1790.322	1801.608	11.286
105	Min	1675.769	1681.522	-57.174
105	Std Dev	57.982	59.970	22.818



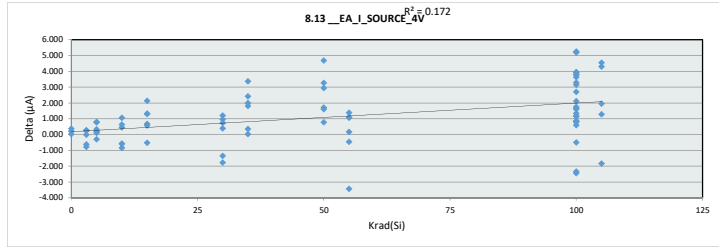
8.12 EA GM 14											
Test Site											
Tester											
Test Number											
Max Limit	2500										
Min Limit	1150										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000	1150.000
Min	1745.045	1712.870	1682.956	1686.576	1681.522	1718.669	1687.781	1723.321	1772.422	1712.310	1712.302
Average	1777.308	1776.876	1819.850	1775.992	1819.540	1787.083	1832.004	1794.760	1851.411	1800.046	1770.110
Max	1818.008	1838.821	1892.342	1842.648	1887.923	1878.895	1904.586	1844.218	1950.442	1861.085	1870.547
UL	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000	2500.000



LDR TID Report  
TPS7H5001-SP

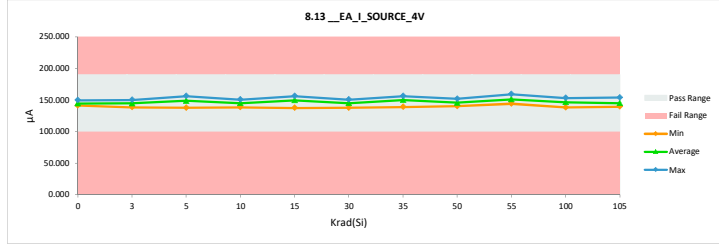
8.13 EA_I_SOURCE_4V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	140.651	140.845	0.194
0	118	143.043	143.056	0.013
0	122	148.956	149.325	0.369
3	61	149.422	148.622	-0.800
3	62	149.498	149.759	0.261
3	63	141.504	141.480	-0.024
3	64	149.209	149.443	0.234
3	65	138.718	138.083	-0.635
3	66	139.106	139.356	0.250
5	67	146.661	146.850	0.189
5	68	146.312	146.630	0.318
5	69	136.469	137.241	0.772
5	70	155.753	155.849	0.096
5	71	154.389	154.090	-0.299
5	72	149.521	150.307	0.786
10	73	149.422	148.824	-0.598
10	74	149.498	148.643	-0.855
10	75	141.504	142.134	0.630
10	76	149.209	150.253	1.044
10	77	138.718	138.119	-0.599
10	78	139.106	139.541	0.435
15	79	146.661	147.944	1.283
15	80	146.312	146.979	0.667
15	81	136.469	137.036	0.567
15	82	155.753	155.232	-0.521
15	83	154.389	155.704	1.315
15	84	149.521	151.637	2.116
30	86	149.422	150.369	0.947
30	89	149.498	150.229	0.731
30	91	141.504	141.899	0.396
30	92	149.209	147.419	-1.790
30	94	138.718	137.367	-1.351
30	95	139.106	140.292	1.186
35	100	146.661	149.075	2.414
35	101	146.312	149.672	3.360
35	102	136.469	138.460	1.991
35	103	155.753	155.768	0.015
35	104	154.389	154.730	0.341
35	105	149.521	151.318	1.797
50	106	140.265	141.965	1.700
50	107	144.182	147.111	2.929
50	108	143.729	146.991	3.262
50	109	146.800	151.469	4.669
50	110	143.722	145.314	1.592
50	111	139.166	139.936	0.770
55	113	143.822	143.988	0.166
55	114	153.088	154.152	1.064
55	115	150.260	151.642	1.382
55	116	146.426	147.488	1.062
55	126	159.480	159.006	-0.474
55	128	151.725	148.288	-3.437
100	129	145.716	148.860	3.144
100	131	142.808	143.585	0.777
100	132	143.352	140.904	-2.448
100	133	148.291	150.031	1.740
100	134	148.271	150.364	2.093
100	135	149.864	147.515	-2.349
100	138	144.962	146.296	1.334
100	139	141.841	145.607	3.766
100	140	146.809	147.587	0.778
100	141	145.189	146.352	1.163
100	142	147.741	148.609	0.868
100	143	147.641	152.881	5.240
100	144	146.013	147.617	1.604
100	220	150.885	152.024	1.139
100	221	138.278	142.215	3.937
100	224	139.091	139.675	0.584
100	225	143.554	145.220	1.666
100	226	142.215	146.118	3.903
100	228	144.558	149.731	5.173
100	232	140.989	144.614	3.625
100	233	146.943	150.226	3.283
100	236	135.132	137.831	2.699
100	238	144.767	148.572	3.805
100	243	139.397	138.889	-0.508
105	244	141.628	145.908	4.280
105	246	139.171	143.699	4.528
105	248	138.750	140.694	1.944
105	374	152.350	153.619	1.269
105	376	140.930	139.080	-1.850
105	Max	159.480	159.006	5.240
105	Average	145.527	146.641	1.114
105	Min	135.132	137.036	-3.437
105	Std Dev	5.370	5.293	1.761



8.13 EA_I_SOURCE	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

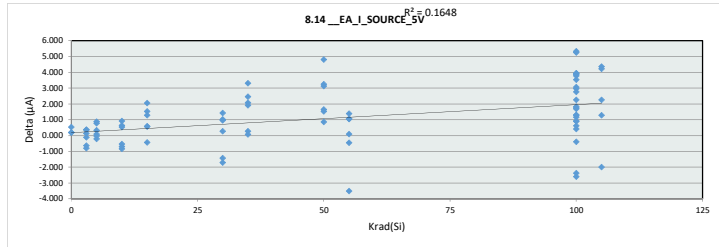
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	140.845	138.083	137.241	138.119	137.036	137.367	138.460	139.936	143.988	137.831	139.080
Average	144.409	144.457	148.495	144.586	149.089	144.594	149.837	145.464	150.761	146.305	144.600
Max	149.325	149.759	155.849	150.253	155.704	150.369	155.768	151.469	159.006	152.881	153.619
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



LDR TID Report  
TPS7H5001-SP

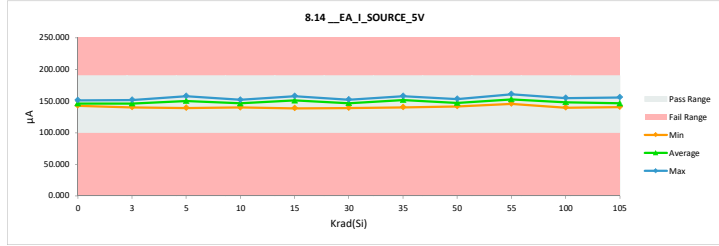
8.14 EA_I_SOURCE_5V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	142.229	142.397	0.168
0	118	144.538	144.707	0.169
0	122	150.453	150.961	0.508
3	61	151.142	150.315	-0.827
3	62	151.076	151.420	0.344
3	63	143.219	143.068	-0.151
3	64	150.915	151.005	0.090
3	65	140.364	139.698	-0.666
3	66	140.653	140.971	0.318
5	67	148.308	148.240	-0.068
5	68	148.000	148.294	0.294
5	69	138.014	138.755	0.741
5	70	157.440	157.497	0.057
5	71	156.018	155.779	-0.239
5	72	151.268	152.119	0.851
10	73	151.142	150.572	-0.570
10	74	151.076	150.197	-0.879
10	75	143.219	143.815	0.596
10	76	150.915	151.796	0.881
10	77	140.364	139.632	-0.732
10	78	140.653	141.161	0.508
15	79	148.308	149.568	1.260
15	80	148.000	148.542	0.542
15	81	138.014	138.554	0.540
15	82	157.440	156.968	-0.472
15	83	156.018	157.506	1.488
15	84	151.268	153.289	2.021
30	86	151.142	152.131	0.989
30	89	151.076	152.012	0.936
30	91	143.219	143.465	0.246
30	92	150.915	149.172	-1.743
30	94	140.364	138.910	-1.454
30	95	140.653	142.042	1.389
35	100	148.308	150.736	2.428
35	101	148.000	151.284	3.284
35	102	138.014	140.048	2.034
35	103	157.440	157.470	0.030
35	104	156.018	156.251	0.233
35	105	151.268	153.157	1.889
50	106	141.837	143.460	1.623
50	107	145.852	148.955	3.103
50	108	145.361	148.584	3.223
50	109	148.365	153.140	4.775
50	110	145.448	146.940	1.492
50	111	140.712	141.543	0.831
55	113	145.549	145.600	0.051
55	114	154.708	155.747	1.039
55	115	152.045	153.389	1.344
55	116	147.833	148.834	1.001
55	126	161.156	160.659	-0.497
55	128	153.469	149.932	-3.537
100	129	147.386	150.323	2.937
100	131	144.541	145.135	0.594
100	132	145.153	142.517	-2.636
100	133	150.196	151.908	1.712
100	134	149.930	152.154	2.224
100	135	151.559	149.157	-2.402
100	138	146.704	147.956	1.252
100	139	143.249	147.158	3.909
100	140	148.448	149.313	0.865
100	141	146.932	148.073	1.141
100	142	149.375	150.268	0.893
100	143	149.355	154.566	5.211
100	144	147.727	149.375	1.648
100	220	152.619	153.882	1.263
100	221	139.870	143.701	3.831
100	224	140.788	141.168	0.380
100	225	145.108	146.901	1.793
100	226	143.843	147.746	3.903
100	228	146.180	151.488	5.308
100	232	142.707	146.207	3.500
100	233	148.801	151.828	3.027
100	236	136.601	139.338	2.737
100	238	146.480	150.216	3.736
100	243	140.947	140.511	-0.436
105	244	143.138	147.322	4.184
105	246	140.839	145.158	4.319
105	248	140.188	142.411	2.223
105	374	154.197	155.451	1.254
105	375	142.514	140.489	-2.025
105	376	161.156	160.659	-0.497
Max				5.308
Average				1.098
Min				-3.537
Std Dev				1.775



8.14 EA_I_SOUR	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.397	139.698	138.755	139.632	138.554	138.910	140.048	141.543	145.600	139.338	140.489
Average	146.022	146.080	150.114	146.196	150.738	146.289	151.491	147.104	152.360	147.954	146.166
Max	150.961	151.420	157.497	151.796	157.506	152.131	157.470	153.140	160.659	154.566	155.451
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000

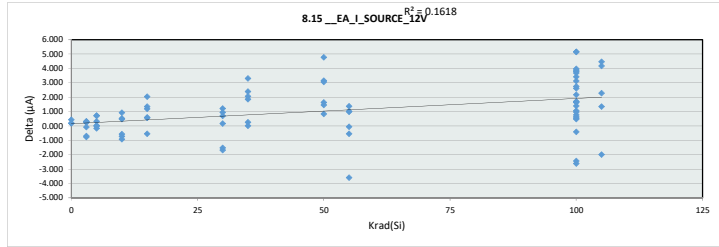




LDR TID Report  
TPS7H5001-SP

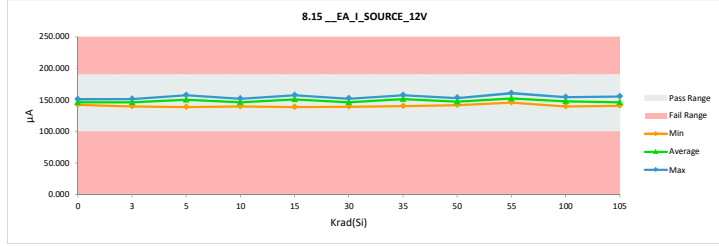
8.15 EA_I_SOURCE_12V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	142.269	142.456	0.187
0	118	144.699	144.876	0.177
0	122	150.635	151.057	0.422
3	61	151.213	150.441	-0.772
3	62	151.231	151.520	0.289
3	63	143.285	143.184	-0.101
3	64	150.936	151.164	0.228
3	65	140.503	139.794	-0.709
3	66	140.814	141.128	0.314
5	67	148.382	148.354	-0.028
5	68	148.033	148.349	0.316
5	69	138.129	138.820	0.691
5	70	157.610	157.620	0.010
5	71	156.078	155.899	-0.179
5	72	151.345	152.049	0.704
10	73	151.213	150.623	-0.590
10	74	151.231	150.293	-0.938
10	75	143.285	143.804	0.519
10	76	150.936	151.853	0.917
10	77	140.503	139.769	-0.734
10	78	140.814	141.277	0.463
15	79	148.382	149.566	1.184
15	80	148.033	148.610	0.577
15	81	138.129	138.688	0.559
15	82	157.610	157.051	-0.559
15	83	156.078	157.420	1.342
15	84	151.345	153.373	2.028
30	86	151.213	152.138	0.925
30	89	151.231	151.924	0.693
30	91	143.285	143.446	0.161
30	92	150.936	149.232	-1.704
30	94	140.503	138.971	-1.532
30	95	140.814	142.014	1.200
35	100	148.382	150.756	2.374
35	101	148.033	151.315	3.282
35	102	138.129	140.183	2.054
35	103	157.610	157.608	-0.002
35	104	156.078	156.331	0.253
35	105	151.345	153.184	1.839
50	106	141.868	143.491	1.623
50	107	146.073	149.200	3.127
50	108	145.504	148.542	3.038
50	109	148.391	153.147	4.756
50	110	145.612	147.058	1.446
50	111	140.913	141.733	0.820
55	113	145.737	145.678	-0.059
55	114	154.751	155.760	1.009
55	115	151.965	153.330	1.365
55	116	147.970	148.944	0.974
55	126	161.258	160.711	-0.547
55	128	153.559	149.951	-3.608
100	129	147.567	150.309	2.742
100	131	144.639	145.175	0.536
100	132	145.160	142.530	-2.630
100	133	150.303	151.988	1.685
100	134	150.026	152.187	2.161
100	135	151.672	149.218	-2.454
100	138	146.691	148.066	1.375
100	139	143.342	147.133	3.791
100	140	148.742	149.372	0.630
100	141	147.227	148.266	1.039
100	142	149.455	150.217	0.762
100	143	149.482	154.630	5.148
100	144	147.838	149.471	1.633
100	220	152.696	153.645	0.949
100	221	139.929	143.794	3.865
100	224	140.853	141.312	0.459
100	225	145.269	146.940	1.671
100	226	143.990	147.940	3.950
100	228	146.301	151.448	5.147
100	232	142.825	146.224	3.399
100	233	148.804	151.912	3.108
100	236	136.872	139.477	2.605
100	238	146.531	150.196	3.665
100	243	141.116	140.687	-0.429
105	244	143.336	147.495	4.159
105	246	140.912	145.367	4.455
105	248	140.304	142.573	2.269
105	374	154.309	155.640	1.331
105	376	142.728	140.720	-2.008
105	Max	161.258	160.711	-0.547
105	Average	147.285	148.345	1.060
105	Min	136.872	138.688	-3.608
105	Std Dev	5.395	5.319	1.768



8.15 EA_I_SOUR	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

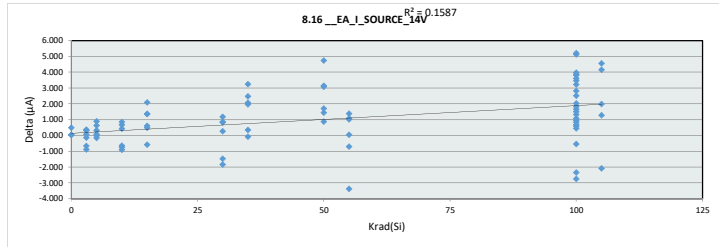
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.456	139.794	138.820	139.769	138.688	138.971	140.183	141.733	145.678	139.477	140.720
Average	146.130	146.205	150.182	146.270	150.785	146.288	151.563	147.195	152.396	148.006	146.359
Max	151.057	151.520	157.620	151.853	157.420	152.138	157.608	153.147	160.711	154.630	155.640
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



LDR TID Report  
TPS7H5001-SP

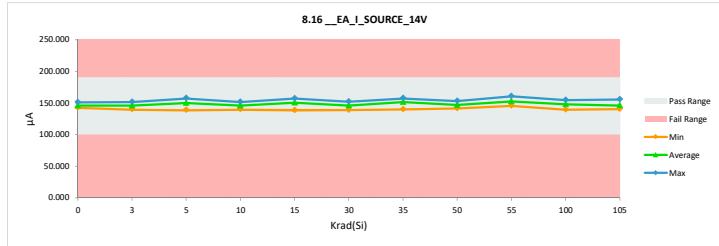
8.16 EA_I_SOURCE_14V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	142.401	142.420	0.019
0	118	144.644	144.641	-0.003
0	122	150.561	151.022	0.461
3	61	151.180	150.269	-0.911
3	62	151.147	151.483	0.336
3	63	143.236	143.067	-0.169
3	64	150.870	150.900	0.030
3	65	140.498	139.803	-0.695
3	66	140.793	141.064	0.271
5	67	148.223	148.280	0.057
5	68	147.968	148.264	0.296
5	69	138.127	138.736	0.609
5	70	157.546	157.510	-0.036
5	71	156.024	155.834	-0.190
5	72	151.279	152.140	0.861
10	73	151.180	150.518	-0.662
10	74	151.147	150.211	-0.936
10	75	143.236	143.876	0.640
10	76	150.870	151.704	0.834
10	77	140.498	139.725	-0.773
10	78	140.793	141.188	0.395
15	79	148.223	149.565	1.342
15	80	147.968	148.559	0.591
15	81	138.127	138.600	0.473
15	82	157.546	156.942	-0.604
15	83	156.024	157.355	1.331
15	84	151.279	153.350	2.071
30	86	151.180	152.002	0.822
30	89	151.147	151.964	0.817
30	91	143.236	143.479	0.243
30	92	150.870	149.014	-1.856
30	94	140.498	138.990	-1.508
30	95	140.793	141.941	1.148
35	100	148.223	150.661	2.438
35	101	147.968	151.190	3.222
35	102	138.127	140.163	2.036
35	103	157.546	157.438	-0.108
35	104	156.024	156.351	0.327
35	105	151.279	153.214	1.935
50	106	141.867	143.547	1.680
50	107	145.981	149.042	3.061
50	108	145.466	148.573	3.107
50	109	148.441	153.164	4.723
50	110	145.598	147.020	1.422
50	111	140.791	141.642	0.851
55	113	145.596	145.623	0.027
55	114	154.741	155.735	0.994
55	115	152.003	153.360	1.357
55	116	147.907	148.961	1.054
55	126	161.248	160.518	-0.730
55	128	153.467	150.063	-3.404
100	129	147.495	150.287	2.792
100	131	144.585	145.222	0.637
100	132	145.232	142.454	-2.778
100	133	150.148	151.943	1.795
100	134	150.024	152.016	1.992
100	135	151.574	149.207	-2.367
100	138	146.730	148.024	1.294
100	139	143.358	147.133	3.775
100	140	148.710	149.130	0.420
100	141	147.167	148.092	0.925
100	142	149.442	150.259	0.817
100	143	149.477	154.656	5.179
100	144	147.881	149.357	1.476
100	220	152.673	153.733	1.060
100	221	139.926	143.770	3.844
100	224	140.836	141.402	0.566
100	225	145.247	146.879	1.632
100	226	143.917	147.866	3.949
100	228	146.237	151.340	5.103
100	232	142.786	146.230	3.444
100	233	148.780	151.972	3.192
100	236	136.879	139.375	2.496
100	238	146.420	150.008	3.588
100	243	141.064	140.493	-0.571
105	244	143.296	147.425	4.129
105	246	140.793	145.319	4.526
105	248	140.428	142.393	1.965
105	374	154.289	155.542	1.253
105	376	142.636	140.533	-2.103
105	375	161.248	160.518	-0.730
105	376	161.248	160.518	-0.730
105	Average	147.243	148.284	1.042
105	Min	136.879	138.600	-3.404
105	Std Dev	5.382	5.316	1.774



8.16 EA_I_SOUR	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

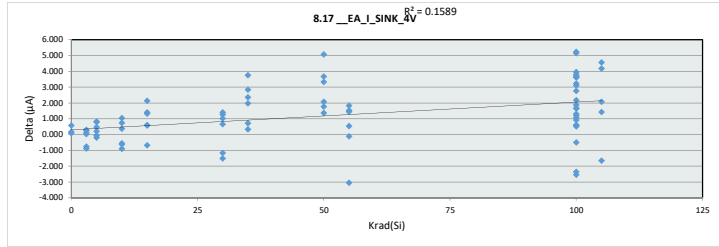
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.420	139.803	138.736	139.725	138.600	138.990	140.163	141.642	145.623	139.375	140.533
Average	146.028	146.098	150.127	146.204	150.729	146.232	151.503	147.165	152.377	147.952	146.242
Max	151.022	151.483	157.510	151.704	157.355	152.002	157.438	153.164	160.518	154.656	155.542
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



LDR TID Report  
TPS7H5001-SP

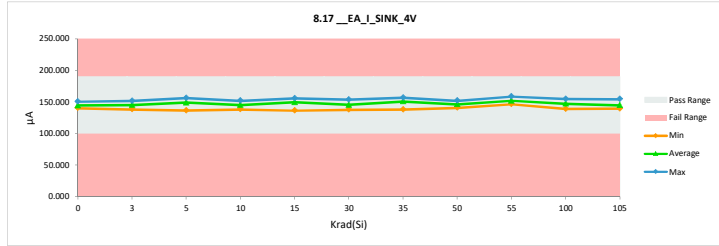
8.17 EA_I_SINK_4V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	140.018	140.172	0.154
0	118	142.967	143.040	0.073
0	122	149.556	150.124	0.568
3	61	152.333	151.445	-0.888
3	62	148.168	148.430	0.262
3	63	142.376	142.391	0.015
3	64	149.422	149.525	0.103
3	65	138.736	137.972	-0.764
3	66	140.251	140.533	0.282
5	67	147.276	147.466	0.190
5	68	147.179	147.642	0.463
5	69	135.543	136.340	0.797
5	70	156.191	156.124	-0.067
5	71	154.011	153.816	-0.195
5	72	151.596	152.411	0.815
10	73	152.333	151.770	-0.563
10	74	148.168	147.255	-0.913
10	75	142.376	143.104	0.728
10	76	149.422	150.455	1.033
10	77	138.736	138.097	-0.639
10	78	140.251	140.614	0.363
15	79	147.276	148.672	1.396
15	80	147.179	147.766	0.587
15	81	135.543	136.106	0.563
15	82	156.191	155.491	-0.700
15	83	154.011	155.323	1.312
15	84	151.596	153.716	2.120
30	86	152.333	153.590	1.257
30	89	148.168	149.185	1.017
30	91	142.376	143.020	0.644
30	92	149.422	147.902	-1.520
30	94	138.736	137.558	-1.178
30	95	140.251	141.652	1.401
35	100	147.276	150.098	2.822
35	101	147.179	150.911	3.732
35	102	135.543	137.879	2.336
35	103	156.191	156.521	0.330
35	104	154.011	154.712	0.701
35	105	151.596	153.547	1.951
50	106	141.086	143.151	2.065
50	107	144.483	147.801	3.318
50	108	144.210	147.874	3.664
50	109	146.625	151.680	5.055
50	110	144.611	146.375	1.764
50	111	139.249	140.600	1.351
55	113	145.667	146.197	0.530
55	114	153.230	154.745	1.515
55	115	150.295	152.112	1.817
55	116	147.675	149.100	1.425
55	126	158.700	158.577	-0.123
55	128	152.617	149.549	-3.068
100	129	148.214	151.298	3.084
100	131	144.315	144.898	0.583
100	132	145.720	143.173	-2.547
100	133	149.941	151.594	1.653
100	134	149.543	151.714	2.171
100	135	149.652	147.289	-2.363
100	138	146.184	147.483	1.299
100	139	141.481	145.116	3.635
100	140	147.730	148.311	0.581
100	141	146.837	147.913	1.076
100	142	147.849	148.765	0.916
100	143	149.496	154.637	5.141
100	144	146.787	148.425	1.638
100	220	151.690	152.908	1.218
100	221	139.826	143.497	3.671
100	224	140.621	141.117	0.496
100	225	145.560	147.423	1.863
100	226	143.464	147.416	3.952
100	228	144.977	150.188	5.211
100	232	140.777	144.371	3.594
100	233	147.128	150.348	3.220
100	236	136.064	138.815	2.751
100	238	144.559	148.334	3.775
100	243	140.298	139.781	-0.517
105	244	140.434	144.603	4.169
105	246	139.344	143.905	4.561
105	248	139.197	141.253	2.056
105	374	152.775	154.191	1.416
105	376	141.010	139.347	-1.663
105	Max	158.700	158.577	5.211
105	Average	146.146	147.353	1.207
105	Min	135.543	136.106	-3.068
105	Std Dev	5.471	5.422	1.766



8.17 EA_I_SINK	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

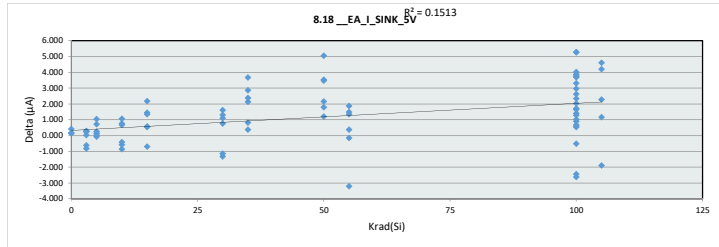
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	140.172	137.972	136.340	138.097	136.106	137.558	137.879	140.600	146.197	138.815	139.347
Average	144.445	145.049	148.967	145.216	149.512	145.485	150.611	146.247	151.713	147.284	144.660
Max	150.124	151.445	156.124	151.770	155.491	153.590	156.521	151.680	158.577	154.637	154.191
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



LDR TID Report  
TPS7H5001-SP

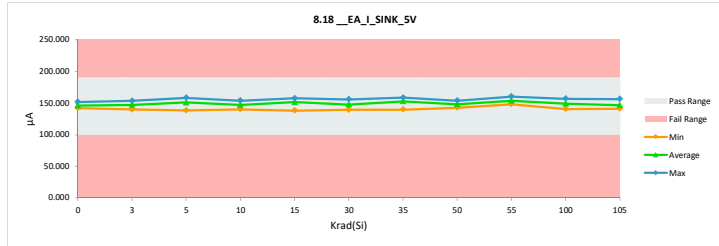
8.18 EA_I_SINK_5V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	141.845	141.951	0.106
0	118	144.567	144.704	0.137
0	122	151.324	151.699	0.375
3	61	154.118	153.261	-0.857
3	62	149.835	150.105	0.270
3	63	144.177	144.181	0.004
3	64	151.003	151.207	0.204
3	65	140.295	139.647	-0.648
3	66	141.977	142.221	0.244
5	67	148.969	149.083	0.114
5	68	149.037	149.258	0.221
5	69	137.352	138.046	0.694
5	70	157.986	157.977	-0.009
5	71	155.732	155.637	-0.095
5	72	153.292	154.311	1.019
10	73	154.118	153.670	-0.448
10	74	149.835	148.934	-0.901
10	75	144.177	144.896	0.719
10	76	151.003	152.024	1.021
10	77	140.295	139.681	-0.614
10	78	141.977	142.614	0.637
15	79	148.969	150.277	1.308
15	80	149.037	149.575	0.538
15	81	137.352	137.852	0.500
15	82	157.986	157.256	-0.730
15	83	155.732	157.150	1.418
15	84	153.292	155.439	2.147
30	86	154.118	155.381	1.263
30	89	149.835	150.896	1.061
30	91	144.177	144.895	0.718
30	92	151.003	149.637	-1.366
30	94	140.295	139.123	-1.172
30	95	141.977	143.555	1.578
35	100	148.969	151.796	2.827
35	101	149.037	152.671	3.634
35	102	137.352	139.445	2.093
35	103	157.986	158.329	0.343
35	104	155.732	156.520	0.788
35	105	153.292	155.647	2.355
50	106	142.742	144.857	2.115
50	107	146.312	149.759	3.447
50	108	146.019	149.512	3.493
50	109	148.334	153.360	5.026
50	110	146.378	148.144	1.766
50	111	141.073	142.242	1.169
55	113	147.581	147.931	0.350
55	114	155.024	156.476	1.452
55	115	152.045	153.878	1.833
55	116	149.290	150.594	1.304
55	126	160.462	160.276	-0.186
55	128	154.497	151.253	-3.244
100	129	150.153	153.079	2.926
100	131	146.071	146.635	0.564
100	132	147.512	144.852	-2.660
100	133	151.693	153.369	1.676
100	134	151.210	153.521	2.311
100	135	151.431	148.969	-2.462
100	138	147.885	149.252	1.367
100	139	143.029	146.805	3.776
100	140	149.629	150.135	0.506
100	141	148.739	149.736	0.997
100	142	149.627	150.501	0.874
100	143	151.304	156.550	5.246
100	144	148.652	150.271	1.619
100	220	153.552	154.808	1.256
100	221	141.409	145.256	3.847
100	224	142.244	142.893	0.649
100	225	147.208	149.217	2.009
100	226	145.156	149.150	3.994
100	228	146.783	152.024	5.241
100	232	142.562	146.223	3.661
100	233	149.032	152.310	3.278
100	236	137.731	140.329	2.598
100	238	146.393	150.064	3.671
100	243	141.911	141.369	-0.542
105	244	142.110	146.278	4.168
105	246	141.136	145.698	4.562
105	248	140.868	143.115	2.247
105	374	154.847	155.984	1.137
105	375	142.677	140.746	-1.931
105	376	160.462	160.276	-0.186
105	Max	147.892	149.100	1.208
105	Average	137.352	137.852	-3.244
105	Min	137.352	137.852	-3.244
105	Std Dev	5.496	5.464	1.785



8.18 EA_I_SINK	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

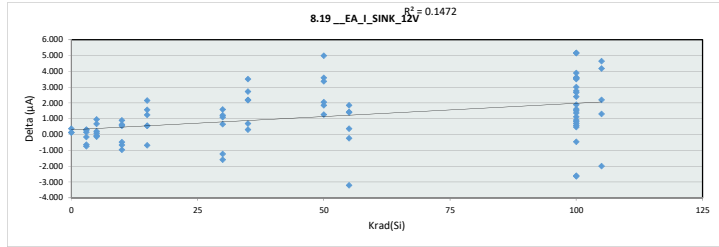
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	141.951	139.647	138.046	139.681	137.852	139.123	139.445	142.242	147.931	140.329	140.746
Average	146.118	146.770	150.719	146.970	151.258	147.248	152.401	147.979	153.401	149.055	146.364
Max	151.699	153.261	157.977	153.670	157.256	155.381	158.329	153.360	160.276	156.550	155.984
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



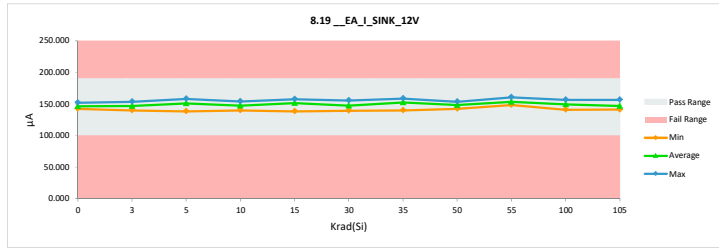
LDR TID Report  
TPS7H5001-SP

8.19 EA I SINK 12V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	141.912	142.038	0.126
0	118	144.694	144.812	0.118
0	122	151.458	151.822	0.364
3	61	154.236	153.489	-0.747
3	62	150.007	150.312	0.305
3	63	144.245	144.089	-0.156
3	64	151.257	151.398	0.141
3	65	140.511	139.871	-0.640
3	66	142.085	142.369	0.284
5	67	149.118	149.198	0.080
5	68	149.139	149.335	0.196
5	69	137.458	138.128	0.670
5	70	158.143	158.067	-0.076
5	71	155.829	155.691	-0.138
5	72	153.489	154.447	0.958
10	73	154.236	153.750	-0.486
10	74	150.007	149.024	-0.983
10	75	144.245	144.871	0.626
10	76	151.257	152.136	0.879
10	77	140.511	139.838	-0.673
10	78	142.085	142.620	0.535
15	79	149.118	150.345	1.227
15	80	149.139	149.679	0.540
15	81	137.458	138.003	0.545
15	82	158.143	157.452	-0.691
15	83	155.829	157.394	1.565
15	84	153.489	155.623	2.134
30	86	154.236	155.449	1.213
30	89	150.007	151.104	1.097
30	91	144.245	144.899	0.654
30	92	151.257	149.663	-1.594
30	94	140.511	139.272	-1.239
30	95	142.085	143.662	1.577
35	100	149.118	151.832	2.714
35	101	149.139	152.631	3.492
35	102	137.458	139.617	2.159
35	103	158.143	158.437	0.294
35	104	155.829	156.511	0.682
35	105	153.489	155.673	2.184
50	106	142.884	144.917	2.033
50	107	146.447	149.813	3.366
50	108	146.137	149.709	3.572
50	109	148.412	153.384	4.972
50	110	146.542	148.376	1.834
50	111	141.178	142.428	1.250
55	113	147.727	148.082	0.355
55	114	155.100	156.510	1.410
55	115	152.234	154.065	1.831
55	116	149.392	150.780	1.388
55	126	160.623	160.371	-0.252
55	128	154.528	151.305	-3.223
100	129	150.271	153.028	2.757
100	131	146.180	146.741	0.561
100	132	147.602	144.943	-2.659
100	133	151.961	153.551	1.590
100	134	151.302	153.695	2.393
100	135	151.687	149.069	-2.618
100	138	147.996	149.341	1.345
100	139	143.120	146.660	3.540
100	140	149.779	150.248	0.469
100	141	148.889	149.799	0.910
100	142	149.734	150.548	0.814
100	143	151.401	156.543	5.142
100	144	148.704	150.200	1.496
100	220	153.704	154.809	1.105
100	221	141.642	145.237	3.595
100	224	142.311	142.996	0.685
100	225	147.413	149.291	1.878
100	226	145.314	149.204	3.890
100	228	146.865	152.021	5.156
100	232	142.626	146.207	3.581
100	233	149.205	152.204	2.999
100	236	137.838	140.494	2.656
100	238	146.468	149.976	3.508
100	243	142.022	141.559	-0.463
105	244	142.265	146.439	4.174
105	246	141.130	145.762	4.632
105	248	141.093	143.279	2.186
105	374	154.981	156.265	1.284
105	376	142.966	140.952	-2.014
105	Max	160.623	160.371	-0.252
105	Average	148.029	149.192	1.163
105	Min	137.458	138.003	-0.545
105	Std Dev	5.503	5.456	1.770



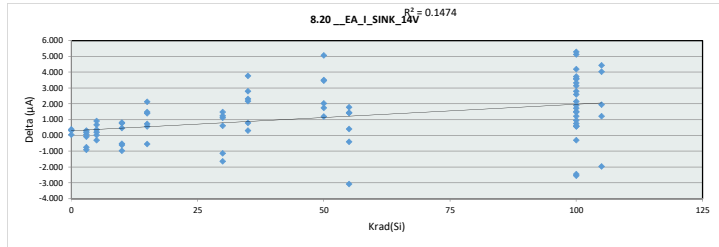
8.19 EA I SINK													
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.038	139.871	138.128	139.838	138.003	139.272	139.617	142.428	148.082	140.494	140.952		
Average	146.224	146.921	150.811	147.040	151.416	147.342	152.450	148.105	153.519	149.099	146.539		
Max	151.822	153.489	158.067	153.750	157.452	155.449	158.437	153.384	160.371	156.543	156.265		
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000		



LDR TID Report  
TPS7H5001-SP

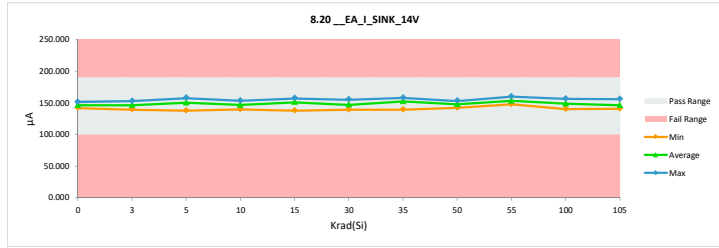
8.20 EA I SINK 14V	
Test Site	
Tester	
Test Number	
Unit	µA
Max Limit	190
Min Limit	100

Krad(Si)	Serial #	PRE	POST	Delta
0	117	141.925	142.215	0.290
0	118	144.791	144.812	0.021
0	122	151.436	151.799	0.363
3	61	154.209	153.280	-0.929
3	62	149.929	150.217	0.288
3	63	144.207	144.098	-0.109
3	64	151.270	151.309	0.039
3	65	140.457	139.680	-0.777
3	66	142.084	142.269	0.185
5	67	148.947	149.121	0.174
5	68	148.949	149.289	0.340
5	69	137.447	138.097	0.650
5	70	157.972	157.973	0.001
5	71	155.760	155.426	-0.334
5	72	153.439	154.328	0.889
10	73	154.209	153.664	-0.545
10	74	149.929	148.939	-0.990
10	75	144.207	144.960	0.753
10	76	151.270	152.048	0.778
10	77	140.457	139.837	-0.620
10	78	142.084	142.524	0.440
15	79	148.947	150.329	1.382
15	80	148.949	149.654	0.705
15	81	137.447	137.985	0.538
15	82	157.972	157.401	-0.571
15	83	155.760	157.220	1.460
15	84	153.439	155.545	2.106
30	86	154.209	155.418	1.209
30	89	149.929	151.022	1.093
30	91	144.207	144.800	0.593
30	92	151.270	149.616	-1.654
30	94	140.457	139.304	-1.153
30	95	142.084	143.547	1.463
35	100	148.947	151.719	2.772
35	101	148.949	152.687	3.738
35	102	137.447	139.593	2.146
35	103	157.972	158.251	0.279
35	104	155.760	156.526	0.766
35	105	153.439	155.730	2.291
50	106	142.888	144.895	2.007
50	107	146.460	149.945	3.485
50	108	146.010	149.455	3.445
50	109	148.325	153.358	5.033
50	110	146.582	148.308	1.726
50	111	141.168	142.336	1.168
55	113	147.660	148.044	0.384
55	114	155.106	156.503	1.397
55	115	152.197	153.954	1.757
55	116	149.295	150.711	1.416
55	126	160.594	160.176	-0.418
55	128	154.532	151.430	-3.102
100	129	150.204	152.971	2.767
100	131	146.173	146.731	0.558
100	132	147.569	144.998	-2.571
100	133	151.851	153.544	1.693
100	134	151.382	153.507	2.125
100	135	151.492	149.020	-2.472
100	138	147.915	149.373	1.458
100	139	143.079	146.626	3.547
100	140	149.696	150.230	0.534
100	141	148.890	149.613	0.723
100	142	149.630	150.566	0.936
100	143	151.425	156.681	5.256
100	144	148.709	150.191	1.482
100	220	153.626	154.818	1.192
100	221	141.598	145.290	3.692
100	224	142.451	143.024	0.573
100	225	147.407	149.290	1.883
100	226	145.230	149.390	4.160
100	228	146.896	151.992	5.096
100	232	142.615	146.185	3.570
100	233	149.156	152.268	3.112
100	236	137.872	140.433	2.561
100	238	146.488	149.793	3.305
100	243	141.998	141.666	-0.332
105	244	142.330	146.332	4.002
105	246	141.183	145.586	4.403
105	248	141.122	143.036	1.914
105	374	154.961	156.143	1.182
105	375	142.790	140.806	-1.984
105	376	160.594	160.176	-0.418
Max		160.594	160.176	5.256
Average		147.984	149.143	1.159
Min		137.447	137.985	-3.102
Std Dev		5.479	5.442	1.762



8.20 EA I SINK	
Test Site	
Tester	
Test Number	
Max Limit	190 µA
Min Limit	100 µA

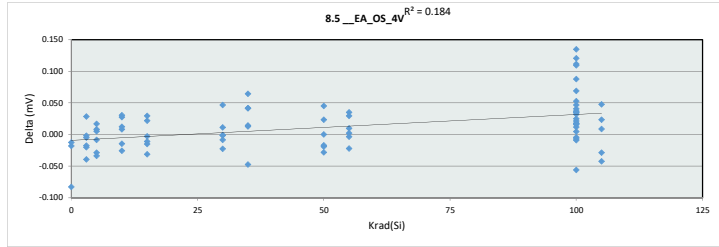
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000	100.000
Min	142.215	139.680	138.097	139.837	137.985	139.304	139.593	142.336	148.044	140.433	140.806
Average	146.275	146.809	150.706	146.995	151.356	147.285	152.418	148.050	153.470	149.092	146.381
Max	151.799	153.280	157.973	153.664	157.401	155.418	158.251	153.358	160.176	156.681	156.143
UL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000



LDR TID Report  
TPS7H5001-SP

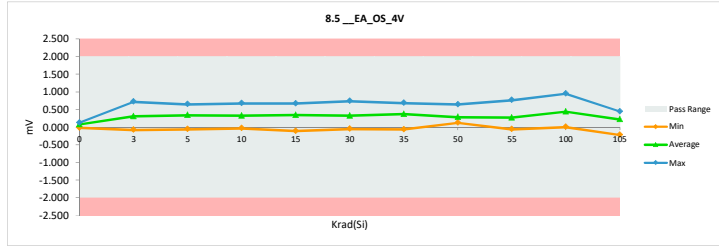
8.5_EA_OS_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	-0.003	-0.021	-0.018
0	118	0.137	0.125	-0.013
0	122	0.208	0.125	-0.083
3	61	0.688	0.716	0.028
3	62	0.010	-0.029	-0.040
3	63	0.373	0.353	-0.020
3	64	0.318	0.313	-0.005
3	65	-0.064	-0.081	-0.018
3	66	0.576	0.573	-0.002
5	67	0.393	0.398	0.005
5	68	0.455	0.426	-0.029
5	69	-0.080	-0.064	0.017
5	70	0.407	0.398	-0.009
5	71	0.256	0.222	-0.034
5	72	0.639	0.647	0.008
10	73	0.688	0.673	-0.015
10	74	0.010	0.019	0.008
10	75	0.373	0.385	0.012
10	76	0.318	0.292	-0.026
10	77	-0.064	-0.036	0.027
10	78	0.576	0.606	0.030
15	79	0.393	0.378	-0.015
15	80	0.455	0.477	0.022
15	81	-0.080	-0.112	-0.031
15	82	0.407	0.404	-0.003
15	83	0.256	0.245	-0.011
15	84	0.639	0.668	0.029
30	86	0.688	0.734	0.046
30	89	0.010	0.009	-0.001
30	91	0.373	0.372	-0.001
30	92	0.318	0.309	-0.009
30	94	-0.064	-0.052	0.011
30	95	0.576	0.553	-0.023
35	100	0.393	0.457	0.064
35	101	0.455	0.496	0.041
35	102	-0.080	-0.066	0.014
35	103	0.407	0.360	-0.047
35	104	0.256	0.269	0.012
35	105	0.639	0.681	0.042
50	106	0.594	0.638	0.045
50	107	0.125	0.124	0.000
50	108	0.236	0.260	0.023
50	109	0.140	0.123	-0.017
50	110	0.364	0.345	-0.019
50	111	0.226	0.197	-0.028
55	113	0.753	0.762	0.010
55	114	0.089	0.067	-0.022
55	115	0.094	0.096	0.002
55	116	0.328	0.362	0.035
55	126	-0.059	-0.062	-0.004
55	128	0.370	0.399	0.029
100	129	0.692	0.704	0.012
100	131	0.612	0.658	0.047
100	132	0.729	0.840	0.111
100	133	0.483	0.518	0.035
100	134	0.542	0.651	0.109
100	135	0.063	0.067	0.005
100	138	0.415	0.484	0.069
100	139	0.037	0.028	-0.009
100	140	0.299	0.324	0.025
100	141	0.493	0.514	0.021
100	142	0.361	0.305	-0.056
100	143	0.488	0.525	0.037
100	144	0.471	0.524	0.053
100	220	0.390	0.407	0.017
100	221	0.380	0.412	0.032
100	224	0.494	0.581	0.087
100	225	0.812	0.947	0.135
100	226	0.436	0.454	0.018
100	228	0.273	0.289	0.016
100	232	-0.118	0.003	0.120
100	233	0.187	0.228	0.041
100	236	0.461	0.456	-0.005
100	238	0.334	0.327	-0.007
100	243	0.285	0.319	0.034
105	244	-0.195	-0.224	-0.029
105	246	0.219	0.266	0.047
105	248	0.432	0.440	0.008
105	374	0.239	0.262	0.023
105	376	0.386	0.344	-0.042
105	Max	0.812	0.947	0.135
105	Average	0.328	0.340	0.012
105	Min	-0.195	-0.224	-0.083
105	Std Dev	0.239	0.254	0.039



8.5_EA_OS_4V	
Test Site	
Tester	
Test Number	
Max Limit	2 mV
Min Limit	-2 mV

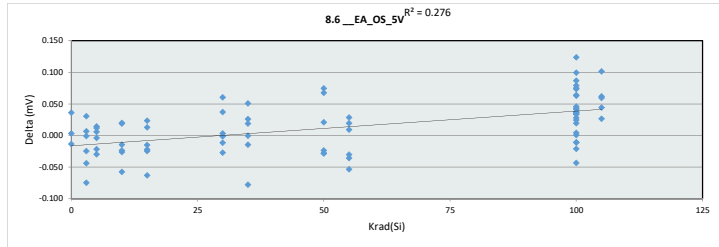
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.021	-0.081	-0.064	-0.036	-0.112	-0.052	-0.066	0.123	-0.062	0.003	-0.224
Average	0.076	0.308	0.338	0.323	0.343	0.321	0.366	0.281	0.271	0.440	0.218
Max	0.125	0.717	0.647	0.673	0.668	0.735	0.681	0.639	0.762	0.947	0.440
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



LDR TID Report  
TPS7H5001-SP

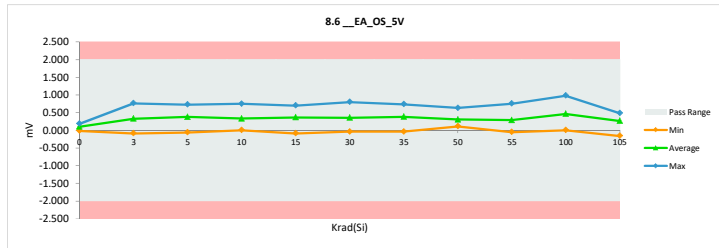
8.6_EA_OS_5V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	-0.023	-0.021	0.002
0	118	0.144	0.129	-0.014
0	122	0.146	0.181	0.036
3	61	0.760	0.759	-0.002
3	62	-0.021	0.009	0.030
3	63	0.389	0.363	-0.025
3	64	0.361	0.368	0.006
3	65	-0.016	-0.091	-0.075
3	66	0.591	0.547	-0.045
5	67	0.455	0.433	-0.023
5	68	0.519	0.488	-0.031
5	69	-0.069	-0.064	0.005
5	70	0.386	0.400	0.014
5	71	0.264	0.260	-0.004
5	72	0.713	0.724	0.011
10	73	0.760	0.745	-0.015
10	74	-0.021	-0.003	0.018
10	75	0.389	0.362	-0.027
10	76	0.361	0.337	-0.024
10	77	-0.016	0.004	0.019
10	78	0.591	0.533	-0.058
15	79	0.455	0.430	-0.025
15	80	0.519	0.455	-0.064
15	81	-0.069	-0.092	-0.023
15	82	0.386	0.409	0.023
15	83	0.264	0.277	0.012
15	84	0.713	0.697	-0.016
30	86	0.760	0.797	0.036
30	89	-0.021	0.038	0.060
30	91	0.389	0.377	-0.012
30	92	0.361	0.359	-0.002
30	94	-0.016	-0.044	-0.028
30	95	0.591	0.594	0.003
35	100	0.455	0.440	-0.015
35	101	0.519	0.518	-0.001
35	102	-0.069	-0.044	0.025
35	103	0.386	0.436	0.050
35	104	0.264	0.186	-0.078
35	105	0.713	0.731	0.018
50	106	0.657	0.628	-0.029
50	107	0.132	0.108	-0.024
50	108	0.270	0.290	0.020
50	109	0.100	0.174	0.074
50	110	0.404	0.375	-0.029
50	111	0.171	0.238	0.067
55	113	0.802	0.748	-0.054
55	114	0.091	0.110	0.019
55	115	0.146	0.109	-0.037
55	116	0.353	0.381	0.028
55	126	-0.026	-0.057	-0.031
55	128	0.390	0.399	0.009
100	129	0.648	0.721	0.073
100	131	0.645	0.677	0.033
100	132	0.817	0.858	0.041
100	133	0.558	0.620	0.062
100	134	0.592	0.691	0.099
100	135	0.057	0.135	0.078
100	138	0.475	0.499	0.024
100	139	0.083	0.061	-0.022
100	140	0.277	0.320	0.042
100	141	0.529	0.532	0.003
100	142	0.348	0.348	0.000
100	143	0.511	0.529	0.019
100	144	0.471	0.516	0.045
100	220	0.399	0.433	0.034
100	221	0.407	0.434	0.027
100	224	0.617	0.573	-0.044
100	225	0.850	0.974	0.123
100	226	0.484	0.472	-0.012
100	228	0.226	0.300	0.074
100	232	-0.067	-0.004	0.063
100	233	0.209	0.197	-0.012
100	236	0.424	0.510	0.086
100	238	0.323	0.360	0.037
100	243	0.331	0.367	0.036
105	244	-0.186	-0.160	0.026
105	246	0.203	0.247	0.044
105	248	0.378	0.479	0.100
105	374	0.249	0.310	0.061
105	376	0.365	0.423	0.058
105	Max	0.850	0.974	0.123
105	Average	0.350	0.362	0.012
105	Min	-0.186	-0.160	-0.078
105	Std Dev	0.255	0.257	0.042



8.6_EA_OS_5V	
Test Site	
Tester	
Test Number	
Max Limit	2
Min Limit	-2

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.021	-0.091	-0.064	-0.003	-0.092	-0.044	-0.044	0.108	-0.057	-0.004	-0.160
Average	0.097	0.326	0.373	0.330	0.363	0.354	0.378	0.302	0.281	0.464	0.260
Max	0.181	0.759	0.724	0.745	0.697	0.797	0.731	0.628	0.748	0.974	0.479
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000

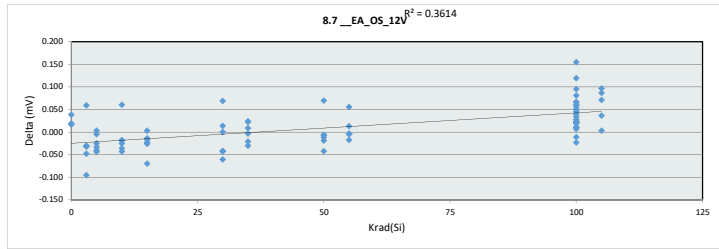




# LDR TID Report TPS7H5001-SP

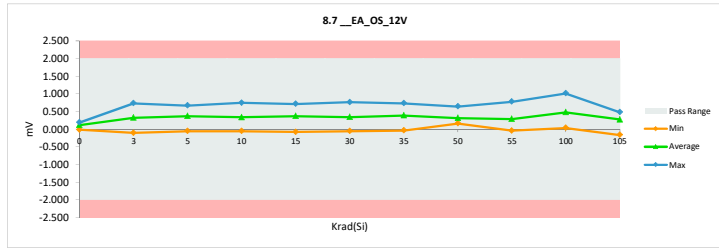
8.7_EA_OS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	-0.038	-0.019	0.019
0	118	0.125	0.163	0.038
0	122	0.167	0.183	0.016
3	61	0.761	0.729	-0.032
3	62	0.043	0.013	-0.031
3	63	0.425	0.377	-0.048
3	64	0.288	0.346	0.058
3	65	-0.014	-0.110	-0.096
3	66	0.584	0.551	-0.033
5	67	0.450	0.417	-0.033
5	68	0.526	0.482	-0.044
5	69	-0.065	-0.063	0.003
5	70	0.424	0.419	-0.005
5	71	0.286	0.261	-0.025
5	72	0.705	0.664	-0.040
10	73	0.761	0.742	-0.018
10	74	0.043	0.024	-0.019
10	75	0.425	0.399	-0.026
10	76	0.288	0.347	0.060
10	77	-0.014	-0.057	-0.043
10	78	0.584	0.548	-0.036
15	79	0.450	0.436	-0.015
15	80	0.526	0.455	-0.071
15	81	-0.065	-0.083	-0.017
15	82	0.424	0.398	-0.026
15	83	0.286	0.263	-0.023
15	84	0.705	0.707	0.003
30	86	0.761	0.760	0.000
30	89	0.043	-0.018	-0.061
30	91	0.425	0.383	-0.042
30	92	0.288	0.356	0.069
30	94	-0.014	-0.058	-0.044
30	95	0.584	0.598	0.014
35	100	0.450	0.459	0.008
35	101	0.526	0.522	-0.004
35	102	-0.065	-0.042	0.023
35	103	0.424	0.394	-0.031
35	104	0.286	0.264	-0.022
35	105	0.705	0.728	0.023
50	106	0.644	0.637	-0.007
50	107	0.166	0.154	-0.012
50	108	0.289	0.247	-0.043
50	109	0.104	0.173	0.069
50	110	0.407	0.400	-0.008
50	111	0.265	0.246	-0.019
55	113	0.780	0.775	-0.005
55	114	0.107	0.120	0.013
55	115	0.070	0.065	-0.005
55	116	0.334	0.389	0.055
55	126	-0.028	-0.046	-0.018
55	128	0.404	0.401	-0.003
100	129	0.679	0.685	0.006
100	131	0.662	0.682	0.021
100	132	0.796	0.829	0.034
100	133	0.555	0.577	0.022
100	134	0.622	0.702	0.080
100	135	0.073	0.131	0.058
100	138	0.515	0.503	-0.011
100	139	0.077	0.087	0.010
100	140	0.308	0.359	0.051
100	141	0.554	0.599	0.045
100	142	0.342	0.352	0.011
100	143	0.511	0.532	0.021
100	144	0.456	0.575	0.119
100	220	0.399	0.422	0.023
100	221	0.388	0.454	0.067
100	224	0.581	0.620	0.039
100	225	0.854	1.008	0.154
100	226	0.478	0.496	0.018
100	228	0.310	0.286	-0.023
100	232	-0.066	0.029	0.095
100	233	0.198	0.263	0.065
100	236	0.490	0.512	0.022
100	238	0.347	0.374	0.027
100	243	0.315	0.375	0.060
105	244	-0.172	-0.169	0.003
105	246	0.260	0.296	0.036
105	248	0.380	0.476	0.096
105	374	0.242	0.313	0.071
105	376	0.351	0.438	0.087
105	Max	0.854	1.008	0.154
105	Average	0.357	0.366	0.010
105	Min	-0.172	-0.169	-0.003
105	Std Dev	0.250	0.257	0.045



8.7_EA_OS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

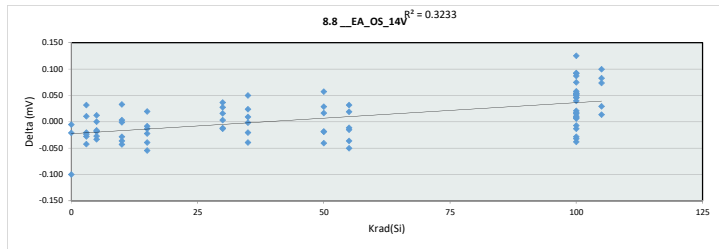
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.019	-0.110	-0.063	-0.057	-0.083	-0.058	-0.042	0.154	-0.046	0.029	-0.169
Average	0.109	0.318	0.363	0.334	0.363	0.337	0.387	0.309	0.284	0.477	0.271
Max	0.183	0.729	0.665	0.742	0.708	0.760	0.728	0.637	0.775	1.008	0.476
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



LDR TID Report  
TPS7H5001-SP

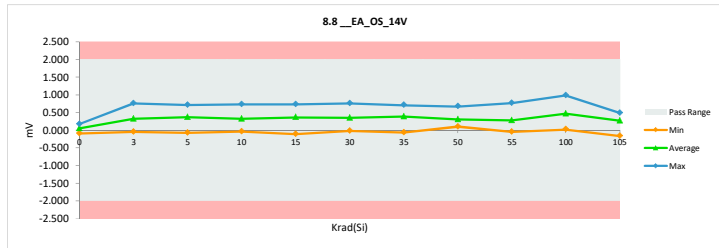
8.8_EA_OS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	2
Min Limit	-2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.012	-0.090	-0.101
0	118	0.107	0.085	-0.022
0	122	0.182	0.176	-0.006
3	61	0.732	0.762	0.030
3	62	0.037	0.016	-0.021
3	63	0.402	0.373	-0.029
3	64	0.323	0.298	-0.024
3	65	-0.005	-0.049	-0.044
3	66	0.558	0.568	0.010
5	67	0.437	0.417	-0.020
5	68	0.526	0.492	-0.034
5	69	-0.086	-0.075	0.011
5	70	0.433	0.405	-0.028
5	71	0.301	0.283	-0.017
5	72	0.713	0.712	-0.001
10	73	0.732	0.730	-0.002
10	74	0.037	0.000	-0.037
10	75	0.402	0.358	-0.044
10	76	0.323	0.325	0.002
10	77	-0.005	-0.034	-0.029
10	78	0.558	0.590	0.032
15	79	0.437	0.428	-0.010
15	80	0.526	0.470	-0.056
15	81	-0.086	-0.109	-0.023
15	82	0.433	0.419	-0.014
15	83	0.301	0.260	-0.040
15	84	0.713	0.732	0.019
30	86	0.732	0.758	0.026
30	89	0.037	0.039	0.002
30	91	0.402	0.388	-0.014
30	92	0.323	0.338	0.015
30	94	-0.005	-0.018	-0.013
30	95	0.558	0.594	0.036
35	100	0.437	0.486	0.049
35	101	0.526	0.534	0.008
35	102	-0.086	-0.063	0.023
35	103	0.433	0.412	-0.021
35	104	0.301	0.260	-0.041
35	105	0.713	0.711	-0.003
50	106	0.646	0.674	0.027
50	107	0.145	0.103	-0.041
50	108	0.297	0.277	-0.020
50	109	0.120	0.176	0.056
50	110	0.410	0.390	-0.020
50	111	0.218	0.234	0.016
55	113	0.782	0.766	-0.016
55	114	0.127	0.090	-0.037
55	115	0.123	0.072	-0.051
55	116	0.337	0.367	0.030
55	126	-0.033	-0.046	-0.013
55	128	0.403	0.421	0.018
100	129	0.694	0.680	-0.014
100	131	0.659	0.669	0.009
100	132	0.777	0.782	0.005
100	133	0.541	0.594	0.053
100	134	0.597	0.688	0.091
100	135	0.087	0.133	0.045
100	138	0.488	0.533	0.045
100	139	0.086	0.106	0.020
100	140	0.323	0.284	-0.039
100	141	0.532	0.581	0.049
100	142	0.363	0.334	-0.030
100	143	0.513	0.527	0.014
100	144	0.472	0.546	0.074
100	220	0.398	0.415	0.016
100	221	0.379	0.429	0.051
100	224	0.563	0.615	0.053
100	225	0.859	0.983	0.124
100	226	0.468	0.435	-0.033
100	228	0.307	0.299	-0.008
100	232	-0.069	0.022	0.091
100	233	0.190	0.276	0.086
100	236	0.471	0.528	0.057
100	238	0.336	0.374	0.039
100	243	0.352	0.359	0.007
105	244	-0.235	-0.162	0.073
105	246	0.210	0.308	0.098
105	248	0.409	0.490	0.082
105	374	0.301	0.313	0.013
105	376	0.381	0.409	0.028
105	Max	0.859	0.983	0.124
105	Average	0.356	0.363	0.008
105	Min	-0.235	-0.162	-0.101
105	Std Dev	0.248	0.258	0.042



8.8_EA_OS_14V	
Test Site	
Tester	
Test Number	
Max Limit	2 mV
Min Limit	-2 mV

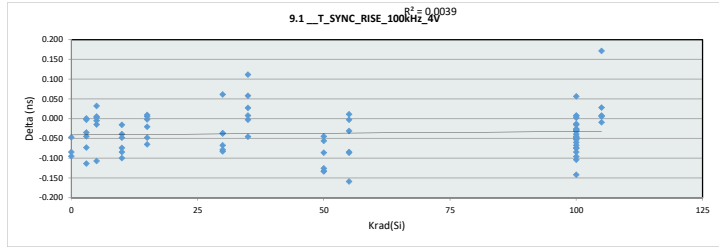
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000	-2.000
Min	-0.090	-0.049	-0.075	-0.034	-0.109	-0.018	-0.063	0.103	-0.046	0.022	-0.162
Average	0.057	0.328	0.373	0.328	0.367	0.350	0.390	0.309	0.278	0.466	0.272
Max	0.176	0.762	0.713	0.730	0.732	0.758	0.711	0.674	0.766	0.983	0.490
UL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000



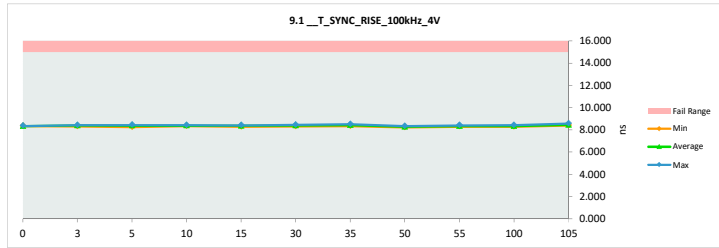
# LDR TID Report TPS7H5001-SP

9.1 T_SYNC_RISE_100kHz_4V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.423	8.328	-0.095
0	118	8.422	8.337	-0.085
0	122	8.400	8.353	-0.047
3	61	8.510	8.396	-0.114
3	62	8.475	8.430	-0.045
3	63	8.378	8.375	-0.003
3	64	8.389	8.316	-0.073
3	65	8.426	8.426	0.000
3	66	8.431	8.396	-0.035
5	67	8.377	8.379	0.002
5	68	8.393	8.388	-0.005
5	69	8.404	8.436	0.032
5	70	8.350	8.243	-0.107
5	71	8.432	8.437	0.005
5	72	8.390	8.375	-0.015
10	73	8.510	8.425	-0.085
10	74	8.475	8.401	-0.074
10	75	8.378	8.330	-0.048
10	76	8.389	8.350	-0.039
10	77	8.426	8.410	-0.016
10	78	8.431	8.331	-0.100
15	79	8.377	8.386	0.009
15	80	8.393	8.398	0.005
15	81	8.404	8.402	-0.002
15	82	8.350	8.285	-0.065
15	83	8.432	8.411	-0.021
15	84	8.390	8.342	-0.048
30	86	8.510	8.442	-0.068
30	89	8.475	8.392	-0.083
30	91	8.378	8.439	0.061
30	92	8.389	8.310	-0.079
30	94	8.426	8.388	-0.038
30	95	8.431	8.394	-0.037
35	100	8.377	8.331	-0.046
35	101	8.393	8.504	0.111
35	102	8.404	8.462	0.058
35	103	8.350	8.347	-0.003
35	104	8.432	8.459	0.027
35	105	8.390	8.398	0.008
50	106	8.391	8.258	-0.133
50	107	8.316	8.230	-0.086
50	108	8.424	8.291	-0.133
50	109	8.351	8.225	-0.126
50	110	8.386	8.330	-0.056
50	111	8.372	8.327	-0.045
55	113	8.454	8.295	-0.159
55	114	8.397	8.311	-0.086
55	115	8.398	8.313	-0.085
55	116	8.376	8.345	-0.031
55	126	8.336	8.333	-0.003
55	128	8.400	8.411	0.011
100	129	8.309	8.311	0.002
100	131	8.373	8.269	-0.104
100	132	8.365	8.317	-0.048
100	133	8.415	8.383	-0.032
100	134	8.375	8.300	-0.075
100	135	8.383	8.298	-0.085
100	138	8.451	8.309	-0.142
100	139	8.284	8.340	0.056
100	140	8.337	8.298	-0.039
100	141	8.426	8.352	-0.074
100	142	8.364	8.371	0.007
100	143	8.419	8.366	-0.053
100	144	8.321	8.329	0.008
100	220	8.372	8.304	-0.068
100	221	8.329	8.302	-0.027
100	224	8.409	8.393	-0.016
100	225	8.373	8.360	-0.013
100	226	8.468	8.423	-0.045
100	228	8.390	8.364	-0.026
100	232	8.361	8.312	-0.049
100	233	8.383	8.287	-0.096
100	236	8.431	8.371	-0.060
100	238	8.401	8.351	-0.050
100	243	8.443	8.413	-0.030
105	244	8.451	8.442	-0.009
105	246	8.393	8.421	0.028
105	248	8.383	8.554	0.171
105	374	8.462	8.470	0.008
105	375	8.375	8.380	0.005
105	Max	8.510	8.554	0.171
105	Average	8.399	8.363	-0.037
105	Min	8.284	8.225	-0.159
105	Std Dev	0.044	0.062	0.056



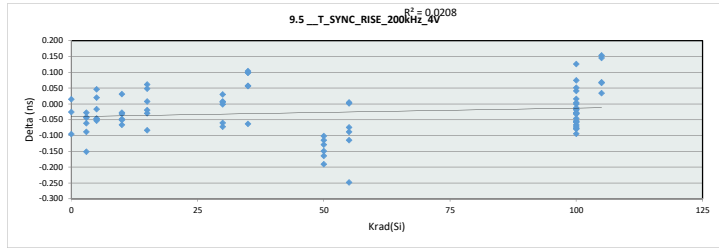
9.1 T_SYNC_RISE												
Test Site												
Tester												
Test Number												
Max Limit	15	ns										
Min Limit		ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	8.328	8.316	8.243	8.330	8.285	8.310	8.331	8.225	8.295	8.269	8.380	
Average	8.339	8.390	8.376	8.375	8.371	8.394	8.417	8.277	8.335	8.338	8.453	
Max	8.353	8.430	8.437	8.425	8.411	8.442	8.504	8.330	8.411	8.423	8.554	
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	



LDR TID Report  
TPS7H5001-SP

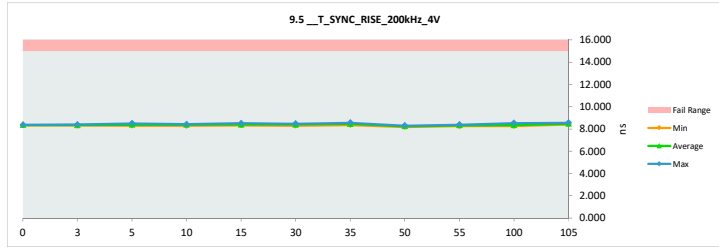
9.5_T_SYNC_RISE_200kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	15
Min Limit	15

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.455	8.358	-0.097
0	118	8.294	8.307	0.013
0	122	8.384	8.357	-0.027
3	61	8.443	8.380	-0.063
3	62	8.408	8.363	-0.045
3	63	8.397	8.307	-0.090
3	64	8.341	8.298	-0.043
3	65	8.366	8.337	-0.029
3	66	8.451	8.298	-0.153
5	67	8.300	8.282	-0.018
5	68	8.433	8.478	0.045
5	69	8.382	8.400	0.018
5	70	8.329	8.274	-0.055
5	71	8.437	8.389	-0.048
5	72	8.401	8.351	-0.050
10	73	8.443	8.410	-0.033
10	74	8.408	8.379	-0.029
10	75	8.397	8.347	-0.050
10	76	8.341	8.273	-0.068
10	77	8.366	8.395	0.029
10	78	8.451	8.398	-0.053
15	79	8.300	8.306	0.006
15	80	8.433	8.493	0.060
15	81	8.382	8.351	-0.031
15	82	8.329	8.308	-0.021
15	83	8.437	8.484	0.047
15	84	8.401	8.316	-0.085
30	86	8.443	8.449	0.006
30	89	8.408	8.412	0.004
30	91	8.397	8.394	-0.003
30	92	8.341	8.279	-0.062
30	94	8.366	8.394	0.028
30	95	8.451	8.377	-0.074
35	100	8.300	8.402	0.102
35	101	8.433	8.530	0.097
35	102	8.382	8.437	0.055
35	103	8.329	8.385	0.056
35	104	8.437	8.534	0.097
35	105	8.401	8.336	-0.065
50	106	8.418	8.226	-0.192
50	107	8.314	8.184	-0.130
50	108	8.396	8.280	-0.116
50	109	8.306	8.203	-0.103
50	110	8.426	8.260	-0.166
50	111	8.423	8.272	-0.151
55	113	8.394	8.278	-0.116
55	114	8.373	8.283	-0.090
55	115	8.534	8.284	-0.250
55	116	8.375	8.375	0.000
55	126	8.320	8.244	-0.076
55	128	8.348	8.351	0.003
100	129	8.379	8.307	-0.072
100	131	8.367	8.309	-0.058
100	132	8.332	8.372	0.040
100	133	8.340	8.320	-0.020
100	134	8.328	8.309	-0.019
100	135	8.343	8.311	-0.032
100	138	8.358	8.298	-0.060
100	139	8.351	8.255	-0.096
100	140	8.326	8.324	-0.002
100	141	8.345	8.347	0.002
100	142	8.386	8.307	-0.079
100	143	8.393	8.360	-0.033
100	144	8.326	8.295	-0.031
100	220	8.312	8.362	0.050
100	221	8.337	8.256	-0.081
100	224	8.312	8.385	0.073
100	225	8.388	8.359	-0.029
100	226	8.407	8.339	-0.068
100	228	8.416	8.360	-0.056
100	232	8.295	8.247	-0.048
100	233	8.332	8.318	-0.014
100	236	8.400	8.322	-0.078
100	238	8.386	8.511	0.125
100	243	8.334	8.348	0.014
105	244	8.418	8.483	0.065
105	246	8.370	8.437	0.067
105	248	8.362	8.394	0.032
105	374	8.400	8.552	0.152
105	376	8.331	8.475	0.144
105	375	8.534	8.552	0.152
	Max	8.377	8.351	-0.027
	Average	8.377	8.351	-0.027
	Min	8.294	8.184	-0.250
	Std Dev	0.048	0.077	0.074



9.5_T_SYNC_RISE	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns

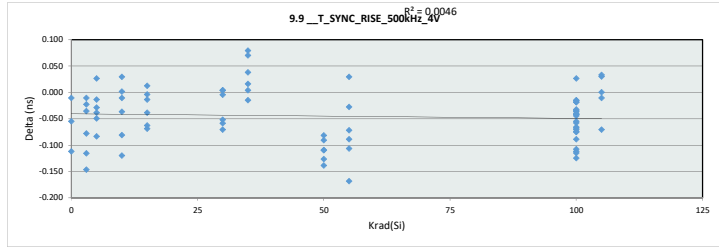
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.307	8.298	8.274	8.273	8.306	8.279	8.336	8.184	8.244	8.247	8.394
Average	8.341	8.331	8.362	8.367	8.376	8.384	8.437	8.238	8.303	8.330	8.468
Max	8.358	8.380	8.478	8.410	8.493	8.449	8.534	8.280	8.375	8.511	8.552
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



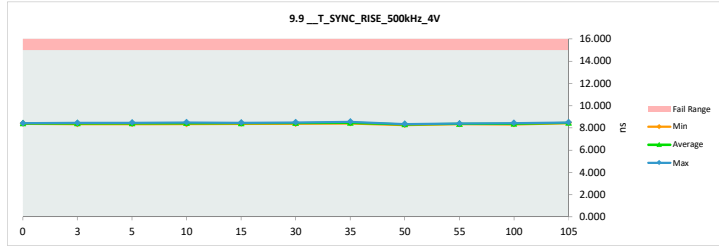
LDR TID Report  
TPS7H5001-SP

9.9_T_SYNC_RISE_500kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.513	8.401	-0.112
0	118	8.379	8.368	-0.011
0	122	8.418	8.363	-0.055
3	61	8.543	8.427	-0.116
3	62	8.449	8.426	-0.023
3	63	8.419	8.383	-0.036
3	64	8.392	8.314	-0.078
3	65	8.462	8.315	-0.147
3	66	8.398	8.387	-0.011
5	67	8.378	8.404	0.026
5	68	8.488	8.438	-0.050
5	69	8.488	8.449	-0.039
5	70	8.336	8.322	-0.014
5	71	8.439	8.410	-0.029
5	72	8.443	8.359	-0.084
10	73	8.543	8.423	-0.120
10	74	8.449	8.438	-0.011
10	75	8.419	8.382	-0.037
10	76	8.392	8.311	-0.081
10	77	8.462	8.463	0.001
10	78	8.398	8.427	0.029
15	79	8.378	8.374	-0.004
15	80	8.488	8.449	-0.039
15	81	8.488	8.419	-0.069
15	82	8.336	8.348	0.012
15	83	8.439	8.376	-0.063
15	84	8.443	8.429	-0.014
30	86	8.543	8.472	-0.071
30	89	8.449	8.390	-0.059
30	91	8.419	8.414	-0.005
30	92	8.392	8.340	-0.052
30	94	8.462	8.466	0.004
30	95	8.398	8.402	0.004
35	100	8.378	8.394	0.016
35	101	8.488	8.526	0.038
35	102	8.488	8.473	-0.015
35	103	8.336	8.415	0.079
35	104	8.439	8.509	0.070
35	105	8.443	8.447	0.004
50	106	8.401	8.319	-0.082
50	107	8.367	8.257	-0.110
50	108	8.415	8.324	-0.091
50	109	8.389	8.250	-0.139
50	110	8.425	8.315	-0.110
50	111	8.419	8.292	-0.127
55	113	8.462	8.355	-0.107
55	114	8.396	8.324	-0.072
55	115	8.460	8.371	-0.089
55	116	8.483	8.314	-0.169
55	126	8.300	8.329	0.029
55	128	8.375	8.347	-0.028
100	129	8.393	8.322	-0.071
100	131	8.413	8.347	-0.066
100	132	8.431	8.316	-0.115
100	133	8.421	8.380	-0.041
100	134	8.371	8.316	-0.055
100	135	8.433	8.365	-0.068
100	138	8.370	8.351	-0.019
100	139	8.342	8.307	-0.035
100	140	8.397	8.423	0.026
100	141	8.465	8.376	-0.089
100	142	8.434	8.376	-0.058
100	143	8.452	8.415	-0.037
100	144	8.386	8.311	-0.075
100	220	8.397	8.364	-0.033
100	221	8.420	8.312	-0.108
100	224	8.422	8.380	-0.042
100	225	8.440	8.395	-0.045
100	226	8.500	8.375	-0.125
100	228	8.429	8.413	-0.016
100	232	8.367	8.292	-0.075
100	233	8.397	8.378	-0.019
100	236	8.451	8.339	-0.112
100	238	8.437	8.422	-0.015
100	243	8.408	8.366	-0.042
105	244	8.504	8.433	-0.071
105	246	8.420	8.409	-0.011
105	248	8.443	8.476	0.033
105	374	8.480	8.480	0.000
105	375	8.406	8.436	0.030
105	Max	8.543	8.526	0.017
105	Average	8.427	8.382	-0.045
105	Min	8.300	8.250	-0.169
105	Std Dev	0.049	0.057	0.051



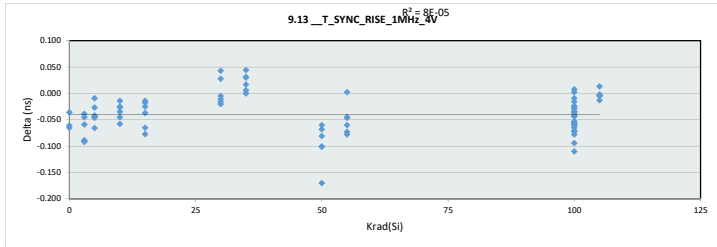
9.9_T_SYNC_RISE											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.363	8.314	8.322	8.311	8.348	8.340	8.394	8.250	8.314	8.292	8.409
Average	8.377	8.375	8.397	8.407	8.399	8.414	8.461	8.293	8.340	8.360	8.447
Max	8.401	8.427	8.449	8.463	8.449	8.472	8.526	8.324	8.371	8.423	8.480
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



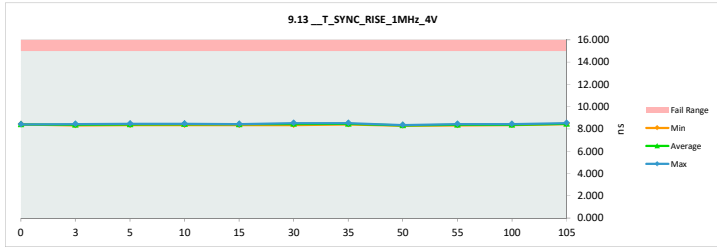
LDR TID Report  
TPS7H5001-SP

9.13 T_SYNC_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.469	8.407	-0.062
0	118	8.433	8.396	-0.037
0	122	8.461	8.395	-0.066
3	61	8.493	8.401	-0.092
3	62	8.476	8.436	-0.040
3	63	8.457	8.364	-0.093
3	64	8.380	8.320	-0.060
3	65	8.470	8.424	-0.046
3	66	8.460	8.370	-0.090
5	67	8.430	8.387	-0.043
5	68	8.511	8.466	-0.045
5	69	8.460	8.450	-0.010
5	70	8.377	8.349	-0.028
5	71	8.469	8.422	-0.047
5	72	8.447	8.380	-0.067
10	73	8.493	8.467	-0.026
10	74	8.476	8.430	-0.046
10	75	8.457	8.429	-0.028
10	76	8.380	8.344	-0.036
10	77	8.470	8.411	-0.059
10	78	8.460	8.445	-0.015
15	79	8.430	8.352	-0.078
15	80	8.511	8.445	-0.066
15	81	8.460	8.445	-0.015
15	82	8.377	8.351	-0.026
15	83	8.469	8.431	-0.038
15	84	8.447	8.428	-0.019
30	86	8.493	8.487	-0.006
30	89	8.476	8.464	-0.012
30	91	8.457	8.484	0.027
30	92	8.380	8.359	-0.021
30	94	8.470	8.512	0.042
30	95	8.460	8.443	-0.017
35	100	8.430	8.446	0.016
35	101	8.511	8.510	-0.001
35	102	8.460	8.465	0.005
35	103	8.377	8.406	0.029
35	104	8.469	8.512	0.043
35	105	8.447	8.477	0.030
50	106	8.437	8.335	-0.102
50	107	8.375	8.314	-0.061
50	108	8.419	8.350	-0.069
50	109	8.393	8.291	-0.102
50	110	8.466	8.295	-0.171
50	111	8.415	8.333	-0.082
55	113	8.440	8.366	-0.074
55	114	8.437	8.391	-0.046
55	115	8.490	8.411	-0.079
55	116	8.469	8.422	-0.047
55	126	8.324	8.325	0.001
55	128	8.447	8.386	-0.061
100	129	8.408	8.365	-0.043
100	131	8.459	8.402	-0.057
100	132	8.363	8.364	0.001
100	133	8.436	8.391	-0.045
100	134	8.372	8.342	-0.030
100	135	8.434	8.361	-0.073
100	138	8.374	8.381	0.007
100	139	8.353	8.336	-0.017
100	140	8.401	8.365	-0.036
100	141	8.463	8.424	-0.039
100	142	8.444	8.408	-0.036
100	143	8.447	8.381	-0.066
100	144	8.405	8.347	-0.058
100	220	8.465	8.354	-0.111
100	221	8.412	8.333	-0.079
100	224	8.462	8.400	-0.062
100	225	8.455	8.383	-0.072
100	226	8.452	8.442	-0.010
100	228	8.464	8.392	-0.072
100	232	8.387	8.333	-0.054
100	233	8.382	8.354	-0.028
100	236	8.455	8.360	-0.095
100	238	8.448	8.423	-0.025
100	243	8.463	8.402	-0.061
105	244	8.513	8.507	-0.006
105	246	8.428	8.440	0.012
105	248	8.471	8.466	-0.005
105	374	8.475	8.461	-0.014
105	376	8.427	8.424	-0.003
105	Max	8.513	8.512	0.043
105	Average	8.441	8.401	-0.040
105	Min	8.324	8.291	-0.171
105	Std Dev	0.041	0.053	0.038



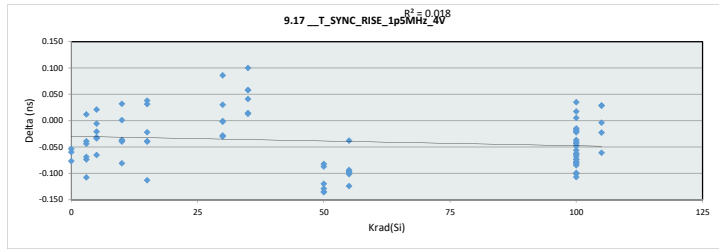
9.13 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.395	8.320	8.349	8.344	8.351	8.359	8.406	8.291	8.325	8.333	8.424
Average	8.399	8.386	8.409	8.421	8.409	8.458	8.469	8.320	8.384	8.377	8.460
Max	8.407	8.436	8.466	8.467	8.445	8.512	8.512	8.350	8.422	8.442	8.507
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



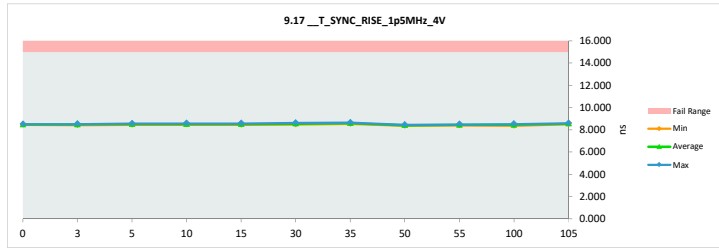
LDR TID Report  
TPS7H5001-SP

9.17_T_SYNC_RISE_1p5MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.537	8.460	-0.077
0	118	8.504	8.444	-0.060
0	122	8.566	8.512	-0.054
3	61	8.617	8.509	-0.108
3	62	8.594	8.520	-0.074
3	63	8.518	8.474	-0.044
3	64	8.501	8.432	-0.069
3	65	8.528	8.489	-0.039
3	66	8.494	8.506	0.012
5	67	8.449	8.470	0.021
5	68	8.639	8.574	-0.065
5	69	8.567	8.533	-0.034
5	70	8.479	8.447	-0.032
5	71	8.541	8.520	-0.021
5	72	8.520	8.514	-0.006
10	73	8.617	8.580	-0.037
10	74	8.594	8.513	-0.081
10	75	8.518	8.519	0.001
10	76	8.501	8.463	-0.038
10	77	8.528	8.488	-0.040
10	78	8.494	8.526	0.032
15	79	8.449	8.480	0.031
15	80	8.639	8.526	-0.113
15	81	8.567	8.528	-0.039
15	82	8.479	8.439	-0.040
15	83	8.541	8.579	0.038
15	84	8.520	8.498	-0.022
30	86	8.617	8.615	-0.002
30	89	8.594	8.564	-0.030
30	91	8.518	8.548	0.030
30	92	8.501	8.473	-0.028
30	94	8.528	8.527	-0.001
30	95	8.494	8.580	0.086
35	100	8.449	8.549	0.100
35	101	8.639	8.652	0.013
35	102	8.567	8.581	0.014
35	103	8.479	8.520	0.041
35	104	8.541	8.599	0.058
35	105	8.520	8.578	0.058
50	106	8.553	8.417	-0.136
50	107	8.435	8.353	-0.082
50	108	8.513	8.426	-0.087
50	109	8.529	8.400	-0.129
50	110	8.571	8.436	-0.135
50	111	8.513	8.393	-0.120
55	113	8.519	8.425	-0.094
55	114	8.557	8.455	-0.102
55	115	8.521	8.423	-0.098
55	116	8.595	8.471	-0.124
55	126	8.489	8.392	-0.097
55	128	8.532	8.494	-0.038
100	129	8.513	8.412	-0.101
100	131	8.552	8.490	-0.062
100	132	8.488	8.466	-0.022
100	133	8.543	8.461	-0.082
100	134	8.487	8.469	-0.018
100	135	8.515	8.435	-0.080
100	138	8.453	8.412	-0.041
100	139	8.446	8.378	-0.068
100	140	8.492	8.455	-0.037
100	141	8.543	8.436	-0.107
100	142	8.534	8.470	-0.064
100	143	8.529	8.509	-0.020
100	144	8.478	8.463	-0.015
100	220	8.518	8.433	-0.085
100	221	8.480	8.381	-0.099
100	224	8.474	8.509	0.035
100	225	8.565	8.491	-0.074
100	226	8.557	8.513	-0.044
100	228	8.504	8.521	0.017
100	232	8.420	8.372	-0.048
100	233	8.484	8.489	0.005
100	236	8.544	8.471	-0.073
100	238	8.528	8.472	-0.056
100	243	8.546	8.468	-0.078
105	244	8.628	8.567	-0.061
105	246	8.516	8.544	0.028
105	248	8.566	8.543	-0.023
105	374	8.574	8.603	0.029
105	376	8.574	8.511	-0.064
105	Max	8.639	8.652	0.100
105	Average	8.528	8.489	-0.039
105	Min	8.420	8.353	-0.136
105	Std Dev	0.049	0.062	0.053



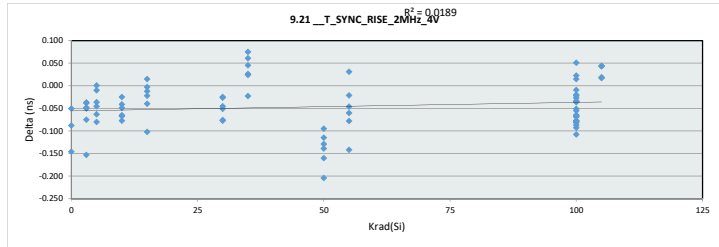
9.17_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.444	8.432	8.447	8.463	8.439	8.473	8.520	8.353	8.392	8.372	8.511
Average	8.472	8.488	8.510	8.515	8.508	8.551	8.580	8.404	8.443	8.457	8.554
Max	8.512	8.520	8.574	8.580	8.579	8.615	8.652	8.436	8.494	8.521	8.603
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



LDR TID Report  
TPS7H5001-SP

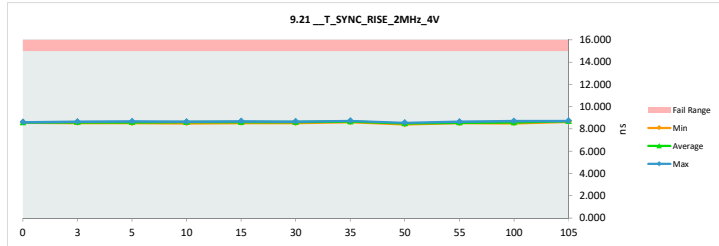
9.21 T_SYNC_RISE_2MHZ_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.712	8.565	-0.147
0	118	8.650	8.561	-0.089
0	122	8.651	8.600	-0.051
3	61	8.722	8.646	-0.076
3	62	8.663	8.614	-0.049
3	63	8.699	8.545	-0.154
3	64	8.578	8.539	-0.039
3	65	8.643	8.592	-0.051
3	66	8.664	8.626	-0.038
5	67	8.577	8.566	-0.011
5	68	8.694	8.613	-0.081
5	69	8.673	8.673	0.000
5	70	8.558	8.521	-0.037
5	71	8.689	8.625	-0.064
5	72	8.637	8.591	-0.046
10	73	8.722	8.644	-0.078
10	74	8.663	8.613	-0.050
10	75	8.699	8.633	-0.066
10	76	8.578	8.510	-0.068
10	77	8.643	8.601	-0.042
10	78	8.664	8.638	-0.026
15	79	8.577	8.564	-0.013
15	80	8.694	8.653	-0.041
15	81	8.673	8.687	0.014
15	82	8.558	8.535	-0.023
15	83	8.689	8.586	-0.103
15	84	8.637	8.633	-0.004
30	86	8.722	8.644	-0.078
30	89	8.663	8.586	-0.077
30	91	8.699	8.653	-0.046
30	92	8.578	8.526	-0.052
30	94	8.643	8.616	-0.027
30	95	8.664	8.638	-0.026
35	100	8.577	8.651	0.074
35	101	8.694	8.717	0.023
35	102	8.673	8.717	0.044
35	103	8.558	8.618	0.060
35	104	8.689	8.665	-0.024
35	105	8.637	8.662	0.025
50	106	8.627	8.531	-0.096
50	107	8.553	8.413	-0.140
50	108	8.721	8.516	-0.205
50	109	8.591	8.430	-0.161
50	110	8.676	8.546	-0.130
50	111	8.657	8.541	-0.116
55	113	8.663	8.520	-0.143
55	114	8.645	8.584	-0.061
55	115	8.646	8.567	-0.079
55	116	8.689	8.642	-0.047
55	126	8.489	8.519	0.030
55	128	8.597	8.575	-0.022
100	129	8.580	8.545	-0.035
100	131	8.644	8.556	-0.088
100	132	8.588	8.505	-0.083
100	133	8.654	8.584	-0.070
100	134	8.615	8.537	-0.078
100	135	8.615	8.535	-0.080
100	138	8.575	8.565	-0.010
100	139	8.579	8.551	-0.028
100	140	8.625	8.588	-0.037
100	141	8.642	8.576	-0.066
100	142	8.655	8.586	-0.069
100	143	8.627	8.677	0.050
100	144	8.582	8.561	-0.021
100	220	8.638	8.559	-0.079
100	221	8.588	8.536	-0.052
100	224	8.581	8.546	-0.035
100	225	8.629	8.643	0.014
100	226	8.679	8.623	-0.056
100	228	8.698	8.604	-0.094
100	232	8.576	8.540	-0.036
100	233	8.627	8.561	-0.066
100	236	8.709	8.600	-0.109
100	238	8.673	8.695	0.022
100	243	8.631	8.608	-0.023
105	244	8.675	8.691	0.016
105	246	8.631	8.673	0.042
105	248	8.673	8.716	0.043
105	374	8.695	8.713	0.018
105	376	8.595	8.638	0.043
	Max	8.722	8.717	0.074
	Average	8.640	8.595	-0.046
	Min	8.489	8.413	-0.205
	Std Dev	0.050	0.062	0.055



9.21 T_SYNC_R	
Test Site	
Tester	
Test Number	
Max Limit	15 ns
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.561	8.539	8.521	8.510	8.535	8.526	8.618	8.413	8.519	8.505	8.638
Average	8.575	8.594	8.598	8.607	8.610	8.611	8.672	8.496	8.568	8.578	8.686
Max	8.600	8.646	8.673	8.644	8.687	8.653	8.717	8.546	8.642	8.695	8.716
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

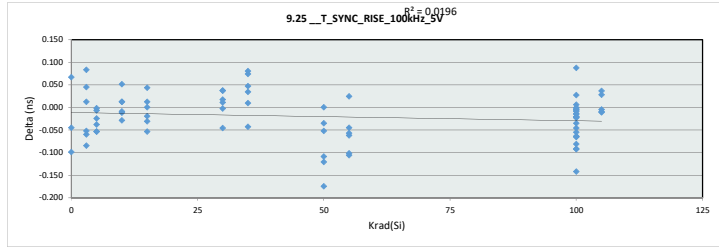




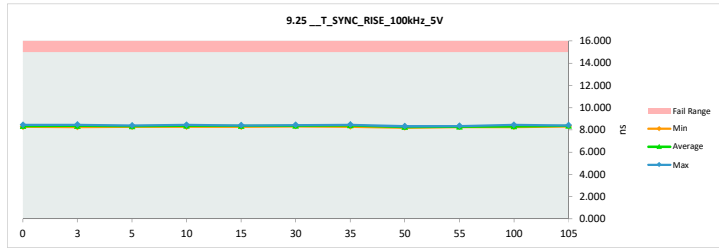
LDR TID Report  
TPS7H5001-SP

9.25_T_SYNC_RISE_100kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.360	8.427	0.067
0	118	8.314	8.269	-0.045
0	122	8.357	8.258	-0.099
3	61	8.374	8.386	0.012
3	62	8.322	8.367	0.045
3	63	8.355	8.438	0.083
3	64	8.283	8.223	-0.060
3	65	8.394	8.309	-0.085
3	66	8.360	8.308	-0.052
5	67	8.293	8.268	-0.025
5	68	8.376	8.369	-0.007
5	69	8.371	8.369	-0.002
5	70	8.307	8.253	-0.054
5	71	8.388	8.334	-0.054
5	72	8.337	8.299	-0.038
10	73	8.374	8.425	0.051
10	74	8.322	8.334	0.012
10	75	8.355	8.343	-0.012
10	76	8.283	8.254	-0.029
10	77	8.394	8.385	-0.009
10	78	8.360	8.372	0.012
15	79	8.293	8.336	0.043
15	80	8.376	8.376	0.000
15	81	8.371	8.383	0.012
15	82	8.307	8.253	-0.054
15	83	8.388	8.357	-0.031
15	84	8.337	8.317	-0.020
30	86	8.374	8.411	0.037
30	89	8.322	8.319	-0.003
30	91	8.355	8.309	-0.046
30	92	8.283	8.300	0.017
30	94	8.394	8.405	0.011
30	95	8.360	8.397	0.037
35	100	8.293	8.373	0.080
35	101	8.376	8.410	0.034
35	102	8.371	8.445	0.074
35	103	8.307	8.264	-0.043
35	104	8.388	8.397	0.009
35	105	8.337	8.384	0.047
50	106	8.380	8.205	-0.175
50	107	8.248	8.213	-0.035
50	108	8.368	8.316	-0.052
50	109	8.308	8.187	-0.121
50	110	8.392	8.283	-0.109
50	111	8.279	8.279	0.000
55	113	8.347	8.241	-0.106
55	114	8.339	8.237	-0.102
55	115	8.348	8.303	-0.045
55	116	8.381	8.324	-0.057
55	126	8.226	8.250	0.024
55	128	8.359	8.297	-0.062
100	129	8.309	8.245	-0.064
100	131	8.341	8.319	-0.022
100	132	8.268	8.247	-0.021
100	133	8.369	8.303	-0.066
100	134	8.294	8.321	0.027
100	135	8.320	8.326	0.006
100	138	8.337	8.322	-0.015
100	139	8.257	8.251	-0.006
100	140	8.341	8.260	-0.081
100	141	8.403	8.311	-0.092
100	142	8.319	8.304	-0.015
100	143	8.389	8.296	-0.093
100	144	8.293	8.270	-0.023
100	220	8.323	8.268	-0.055
100	221	8.273	8.238	-0.035
100	224	8.448	8.306	-0.142
100	225	8.371	8.350	-0.021
100	226	8.339	8.426	0.087
100	228	8.326	8.317	-0.009
100	232	8.298	8.234	-0.064
100	233	8.292	8.291	-0.001
100	236	8.362	8.316	-0.046
100	238	8.325	8.320	-0.005
100	243	8.334	8.333	-0.001
105	244	8.383	8.378	-0.005
105	246	8.325	8.315	-0.010
105	248	8.376	8.365	-0.011
105	374	8.346	8.382	0.036
105	376	8.315	8.343	0.028
105	Max	8.448	8.445	0.087
105	Average	8.340	8.319	-0.021
105	Min	8.226	8.187	-0.175
105	Std Dev	0.041	0.060	0.052



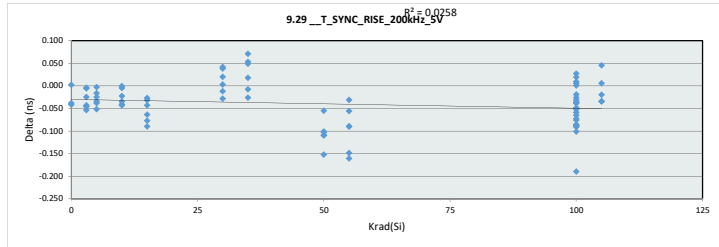
9.25_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.258	8.223	8.253	8.254	8.253	8.300	8.264	8.187	8.237	8.234	8.315
Average	8.318	8.339	8.315	8.352	8.337	8.357	8.379	8.247	8.275	8.299	8.357
Max	8.427	8.438	8.369	8.425	8.383	8.411	8.445	8.316	8.324	8.426	8.382
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



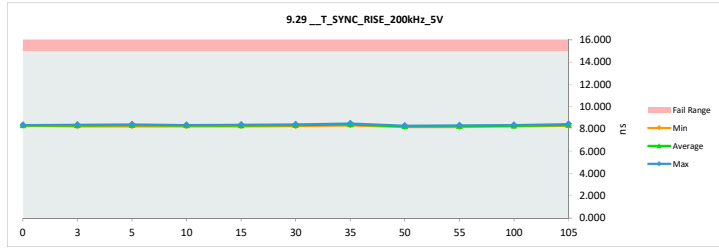
LDR TID Report  
TPS7H5001-SP

9.29_T_SYNC_RISE_200kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.331	8.332	0.001
0	118	8.352	8.309	-0.043
0	122	8.323	8.284	-0.039
3	61	8.372	8.365	-0.007
3	62	8.370	8.326	-0.044
3	63	8.370	8.315	-0.055
3	64	8.272	8.225	-0.047
3	65	8.333	8.307	-0.026
3	66	8.348	8.343	-0.005
5	67	8.334	8.317	-0.017
5	68	8.408	8.404	-0.004
5	69	8.415	8.376	-0.039
5	70	8.262	8.236	-0.026
5	71	8.375	8.322	-0.053
5	72	8.407	8.372	-0.035
10	73	8.372	8.336	-0.036
10	74	8.370	8.328	-0.042
10	75	8.370	8.346	-0.024
10	76	8.272	8.228	-0.044
10	77	8.333	8.331	-0.002
10	78	8.348	8.342	-0.006
15	79	8.334	8.290	-0.044
15	80	8.408	8.375	-0.033
15	81	8.415	8.337	-0.078
15	82	8.262	8.234	-0.028
15	83	8.375	8.310	-0.065
15	84	8.407	8.316	-0.091
30	86	8.372	8.413	0.041
30	89	8.370	8.357	-0.013
30	91	8.370	8.372	0.002
30	92	8.272	8.243	-0.029
30	94	8.333	8.370	0.037
30	95	8.348	8.367	0.019
35	100	8.334	8.351	0.017
35	101	8.408	8.478	0.070
35	102	8.415	8.406	-0.009
35	103	8.262	8.310	0.048
35	104	8.375	8.427	0.052
35	105	8.407	8.380	-0.027
50	106	8.373	8.220	-0.153
50	107	8.280	8.170	-0.110
50	108	8.360	8.249	-0.111
50	109	8.265	8.209	-0.056
50	110	8.385	8.281	-0.104
50	111	8.351	8.249	-0.102
55	113	8.392	8.242	-0.150
55	114	8.357	8.300	-0.057
55	115	8.391	8.229	-0.162
55	116	8.395	8.304	-0.091
55	126	8.269	8.178	-0.091
55	128	8.358	8.326	-0.032
100	129	8.409	8.218	-0.191
100	131	8.366	8.313	-0.053
100	132	8.303	8.266	-0.037
100	133	8.362	8.302	-0.060
100	134	8.309	8.282	-0.027
100	135	8.345	8.256	-0.089
100	138	8.316	8.284	-0.032
100	139	8.253	8.271	0.018
100	140	8.317	8.251	-0.066
100	141	8.395	8.318	-0.077
100	142	8.360	8.258	-0.102
100	143	8.344	8.349	0.005
100	144	8.310	8.290	-0.020
100	220	8.328	8.354	0.026
100	221	8.262	8.262	0.000
100	224	8.363	8.289	-0.074
100	225	8.392	8.355	-0.037
100	226	8.404	8.353	-0.051
100	228	8.379	8.292	-0.087
100	232	8.308	8.216	-0.092
100	233	8.305	8.313	0.008
100	236	8.359	8.309	-0.050
100	238	8.410	8.319	-0.091
100	243	8.343	8.303	-0.040
105	244	8.380	8.424	0.044
105	246	8.319	8.298	-0.021
105	248	8.370	8.375	0.005
105	374	8.372	8.336	-0.036
105	376	8.360	8.325	-0.035
105	Max	8.415	8.478	0.070
105	Average	8.350	8.310	-0.040
105	Min	8.253	8.170	-0.191
105	Std Dev	0.044	0.059	0.049



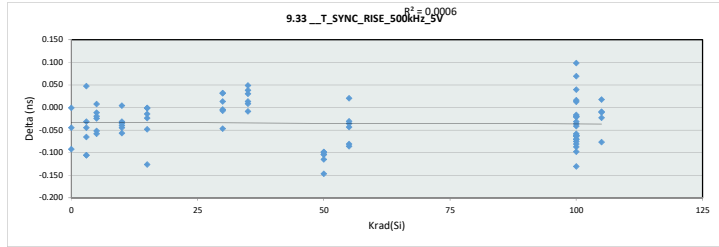
9.29_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.284	8.225	8.236	8.228	8.234	8.243	8.310	8.170	8.178	8.216	8.298
Average	8.308	8.314	8.338	8.319	8.310	8.354	8.392	8.230	8.263	8.293	8.352
Max	8.332	8.365	8.404	8.346	8.375	8.413	8.478	8.281	8.326	8.355	8.424
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



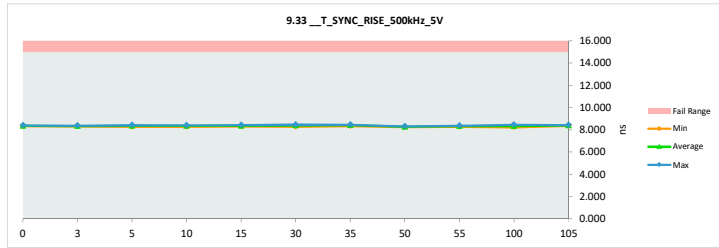
LDR TID Report  
TPS7H5001-SP

9.33 T_SYNC_RISE_500kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.419	8.374	-0.045
0	118	8.395	8.394	-0.001
0	122	8.425	8.332	-0.093
3	61	8.432	8.366	-0.066
3	62	8.395	8.350	-0.045
3	63	8.368	8.336	-0.032
3	64	8.273	8.320	0.047
3	65	8.423	8.317	-0.106
3	66	8.429	8.323	-0.106
5	67	8.357	8.332	-0.025
5	68	8.439	8.427	-0.012
5	69	8.449	8.390	-0.059
5	70	8.295	8.275	-0.020
5	71	8.434	8.382	-0.052
5	72	8.377	8.384	0.007
10	73	8.432	8.400	-0.032
10	74	8.395	8.360	-0.035
10	75	8.368	8.323	-0.045
10	76	8.273	8.277	0.004
10	77	8.423	8.383	-0.040
10	78	8.429	8.372	-0.057
15	79	8.357	8.355	-0.002
15	80	8.439	8.390	-0.049
15	81	8.449	8.322	-0.127
15	82	8.295	8.294	-0.001
15	83	8.434	8.419	-0.015
15	84	8.377	8.353	-0.024
30	86	8.432	8.445	0.013
30	89	8.395	8.387	-0.008
30	91	8.368	8.399	0.031
30	92	8.273	8.268	-0.005
30	94	8.423	8.376	-0.047
30	95	8.429	8.460	0.031
35	100	8.357	8.395	0.038
35	101	8.439	8.452	0.013
35	102	8.449	8.440	-0.009
35	103	8.295	8.325	0.030
35	104	8.434	8.442	0.008
35	105	8.377	8.425	0.048
50	106	8.357	8.253	-0.104
50	107	8.334	8.235	-0.099
50	108	8.376	8.277	-0.099
50	109	8.337	8.232	-0.105
50	110	8.442	8.295	-0.147
50	111	8.406	8.291	-0.115
55	113	8.428	8.347	-0.081
55	114	8.369	8.333	-0.036
55	115	8.348	8.368	0.020
55	116	8.407	8.321	-0.086
55	126	8.327	8.283	-0.044
55	128	8.353	8.322	-0.031
100	129	8.314	8.326	0.012
100	131	8.372	8.330	-0.042
100	132	8.334	8.313	-0.021
100	133	8.347	8.445	0.098
100	134	8.332	8.261	-0.071
100	135	8.426	8.328	-0.098
100	138	8.331	8.311	-0.020
100	139	8.331	8.268	-0.063
100	140	8.358	8.326	-0.032
100	141	8.374	8.303	-0.071
100	142	8.399	8.318	-0.081
100	143	8.426	8.367	-0.059
100	144	8.321	8.304	-0.017
100	220	8.347	8.386	0.039
100	221	8.344	8.256	-0.088
100	224	8.303	8.372	0.069
100	225	8.398	8.377	-0.021
100	226	8.400	8.416	0.016
100	228	8.416	8.354	-0.062
100	232	8.298	8.234	-0.064
100	233	8.366	8.290	-0.076
100	236	8.398	8.361	-0.037
100	238	8.411	8.280	-0.131
100	243	8.421	8.359	-0.062
105	244	8.431	8.408	-0.023
105	246	8.447	8.370	-0.077
105	248	8.371	8.388	0.017
105	374	8.407	8.397	-0.010
105	376	8.405	8.402	-0.011
105	Max	8.449	8.460	0.098
105	Average	8.382	8.347	-0.035
105	Min	8.273	8.232	-0.147
105	Std Dev	0.048	0.056	0.049



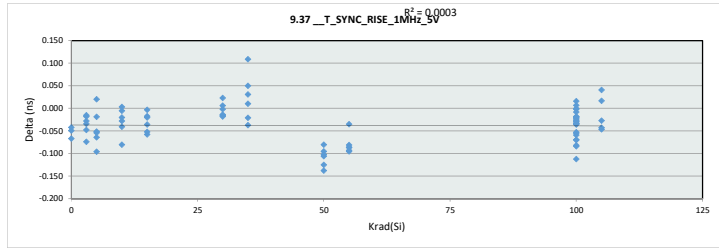
9.33 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.332	8.317	8.275	8.277	8.294	8.268	8.325	8.232	8.283	8.234	8.370
Average	8.367	8.335	8.365	8.353	8.356	8.389	8.413	8.264	8.329	8.329	8.393
Max	8.394	8.366	8.427	8.400	8.419	8.460	8.452	8.295	8.368	8.445	8.408
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



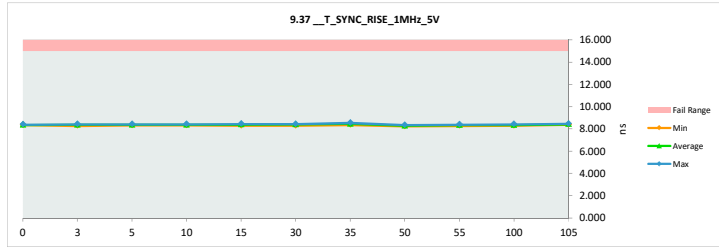
LDR TID Report  
TPS7H5001-SP

9.37_T_SYNC_RISE_1MHZ_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.443	8.393	-0.050
0	118	8.416	8.348	-0.068
0	122	8.411	8.368	-0.043
3	61	8.435	8.419	-0.016
3	62	8.425	8.376	-0.049
3	63	8.444	8.369	-0.075
3	64	8.312	8.283	-0.029
3	65	8.413	8.378	-0.035
3	66	8.427	8.408	-0.019
5	67	8.367	8.386	0.019
5	68	8.484	8.419	-0.065
5	69	8.474	8.419	-0.055
5	70	8.338	8.318	-0.020
5	71	8.484	8.387	-0.097
5	72	8.424	8.372	-0.052
10	73	8.435	8.406	-0.029
10	74	8.425	8.404	-0.021
10	75	8.444	8.363	-0.081
10	76	8.312	8.314	0.002
10	77	8.413	8.371	-0.042
10	78	8.427	8.421	-0.006
15	79	8.367	8.363	-0.004
15	80	8.484	8.431	-0.053
15	81	8.474	8.456	-0.018
15	82	8.338	8.301	-0.037
15	83	8.484	8.425	-0.059
15	84	8.424	8.403	-0.021
30	86	8.435	8.440	0.005
30	89	8.425	8.422	-0.003
30	91	8.444	8.429	-0.015
30	92	8.312	8.293	-0.019
30	94	8.413	8.435	0.022
30	95	8.427	8.410	-0.017
35	100	8.367	8.475	0.108
35	101	8.484	8.533	0.049
35	102	8.474	8.436	-0.038
35	103	8.338	8.368	0.030
35	104	8.484	8.462	-0.022
35	105	8.424	8.433	0.009
50	106	8.425	8.286	-0.139
50	107	8.358	8.251	-0.107
50	108	8.420	8.294	-0.126
50	109	8.313	8.232	-0.081
50	110	8.433	8.337	-0.096
50	111	8.431	8.328	-0.103
55	113	8.436	8.348	-0.088
55	114	8.444	8.357	-0.087
55	115	8.466	8.370	-0.096
55	116	8.428	8.392	-0.036
55	126	8.372	8.278	-0.094
55	128	8.424	8.342	-0.082
100	129	8.343	8.306	-0.037
100	131	8.425	8.371	-0.054
100	132	8.316	8.296	-0.020
100	133	8.383	8.382	-0.001
100	134	8.384	8.313	-0.071
100	135	8.375	8.390	0.015
100	138	8.343	8.324	-0.019
100	139	8.377	8.321	-0.056
100	140	8.399	8.286	-0.113
100	141	8.397	8.388	-0.009
100	142	8.401	8.372	-0.029
100	143	8.406	8.379	-0.027
100	144	8.365	8.331	-0.034
100	220	8.384	8.324	-0.060
100	221	8.361	8.326	-0.035
100	224	8.363	8.369	0.006
100	225	8.419	8.395	-0.024
100	226	8.447	8.417	-0.030
100	228	8.439	8.418	-0.021
100	232	8.341	8.338	-0.003
100	233	8.387	8.353	-0.034
100	236	8.429	8.346	-0.083
100	238	8.439	8.354	-0.085
100	243	8.427	8.356	-0.071
105	244	8.438	8.410	-0.028
105	246	8.470	8.426	-0.044
105	248	8.440	8.456	0.016
105	374	8.418	8.458	0.040
105	376	8.420	8.373	-0.047
105	Max	8.484	8.533	0.108
105	Average	8.410	8.373	-0.038
105	Min	8.312	8.232	-0.139
105	Std Dev	0.046	0.055	0.042



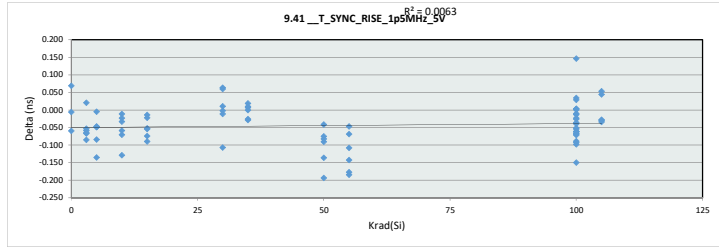
9.37_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.348	8.283	8.318	8.314	8.301	8.293	8.368	8.232	8.278	8.286	8.373
Average	8.370	8.372	8.384	8.380	8.397	8.405	8.451	8.288	8.348	8.352	8.425
Max	8.393	8.419	8.419	8.421	8.456	8.440	8.533	8.337	8.392	8.418	8.458
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



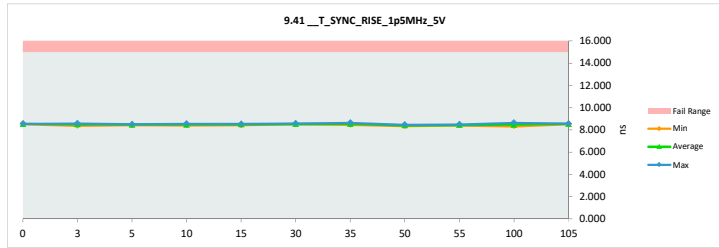
LDR TID Report  
TPS7H5001-SP

9.41 T_SYNC_RISE_1p5MHz_5V			
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.563	8.503	-0.060
0	118	8.501	8.495	-0.006
0	122	8.456	8.524	0.068
3	61	8.538	8.558	0.020
3	62	8.598	8.539	-0.059
3	63	8.493	8.407	-0.086
3	64	8.422	8.356	-0.066
3	65	8.492	8.425	-0.067
3	66	8.544	8.490	-0.054
5	67	8.462	8.457	-0.005
5	68	8.606	8.470	-0.136
5	69	8.531	8.482	-0.049
5	70	8.447	8.398	-0.049
5	71	8.560	8.475	-0.085
5	72	8.495	8.448	-0.047
10	73	8.538	8.526	-0.012
10	74	8.598	8.469	-0.129
10	75	8.493	8.434	-0.059
10	76	8.422	8.388	-0.034
10	77	8.492	8.468	-0.024
10	78	8.544	8.473	-0.071
15	79	8.462	8.439	-0.023
15	80	8.606	8.516	-0.090
15	81	8.531	8.476	-0.055
15	82	8.447	8.394	-0.053
15	83	8.560	8.485	-0.075
15	84	8.495	8.480	-0.015
30	86	8.538	8.548	0.010
30	89	8.598	8.491	-0.107
30	91	8.493	8.490	-0.003
30	92	8.422	8.485	0.063
30	94	8.492	8.551	0.059
30	95	8.544	8.532	-0.012
35	100	8.462	8.436	-0.026
35	101	8.606	8.612	0.006
35	102	8.531	8.540	0.009
35	103	8.447	8.465	0.018
35	104	8.560	8.531	-0.029
35	105	8.495	8.494	-0.001
50	106	8.553	8.359	-0.194
50	107	8.412	8.329	-0.083
50	108	8.534	8.443	-0.091
50	109	8.491	8.354	-0.137
50	110	8.451	8.409	-0.042
50	111	8.475	8.399	-0.076
55	113	8.562	8.377	-0.185
55	114	8.489	8.381	-0.108
55	115	8.576	8.433	-0.143
55	116	8.509	8.440	-0.069
55	126	8.568	8.390	-0.178
55	128	8.519	8.472	-0.047
100	129	8.456	8.432	-0.024
100	131	8.544	8.446	-0.098
100	132	8.468	8.318	-0.150
100	133	8.472	8.475	0.003
100	134	8.395	8.429	0.034
100	135	8.485	8.423	-0.062
100	138	8.456	8.430	-0.026
100	139	8.460	8.408	-0.052
100	140	8.476	8.421	-0.055
100	141	8.554	8.491	-0.063
100	142	8.464	8.610	0.146
100	143	8.489	8.453	-0.036
100	144	8.485	8.396	-0.089
100	220	8.466	8.452	-0.014
100	221	8.501	8.408	-0.093
100	224	8.483	8.486	0.003
100	225	8.543	8.476	-0.067
100	226	8.530	8.490	-0.040
100	228	8.523	8.454	-0.069
100	232	8.411	8.439	0.028
100	233	8.434	8.423	-0.011
100	236	8.530	8.468	-0.062
100	238	8.579	8.507	-0.072
100	243	8.465	8.465	0.000
105	244	8.525	8.494	-0.031
105	246	8.509	8.481	-0.028
105	248	8.564	8.529	-0.035
105	374	8.473	8.526	0.053
105	376	8.496	8.540	0.044
105	Max	8.606	8.612	0.146
105	Average	8.506	8.461	-0.044
105	Min	8.395	8.318	-0.194
105	Std Dev	0.051	0.058	0.059



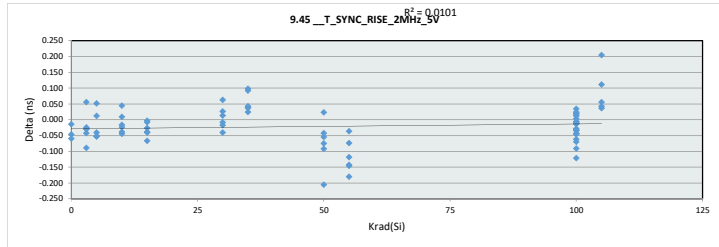
9.41 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.495	8.356	8.398	8.388	8.394	8.485	8.436	8.329	8.377	8.318	8.481
Average	8.507	8.463	8.455	8.460	8.465	8.516	8.513	8.382	8.416	8.450	8.514
Max	8.524	8.558	8.482	8.526	8.516	8.551	8.612	8.443	8.472	8.610	8.540
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



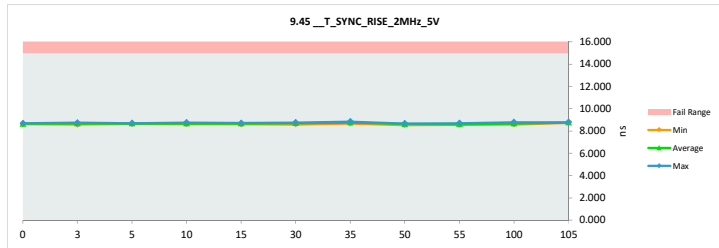
LDR TID Report  
TPS7H5001-SP

9.45_T_SYNC_RISE_2MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.672	8.611	-0.061
0	118	8.720	8.672	-0.048
0	122	8.659	8.643	-0.016
3	61	8.707	8.681	-0.026
3	62	8.676	8.730	0.054
3	63	8.665	8.574	-0.091
3	64	8.600	8.556	-0.044
3	65	8.701	8.670	-0.031
3	66	8.734	8.703	-0.031
5	67	8.655	8.613	-0.042
5	68	8.748	8.695	-0.053
5	69	8.716	8.662	-0.054
5	70	8.626	8.637	0.011
5	71	8.711	8.657	-0.054
5	72	8.646	8.696	0.050
10	73	8.707	8.750	0.043
10	74	8.676	8.636	-0.040
10	75	8.665	8.619	-0.046
10	76	8.600	8.608	0.008
10	77	8.701	8.683	-0.018
10	78	8.734	8.709	-0.025
15	79	8.655	8.613	-0.042
15	80	8.748	8.680	-0.068
15	81	8.716	8.688	-0.028
15	82	8.626	8.586	-0.040
15	83	8.711	8.701	-0.010
15	84	8.646	8.641	-0.005
30	86	8.707	8.697	-0.010
30	89	8.676	8.737	0.061
30	91	8.665	8.677	0.012
30	92	8.600	8.581	-0.019
30	94	8.701	8.726	0.025
30	95	8.734	8.692	-0.042
35	100	8.655	8.752	0.097
35	101	8.748	8.839	0.091
35	102	8.716	8.757	0.041
35	103	8.626	8.649	0.023
35	104	8.711	8.748	0.037
35	105	8.646	8.682	0.036
50	106	8.686	8.610	-0.076
50	107	8.718	8.511	-0.207
50	108	8.630	8.652	0.022
50	109	8.699	8.643	-0.056
50	110	8.713	8.669	-0.044
50	111	8.703	8.609	-0.094
55	113	8.725	8.687	-0.038
55	114	8.776	8.629	-0.147
55	115	8.752	8.608	-0.144
55	116	8.766	8.584	-0.182
55	126	8.659	8.539	-0.120
55	128	8.671	8.596	-0.075
100	129	8.651	8.669	0.018
100	131	8.701	8.630	-0.071
100	132	8.671	8.608	-0.063
100	133	8.712	8.677	-0.035
100	134	8.642	8.665	0.023
100	135	8.661	8.568	-0.093
100	138	8.665	8.635	-0.030
100	139	8.644	8.630	-0.014
100	140	8.659	8.644	-0.015
100	141	8.693	8.694	0.001
100	142	8.733	8.610	-0.123
100	143	8.680	8.713	0.033
100	144	8.655	8.675	0.020
100	220	8.688	8.675	-0.013
100	221	8.648	8.614	-0.034
100	224	8.653	8.645	-0.008
100	225	8.729	8.716	-0.013
100	226	8.772	8.783	0.011
100	228	8.747	8.739	-0.008
100	232	8.650	8.664	0.014
100	233	8.663	8.617	-0.046
100	236	8.720	8.675	-0.045
100	238	8.715	8.679	-0.036
100	243	8.745	8.698	-0.047
105	244	8.667	8.777	0.110
105	246	8.701	8.755	0.054
105	248	8.702	8.743	0.041
105	374	8.576	8.779	0.203
105	376	8.674	8.709	0.035
105	Max	8.776	8.839	0.203
105	Average	8.687	8.666	-0.021
105	Min	8.576	8.511	-0.207
105	Std Dev	0.043	0.061	0.063



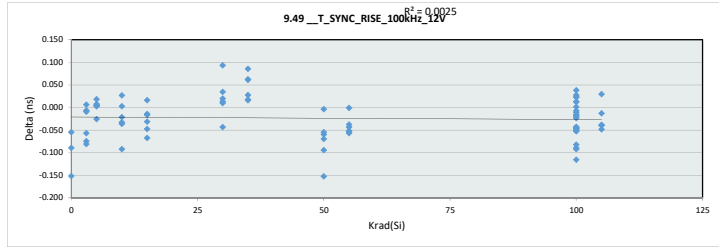
9.45_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.611	8.556	8.613	8.608	8.586	8.581	8.649	8.511	8.539	8.568	8.709
Average	8.642	8.652	8.660	8.668	8.652	8.685	8.738	8.616	8.607	8.663	8.753
Max	8.672	8.730	8.696	8.750	8.701	8.737	8.839	8.669	8.687	8.783	8.779
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



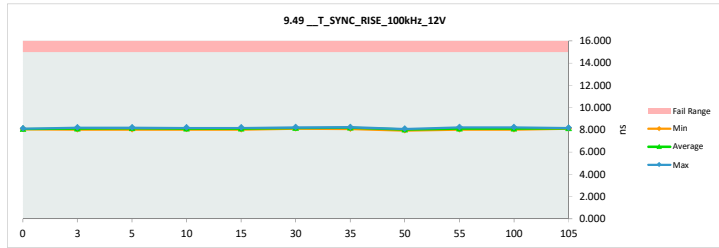
LDR TID Report  
TPS7H5001-SP

9.49_T_SYNC_RISE_100kHz_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.254	8.102	-0.152
0	118	8.087	8.032	-0.055
0	122	8.197	8.107	-0.090
3	61	8.168	8.087	-0.081
3	62	8.172	8.162	-0.010
3	63	8.195	8.188	-0.007
3	64	8.077	8.002	-0.075
3	65	8.004	8.010	0.006
3	66	8.193	8.136	-0.057
5	67	8.130	8.134	0.004
5	68	8.188	8.195	0.007
5	69	8.200	8.174	-0.026
5	70	7.998	8.003	0.005
5	71	8.143	8.145	0.002
5	72	8.143	8.161	0.018
10	73	8.168	8.131	-0.037
10	74	8.172	8.174	0.002
10	75	8.195	8.102	-0.093
10	76	8.077	8.055	-0.022
10	77	8.004	8.030	0.026
10	78	8.193	8.160	-0.033
15	79	8.130	8.062	-0.068
15	80	8.188	8.173	-0.015
15	81	8.200	8.168	-0.032
15	82	7.998	8.014	0.016
15	83	8.143	8.126	-0.017
15	84	8.143	8.095	-0.048
30	86	8.168	8.124	-0.044
30	89	8.172	8.206	0.034
30	91	8.195	8.204	0.009
30	92	8.077	8.096	0.019
30	94	8.004	8.097	0.093
30	95	8.193	8.206	0.013
35	100	8.130	8.191	0.061
35	101	8.188	8.215	0.027
35	102	8.200	8.216	0.016
35	103	7.998	8.060	0.062
35	104	8.143	8.228	0.085
35	105	8.143	8.160	0.017
50	106	8.143	8.073	-0.070
50	107	8.083	7.930	-0.153
50	108	8.090	8.030	-0.060
50	109	8.191	8.046	-0.055
50	110	8.032	8.028	-0.004
50	111	8.160	8.065	-0.095
55	113	8.043	8.005	-0.038
55	114	8.191	8.147	-0.044
55	115	8.183	8.129	-0.054
55	116	8.180	8.123	-0.057
55	126	8.076	8.024	-0.052
55	128	8.223	8.222	-0.001
100	129	8.056	8.012	-0.044
100	131	8.219	8.136	-0.083
100	132	8.118	8.110	-0.008
100	133	8.038	8.050	0.012
100	134	8.075	8.097	0.022
100	135	8.191	8.175	-0.016
100	138	8.133	8.086	-0.047
100	139	8.125	8.035	-0.090
100	140	8.120	8.071	-0.049
100	141	8.237	8.226	-0.011
100	142	8.145	8.146	0.001
100	143	8.161	8.143	-0.018
100	144	8.122	8.134	0.012
100	220	8.165	8.141	-0.024
100	221	8.153	8.132	-0.021
100	224	8.210	8.094	-0.116
100	225	8.062	8.016	-0.046
100	226	8.117	8.144	0.027
100	228	8.135	8.083	-0.052
100	232	8.023	8.046	0.023
100	233	8.099	8.055	-0.044
100	236	8.153	8.100	-0.053
100	238	8.266	8.173	-0.093
100	243	8.115	8.153	0.038
105	244	8.163	8.123	-0.040
105	246	8.153	8.140	-0.013
105	248	8.133	8.162	0.029
105	374	8.152	8.113	-0.039
105	375	8.157	8.108	-0.049
105	Max	8.266	8.228	0.093
105	Average	8.136	8.112	-0.024
105	Min	7.998	7.930	-0.153
105	Std Dev	0.064	0.065	0.047



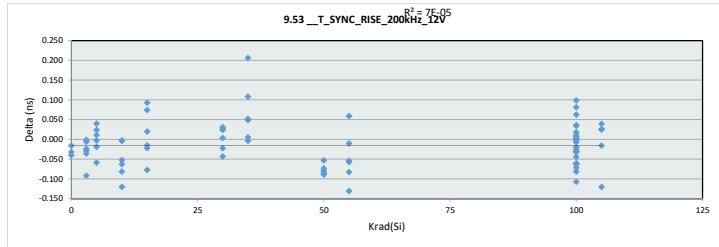
9.49_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.032	8.002	8.003	8.030	8.014	8.096	8.060	7.930	8.005	8.012	8.108
Average	8.080	8.098	8.135	8.109	8.106	8.156	8.178	8.029	8.108	8.107	8.129
Max	8.107	8.188	8.195	8.174	8.173	8.206	8.228	8.073	8.222	8.226	8.162
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



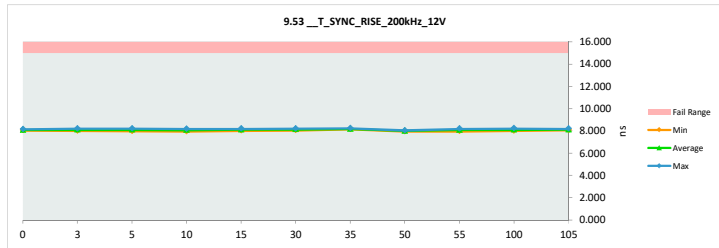
LDR TID Report  
TPS7H5001-SP

9.53 T_SYNC_RISE_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.189	8.156	-0.033
0	118	8.068	8.047	-0.041
0	122	8.112	8.095	-0.017
3	61	8.149	8.112	-0.037
3	62	8.175	8.150	-0.025
3	63	8.208	8.201	-0.007
3	64	8.093	8.000	-0.093
3	65	8.038	8.008	-0.030
3	66	8.139	8.137	-0.002
5	67	8.103	8.083	-0.020
5	68	8.186	8.196	0.010
5	69	8.175	8.171	-0.004
5	70	7.945	7.968	0.023
5	71	8.197	8.137	-0.060
5	72	8.052	8.091	0.039
10	73	8.149	8.096	-0.053
10	74	8.175	8.171	-0.004
10	75	8.208	8.144	-0.064
10	76	8.093	7.972	-0.121
10	77	8.038	7.956	-0.082
10	78	8.139	8.134	-0.005
15	79	8.103	8.087	-0.016
15	80	8.186	8.163	-0.023
15	81	8.175	8.194	0.019
15	82	7.945	8.018	0.073
15	83	8.197	8.119	-0.078
15	84	8.052	8.144	0.092
30	86	8.149	8.152	0.003
30	89	8.175	8.198	0.023
30	91	8.208	8.185	-0.023
30	92	8.093	8.049	-0.044
30	94	8.038	8.063	0.025
30	95	8.139	8.169	0.030
35	100	8.103	8.154	0.051
35	101	8.186	8.190	0.004
35	102	8.175	8.223	0.048
35	103	7.945	8.150	0.205
35	104	8.197	8.192	-0.005
35	105	8.052	8.159	0.107
50	106	8.105	8.016	-0.089
50	107	8.012	7.938	-0.074
50	108	8.093	8.007	-0.086
50	109	8.112	8.041	-0.090
50	110	8.064	8.010	-0.054
50	111	8.099	8.019	-0.080
55	113	8.093	7.961	-0.132
55	114	8.206	8.151	-0.055
55	115	8.235	8.151	-0.084
55	116	8.153	8.142	-0.011
55	126	8.051	7.993	-0.058
55	128	8.135	8.193	0.058
100	129	8.093	8.011	-0.082
100	131	8.133	8.136	0.003
100	132	8.113	8.085	-0.028
100	133	8.086	8.053	-0.033
100	134	8.115	8.053	-0.062
100	135	8.164	8.164	0.000
100	138	8.106	8.123	0.017
100	139	8.070	8.006	-0.064
100	140	8.097	8.104	0.007
100	141	8.184	8.162	-0.022
100	142	8.159	8.194	0.035
100	143	8.152	8.162	0.010
100	144	8.129	8.121	-0.008
100	220	8.106	8.186	0.080
100	221	8.155	8.123	-0.032
100	224	8.122	8.101	-0.021
100	225	8.059	8.014	-0.045
100	226	8.123	8.050	-0.073
100	228	8.048	8.145	0.097
100	232	8.030	8.063	0.033
100	233	8.119	8.058	-0.061
100	236	8.122	8.014	-0.108
100	238	8.155	8.217	0.062
100	243	8.095	8.090	-0.005
105	244	8.215	8.094	-0.121
105	246	8.152	8.190	0.038
105	248	8.085	8.109	0.024
105	374	8.078	8.061	-0.017
105	376	8.137	8.162	0.025
105	Max	8.235	8.223	0.205
105	Average	8.119	8.104	-0.016
105	Min	7.945	7.938	-0.132
105	Std Dev	0.062	0.073	0.058



9.53 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.047	8.000	7.968	7.956	8.018	8.049	8.150	7.938	7.961	8.006	8.061
Average	8.099	8.101	8.108	8.079	8.121	8.136	8.178	8.005	8.099	8.101	8.123
Max	8.156	8.201	8.196	8.171	8.194	8.198	8.223	8.041	8.193	8.217	8.190
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

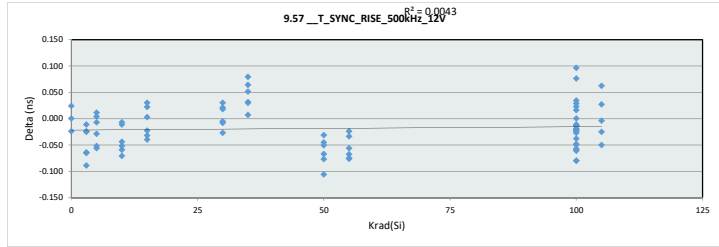




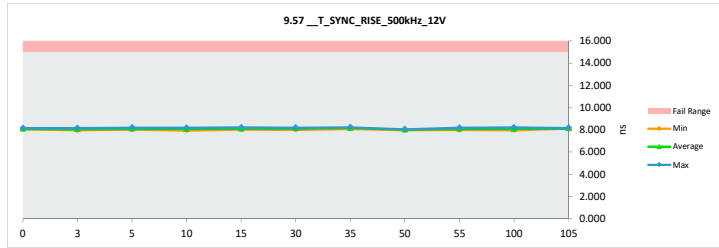
LDR TID Report  
TPS7H5001-SP

9.57_T_SYNC_RISE_500kHz_12V			
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.162	8.138	-0.024
0	118	8.063	8.063	0.000
0	122	8.046	8.070	0.024
3	61	8.133	8.044	-0.089
3	62	8.154	8.130	-0.024
3	63	8.209	8.144	-0.065
3	64	8.034	7.970	-0.064
3	65	8.013	7.988	-0.025
3	66	8.133	8.122	-0.011
5	67	8.070	8.063	-0.007
5	68	8.172	8.176	0.004
5	69	8.192	8.140	-0.052
5	70	8.019	8.030	0.011
5	71	8.139	8.110	-0.029
5	72	8.137	8.081	-0.056
10	73	8.133	8.062	-0.071
10	74	8.154	8.147	-0.007
10	75	8.209	8.165	-0.044
10	76	8.034	8.023	-0.011
10	77	8.013	7.954	-0.059
10	78	8.133	8.081	-0.052
15	79	8.070	8.100	0.030
15	80	8.172	8.149	-0.023
15	81	8.192	8.195	0.003
15	82	8.019	8.041	0.022
15	83	8.139	8.099	-0.040
15	84	8.137	8.105	-0.032
30	86	8.133	8.125	-0.008
30	89	8.154	8.172	0.018
30	91	8.209	8.182	-0.027
30	92	8.034	8.055	0.021
30	94	8.013	8.008	-0.005
30	95	8.133	8.163	0.030
35	100	8.070	8.134	0.064
35	101	8.172	8.223	0.051
35	102	8.192	8.199	0.007
35	103	8.019	8.098	0.079
35	104	8.139	8.169	0.030
35	105	8.137	8.168	0.031
50	106	8.089	7.983	-0.106
50	107	8.002	7.951	-0.051
50	108	8.083	8.038	-0.045
50	109	8.113	8.046	-0.067
50	110	8.024	7.993	-0.031
50	111	8.092	8.015	-0.077
55	113	8.101	8.026	-0.075
55	114	8.201	8.125	-0.076
55	115	8.180	8.146	-0.034
55	116	8.175	8.119	-0.056
55	126	8.054	7.986	-0.068
55	128	8.184	8.160	-0.024
100	129	8.000	7.980	-0.020
100	131	8.172	8.092	-0.080
100	132	8.063	8.049	-0.014
100	133	7.999	7.977	-0.022
100	134	8.022	8.098	0.076
100	135	8.122	8.097	-0.025
100	138	8.114	8.066	-0.048
100	139	8.067	8.009	-0.058
100	140	8.008	8.104	0.096
100	141	8.167	8.190	0.023
100	142	8.097	8.097	0.000
100	143	8.140	8.127	-0.013
100	144	8.115	8.035	-0.080
100	220	8.103	8.090	-0.013
100	221	8.144	8.083	-0.061
100	224	8.114	8.094	-0.020
100	225	8.027	7.989	-0.038
100	226	8.069	8.098	0.029
100	228	8.120	8.070	-0.050
100	232	8.024	8.058	0.034
100	233	8.026	8.042	0.016
100	236	8.117	8.059	-0.058
100	238	8.187	8.176	-0.011
100	243	8.116	8.089	-0.027
105	244	8.176	8.126	-0.050
105	246	8.088	8.150	0.062
105	248	8.091	8.118	0.027
105	374	8.141	8.116	-0.025
105	376	8.106	8.102	-0.004
105	Max	8.209	8.223	0.096
105	Average	8.106	8.088	-0.018
105	Min	7.999	7.951	-0.106
105	Std Dev	0.061	0.064	0.042



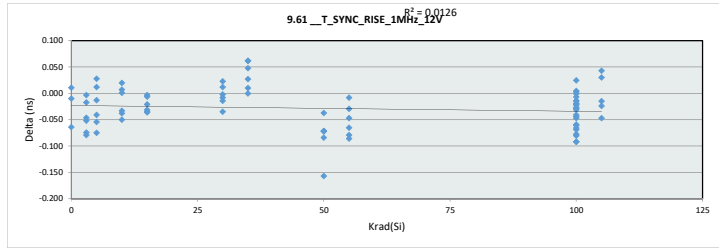
9.57_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.063	7.970	8.030	7.954	8.041	8.008	8.098	7.951	7.986	7.977	8.102
Average	8.090	8.066	8.100	8.072	8.115	8.118	8.165	8.004	8.094	8.074	8.122
Max	8.138	8.144	8.176	8.165	8.195	8.182	8.223	8.046	8.160	8.190	8.150
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



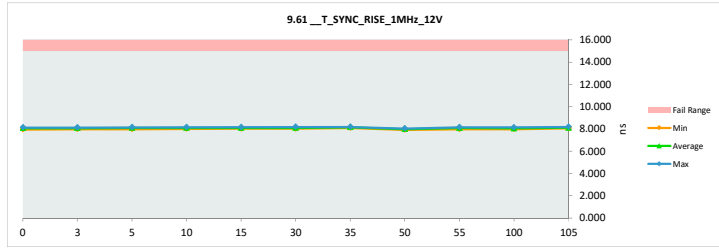
LDR TID Report  
TPS7H5001-SP

9.61_T_SYNC_RISE_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.130	8.119	-0.011
0	118	8.012	7.947	-0.065
0	122	8.088	8.098	0.010
3	61	8.092	8.039	-0.053
3	62	8.137	8.119	-0.018
3	63	8.183	8.103	-0.080
3	64	8.046	7.971	-0.075
3	65	7.996	7.992	-0.004
3	66	8.103	8.056	-0.047
5	67	8.080	8.107	0.027
5	68	8.180	8.125	-0.055
5	69	8.132	8.143	0.011
5	70	8.017	7.975	-0.042
5	71	8.132	8.056	-0.076
5	72	8.094	8.080	-0.014
10	73	8.092	8.098	0.006
10	74	8.137	8.163	-0.034
10	75	8.183	8.144	-0.039
10	76	8.046	7.995	-0.051
10	77	7.996	7.996	0.000
10	78	8.103	8.122	0.019
15	79	8.080	8.044	-0.036
15	80	8.180	8.143	-0.037
15	81	8.132	8.100	-0.032
15	82	8.017	8.010	-0.007
15	83	8.132	8.110	-0.022
15	84	8.094	8.090	-0.004
30	86	8.092	8.083	-0.009
30	89	8.137	8.134	-0.003
30	91	8.183	8.168	-0.015
30	92	8.046	8.068	0.022
30	94	7.996	8.007	0.011
30	95	8.103	8.067	-0.036
35	100	8.080	8.141	0.061
35	101	8.180	8.179	-0.001
35	102	8.132	8.179	0.047
35	103	8.017	8.078	0.061
35	104	8.132	8.141	0.009
35	105	8.094	8.120	0.026
50	106	8.113	7.955	-0.158
50	107	7.999	7.914	-0.085
50	108	8.062	7.989	-0.073
50	109	8.110	8.037	-0.073
50	110	7.972	7.934	-0.038
50	111	8.106	8.034	-0.072
55	113	8.048	7.961	-0.087
55	114	8.140	8.092	-0.048
55	115	8.167	8.101	-0.066
55	116	8.170	8.090	-0.080
55	126	8.025	7.995	-0.030
55	128	8.157	8.148	-0.009
100	129	8.007	7.992	-0.015
100	131	8.104	8.096	-0.008
100	132	8.089	8.029	-0.060
100	133	7.995	8.019	0.024
100	134	8.083	8.016	-0.067
100	135	8.135	8.112	-0.023
100	138	8.061	8.032	-0.029
100	139	8.036	8.034	-0.002
100	140	8.068	7.998	-0.070
100	141	8.148	8.128	-0.020
100	142	8.122	8.044	-0.078
100	143	8.083	8.038	-0.045
100	144	8.106	8.013	-0.093
100	220	8.098	8.066	-0.032
100	221	8.088	8.090	0.002
100	224	8.070	8.049	-0.021
100	225	8.069	7.988	-0.081
100	226	8.070	8.028	-0.042
100	228	8.116	8.023	-0.093
100	232	8.023	7.975	-0.048
100	233	8.055	7.993	-0.062
100	236	8.083	8.055	-0.028
100	238	8.118	8.122	0.004
100	243	8.087	8.072	-0.015
105	244	8.076	8.118	0.042
105	246	8.175	8.159	-0.016
105	248	8.099	8.051	-0.048
105	374	8.087	8.062	-0.025
105	376	8.073	8.102	0.029
105	Max	8.183	8.179	0.061
105	Average	8.091	8.063	-0.029
105	Min	7.972	7.914	-0.158
105	Std Dev	0.052	0.062	0.040



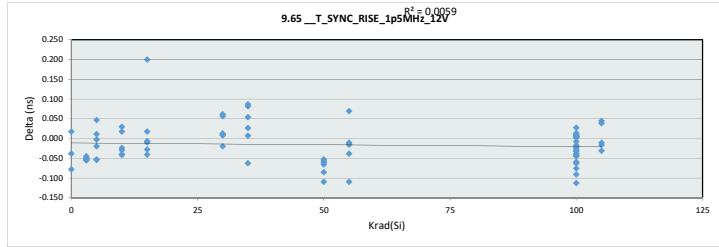
9.61_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.947	7.971	7.975	7.995	8.010	8.007	8.078	7.914	7.961	7.975	8.051
Average	8.055	8.047	8.081	8.076	8.083	8.088	8.140	7.977	8.065	8.042	8.098
Max	8.119	8.119	8.143	8.144	8.143	8.168	8.179	8.037	8.148	8.128	8.159
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



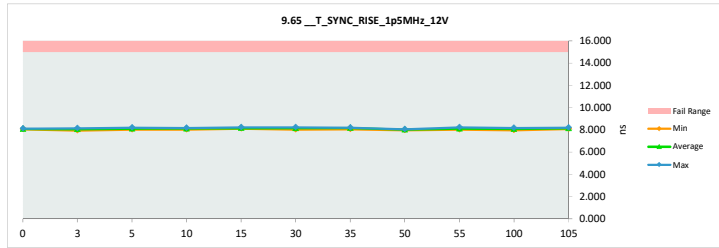
LDR TID Report  
TPS7H5001-SP

9.65_T_SYNC_RISE_1p5MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.131	8.092	-0.039
0	118	8.019	8.036	0.017
0	122	8.159	8.081	-0.078
3	61	8.123	8.069	-0.054
3	62	8.198	8.142	-0.056
3	63	8.165	8.112	-0.053
3	64	7.991	7.942	-0.049
3	65	8.008	7.963	-0.045
3	66	8.148	8.096	-0.052
5	67	8.096	8.093	-0.003
5	68	8.236	8.182	-0.054
5	69	8.159	8.105	-0.054
5	70	7.969	8.015	0.046
5	71	8.163	8.143	-0.020
5	72	8.115	8.126	0.011
10	73	8.123	8.099	-0.024
10	74	8.198	8.158	-0.040
10	75	8.165	8.135	-0.030
10	76	7.991	8.020	0.029
10	77	8.008	8.025	0.017
10	78	8.148	8.106	-0.042
15	79	8.096	8.113	0.017
15	80	8.236	8.208	-0.028
15	81	8.159	8.118	-0.041
15	82	7.969	8.168	0.199
15	83	8.163	8.156	-0.007
15	84	8.115	8.105	-0.010
30	86	8.123	8.131	0.008
30	89	8.198	8.178	-0.020
30	91	8.165	8.173	0.008
30	92	7.991	8.052	0.061
30	94	8.008	8.020	0.012
30	95	8.148	8.204	0.056
35	100	8.096	8.150	0.054
35	101	8.236	8.173	-0.063
35	102	8.159	8.166	0.007
35	103	7.969	8.055	0.086
35	104	8.163	8.189	0.026
35	105	8.115	8.196	0.081
50	106	8.118	8.008	-0.110
50	107	8.010	7.953	-0.057
50	108	8.094	8.041	-0.053
50	109	8.113	8.027	-0.086
50	110	8.046	7.985	-0.061
50	111	8.083	8.017	-0.066
55	113	8.032	8.017	-0.015
55	114	8.124	8.085	-0.039
55	115	8.139	8.208	0.069
55	116	8.177	8.067	-0.110
55	126	8.032	8.017	-0.015
55	128	8.193	8.182	-0.011
100	129	8.000	7.957	-0.043
100	131	8.183	8.153	-0.030
100	132	8.113	8.119	0.006
100	133	8.010	7.965	-0.045
100	134	8.105	8.080	-0.025
100	135	8.108	8.100	-0.008
100	138	8.068	8.095	0.027
100	139	8.107	8.031	-0.076
100	140	8.100	8.037	-0.063
100	141	8.125	8.107	-0.018
100	142	8.137	8.104	-0.033
100	143	8.124	8.131	0.007
100	144	8.154	8.041	-0.113
100	220	8.092	8.101	0.009
100	221	8.127	8.036	-0.091
100	224	8.082	8.084	0.002
100	225	8.026	7.987	-0.039
100	226	8.075	8.032	-0.043
100	228	8.117	8.057	-0.060
100	232	8.002	8.006	0.004
100	233	8.004	8.007	0.003
100	236	8.096	8.101	0.005
100	238	8.164	8.143	-0.021
100	243	8.064	8.077	0.013
105	244	8.136	8.119	-0.017
105	246	8.197	8.186	-0.011
105	248	8.092	8.130	0.038
105	374	8.036	8.080	0.044
105	376	8.109	8.078	-0.031
	Max	8.236	8.208	0.199
	Average	8.104	8.088	-0.016
	Min	7.969	7.942	-0.113
	Std Dev	0.068	0.067	0.049



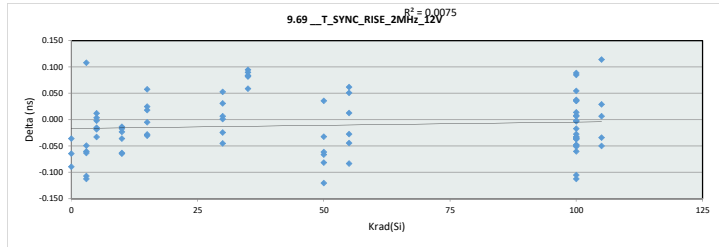
9.65_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.036	7.942	8.015	8.020	8.105	8.020	8.055	7.953	8.017	7.957	8.078
Average	8.070	8.054	8.111	8.091	8.145	8.126	8.155	8.005	8.096	8.065	8.119
Max	8.092	8.142	8.182	8.158	8.208	8.204	8.196	8.041	8.208	8.153	8.186
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



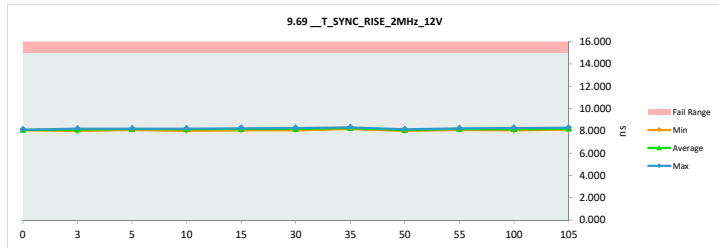
LDR TID Report  
TPS7H5001-SP

9.69_T_SYNC_RISE_2MHz_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	15	15
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.158	8.068	-0.090
0	118	8.098	8.061	-0.037
0	122	8.178	8.113	-0.065
3	61	8.189	8.139	-0.050
3	62	8.260	8.199	-0.061
3	63	8.239	8.175	-0.064
3	64	8.105	7.997	-0.108
3	65	8.024	8.131	0.107
3	66	8.218	8.105	-0.113
5	67	8.145	8.129	-0.016
5	68	8.224	8.190	-0.034
5	69	8.186	8.189	0.003
5	70	8.093	8.104	0.011
5	71	8.226	8.207	-0.019
5	72	8.172	8.169	-0.003
10	73	8.189	8.175	-0.014
10	74	8.260	8.195	-0.065
10	75	8.239	8.215	-0.024
10	76	8.105	8.068	-0.037
10	77	8.024	8.006	-0.018
10	78	8.218	8.154	-0.064
15	79	8.145	8.202	0.057
15	80	8.224	8.248	0.024
15	81	8.186	8.203	0.017
15	82	8.093	8.062	-0.031
15	83	8.226	8.220	-0.006
15	84	8.172	8.143	-0.029
30	86	8.189	8.164	-0.025
30	89	8.260	8.260	0.000
30	91	8.239	8.193	-0.046
30	92	8.105	8.111	0.006
30	94	8.024	8.076	0.052
30	95	8.218	8.248	0.030
35	100	8.145	8.234	0.089
35	101	8.224	8.307	0.083
35	102	8.186	8.267	0.081
35	103	8.093	8.187	0.094
35	104	8.226	8.284	0.058
35	105	8.172	8.254	0.082
50	106	8.175	8.108	-0.067
50	107	8.092	8.010	-0.082
50	108	8.049	8.084	0.035
50	109	8.180	8.147	-0.033
50	110	8.113	8.051	-0.062
50	111	8.166	8.045	-0.121
55	113	8.024	8.085	0.061
55	114	8.173	8.185	0.012
55	115	8.305	8.221	-0.084
55	116	8.248	8.203	-0.045
55	126	8.071	8.121	0.050
55	128	8.271	8.243	-0.028
100	129	8.052	8.089	0.037
100	131	8.225	8.176	-0.049
100	132	8.131	8.185	0.054
100	133	8.054	8.089	0.035
100	134	8.152	8.114	-0.038
100	135	8.225	8.260	0.035
100	138	8.163	8.057	-0.106
100	139	8.162	8.113	-0.049
100	140	8.131	8.095	-0.036
100	141	8.241	8.128	-0.113
100	142	8.236	8.175	-0.061
100	143	8.190	8.196	0.006
100	144	8.178	8.141	-0.037
100	220	8.211	8.193	-0.018
100	221	8.193	8.159	-0.034
100	224	8.177	8.174	-0.003
100	225	8.045	8.129	0.084
100	226	8.169	8.141	-0.028
100	228	8.211	8.159	-0.052
100	232	8.060	8.056	-0.004
100	233	8.122	8.088	-0.034
100	236	8.168	8.256	0.088
100	238	8.245	8.252	0.007
100	243	8.186	8.199	0.013
105	244	8.209	8.215	0.006
105	246	8.180	8.293	0.113
105	248	8.156	8.184	0.028
105	374	8.226	8.175	-0.051
105	376	8.160	8.125	-0.035
105		8.305	8.307	0.113
	Max			
	Average	8.168	8.157	-0.010
	Min	8.024	7.997	-0.121
	Std Dev	0.066	0.071	0.056



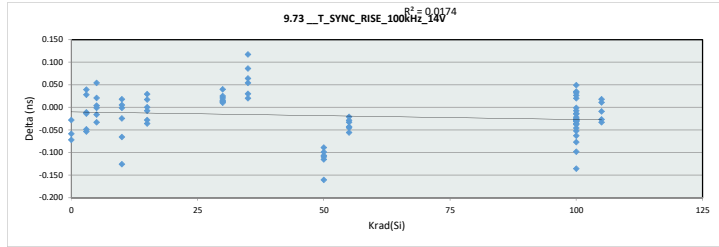
9.69_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.061	7.997	8.104	8.006	8.062	8.076	8.187	8.010	8.085	8.056	8.125
Average	8.081	8.124	8.165	8.136	8.180	8.175	8.256	8.074	8.176	8.151	8.198
Max	8.113	8.199	8.207	8.215	8.248	8.260	8.307	8.147	8.243	8.260	8.293
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



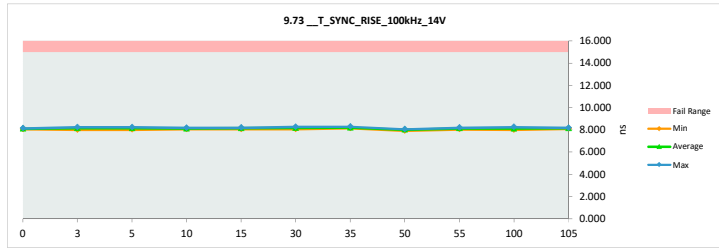
LDR TID Report  
TPS7H5001-SP

9.73 T_SYNC_RISE_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.197	8.125	-0.072
0	118	8.105	8.046	-0.059
0	122	8.120	8.092	-0.028
3	61	8.106	8.145	0.039
3	62	8.229	8.218	-0.011
3	63	8.219	8.165	-0.054
3	64	8.036	7.987	-0.049
3	65	8.055	8.083	0.028
3	66	8.181	8.167	-0.014
5	67	8.113	8.117	0.004
5	68	8.183	8.167	-0.016
5	69	8.160	8.214	0.054
5	70	8.043	8.010	-0.033
5	71	8.143	8.164	0.021
5	72	8.158	8.157	-0.001
10	73	8.106	8.111	0.005
10	74	8.229	8.163	-0.066
10	75	8.219	8.093	-0.126
10	76	8.036	8.054	0.018
10	77	8.055	8.054	-0.001
10	78	8.181	8.156	-0.025
15	79	8.113	8.142	0.029
15	80	8.183	8.147	-0.036
15	81	8.160	8.177	0.017
15	82	8.043	8.043	0.000
15	83	8.143	8.115	-0.028
15	84	8.158	8.150	-0.008
30	86	8.106	8.131	0.025
30	89	8.229	8.245	0.016
30	91	8.219	8.229	0.010
30	92	8.036	8.057	0.021
30	94	8.055	8.095	0.040
30	95	8.181	8.194	0.013
35	100	8.113	8.133	0.020
35	101	8.183	8.213	0.030
35	102	8.160	8.277	0.117
35	103	8.043	8.129	0.086
35	104	8.143	8.207	0.064
35	105	8.158	8.212	0.054
50	106	8.116	8.027	-0.089
50	107	8.064	7.903	-0.161
50	108	8.149	8.127	-0.022
50	109	8.147	8.040	-0.107
50	110	8.060	7.950	-0.110
50	111	8.150	8.051	-0.099
55	113	8.078	8.033	-0.045
55	114	8.216	8.183	-0.033
55	115	8.178	8.149	-0.029
55	116	8.173	8.130	-0.043
55	126	8.096	8.040	-0.056
55	128	8.205	8.184	-0.021
100	129	8.019	8.004	-0.015
100	131	8.156	8.182	0.026
100	132	8.163	8.127	-0.036
100	133	8.020	7.989	-0.031
100	134	8.060	8.109	0.049
100	135	8.172	8.204	0.032
100	138	8.113	8.061	-0.052
100	139	8.075	8.067	-0.008
100	140	8.082	8.081	-0.001
100	141	8.219	8.189	-0.030
100	142	8.136	8.073	-0.063
100	143	8.121	8.092	-0.029
100	144	8.128	8.114	-0.014
100	220	8.173	8.147	-0.026
100	221	8.151	8.105	-0.046
100	224	8.133	8.095	-0.038
100	225	8.103	8.026	-0.077
100	226	8.142	8.006	-0.136
100	228	8.144	8.118	-0.026
100	232	8.113	8.015	-0.098
100	233	8.091	8.069	-0.022
100	236	8.122	8.094	-0.028
100	238	8.205	8.240	0.035
100	243	8.145	8.165	0.020
105	244	8.156	8.147	-0.009
105	246	8.159	8.170	0.011
105	248	8.162	8.135	-0.027
105	374	8.128	8.095	-0.033
105	376	8.069	8.087	0.018
105	Max	8.229	8.277	0.117
105	Average	8.132	8.114	-0.018
105	Min	8.019	7.903	-0.161
105	Std Dev	0.056	0.073	0.050



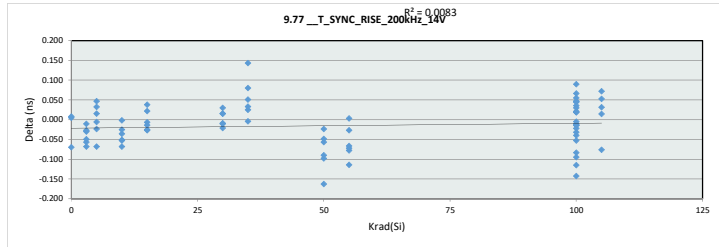
9.73 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.046	7.987	8.010	8.054	8.043	8.057	8.129	7.903	8.033	7.989	8.087
Average	8.088	8.128	8.138	8.105	8.129	8.159	8.195	8.001	8.120	8.099	8.127
Max	8.125	8.218	8.214	8.163	8.177	8.245	8.277	8.051	8.184	8.240	8.170
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



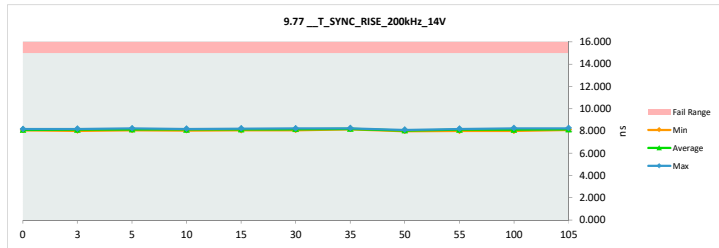
LDR TID Report  
TPS7H5001-SP

9.77_T_SYNC_RISE_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.148	8.152	0.004
0	118	8.039	8.046	0.007
0	122	8.132	8.061	-0.071
3	61	8.146	8.134	-0.012
3	62	8.215	8.146	-0.069
3	63	8.225	8.167	-0.058
3	64	8.027	7.996	-0.031
3	65	8.067	8.016	-0.051
3	66	8.160	8.132	-0.028
5	67	8.134	8.127	-0.007
5	68	8.176	8.207	0.031
5	69	8.206	8.137	-0.069
5	70	7.998	8.044	0.046
5	71	8.177	8.152	-0.025
5	72	8.089	8.103	0.014
10	73	8.146	8.092	-0.054
10	74	8.215	8.163	-0.052
10	75	8.225	8.156	-0.069
10	76	8.027	8.024	-0.003
10	77	8.067	8.030	-0.037
10	78	8.160	8.134	-0.026
15	79	8.134	8.108	-0.026
15	80	8.176	8.197	0.021
15	81	8.206	8.191	-0.015
15	82	7.998	8.035	0.037
15	83	8.177	8.169	-0.008
15	84	8.089	8.061	-0.028
30	86	8.146	8.124	-0.022
30	89	8.215	8.203	-0.012
30	91	8.225	8.215	-0.010
30	92	8.027	8.056	0.029
30	94	8.067	8.081	0.014
30	95	8.160	8.174	0.014
35	100	8.134	8.158	0.024
35	101	8.176	8.208	0.032
35	102	8.206	8.201	-0.005
35	103	7.998	8.140	0.142
35	104	8.177	8.227	0.050
35	105	8.089	8.168	0.079
50	106	8.126	8.035	-0.091
50	107	8.025	7.975	-0.050
50	108	8.130	8.031	-0.099
50	109	8.164	8.000	-0.164
50	110	8.105	8.047	-0.058
50	111	8.103	8.078	-0.025
55	113	8.047	7.979	-0.068
55	114	8.148	8.150	0.002
55	115	8.250	8.135	-0.115
55	116	8.182	8.154	-0.028
55	126	8.075	8.002	-0.073
55	128	8.249	8.170	-0.079
100	129	8.101	7.985	-0.116
100	131	8.154	8.148	-0.006
100	132	8.114	8.099	-0.015
100	133	8.052	8.070	0.018
100	134	8.121	8.025	-0.096
100	135	8.131	8.177	0.046
100	138	8.113	8.156	0.043
100	139	8.074	8.033	-0.041
100	140	8.086	8.071	-0.015
100	141	8.178	8.195	0.017
100	142	8.128	8.217	0.089
100	143	8.153	8.198	0.045
100	144	8.090	8.110	0.020
100	220	8.121	8.067	-0.054
100	221	8.138	8.123	-0.015
100	224	8.086	8.140	0.054
100	225	8.032	7.999	-0.033
100	226	8.072	8.101	0.029
100	228	8.149	8.005	-0.144
100	232	8.004	8.038	0.034
100	233	8.085	8.073	-0.012
100	236	8.087	8.152	0.065
100	238	8.197	8.112	-0.085
100	243	8.107	8.084	-0.023
105	244	8.151	8.074	-0.077
105	246	8.146	8.217	0.071
105	248	8.116	8.146	0.030
105	374	8.060	8.073	0.013
105	376	8.079	8.130	0.051
105	Max	8.250	8.227	0.142
105	Average	8.124	8.109	-0.015
105	Min	7.998	7.975	-0.164
105	Std Dev	0.063	0.068	0.055



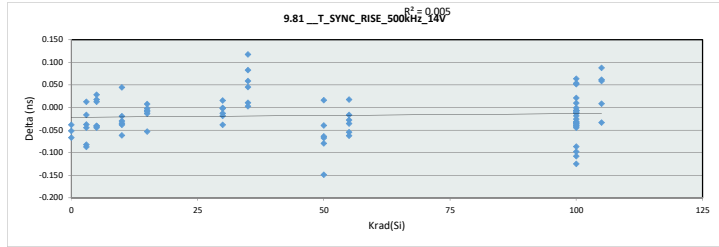
9.77_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.046	7.996	8.044	8.024	8.035	8.056	8.140	7.975	7.979	7.985	8.073
Average	8.086	8.099	8.128	8.100	8.127	8.142	8.184	8.028	8.098	8.099	8.128
Max	8.152	8.167	8.207	8.163	8.197	8.215	8.227	8.078	8.170	8.217	8.217
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



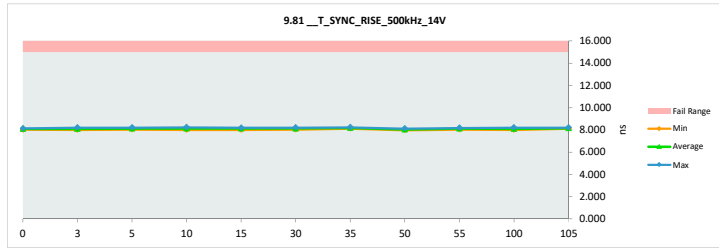
LDR TID Report  
TPS7H5001-SP

9.81_T_SYNC_RISE_500kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.163	8.111	-0.052
0	118	8.066	7.999	-0.067
0	122	8.122	8.083	-0.039
3	61	8.126	8.088	-0.038
3	62	8.165	8.177	0.012
3	63	8.174	8.157	-0.017
3	64	8.083	8.000	-0.083
3	65	8.023	7.978	-0.045
3	66	8.172	8.084	-0.088
5	67	8.067	8.095	0.028
5	68	8.224	8.183	-0.041
5	69	8.178	8.136	-0.042
5	70	7.989	8.006	0.017
5	71	8.118	8.130	0.012
5	72	8.127	8.082	-0.045
10	73	8.126	8.096	-0.030
10	74	8.165	8.209	0.044
10	75	8.174	8.154	-0.020
10	76	8.083	8.021	-0.062
10	77	8.023	7.984	-0.039
10	78	8.172	8.137	-0.035
15	79	8.067	8.074	0.007
15	80	8.224	8.170	-0.054
15	81	8.178	8.164	-0.014
15	82	7.989	7.982	-0.007
15	83	8.118	8.115	-0.003
15	84	8.127	8.117	-0.010
30	86	8.126	8.123	-0.003
30	89	8.165	8.146	-0.019
30	91	8.174	8.189	0.015
30	92	8.083	8.082	-0.001
30	94	8.023	8.010	-0.013
30	95	8.172	8.133	-0.039
35	100	8.067	8.125	0.058
35	101	8.224	8.226	0.002
35	102	8.178	8.188	0.010
35	103	7.989	8.106	0.117
35	104	8.118	8.200	0.082
35	105	8.127	8.172	0.045
50	106	8.094	8.030	-0.064
50	107	7.957	7.973	0.016
50	108	8.051	7.971	-0.080
50	109	8.126	8.058	-0.068
50	110	8.155	8.006	-0.149
50	111	8.138	8.098	-0.040
55	113	8.072	8.009	-0.063
55	114	8.163	8.135	-0.028
55	115	8.206	8.151	-0.055
55	116	8.171	8.135	-0.036
55	126	8.027	8.010	-0.017
55	128	8.150	8.167	0.017
100	129	8.000	7.981	-0.019
100	131	8.123	8.115	-0.008
100	132	8.075	8.062	-0.013
100	133	8.045	8.013	-0.032
100	134	8.110	8.068	-0.042
100	135	8.113	8.164	0.051
100	138	8.099	8.062	-0.037
100	139	8.039	8.031	-0.008
100	140	8.137	8.039	-0.098
100	141	8.140	8.133	-0.007
100	142	8.168	8.060	-0.108
100	143	8.086	8.149	0.063
100	144	8.102	8.068	-0.034
100	220	8.095	8.104	0.009
100	221	8.149	8.024	-0.125
100	224	8.120	8.094	-0.026
100	225	8.060	7.973	-0.087
100	226	8.088	8.087	-0.001
100	228	8.119	8.074	-0.045
100	232	8.012	7.972	-0.040
100	233	8.035	8.024	-0.011
100	236	8.096	8.117	0.021
100	238	8.136	8.189	0.053
100	243	8.090	8.053	-0.037
105	244	8.135	8.101	-0.034
105	246	8.169	8.177	0.008
105	248	8.050	8.137	0.087
105	374	8.060	8.118	0.058
105	376	8.067	8.128	0.061
105	Max	8.224	8.226	0.117
105	Average	8.109	8.091	-0.018
105	Min	7.957	7.971	-0.149
105	Std Dev	0.060	0.067	0.048



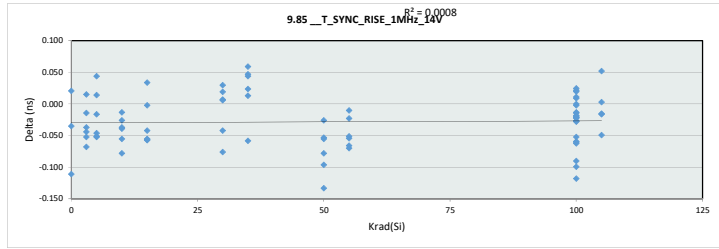
9.81_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.999	7.978	8.006	7.984	7.982	8.010	8.106	7.971	8.009	7.972	8.101
Average	8.064	8.081	8.105	8.100	8.104	8.114	8.170	8.023	8.101	8.069	8.132
Max	8.111	8.177	8.183	8.209	8.170	8.189	8.226	8.098	8.167	8.189	8.177
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



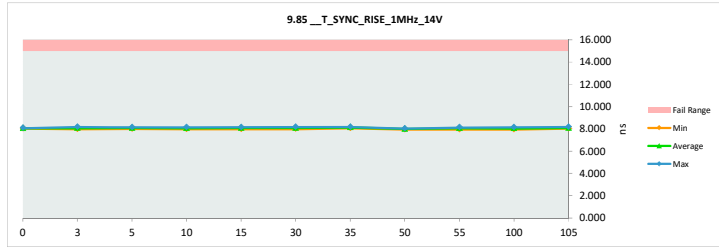
LDR TID Report  
TPS7H5001-SP

9.85_T_SYNC_RISE_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.184	8.072	-0.112
0	118	8.006	8.026	0.020
0	122	8.089	8.053	-0.036
3	61	8.091	8.053	-0.038
3	62	8.157	8.171	0.014
3	63	8.170	8.117	-0.053
3	64	8.018	8.003	-0.015
3	65	8.029	7.984	-0.045
3	66	8.173	8.104	-0.069
5	67	8.070	8.083	0.013
5	68	8.193	8.140	-0.053
5	69	8.109	8.152	0.043
5	70	8.009	7.992	-0.017
5	71	8.144	8.092	-0.052
5	72	8.135	8.088	-0.047
10	73	8.091	8.051	-0.040
10	74	8.157	8.130	-0.027
10	75	8.170	8.132	-0.038
10	76	8.018	8.004	-0.014
10	77	8.029	7.973	-0.056
10	78	8.173	8.094	-0.079
15	79	8.070	8.067	-0.003
15	80	8.193	8.135	-0.058
15	81	8.109	8.142	0.033
15	82	8.009	7.966	-0.043
15	83	8.144	8.087	-0.057
15	84	8.135	8.079	-0.056
30	86	8.091	8.109	0.018
30	89	8.157	8.162	0.005
30	91	8.170	8.176	0.006
30	92	8.018	8.047	0.029
30	94	8.029	7.986	-0.043
30	95	8.173	8.096	-0.077
35	100	8.070	8.128	0.058
35	101	8.193	8.134	-0.059
35	102	8.109	8.152	0.043
35	103	8.009	8.055	0.046
35	104	8.144	8.167	0.023
35	105	8.135	8.147	0.012
50	106	8.104	8.007	-0.097
50	107	7.990	7.934	-0.056
50	108	8.051	8.024	-0.027
50	109	8.088	8.034	-0.054
50	110	8.077	7.943	-0.134
50	111	8.092	8.013	-0.079
55	113	8.003	7.992	-0.011
55	114	8.145	8.074	-0.071
55	115	8.154	8.130	-0.024
55	116	8.155	8.088	-0.067
55	126	8.007	7.955	-0.052
55	128	8.169	8.114	-0.055
100	129	7.973	7.983	0.010
100	131	8.169	8.069	-0.100
100	132	8.046	8.065	0.019
100	133	7.987	7.995	0.008
100	134	8.030	8.010	-0.020
100	135	8.118	8.115	-0.003
100	138	8.048	8.047	-0.001
100	139	8.064	8.041	-0.023
100	140	8.084	8.056	-0.028
100	141	8.136	8.073	-0.063
100	142	8.079	8.057	-0.022
100	143	8.104	8.089	-0.015
100	144	8.126	8.035	-0.091
100	220	8.102	8.088	-0.014
100	221	8.122	8.069	-0.053
100	224	8.072	8.044	-0.028
100	225	8.105	7.986	-0.119
100	226	8.019	8.043	0.024
100	228	8.086	8.026	-0.060
100	232	8.006	7.945	-0.061
100	233	7.985	8.006	0.021
100	236	8.075	8.056	-0.019
100	238	8.157	8.135	-0.022
100	243	8.069	8.040	-0.029
105	244	8.126	8.076	-0.050
105	246	8.113	8.164	0.051
105	248	8.055	8.038	-0.017
105	374	8.063	8.065	0.002
105	376	8.090	8.074	-0.016
105	Max	8.193	8.176	0.058
105	Average	8.093	8.064	-0.028
105	Min	7.973	7.934	-0.134
105	Std Dev	0.060	0.060	0.041



9.85_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.026	7.984	7.992	7.973	7.966	7.986	8.055	7.934	7.955	7.945	8.038
Average	8.050	8.072	8.091	8.064	8.079	8.096	8.131	7.993	8.059	8.045	8.083
Max	8.072	8.171	8.152	8.132	8.142	8.176	8.167	8.034	8.130	8.135	8.164
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000

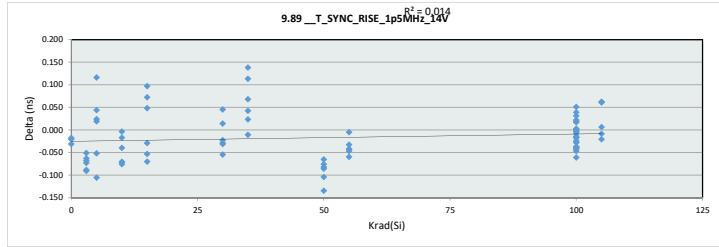




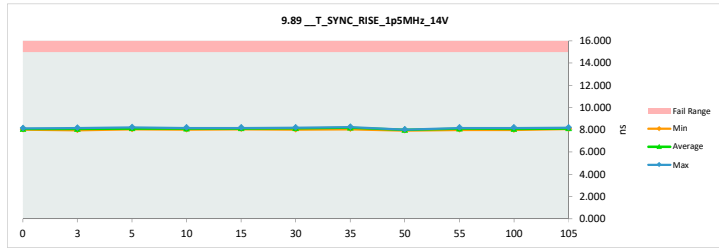
LDR TID Report  
TPS7H5001-SP

9.89_T_SYNC_RISE_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	15 15
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.156	8.138	-0.018
0	118	8.039	8.018	-0.021
0	122	8.130	8.099	-0.031
3	61	8.139	8.071	-0.068
3	62	8.219	8.156	-0.063
3	63	8.223	8.150	-0.073
3	64	8.092	8.003	-0.089
3	65	8.048	7.957	-0.091
3	66	8.137	8.086	-0.051
5	67	8.022	8.041	0.019
5	68	8.187	8.211	0.024
5	69	8.131	8.175	0.044
5	70	7.994	8.110	0.116
5	71	8.183	8.077	-0.106
5	72	8.159	8.107	-0.052
10	73	8.139	8.068	-0.071
10	74	8.219	8.179	-0.040
10	75	8.223	8.147	-0.076
10	76	8.092	8.021	-0.071
10	77	8.048	8.031	-0.017
10	78	8.137	8.133	-0.004
15	79	8.022	8.119	0.097
15	80	8.187	8.158	-0.029
15	81	8.131	8.179	0.048
15	82	7.994	8.066	0.072
15	83	8.183	8.113	-0.070
15	84	8.159	8.106	-0.053
30	86	8.139	8.153	0.014
30	89	8.219	8.188	-0.031
30	91	8.223	8.168	-0.055
30	92	8.092	8.063	-0.029
30	94	8.048	8.025	-0.023
30	95	8.137	8.182	0.045
35	100	8.022	8.160	0.138
35	101	8.187	8.176	-0.011
35	102	8.131	8.244	0.113
35	103	7.994	8.062	0.068
35	104	8.183	8.206	0.023
35	105	8.159	8.201	0.042
50	106	8.109	8.005	-0.104
50	107	8.003	7.938	-0.065
50	108	8.103	8.021	-0.082
50	109	8.152	8.017	-0.135
50	110	8.100	8.014	-0.086
50	111	8.080	8.004	-0.076
55	113	8.041	8.008	-0.033
55	114	8.146	8.099	-0.047
55	115	8.222	8.162	-0.060
55	116	8.145	8.102	-0.043
55	126	8.077	8.034	-0.043
55	128	8.183	8.178	-0.005
100	129	8.048	8.008	-0.040
100	131	8.148	8.134	-0.014
100	132	8.100	8.096	-0.004
100	133	8.009	8.010	0.001
100	134	8.112	8.095	-0.017
100	135	8.105	8.108	0.003
100	138	8.084	8.103	0.019
100	139	8.063	8.080	0.017
100	140	8.054	8.015	-0.039
100	141	8.140	8.179	0.039
100	142	8.185	8.142	-0.043
100	143	8.141	8.104	-0.037
100	144	8.104	8.102	-0.002
100	220	8.073	8.104	0.031
100	221	8.103	8.064	-0.039
100	224	8.141	8.113	-0.028
100	225	7.996	8.018	0.022
100	226	8.091	8.084	-0.007
100	228	8.073	8.049	-0.024
100	232	8.050	7.989	-0.061
100	233	7.980	8.031	0.051
100	236	8.079	8.062	-0.017
100	238	8.206	8.159	-0.047
100	243	8.067	8.039	-0.028
105	244	8.150	8.129	-0.021
105	246	8.125	8.187	0.062
105	248	8.109	8.100	-0.009
105	374	8.052	8.113	0.061
105	376	8.073	8.079	0.006
105	Max	8.223	8.244	0.138
105	Average	8.112	8.095	-0.017
105	Min	7.980	7.938	-0.135
105	Std Dev	0.065	0.066	0.054



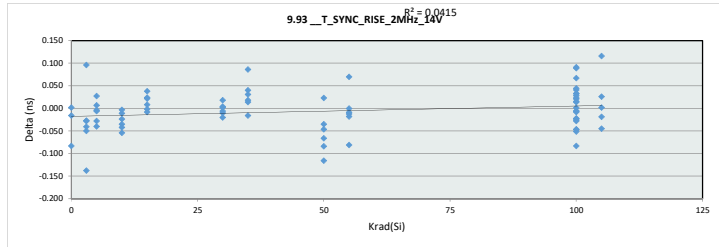
9.89_T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.018	7.957	8.041	8.021	8.066	8.025	8.062	7.938	8.008	7.989	8.079
Average	8.085	8.071	8.120	8.097	8.124	8.130	8.175	8.000	8.097	8.079	8.122
Max	8.138	8.156	8.211	8.179	8.179	8.188	8.244	8.021	8.178	8.179	8.187
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



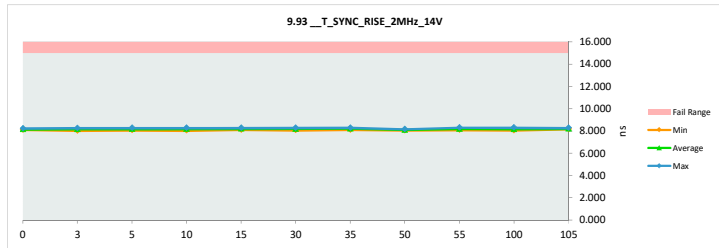
LDR TID Report  
TPS7H5001-SP

9.93 T_SYNC_RISE_2MHz_14V			
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	15	15	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.209	8.210	0.001
0	118	8.111	8.094	-0.017
0	122	8.206	8.122	-0.084
3	61	8.199	8.157	-0.042
3	62	8.260	8.232	-0.028
3	63	8.246	8.195	-0.051
3	64	8.129	7.990	-0.139
3	65	8.045	8.140	0.095
3	66	8.254	8.225	-0.029
5	67	8.151	8.157	0.006
5	68	8.232	8.258	0.026
5	69	8.209	8.202	-0.007
5	70	8.087	8.058	-0.029
5	71	8.220	8.179	-0.041
5	72	8.179	8.174	-0.005
10	73	8.199	8.163	-0.036
10	74	8.260	8.248	-0.012
10	75	8.246	8.242	-0.004
10	76	8.129	8.074	-0.055
10	77	8.045	8.020	-0.025
10	78	8.254	8.211	-0.043
15	79	8.151	8.174	0.023
15	80	8.232	8.229	-0.003
15	81	8.209	8.246	0.037
15	82	8.087	8.094	0.007
15	83	8.220	8.211	-0.009
15	84	8.179	8.200	0.021
30	86	8.199	8.216	0.017
30	89	8.260	8.261	0.001
30	91	8.246	8.225	-0.021
30	92	8.129	8.118	-0.011
30	94	8.045	8.048	0.003
30	95	8.254	8.247	-0.007
35	100	8.151	8.236	0.085
35	101	8.232	8.262	0.030
35	102	8.209	8.192	-0.017
35	103	8.087	8.100	0.013
35	104	8.220	8.237	0.017
35	105	8.179	8.218	0.039
50	106	8.194	8.109	-0.085
50	107	8.073	8.037	-0.036
50	108	8.041	8.069	0.022
50	109	8.181	8.134	-0.047
50	110	8.130	8.063	-0.067
50	111	8.185	8.068	-0.117
55	113	8.015	8.084	0.069
55	114	8.247	8.165	-0.082
55	115	8.269	8.259	-0.010
55	116	8.207	8.188	-0.019
55	126	8.098	8.085	-0.013
55	128	8.288	8.287	-0.001
100	129	8.050	8.041	-0.009
100	131	8.221	8.198	-0.023
100	132	8.157	8.171	0.014
100	133	8.078	8.100	0.022
100	134	8.141	8.112	-0.029
100	135	8.194	8.284	0.090
100	138	8.158	8.110	-0.048
100	139	8.119	8.067	-0.052
100	140	8.129	8.102	-0.027
100	141	8.212	8.212	0.000
100	142	8.215	8.242	0.027
100	143	8.165	8.205	0.040
100	144	8.204	8.120	-0.084
100	220	8.162	8.175	0.013
100	221	8.178	8.154	-0.024
100	224	8.146	8.177	0.031
100	225	8.049	8.069	0.020
100	226	8.143	8.136	-0.007
100	228	8.196	8.189	-0.007
100	232	8.104	8.057	-0.047
100	233	8.050	8.093	0.043
100	236	8.154	8.242	0.088
100	238	8.219	8.285	0.066
100	243	8.159	8.191	0.032
105	244	8.199	8.179	-0.020
105	246	8.257	8.258	0.001
105	248	8.181	8.206	0.025
105	374	8.062	8.177	0.115
105	375	8.188	8.142	-0.046
105	Max	8.288	8.287	0.115
105	Average	8.170	8.164	-0.006
105	Min	8.015	7.990	-0.139
105	Std Dev	0.067	0.072	0.046



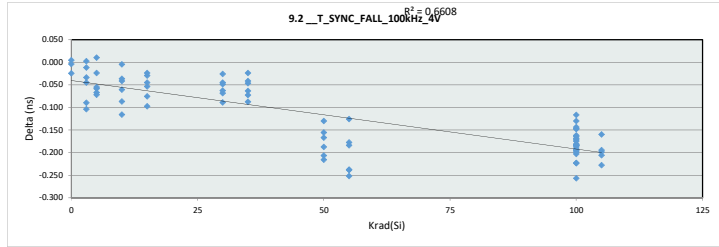
9.93 T_SYNC_R											
Test Site											
Tester											
Test Number											
Max Limit	15	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.094	7.990	8.058	8.020	8.094	8.048	8.100	8.037	8.084	8.041	8.142
Average	8.142	8.157	8.171	8.160	8.192	8.186	8.208	8.080	8.178	8.156	8.192
Max	8.210	8.232	8.258	8.248	8.246	8.261	8.262	8.134	8.287	8.285	8.258
UL	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000	15.000



LDR TID Report  
TPS7H5001-SP

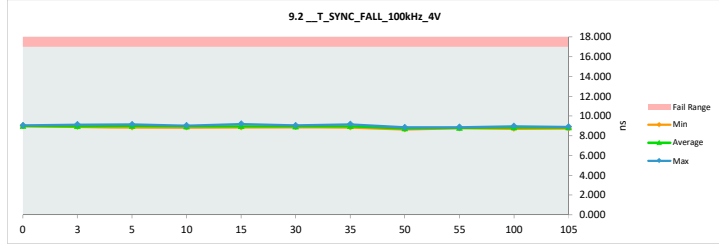
9.2_T_SYNC_FALL_100kHz_4V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	17	17
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.007	8.982	-0.025
0	118	9.034	9.030	-0.004
0	122	8.952	8.956	0.004
3	61	9.014	8.910	-0.104
3	62	9.097	9.099	0.002
3	63	8.939	8.894	-0.045
3	64	8.890	8.878	-0.012
3	65	8.935	8.901	-0.034
3	66	9.044	8.954	-0.090
5	67	8.882	8.827	-0.055
5	68	9.196	9.124	-0.072
5	69	9.038	8.979	-0.059
5	70	8.885	8.861	-0.024
5	71	8.940	8.950	0.010
5	72	9.140	9.073	-0.067
10	73	9.014	8.898	-0.116
10	74	9.097	9.010	-0.087
10	75	8.939	8.934	-0.005
10	76	8.890	8.829	-0.061
10	77	8.935	8.893	-0.042
10	78	9.044	9.007	-0.037
15	79	8.882	8.806	-0.076
15	80	9.196	9.172	-0.024
15	81	9.038	9.008	-0.030
15	82	8.885	8.831	-0.054
15	83	8.940	8.895	-0.045
15	84	9.140	9.042	-0.098
30	86	9.014	8.925	-0.089
30	89	9.097	9.028	-0.069
30	91	8.939	8.876	-0.063
30	92	8.890	8.845	-0.045
30	94	8.935	8.909	-0.026
30	95	9.044	8.995	-0.049
35	100	8.882	8.818	-0.064
35	101	9.196	9.149	-0.047
35	102	9.038	8.950	-0.088
35	103	8.885	8.861	-0.024
35	104	8.940	8.898	-0.042
35	105	9.140	9.067	-0.073
50	106	8.913	8.746	-0.167
50	107	8.809	8.621	-0.188
50	108	8.992	8.836	-0.156
50	109	8.956	8.636	-0.130
50	110	9.002	8.795	-0.207
50	111	8.930	8.714	-0.216
55	113	9.067	8.815	-0.252
55	114	8.998	8.759	-0.239
55	115	9.025	8.787	-0.238
55	116	8.982	8.798	-0.184
55	126	8.911	8.733	-0.178
55	128	8.967	8.841	-0.126
100	129	9.000	8.804	-0.196
100	131	9.086	8.893	-0.193
100	132	9.117	8.934	-0.183
100	133	9.121	8.937	-0.184
100	134	9.054	8.860	-0.194
100	135	9.027	8.852	-0.175
100	138	8.969	8.807	-0.162
100	139	8.949	8.765	-0.184
100	140	9.034	8.904	-0.130
100	141	9.004	8.823	-0.181
100	142	9.004	8.819	-0.185
100	143	8.942	8.798	-0.144
100	144	8.889	8.686	-0.203
100	220	9.036	8.889	-0.147
100	221	8.916	8.693	-0.223
100	224	8.989	8.872	-0.117
100	225	9.085	8.886	-0.199
100	226	8.992	8.768	-0.224
100	228	9.065	8.917	-0.148
100	232	8.962	8.793	-0.169
100	233	8.966	8.778	-0.188
100	236	8.945	8.688	-0.257
100	238	9.043	8.871	-0.172
100	243	9.001	8.836	-0.165
105	244	9.017	8.811	-0.206
105	246	9.100	8.872	-0.228
105	248	8.987	8.827	-0.160
105	374	8.918	8.723	-0.195
105	376	9.039	8.860	-0.179
105	375	9.196	9.172	0.010
Max		8.995	8.876	-0.119
Average		8.995	8.876	-0.119
Min		8.766	8.621	-0.257
Std Dev		0.087	0.112	0.076



9.2_T_SYNC_FALL		
Test Site		
Tester		
Test Number		
Max Limit	17	ns
Min Limit	ns	

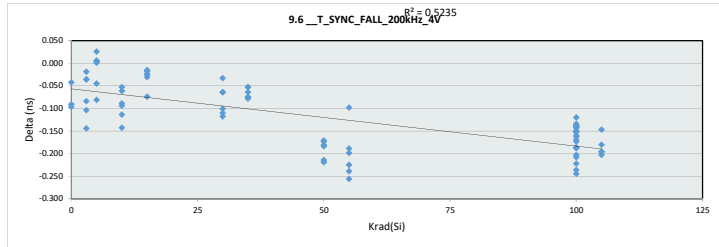
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.956	8.878	8.827	8.829	8.806	8.845	8.818	8.621	8.733	8.686	8.723
Average	8.989	8.939	8.969	8.929	8.959	8.930	8.957	8.725	8.789	8.828	8.819
Max	9.030	9.099	9.124	9.010	9.172	9.028	9.149	8.836	8.841	8.937	8.872
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



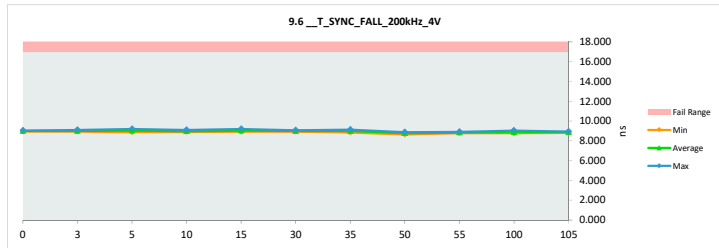
LDR TID Report  
TPS7H5001-SP

9.6_T_SYNC_FALL_200kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.072	9.029	-0.043
0	118	9.137	9.045	-0.092
0	122	9.047	8.949	-0.098
3	61	9.081	8.976	-0.105
3	62	9.190	9.105	-0.085
3	63	9.071	8.926	-0.145
3	64	8.968	8.931	-0.037
3	65	8.995	8.975	-0.020
3	66	9.093	9.056	-0.037
5	67	8.917	8.917	0.000
5	68	9.194	9.199	0.005
5	69	9.023	9.026	0.003
5	70	8.966	8.884	-0.082
5	71	9.042	8.996	-0.046
5	72	9.181	9.206	0.025
10	73	9.081	8.937	-0.144
10	74	9.190	9.095	-0.095
10	75	9.071	8.956	-0.115
10	76	8.968	8.906	-0.062
10	77	8.995	8.941	-0.054
10	78	9.093	9.003	-0.090
15	79	8.917	8.898	-0.019
15	80	9.194	9.178	-0.016
15	81	9.023	8.998	-0.025
15	82	8.966	8.891	-0.075
15	83	9.042	9.010	-0.032
15	84	9.181	9.155	-0.026
30	86	9.081	8.962	-0.119
30	89	9.190	9.079	-0.111
30	91	9.071	8.969	-0.102
30	92	8.968	8.934	-0.034
30	94	8.995	8.930	-0.065
30	95	9.093	9.028	-0.065
35	100	8.917	8.841	-0.076
35	101	9.194	9.140	-0.054
35	102	9.023	8.969	-0.054
35	103	8.966	8.901	-0.065
35	104	9.042	8.963	-0.079
35	105	9.181	9.105	-0.076
50	106	8.982	8.797	-0.185
50	107	8.871	8.699	-0.172
50	108	9.100	8.880	-0.220
50	109	8.814	8.632	-0.182
50	110	9.028	8.813	-0.215
50	111	8.953	8.779	-0.174
55	113	9.149	8.892	-0.257
55	114	9.024	8.784	-0.240
55	115	9.062	8.836	-0.226
55	116	9.068	8.868	-0.200
55	126	8.974	8.784	-0.190
55	128	8.967	8.868	-0.099
100	129	9.058	8.905	-0.153
100	131	9.123	8.935	-0.188
100	132	9.171	9.036	-0.135
100	133	9.113	8.939	-0.174
100	134	9.076	8.913	-0.163
100	135	9.054	8.865	-0.189
100	138	9.046	8.902	-0.144
100	139	8.946	8.784	-0.162
100	140	9.066	8.945	-0.121
100	141	9.032	8.823	-0.209
100	142	9.053	8.807	-0.246
100	143	8.963	8.825	-0.138
100	144	8.949	8.762	-0.187
100	220	9.115	8.961	-0.154
100	221	8.961	8.757	-0.204
100	224	9.100	8.950	-0.150
100	225	9.066	8.904	-0.162
100	226	9.013	8.872	-0.141
100	228	9.093	8.953	-0.140
100	232	9.009	8.847	-0.162
100	233	9.025	8.788	-0.237
100	236	8.984	8.761	-0.223
100	238	9.056	8.914	-0.142
100	243	9.028	8.857	-0.171
105	244	9.047	8.850	-0.197
105	246	9.109	8.928	-0.181
105	248	9.060	8.856	-0.204
105	374	8.959	8.811	-0.148
105	376	9.133	8.936	-0.197
105	375	9.194	9.206	0.025
Max		9.048	8.925	-0.122
Average		9.048	8.925	-0.122
Min		8.814	8.632	-0.257
Std Dev		0.081	0.113	0.071



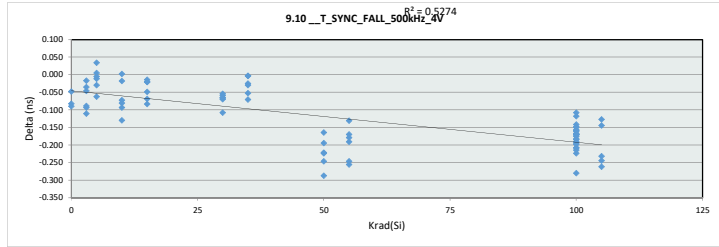
9.6_T_SYNC_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.949	8.926	8.884	8.906	8.891	8.930	8.841	8.632	8.784	8.757	8.811
Average	9.008	8.995	9.038	8.973	9.022	8.984	8.987	8.767	8.839	8.875	8.876
Max	9.045	9.105	9.206	9.095	9.178	9.079	9.140	8.880	8.892	9.036	8.936
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



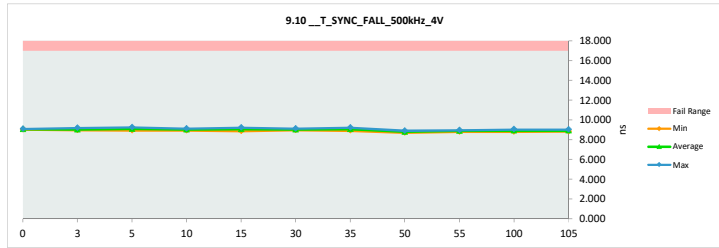
LDR TID Report  
TPS7H5001-SP

9.10_T_SYNC_FALL_500kHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.155	9.072	-0.083
0	118	9.129	9.039	-0.090
0	122	9.067	9.019	-0.048
3	61	9.008	8.973	-0.035
3	62	9.191	9.174	-0.017
3	63	9.065	8.971	-0.094
3	64	9.065	8.954	-0.111
3	65	9.094	9.005	-0.089
3	66	9.109	9.063	-0.046
5	67	8.930	8.918	-0.012
5	68	9.273	9.243	-0.030
5	69	9.068	9.073	0.005
5	70	8.939	8.934	-0.005
5	71	9.087	9.024	-0.063
5	72	9.206	9.240	0.034
10	73	9.008	8.990	-0.018
10	74	9.191	9.097	-0.094
10	75	9.065	8.992	-0.073
10	76	9.065	8.935	-0.130
10	77	9.094	9.013	-0.081
10	78	9.109	9.111	0.002
15	79	8.930	8.861	-0.069
15	80	9.273	9.224	-0.049
15	81	9.068	9.047	-0.021
15	82	8.939	8.925	-0.014
15	83	9.087	9.003	-0.084
15	84	9.206	9.185	-0.021
30	86	9.008	8.954	-0.054
30	89	9.191	9.126	-0.065
30	91	9.065	9.006	-0.059
30	92	9.065	8.957	-0.108
30	94	9.094	9.024	-0.070
30	95	9.109	9.043	-0.066
35	100	8.930	8.905	-0.025
35	101	9.273	9.220	-0.053
35	102	9.068	9.065	-0.003
35	103	8.939	8.936	-0.003
35	104	9.087	9.016	-0.071
35	105	9.206	9.176	-0.030
50	106	8.981	8.759	-0.222
50	107	8.897	8.732	-0.165
50	108	9.190	8.902	-0.288
50	109	8.940	8.693	-0.247
50	110	9.100	8.876	-0.224
50	111	9.009	8.814	-0.195
55	113	9.160	8.913	-0.247
55	114	9.037	8.846	-0.191
55	115	9.065	8.886	-0.179
55	116	9.083	8.952	-0.131
55	126	9.046	8.790	-0.256
55	128	9.052	8.882	-0.170
100	129	9.035	8.885	-0.150
100	131	9.243	9.035	-0.208
100	132	9.198	9.022	-0.176
100	133	9.148	8.978	-0.170
100	134	9.117	8.934	-0.183
100	135	9.125	8.910	-0.215
100	138	9.058	8.897	-0.161
100	139	8.994	8.824	-0.170
100	140	9.159	8.988	-0.171
100	141	9.031	8.874	-0.157
100	142	9.088	8.903	-0.185
100	143	8.956	8.848	-0.108
100	144	8.982	8.834	-0.148
100	220	9.154	8.930	-0.224
100	221	8.986	8.791	-0.195
100	224	9.088	8.946	-0.142
100	225	9.185	8.905	-0.280
100	226	9.035	8.917	-0.118
100	228	9.204	9.013	-0.191
100	232	9.049	8.882	-0.167
100	233	9.065	8.894	-0.171
100	236	8.998	8.790	-0.208
100	238	9.098	8.900	-0.198
100	243	9.061	8.903	-0.158
105	244	9.141	8.909	-0.232
105	246	9.150	9.023	-0.127
105	248	9.138	8.876	-0.262
105	374	8.962	8.817	-0.145
105	376	9.165	8.920	-0.245
105	Max	9.273	9.243	0.034
105	Average	9.083	8.961	-0.122
105	Min	8.897	8.693	-0.288
105	Std Dev	0.089	0.116	0.081



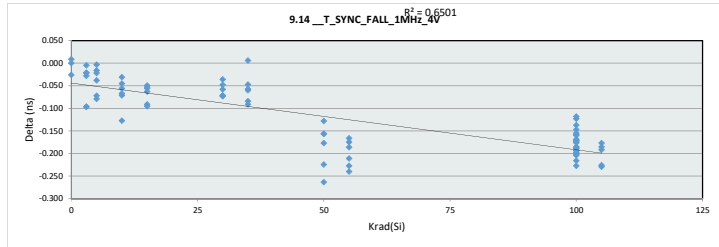
9.10_T_SYNC_FALL_500kHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.019	8.954	8.918	8.935	8.861	8.954	8.905	8.693	8.790	8.790	8.817
Average	9.043	9.023	9.072	9.023	9.041	9.018	9.053	8.796	8.878	8.908	8.909
Max	9.072	9.174	9.243	9.111	9.224	9.126	9.220	8.902	8.952	9.035	9.023
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



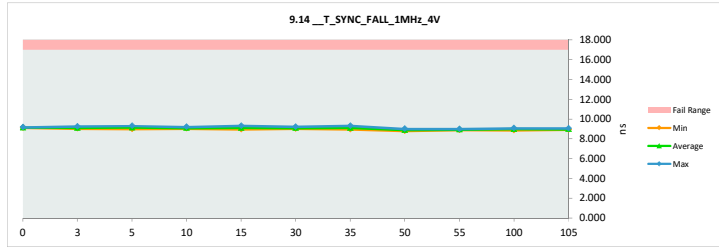
LDR TID Report  
TPS7H5001-SP

9.14_T_SYNC_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.159	9.158	-0.001
0	118	9.173	9.146	-0.027
0	122	9.076	9.084	0.008
3	61	9.133	9.104	-0.029
3	62	9.257	9.235	-0.022
3	63	9.136	9.039	-0.097
3	64	9.050	9.027	-0.023
3	65	9.151	9.053	-0.098
3	66	9.189	9.183	-0.006
5	67	8.999	8.982	-0.017
5	68	9.383	9.303	-0.080
5	69	9.165	9.161	-0.004
5	70	9.004	8.981	-0.023
5	71	9.158	9.085	-0.073
5	72	9.299	9.260	-0.039
10	73	9.133	9.076	-0.057
10	74	9.257	9.189	-0.068
10	75	9.136	9.064	-0.072
10	76	9.050	9.018	-0.032
10	77	9.151	9.023	-0.128
10	78	9.189	9.143	-0.046
15	79	8.999	8.945	-0.054
15	80	9.383	9.291	-0.092
15	81	9.165	9.115	-0.050
15	82	9.004	8.940	-0.064
15	83	9.158	9.062	-0.096
15	84	9.299	9.242	-0.057
30	86	9.133	9.096	-0.037
30	89	9.257	9.198	-0.059
30	91	9.136	9.064	-0.072
30	92	9.050	9.001	-0.049
30	94	9.151	9.077	-0.074
30	95	9.189	9.115	-0.074
35	100	8.999	8.951	-0.048
35	101	9.383	9.291	-0.092
35	102	9.165	9.107	-0.058
35	103	9.004	9.009	0.005
35	104	9.158	9.097	-0.061
35	105	9.299	9.214	-0.085
50	106	9.136	8.872	-0.264
50	107	8.990	8.765	-0.225
50	108	9.121	8.992	-0.129
50	109	8.949	8.771	-0.178
50	110	9.122	8.965	-0.157
50	111	9.043	8.886	-0.157
55	113	9.211	8.983	-0.228
55	114	9.136	8.895	-0.241
55	115	9.143	8.931	-0.212
55	116	9.102	8.926	-0.176
55	126	9.052	8.885	-0.167
55	128	9.124	8.937	-0.187
100	129	9.164	8.947	-0.217
100	131	9.227	9.027	-0.200
100	132	9.256	9.051	-0.205
100	133	9.223	9.048	-0.175
100	134	9.191	9.004	-0.187
100	135	9.126	9.007	-0.119
100	138	9.153	8.976	-0.177
100	139	9.043	8.882	-0.161
100	140	9.248	9.045	-0.203
100	141	9.143	8.984	-0.159
100	142	9.139	8.983	-0.156
100	143	9.105	8.981	-0.124
100	144	9.030	8.841	-0.189
100	220	9.206	9.034	-0.172
100	221	9.039	8.866	-0.173
100	224	9.181	9.043	-0.138
100	225	9.211	9.017	-0.194
100	226	9.125	8.977	-0.148
100	228	9.225	9.030	-0.195
100	232	9.095	8.909	-0.186
100	233	9.115	8.915	-0.200
100	236	9.071	8.843	-0.228
100	238	9.176	9.007	-0.169
100	243	9.158	8.968	-0.190
105	244	9.155	8.925	-0.230
105	246	9.201	8.974	-0.227
105	248	9.133	8.947	-0.186
105	374	9.082	8.904	-0.178
105	376	9.226	9.033	-0.193
	Max	9.383	9.303	0.008
	Average	9.147	9.026	-0.121
	Min	8.949	8.765	-0.264
	Std Dev	0.091	0.116	0.074



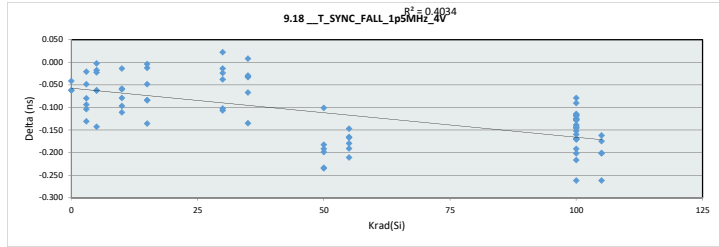
9.14_T_SYNC_FALL_1MHz_4V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.084	9.027	8.981	9.018	8.940	9.001	8.951	8.765	8.885	8.841	8.904
Average	9.129	9.107	9.129	9.086	9.099	9.092	9.112	8.875	8.926	8.974	8.957
Max	9.158	9.235	9.303	9.189	9.291	9.198	9.291	8.992	8.983	9.051	9.033
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



LDR TID Report  
TPS7H5001-SP

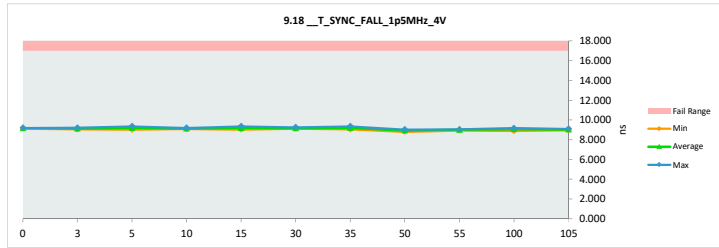
9.18_T_SYNC_FALL_1p5MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.244	9.182	-0.062
0	118	9.230	9.168	-0.062
0	122	9.145	9.103	-0.042
3	61	9.208	9.077	-0.131
3	62	9.289	9.185	-0.104
3	63	9.149	9.100	-0.049
3	64	9.120	9.026	-0.094
3	65	9.148	9.068	-0.080
3	66	9.209	9.188	-0.021
5	67	9.061	8.998	-0.063
5	68	9.480	9.337	-0.143
5	69	9.209	9.191	-0.018
5	70	9.055	9.052	-0.003
5	71	9.158	9.135	-0.023
5	72	9.347	9.285	-0.062
10	73	9.208	9.111	-0.097
10	74	9.289	9.178	-0.111
10	75	9.149	9.135	-0.014
10	76	9.120	9.060	-0.060
10	77	9.148	9.069	-0.079
10	78	9.209	9.150	-0.059
15	79	9.061	9.057	-0.004
15	80	9.480	9.344	-0.136
15	81	9.209	9.196	-0.013
15	82	9.055	9.006	-0.049
15	83	9.158	9.074	-0.084
15	84	9.347	9.263	-0.084
30	86	9.208	9.101	-0.107
30	89	9.289	9.186	-0.103
30	91	9.149	9.111	-0.038
30	92	9.120	9.106	-0.014
30	94	9.148	9.124	-0.024
30	95	9.209	9.231	0.022
35	100	9.061	9.031	-0.030
35	101	9.480	9.345	-0.135
35	102	9.209	9.177	-0.032
35	103	9.055	9.063	0.008
35	104	9.158	9.125	-0.033
35	105	9.347	9.280	-0.067
50	106	9.138	8.904	-0.234
50	107	8.962	8.881	-0.101
50	108	9.216	9.017	-0.199
50	109	8.968	8.776	-0.192
50	110	9.183	9.000	-0.183
50	111	9.152	8.917	-0.235
55	113	9.173	9.026	-0.147
55	114	9.202	9.011	-0.191
55	115	9.172	8.992	-0.180
55	116	9.214	9.003	-0.211
55	126	9.077	8.911	-0.166
55	128	9.160	8.993	-0.167
100	129	9.184	9.024	-0.160
100	131	9.284	9.115	-0.169
100	132	9.282	9.155	-0.127
100	133	9.291	9.146	-0.145
100	134	9.254	9.062	-0.192
100	135	9.202	9.075	-0.127
100	138	9.164	9.039	-0.125
100	139	9.077	8.935	-0.142
100	140	9.217	9.088	-0.129
100	141	9.249	8.987	-0.262
100	142	9.214	9.068	-0.146
100	143	9.102	9.011	-0.091
100	144	9.062	8.946	-0.116
100	220	9.242	9.071	-0.171
100	221	9.035	8.864	-0.171
100	224	9.223	9.104	-0.119
100	225	9.234	9.032	-0.202
100	226	9.139	9.024	-0.115
100	228	9.229	9.150	-0.079
100	232	9.127	8.988	-0.139
100	233	9.120	9.002	-0.118
100	236	9.122	8.951	-0.171
100	238	9.211	9.059	-0.152
100	243	9.251	9.034	-0.217
105	244	9.255	8.993	-0.262
105	246	9.230	9.068	-0.162
105	248	9.208	9.006	-0.202
105	374	9.112	8.937	-0.175
105	376	9.253	9.052	-0.201
105	Max	9.480	9.345	0.022
105	Average	9.189	9.075	-0.114
105	Min	8.968	8.776	-0.262
105	Std Dev	0.098	0.109	0.069



9.18_T_SYNC_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

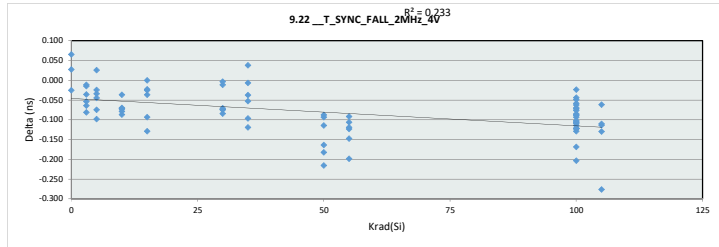
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.103	9.026	8.998	9.060	9.006	9.101	9.031	8.776	8.911	8.864	8.937
Average	9.151	9.107	9.166	9.117	9.157	9.143	9.170	8.916	8.989	9.039	9.011
Max	9.182	9.188	9.337	9.178	9.344	9.231	9.345	9.017	9.026	9.155	9.068
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



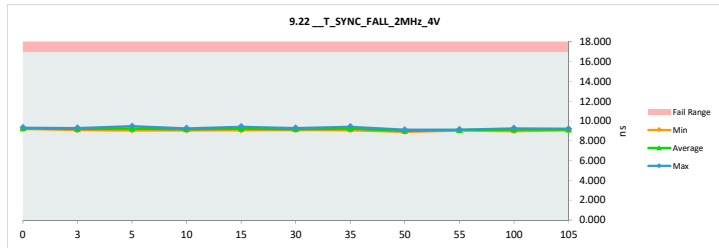
LDR TID Report  
TPS7H5001-SP

9.22_T_SYNC_FALL_2MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.207	9.271	0.064
0	118	9.335	9.308	-0.027
0	122	9.161	9.187	0.026
3	61	9.206	9.140	-0.066
3	62	9.351	9.296	-0.055
3	63	9.244	9.161	-0.083
3	64	9.116	9.079	-0.037
3	65	9.142	9.129	-0.013
3	66	9.264	9.248	-0.016
5	67	9.072	9.096	0.024
5	68	9.542	9.466	-0.076
5	69	9.246	9.200	-0.046
5	70	9.072	9.046	-0.026
5	71	9.236	9.136	-0.100
5	72	9.426	9.390	-0.036
10	73	9.206	9.135	-0.071
10	74	9.351	9.263	-0.088
10	75	9.244	9.164	-0.080
10	76	9.116	9.042	-0.074
10	77	9.142	9.104	-0.038
10	78	9.264	9.190	-0.074
15	79	9.072	9.070	-0.002
15	80	9.542	9.412	-0.130
15	81	9.246	9.208	-0.038
15	82	9.072	9.048	-0.024
15	83	9.236	9.141	-0.095
15	84	9.426	9.399	-0.027
30	86	9.206	9.130	-0.076
30	89	9.351	9.276	-0.075
30	91	9.244	9.158	-0.086
30	92	9.116	9.103	-0.013
30	94	9.142	9.137	-0.005
30	95	9.264	9.192	-0.072
35	100	9.072	9.064	-0.008
35	101	9.542	9.421	-0.121
35	102	9.246	9.207	-0.039
35	103	9.072	9.108	0.036
35	104	9.236	9.138	-0.098
35	105	9.426	9.372	-0.054
50	106	9.163	8.998	-0.165
50	107	9.018	8.902	-0.116
50	108	9.200	9.111	-0.089
50	109	9.013	8.918	-0.095
50	110	9.271	9.054	-0.217
50	111	9.118	8.934	-0.184
55	113	9.208	9.087	-0.121
55	114	9.240	9.091	-0.149
55	115	9.257	9.057	-0.200
55	116	9.247	9.139	-0.108
55	126	9.153	9.028	-0.125
55	128	9.175	9.082	-0.093
100	129	9.276	9.152	-0.124
100	131	9.383	9.213	-0.170
100	132	9.316	9.238	-0.078
100	133	9.412	9.207	-0.205
100	134	9.245	9.181	-0.064
100	135	9.141	9.091	-0.050
100	138	9.211	9.124	-0.087
100	139	9.087	9.062	-0.025
100	140	9.353	9.263	-0.090
100	141	9.158	9.113	-0.045
100	142	9.314	9.198	-0.116
100	143	9.191	9.102	-0.089
100	144	9.138	9.030	-0.108
100	220	9.340	9.229	-0.111
100	221	9.100	8.977	-0.123
100	224	9.297	9.226	-0.071
100	225	9.184	9.054	-0.130
100	226	9.205	9.081	-0.124
100	228	9.327	9.232	-0.095
100	232	9.186	9.112	-0.074
100	233	9.150	9.043	-0.107
100	236	9.070	9.010	-0.060
100	238	9.259	9.155	-0.104
100	243	9.270	9.147	-0.123
105	244	9.352	9.074	-0.278
105	246	9.340	9.209	-0.131
105	248	9.249	9.137	-0.112
105	374	9.151	9.088	-0.063
105	376	9.278	9.163	-0.115
	Max	9.542	9.466	0.064
	Average	9.231	9.149	-0.082
	Min	9.013	8.902	-0.278
	Std Dev	0.116	0.112	0.058



9.22_T_SYNC_Fa											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.187	9.079	9.046	9.042	9.048	9.103	9.064	8.902	9.028	8.977	9.074
Average	9.255	9.176	9.222	9.150	9.213	9.166	9.218	8.986	9.081	9.135	9.134
Max	9.308	9.296	9.466	9.263	9.412	9.276	9.421	9.111	9.139	9.263	9.209
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

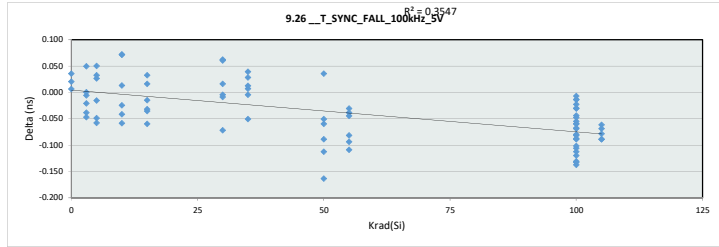




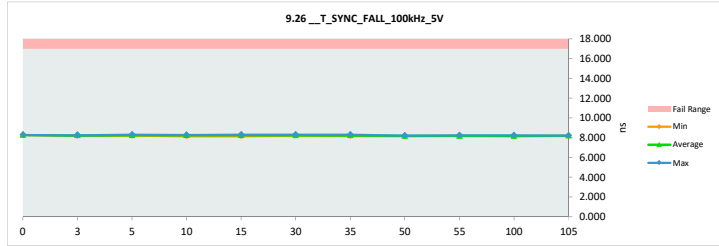
LDR TID Report  
TPS7H5001-SP

9.26_T_SYNC_FALL_100kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.261	8.281	0.020
0	118	8.236	8.271	0.035
0	122	8.261	8.267	0.006
3	61	8.186	8.180	-0.006
3	62	8.304	8.257	-0.047
3	63	8.230	8.209	-0.021
3	64	8.213	8.213	0.000
3	65	8.192	8.241	0.049
3	66	8.282	8.243	-0.039
5	67	8.174	8.224	0.050
5	68	8.266	8.298	0.032
5	69	8.277	8.219	-0.058
5	70	8.193	8.219	0.026
5	71	8.275	8.226	-0.049
5	72	8.284	8.268	-0.016
10	73	8.186	8.257	0.071
10	74	8.304	8.262	-0.042
10	75	8.230	8.243	0.013
10	76	8.213	8.154	-0.059
10	77	8.192	8.264	0.072
10	78	8.282	8.257	-0.025
15	79	8.174	8.138	-0.036
15	80	8.266	8.298	0.032
15	81	8.277	8.245	-0.032
15	82	8.193	8.209	0.016
15	83	8.275	8.260	-0.015
15	84	8.284	8.224	-0.060
30	86	8.186	8.246	0.060
30	89	8.304	8.299	-0.005
30	91	8.230	8.221	-0.009
30	92	8.213	8.229	0.016
30	94	8.192	8.254	0.062
30	95	8.282	8.210	-0.072
35	100	8.174	8.181	0.007
35	101	8.266	8.294	0.028
35	102	8.277	8.272	-0.005
35	103	8.193	8.232	0.039
35	104	8.275	8.224	-0.051
35	105	8.284	8.296	0.012
50	106	8.260	8.209	-0.051
50	107	8.216	8.156	-0.060
50	108	8.297	8.133	-0.164
50	109	8.262	8.149	-0.113
50	110	8.257	8.168	-0.089
50	111	8.161	8.196	0.035
55	113	8.218	8.173	-0.045
55	114	8.258	8.164	-0.094
55	115	8.277	8.238	-0.039
55	116	8.247	8.216	-0.031
55	126	8.276	8.167	-0.109
55	128	8.252	8.170	-0.082
100	129	8.297	8.191	-0.106
100	131	8.284	8.253	-0.031
100	132	8.291	8.231	-0.060
100	133	8.317	8.179	-0.138
100	134	8.329	8.196	-0.133
100	135	8.236	8.176	-0.060
100	138	8.266	8.164	-0.102
100	139	8.235	8.146	-0.089
100	140	8.259	8.179	-0.080
100	141	8.259	8.177	-0.082
100	142	8.277	8.189	-0.088
100	143	8.185	8.171	-0.014
100	144	8.239	8.195	-0.044
100	220	8.205	8.198	-0.007
100	221	8.271	8.151	-0.120
100	224	8.304	8.191	-0.113
100	225	8.286	8.217	-0.069
100	226	8.216	8.202	-0.014
100	228	8.292	8.224	-0.068
100	232	8.219	8.189	-0.030
100	233	8.245	8.197	-0.048
100	236	8.204	8.149	-0.055
100	238	8.315	8.184	-0.131
100	243	8.207	8.184	-0.023
105	244	8.299	8.210	-0.089
105	246	8.295	8.206	-0.089
105	248	8.255	8.193	-0.062
105	374	8.237	8.168	-0.069
105	376	8.277	8.198	-0.079
105	Max	8.329	8.299	0.072
105	Average	8.250	8.213	-0.037
105	Min	8.161	8.133	-0.164
105	Std Dev	0.041	0.042	0.054



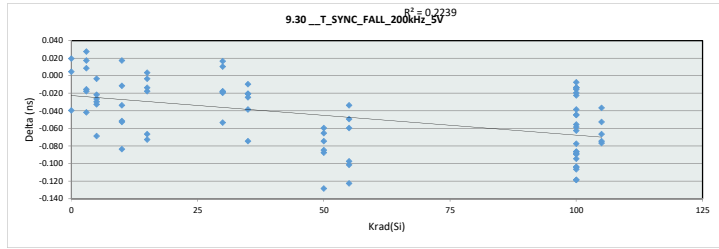
9.26_T_SYNC_Fa											
Test Site											
Tester											
Test Number											
Max Limit	17										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.267	8.180	8.219	8.154	8.138	8.210	8.181	8.133	8.164	8.146	8.168
Average	8.273	8.224	8.242	8.240	8.229	8.243	8.250	8.169	8.188	8.189	8.195
Max	8.281	8.257	8.298	8.264	8.298	8.299	8.296	8.209	8.238	8.253	8.210
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



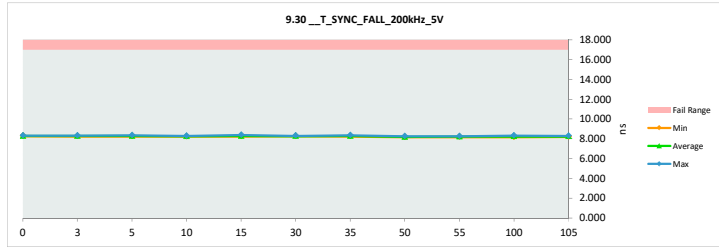
LDR TID Report  
TPS7H5001-SP

9.30_T_SYNC_FALL_200kHz_5V			
Test Site			
Tester			
Test Number			
Unit	ns	ns	
Max Limit	17	17	
Min Limit			

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.313	8.332	0.019
0	118	8.323	8.283	-0.040
0	122	8.269	8.273	0.004
3	61	8.307	8.324	0.017
3	62	8.332	8.316	-0.016
3	63	8.282	8.290	0.008
3	64	8.284	8.242	-0.042
3	65	8.241	8.268	0.027
3	66	8.336	8.318	-0.018
5	67	8.280	8.258	-0.022
5	68	8.372	8.368	-0.004
5	69	8.312	8.282	-0.030
5	70	8.274	8.241	-0.033
5	71	8.339	8.270	-0.069
5	72	8.326	8.299	-0.027
10	73	8.307	8.223	-0.084
10	74	8.332	8.279	-0.053
10	75	8.282	8.248	-0.034
10	76	8.284	8.272	-0.012
10	77	8.241	8.258	0.017
10	78	8.336	8.284	-0.052
15	79	8.280	8.276	-0.004
15	80	8.372	8.375	0.003
15	81	8.312	8.245	-0.067
15	82	8.274	8.256	-0.018
15	83	8.339	8.266	-0.073
15	84	8.326	8.312	-0.014
30	86	8.307	8.289	-0.018
30	89	8.332	8.312	-0.020
30	91	8.282	8.298	0.016
30	92	8.284	8.265	-0.019
30	94	8.241	8.251	0.010
30	95	8.336	8.282	-0.054
35	100	8.280	8.241	-0.039
35	101	8.372	8.351	-0.021
35	102	8.312	8.287	-0.025
35	103	8.274	8.253	-0.021
35	104	8.339	8.264	-0.075
35	105	8.326	8.316	-0.010
50	106	8.266	8.178	-0.088
50	107	8.288	8.159	-0.129
50	108	8.327	8.252	-0.075
50	109	8.234	8.168	-0.066
50	110	8.308	8.223	-0.085
50	111	8.271	8.211	-0.060
55	113	8.279	8.245	-0.034
55	114	8.321	8.219	-0.102
55	115	8.304	8.206	-0.098
55	116	8.299	8.239	-0.060
55	126	8.307	8.184	-0.123
55	128	8.294	8.244	-0.050
100	129	8.337	8.250	-0.087
100	131	8.354	8.235	-0.119
100	132	8.349	8.260	-0.089
100	133	8.328	8.265	-0.063
100	134	8.325	8.220	-0.105
100	135	8.305	8.282	-0.023
100	138	8.298	8.239	-0.059
100	139	8.320	8.213	-0.107
100	140	8.336	8.291	-0.045
100	141	8.284	8.165	-0.119
100	142	8.329	8.273	-0.056
100	143	8.286	8.191	-0.095
100	144	8.254	8.209	-0.045
100	220	8.333	8.317	-0.016
100	221	8.269	8.249	-0.020
100	224	8.323	8.284	-0.039
100	225	8.316	8.238	-0.078
100	226	8.270	8.256	-0.014
100	228	8.329	8.225	-0.104
100	232	8.307	8.217	-0.090
100	233	8.275	8.267	-0.008
100	236	8.252	8.238	-0.014
100	238	8.295	8.281	-0.014
100	243	8.282	8.195	-0.087
105	244	8.327	8.290	-0.037
105	246	8.331	8.254	-0.077
105	248	8.310	8.257	-0.053
105	374	8.272	8.205	-0.067
105	376	8.307	8.232	-0.075
105	Max	8.372	8.375	0.027
105	Average	8.305	8.259	-0.046
105	Min	8.234	8.159	-0.129
105	Std Dev	0.031	0.044	0.038



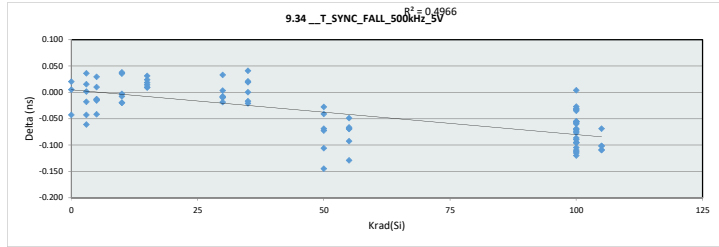
9.30_T_SYNC_FALL_200kHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.273	8.242	8.241	8.223	8.245	8.251	8.241	8.159	8.184	8.165	8.205
Average	8.296	8.293	8.286	8.261	8.288	8.283	8.285	8.199	8.223	8.244	8.248
Max	8.332	8.324	8.368	8.284	8.375	8.312	8.351	8.252	8.245	8.317	8.290
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



LDR TID Report  
TPS7H5001-SP

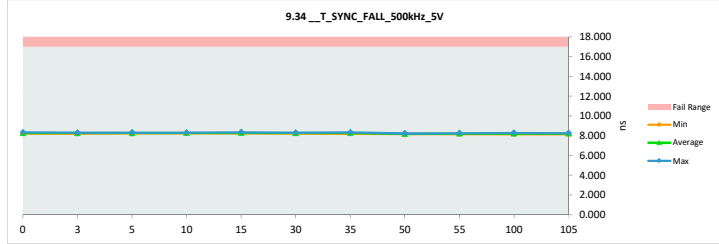
9.34 T_SYNC_FALL_500kHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.303	8.308	0.005
0	118	8.240	8.197	-0.043
0	122	8.251	8.251	0.020
3	61	8.264	8.279	0.015
3	62	8.287	8.226	-0.061
3	63	8.236	8.218	-0.018
3	64	8.233	8.234	0.001
3	65	8.251	8.208	-0.043
3	66	8.246	8.282	0.036
5	67	8.199	8.186	-0.013
5	68	8.307	8.294	-0.013
5	69	8.246	8.230	-0.016
5	70	8.216	8.245	0.029
5	71	8.264	8.222	-0.042
5	72	8.270	8.280	0.010
10	73	8.264	8.244	-0.020
10	74	8.287	8.267	-0.020
10	75	8.236	8.271	0.035
10	76	8.233	8.225	-0.008
10	77	8.251	8.248	-0.003
10	78	8.246	8.284	0.038
15	79	8.199	8.230	0.031
15	80	8.307	8.325	0.018
15	81	8.246	8.270	0.024
15	82	8.216	8.225	0.009
15	83	8.264	8.273	0.009
15	84	8.270	8.284	0.014
30	86	8.264	8.245	-0.019
30	89	8.287	8.278	-0.009
30	91	8.236	8.269	0.033
30	92	8.233	8.223	-0.010
30	94	8.251	8.243	-0.008
30	95	8.246	8.249	0.003
35	100	8.199	8.218	0.019
35	101	8.307	8.307	0.000
35	102	8.246	8.267	0.021
35	103	8.216	8.199	-0.017
35	104	8.264	8.243	-0.021
35	105	8.270	8.311	0.041
50	106	8.291	8.185	-0.106
50	107	8.214	8.173	-0.041
50	108	8.293	8.220	-0.073
50	109	8.200	8.172	-0.028
50	110	8.272	8.127	-0.145
50	111	8.245	8.176	-0.069
55	113	8.272	8.223	-0.049
55	114	8.258	8.189	-0.069
55	115	8.296	8.203	-0.093
55	116	8.252	8.182	-0.070
55	126	8.259	8.130	-0.129
55	128	8.293	8.226	-0.067
100	129	8.248	8.143	-0.105
100	131	8.325	8.230	-0.095
100	132	8.314	8.218	-0.096
100	133	8.281	8.166	-0.115
100	134	8.299	8.179	-0.120
100	135	8.292	8.182	-0.110
100	138	8.220	8.162	-0.058
100	139	8.214	8.137	-0.077
100	140	8.277	8.206	-0.071
100	141	8.286	8.190	-0.096
100	142	8.266	8.192	-0.074
100	143	8.224	8.228	0.004
100	144	8.196	8.169	-0.027
100	220	8.294	8.207	-0.087
100	221	8.230	8.197	-0.033
100	224	8.296	8.261	-0.035
100	225	8.285	8.225	-0.060
100	226	8.261	8.205	-0.056
100	228	8.279	8.224	-0.055
100	232	8.233	8.202	-0.031
100	233	8.243	8.187	-0.056
100	236	8.268	8.178	-0.090
100	238	8.293	8.180	-0.113
100	243	8.241	8.172	-0.069
105	244	8.269	8.200	-0.069
105	246	8.327	8.225	-0.102
105	248	8.298	8.188	-0.110
105	374	8.238	8.129	-0.109
105	376	8.330	8.228	-0.102
105	Max	8.330	8.325	0.041
105	Average	8.260	8.221	-0.039
105	Min	8.196	8.127	-0.145
105	Std Dev	0.033	0.045	0.048



9.34 T_SYNC FA	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

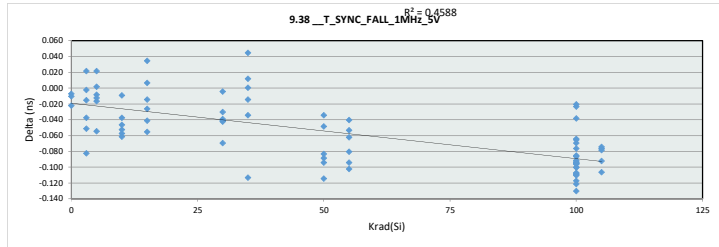
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.197	8.208	8.186	8.225	8.225	8.223	8.199	8.127	8.130	8.137	8.129
Average	8.252	8.241	8.243	8.257	8.268	8.251	8.258	8.176	8.192	8.193	8.194
Max	8.308	8.282	8.294	8.284	8.325	8.278	8.311	8.220	8.226	8.261	8.228
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



LDR TID Report  
TPS7H5001-SP

9.38 T_SYNC_FALL_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

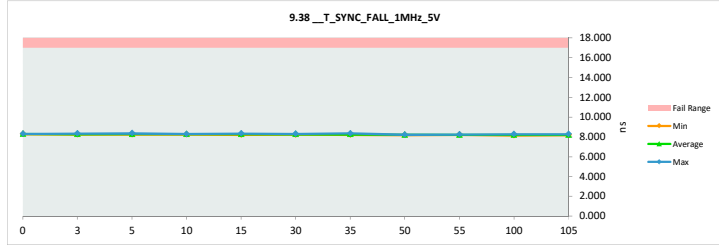
Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.313	8.290	-0.023
0	118	8.291	8.283	-0.008
0	122	8.273	8.262	-0.011
3	61	8.262	8.283	0.021
3	62	8.331	8.328	-0.003
3	63	8.296	8.244	-0.052
3	64	8.249	8.233	-0.016
3	65	8.303	8.220	-0.083
3	66	8.298	8.260	-0.038
5	67	8.227	8.218	-0.009
5	68	8.336	8.337	0.001
5	69	8.274	8.295	0.021
5	70	8.242	8.229	-0.013
5	71	8.295	8.240	-0.055
5	72	8.316	8.299	-0.017
10	73	8.262	8.252	-0.010
10	74	8.331	8.269	-0.062
10	75	8.296	8.249	-0.047
10	76	8.249	8.211	-0.038
10	77	8.303	8.250	-0.053
10	78	8.298	8.240	-0.058
15	79	8.227	8.261	0.034
15	80	8.336	8.321	-0.015
15	81	8.274	8.280	0.006
15	82	8.242	8.200	-0.042
15	83	8.295	8.239	-0.056
15	84	8.316	8.289	-0.027
30	86	8.262	8.222	-0.040
30	89	8.331	8.288	-0.043
30	91	8.296	8.265	-0.031
30	92	8.249	8.207	-0.042
30	94	8.303	8.233	-0.070
30	95	8.298	8.293	-0.005
35	100	8.227	8.271	0.044
35	101	8.336	8.336	0.000
35	102	8.274	8.239	-0.035
35	103	8.242	8.227	-0.015
35	104	8.295	8.181	-0.114
35	105	8.316	8.327	0.011
50	106	8.254	8.205	-0.049
50	107	8.252	8.168	-0.084
50	108	8.294	8.199	-0.095
50	109	8.275	8.161	-0.115
50	110	8.277	8.188	-0.089
50	111	8.211	8.176	-0.035
55	113	8.251	8.197	-0.054
55	114	8.262	8.181	-0.081
55	115	8.258	8.217	-0.041
55	116	8.263	8.200	-0.063
55	126	8.285	8.190	-0.095
55	128	8.307	8.204	-0.103
100	129	8.306	8.205	-0.101
100	131	8.343	8.247	-0.096
100	132	8.326	8.256	-0.070
100	133	8.293	8.171	-0.122
100	134	8.326	8.208	-0.118
100	135	8.274	8.209	-0.065
100	138	8.308	8.214	-0.094
100	139	8.268	8.191	-0.077
100	140	8.294	8.183	-0.111
100	141	8.286	8.190	-0.096
100	142	8.274	8.208	-0.066
100	143	8.268	8.160	-0.108
100	144	8.252	8.157	-0.095
100	220	8.318	8.226	-0.092
100	221	8.251	8.164	-0.087
100	224	8.294	8.185	-0.109
100	225	8.316	8.215	-0.101
100	226	8.258	8.219	-0.039
100	228	8.283	8.262	-0.021
100	232	8.271	8.140	-0.131
100	233	8.264	8.240	-0.024
100	236	8.246	8.160	-0.086
100	238	8.317	8.207	-0.110
100	243	8.274	8.177	-0.097
105	244	8.262	8.155	-0.107
105	246	8.317	8.240	-0.077
105	248	8.280	8.187	-0.093
105	374	8.225	8.150	-0.075
105	375	8.318	8.239	-0.079
105	376	8.343	8.337	0.004
	Max	8.283	8.228	-0.056
	Average	8.278	8.261	-0.017
	Min	8.249	8.200	-0.049
	Std Dev	0.031	0.047	0.042



9.38 T_SYNC FA	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
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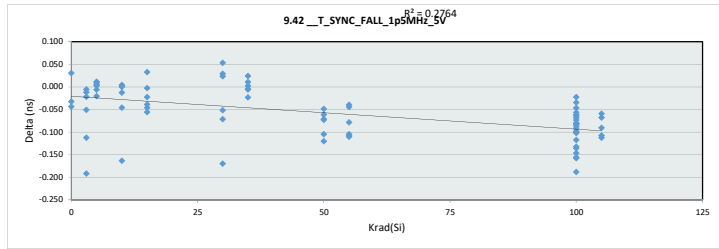
LL											
Min	8.262	8.220	8.218	8.211	8.200	8.207	8.181	8.161	8.181	8.140	8.150
Average	8.278	8.261	8.270	8.245	8.265	8.251	8.264	8.183	8.198	8.200	8.194
Max	8.290	8.328	8.337	8.269	8.321	8.293	8.336	8.205	8.217	8.262	8.240
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



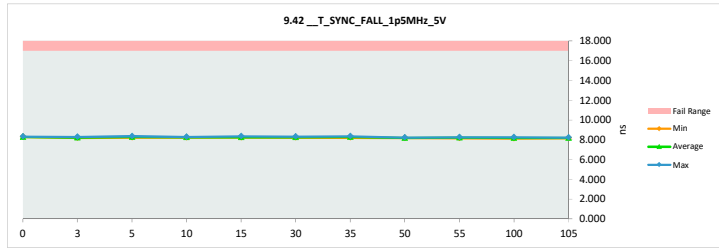
LDR TID Report  
TPS7H5001-SP

9.42_T_SYNC_FALL_1p5MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.302	8.258	-0.044
0	118	8.282	8.312	0.030
0	122	8.300	8.267	-0.033
3	61	8.300	8.187	-0.113
3	62	8.419	8.227	-0.192
3	63	8.220	8.208	-0.012
3	64	8.216	8.194	-0.022
3	65	8.249	8.198	-0.051
3	66	8.272	8.266	-0.006
5	67	8.215	8.208	-0.007
5	68	8.340	8.351	0.011
5	69	8.244	8.223	-0.021
5	70	8.238	8.240	0.002
5	71	8.277	8.282	0.005
5	72	8.332	8.341	0.009
10	73	8.300	8.254	-0.046
10	74	8.419	8.255	-0.164
10	75	8.220	8.221	0.001
10	76	8.216	8.220	0.004
10	77	8.249	8.249	0.000
10	78	8.272	8.259	-0.013
15	79	8.215	8.192	-0.023
15	80	8.340	8.337	-0.003
15	81	8.244	8.276	0.032
15	82	8.238	8.199	-0.039
15	83	8.277	8.231	-0.046
15	84	8.332	8.276	-0.056
30	86	8.300	8.228	-0.072
30	89	8.419	8.249	-0.170
30	91	8.220	8.249	0.029
30	92	8.216	8.239	0.023
30	94	8.249	8.302	0.053
30	95	8.272	8.220	-0.052
35	100	8.215	8.239	0.024
35	101	8.340	8.334	-0.006
35	102	8.244	8.254	0.010
35	103	8.238	8.214	-0.024
35	104	8.277	8.279	0.002
35	105	8.332	8.327	-0.005
50	106	8.258	8.137	-0.121
50	107	8.211	8.162	-0.049
50	108	8.300	8.195	-0.105
50	109	8.250	8.177	-0.073
50	110	8.272	8.200	-0.072
50	111	8.244	8.182	-0.062
55	113	8.243	8.138	-0.105
55	114	8.274	8.167	-0.107
55	115	8.253	8.213	-0.040
55	116	8.221	8.176	-0.045
55	126	8.321	8.242	-0.079
55	128	8.325	8.214	-0.111
100	129	8.280	8.199	-0.081
100	131	8.265	8.230	-0.035
100	132	8.394	8.205	-0.189
100	133	8.385	8.249	-0.136
100	134	8.342	8.184	-0.158
100	135	8.230	8.147	-0.083
100	138	8.256	8.164	-0.092
100	139	8.252	8.151	-0.101
100	140	8.283	8.218	-0.065
100	141	8.270	8.183	-0.087
100	142	8.310	8.241	-0.069
100	143	8.220	8.158	-0.062
100	144	8.287	8.188	-0.099
100	220	8.284	8.237	-0.047
100	221	8.245	8.112	-0.133
100	224	8.258	8.177	-0.081
100	225	8.302	8.245	-0.057
100	226	8.302	8.184	-0.118
100	228	8.299	8.152	-0.147
100	232	8.254	8.231	-0.023
100	233	8.272	8.190	-0.082
100	236	8.218	8.116	-0.102
100	238	8.248	8.174	-0.074
100	243	8.294	8.138	-0.156
105	244	8.237	8.169	-0.068
105	246	8.306	8.198	-0.108
105	248	8.296	8.205	-0.091
105	374	8.190	8.130	-0.060
105	376	8.302	8.189	-0.113
105	Max	8.419	8.351	0.053
105	Average	8.276	8.218	-0.058
105	Min	8.190	8.112	-0.192
105	Std Dev	0.050	0.053	0.056



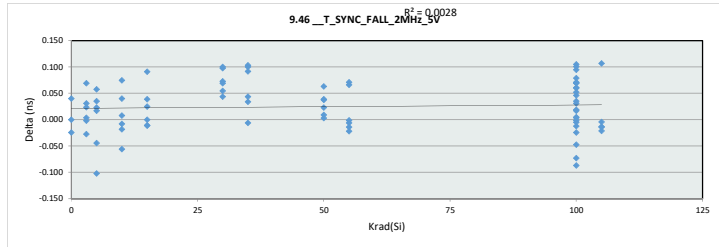
9.42_T_SYNC_Fa											
Test Site											
Tester											
Test Number											
Max Limit	17										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.258	8.187	8.208	8.220	8.192	8.220	8.214	8.137	8.138	8.112	8.130
Average	8.279	8.213	8.274	8.243	8.252	8.248	8.275	8.176	8.192	8.186	8.178
Max	8.312	8.266	8.351	8.259	8.337	8.302	8.334	8.200	8.242	8.249	8.205
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



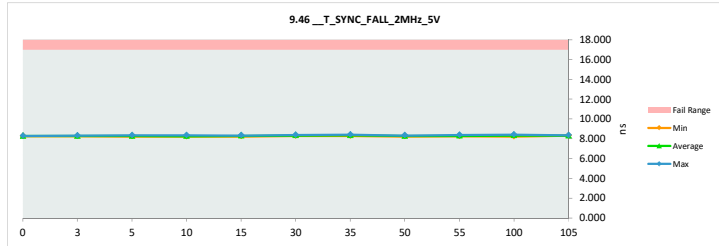
LDR TID Report  
TPS7H5001-SP

9.46 T_SYNC_FALL_2MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.228	8.267	0.039
0	118	8.296	8.295	-0.001
0	122	8.289	8.264	-0.025
3	61	8.283	8.280	-0.003
3	62	8.291	8.314	0.023
3	63	8.232	8.300	0.068
3	64	8.283	8.255	-0.028
3	65	8.233	8.263	0.030
3	66	8.296	8.299	0.003
5	67	8.330	8.285	-0.045
5	68	8.313	8.347	0.034
5	69	8.254	8.270	0.016
5	70	8.228	8.285	0.057
5	71	8.272	8.294	0.022
5	72	8.345	8.242	-0.103
10	73	8.283	8.226	-0.057
10	74	8.291	8.365	0.074
10	75	8.232	8.271	0.039
10	76	8.283	8.274	-0.009
10	77	8.233	8.214	-0.019
10	78	8.296	8.303	0.007
15	79	8.330	8.329	-0.001
15	80	8.313	8.337	0.024
15	81	8.254	8.242	-0.012
15	82	8.228	8.318	0.090
15	83	8.272	8.310	0.038
15	84	8.345	8.333	-0.012
30	86	8.283	8.380	0.097
30	89	8.291	8.363	0.072
30	91	8.232	8.286	0.054
30	92	8.283	8.326	0.043
30	94	8.233	8.301	0.068
30	95	8.296	8.395	0.099
35	100	8.330	8.363	0.033
35	101	8.313	8.412	0.099
35	102	8.254	8.297	0.043
35	103	8.228	8.330	0.102
35	104	8.272	8.363	0.091
35	105	8.345	8.338	-0.007
50	106	8.326	8.334	0.008
50	107	8.249	8.251	0.002
50	108	8.263	8.300	0.037
50	109	8.207	8.245	0.038
50	110	8.281	8.343	0.062
50	111	8.260	8.282	0.022
55	113	8.317	8.387	0.070
55	114	8.257	8.322	0.065
55	115	8.299	8.292	-0.007
55	116	8.312	8.297	-0.015
55	126	8.280	8.257	-0.023
55	128	8.305	8.303	-0.002
100	129	8.278	8.347	0.069
100	131	8.377	8.411	0.034
100	132	8.318	8.422	0.104
100	133	8.370	8.388	0.018
100	134	8.330	8.408	0.078
100	135	8.282	8.234	-0.048
100	138	8.261	8.361	0.100
100	139	8.252	8.320	0.068
100	140	8.312	8.406	0.094
100	141	8.312	8.310	-0.002
100	142	8.313	8.225	-0.088
100	143	8.299	8.286	-0.013
100	144	8.328	8.322	-0.006
100	220	8.389	8.315	-0.074
100	221	8.283	8.353	0.070
100	224	8.287	8.346	0.059
100	225	8.311	8.356	0.045
100	226	8.337	8.388	0.051
100	228	8.394	8.369	-0.025
100	232	8.341	8.344	0.003
100	233	8.266	8.326	0.060
100	236	8.275	8.279	0.004
100	238	8.344	8.374	0.030
100	243	8.335	8.351	0.016
105	244	8.351	8.329	-0.022
105	246	8.343	8.338	-0.005
105	248	8.354	8.339	-0.015
105	374	8.208	8.314	0.106
105	375	8.363	8.349	-0.014
105	376	8.394	8.422	0.028
Max		8.394	8.422	0.028
Average		8.293	8.318	0.025
Min		8.207	8.214	-0.103
Std Dev		0.043	0.049	0.047



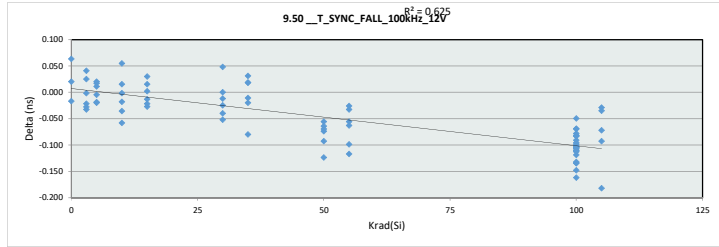
9.46 T_SYNC FA											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.264	8.255	8.242	8.214	8.242	8.286	8.297	8.245	8.257	8.225	8.314
Average	8.275	8.285	8.287	8.276	8.312	8.342	8.351	8.293	8.310	8.343	8.334
Max	8.295	8.314	8.347	8.365	8.337	8.395	8.412	8.343	8.387	8.422	8.349
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



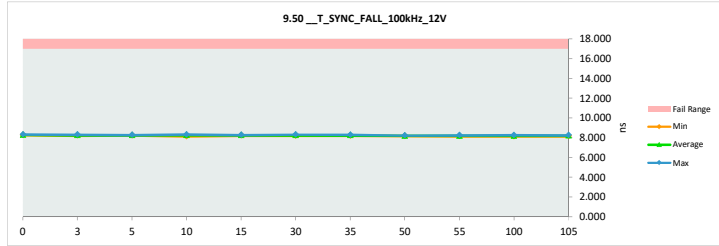
LDR TID Report  
TPS7H5001-SP

9.50_T_SYNC_FALL_100kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.242	8.305	0.063
0	118	8.306	8.289	-0.017
0	122	8.213	8.233	0.020
3	61	8.267	8.234	-0.033
3	62	8.236	8.234	-0.002
3	63	8.161	8.202	0.041
3	64	8.189	8.214	0.025
3	65	8.323	8.301	-0.022
3	66	8.263	8.235	-0.028
5	67	8.220	8.231	0.011
5	68	8.256	8.236	-0.020
5	69	8.203	8.220	0.017
5	70	8.268	8.249	-0.019
5	71	8.230	8.225	-0.005
5	72	8.246	8.266	0.020
10	73	8.267	8.282	0.015
10	74	8.236	8.234	-0.002
10	75	8.161	8.125	-0.036
10	76	8.189	8.244	0.055
10	77	8.323	8.305	-0.018
10	78	8.263	8.205	-0.058
15	79	8.220	8.193	-0.027
15	80	8.256	8.234	-0.022
15	81	8.203	8.233	0.030
15	82	8.268	8.255	-0.013
15	83	8.230	8.232	0.002
15	84	8.246	8.261	0.015
30	86	8.267	8.215	-0.052
30	89	8.236	8.196	-0.040
30	91	8.161	8.209	0.048
30	92	8.189	8.189	0.000
30	94	8.323	8.311	-0.012
30	95	8.263	8.238	-0.025
35	100	8.220	8.251	0.031
35	101	8.256	8.274	0.018
35	102	8.203	8.192	-0.011
35	103	8.268	8.188	-0.080
35	104	8.230	8.210	-0.020
35	105	8.246	8.264	0.018
50	106	8.264	8.190	-0.074
50	107	8.265	8.172	-0.093
50	108	8.263	8.207	-0.056
50	109	8.209	8.145	-0.064
50	110	8.324	8.200	-0.124
50	111	8.257	8.187	-0.070
55	113	8.282	8.226	-0.056
55	114	8.229	8.166	-0.063
55	115	8.183	8.150	-0.033
55	116	8.248	8.131	-0.117
55	126	8.292	8.193	-0.099
55	128	8.213	8.187	-0.026
100	129	8.317	8.214	-0.103
100	131	8.306	8.158	-0.148
100	132	8.264	8.195	-0.069
100	133	8.340	8.242	-0.098
100	134	8.280	8.145	-0.135
100	135	8.244	8.161	-0.083
100	138	8.280	8.173	-0.107
100	139	8.286	8.181	-0.105
100	140	8.302	8.224	-0.078
100	141	8.269	8.159	-0.110
100	142	8.233	8.128	-0.105
100	143	8.287	8.153	-0.134
100	144	8.240	8.157	-0.083
100	220	8.277	8.145	-0.132
100	221	8.225	8.114	-0.111
100	224	8.311	8.149	-0.162
100	225	8.338	8.247	-0.091
100	226	8.260	8.190	-0.070
100	228	8.278	8.228	-0.050
100	232	8.274	8.192	-0.082
100	233	8.312	8.193	-0.119
100	236	8.281	8.168	-0.113
100	238	8.299	8.198	-0.101
100	243	8.314	8.218	-0.096
105	244	8.292	8.220	-0.072
105	246	8.216	8.187	-0.029
105	248	8.280	8.187	-0.093
105	374	8.270	8.235	-0.035
105	376	8.316	8.134	-0.182
105	Max	8.340	8.311	0.029
105	Average	8.257	8.208	-0.049
105	Min	8.161	8.114	-0.047
105	Std Dev	0.042	0.045	0.055



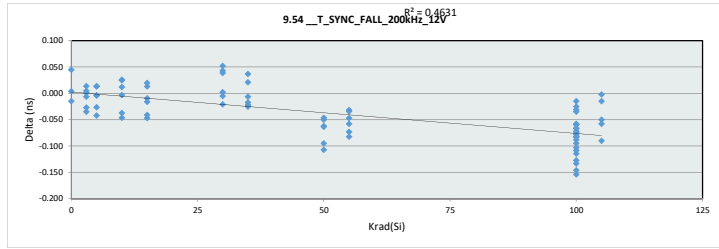
9.50_T_SYNC_Fa											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.233	8.202	8.220	8.125	8.193	8.189	8.188	8.145	8.131	8.114	8.134
Average	8.276	8.237	8.238	8.233	8.235	8.226	8.230	8.184	8.176	8.181	8.193
Max	8.305	8.301	8.266	8.305	8.261	8.311	8.274	8.207	8.226	8.247	8.235
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



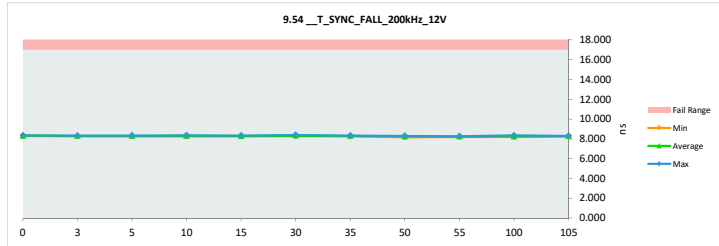
LDR TID Report  
TPS7H5001-SP

9.54_T_SYNC_FALL_200kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.302	8.286	-0.016
0	118	8.333	8.336	0.003
0	122	8.246	8.290	0.044
3	61	8.291	8.295	0.004
3	62	8.302	8.274	-0.028
3	63	8.230	8.243	0.013
3	64	8.273	8.273	0.000
3	65	8.339	8.303	-0.036
3	66	8.291	8.284	-0.007
5	67	8.249	8.244	-0.005
5	68	8.314	8.326	0.012
5	69	8.298	8.311	0.013
5	70	8.280	8.253	-0.027
5	71	8.263	8.258	-0.005
5	72	8.309	8.266	-0.043
10	73	8.291	8.287	-0.004
10	74	8.302	8.255	-0.047
10	75	8.230	8.254	0.024
10	76	8.273	8.235	-0.038
10	77	8.339	8.350	0.011
10	78	8.291	8.316	0.025
15	79	8.249	8.268	0.019
15	80	8.314	8.304	-0.010
15	81	8.298	8.310	0.012
15	82	8.280	8.232	-0.048
15	83	8.263	8.246	-0.017
15	84	8.309	8.267	-0.042
30	86	8.291	8.342	0.051
30	89	8.302	8.280	-0.022
30	91	8.230	8.268	0.038
30	92	8.273	8.274	0.001
30	94	8.339	8.381	0.042
30	95	8.291	8.285	-0.006
35	100	8.249	8.231	-0.018
35	101	8.314	8.307	-0.007
35	102	8.298	8.272	-0.026
35	103	8.280	8.316	0.036
35	104	8.263	8.283	0.020
35	105	8.309	8.286	-0.023
50	106	8.300	8.253	-0.047
50	107	8.298	8.202	-0.096
50	108	8.357	8.293	-0.064
50	109	8.245	8.157	-0.108
50	110	8.340	8.277	-0.063
50	111	8.270	8.219	-0.051
55	113	8.340	8.257	-0.083
55	114	8.236	8.203	-0.033
55	115	8.252	8.178	-0.074
55	116	8.263	8.228	-0.035
55	126	8.261	8.213	-0.048
55	128	8.253	8.194	-0.059
100	129	8.347	8.288	-0.059
100	131	8.351	8.236	-0.115
100	132	8.339	8.255	-0.084
100	133	8.357	8.268	-0.089
100	134	8.312	8.229	-0.083
100	135	8.279	8.196	-0.083
100	138	8.324	8.249	-0.075
100	139	8.342	8.195	-0.147
100	140	8.363	8.331	-0.032
100	141	8.267	8.171	-0.096
100	142	8.324	8.220	-0.104
100	143	8.285	8.206	-0.079
100	144	8.273	8.213	-0.060
100	220	8.289	8.180	-0.109
100	221	8.262	8.202	-0.060
100	224	8.308	8.292	-0.016
100	225	8.388	8.320	-0.068
100	226	8.350	8.283	-0.067
100	228	8.384	8.229	-0.155
100	232	8.303	8.169	-0.134
100	233	8.275	8.249	-0.026
100	236	8.326	8.198	-0.128
100	238	8.285	8.249	-0.036
100	243	8.318	8.245	-0.073
105	244	8.292	8.241	-0.051
105	246	8.302	8.243	-0.059
105	248	8.291	8.288	-0.003
105	374	8.304	8.288	-0.016
105	376	8.330	8.239	-0.091
	Max	8.388	8.381	0.051
	Average	8.298	8.259	-0.038
	Min	8.230	8.157	-0.155
	Std Dev	0.036	0.046	0.047



9.54_T_SYNC_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105

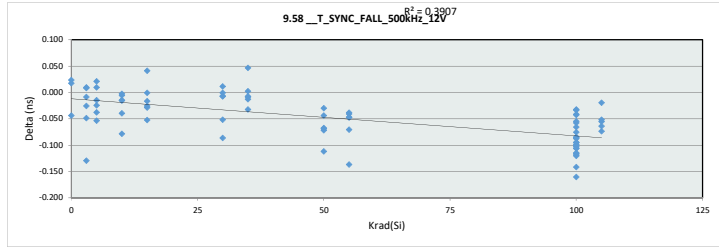




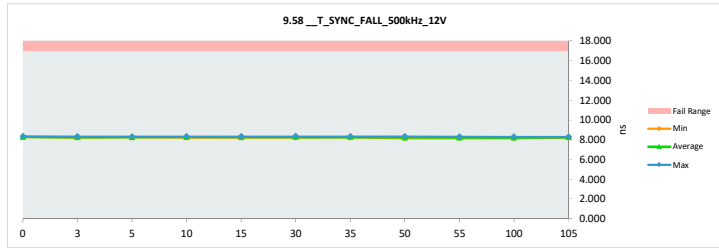
LDR TID Report  
TPS7H5001-SP

9.58_T_SYNC_FALL_500kHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.312	8.268	-0.044
0	118	8.302	8.319	0.017
0	122	8.240	8.263	0.023
3	61	8.311	8.285	-0.026
3	62	8.243	8.251	0.008
3	63	8.198	8.207	0.009
3	64	8.321	8.191	-0.130
3	65	8.349	8.300	-0.049
3	66	8.273	8.264	-0.009
5	67	8.235	8.220	-0.015
5	68	8.298	8.273	-0.025
5	69	8.295	8.241	-0.054
5	70	8.231	8.240	0.009
5	71	8.290	8.252	-0.038
5	72	8.238	8.259	0.021
10	73	8.311	8.308	-0.003
10	74	8.243	8.227	-0.016
10	75	8.198	8.192	-0.006
10	76	8.321	8.242	-0.079
10	77	8.349	8.309	-0.040
10	78	8.273	8.258	-0.015
15	79	8.235	8.206	-0.029
15	80	8.298	8.297	-0.001
15	81	8.295	8.278	-0.017
15	82	8.231	8.205	-0.026
15	83	8.290	8.237	-0.053
15	84	8.238	8.279	0.041
30	86	8.311	8.259	-0.052
30	89	8.243	8.236	-0.007
30	91	8.198	8.197	-0.001
30	92	8.321	8.234	-0.087
30	94	8.349	8.341	-0.008
30	95	8.273	8.284	0.011
35	100	8.235	8.227	-0.008
35	101	8.298	8.265	-0.033
35	102	8.295	8.297	0.002
35	103	8.231	8.222	-0.009
35	104	8.290	8.277	-0.013
35	105	8.238	8.284	0.046
50	106	8.289	8.217	-0.072
50	107	8.271	8.241	-0.030
50	108	8.293	8.249	-0.044
50	109	8.226	8.158	-0.068
50	110	8.357	8.288	-0.069
50	111	8.248	8.136	-0.112
55	113	8.322	8.283	-0.039
55	114	8.230	8.182	-0.048
55	115	8.243	8.202	-0.041
55	116	8.281	8.210	-0.071
55	126	8.236	8.189	-0.047
55	128	8.274	8.137	-0.137
100	129	8.323	8.228	-0.095
100	131	8.333	8.214	-0.119
100	132	8.295	8.194	-0.101
100	133	8.381	8.282	-0.099
100	134	8.351	8.275	-0.076
100	135	8.237	8.152	-0.085
100	138	8.296	8.201	-0.095
100	139	8.303	8.142	-0.161
100	140	8.305	8.217	-0.088
100	141	8.294	8.152	-0.142
100	142	8.292	8.206	-0.086
100	143	8.294	8.236	-0.058
100	144	8.231	8.197	-0.034
100	220	8.300	8.185	-0.115
100	221	8.206	8.163	-0.043
100	224	8.286	8.231	-0.055
100	225	8.337	8.271	-0.066
100	226	8.317	8.214	-0.103
100	228	8.329	8.222	-0.107
100	232	8.323	8.207	-0.116
100	233	8.320	8.199	-0.121
100	236	8.289	8.247	-0.042
100	238	8.300	8.194	-0.106
100	243	8.289	8.256	-0.033
105	244	8.290	8.234	-0.056
105	246	8.276	8.212	-0.064
105	248	8.290	8.238	-0.052
105	374	8.290	8.270	-0.020
105	376	8.276	8.202	-0.074
105	Max	8.381	8.341	0.046
105	Average	8.283	8.234	-0.049
105	Min	8.198	8.136	-0.161
105	Std Dev	0.041	0.044	0.045



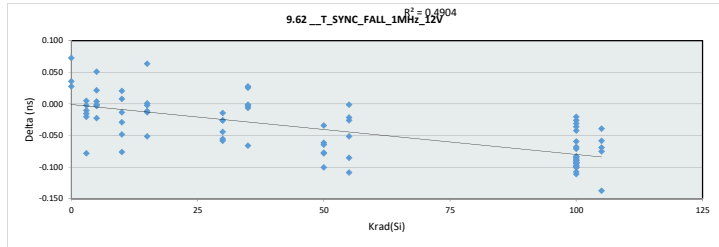
9.58_T_SYNC_FALL_500kHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.263	8.191	8.220	8.192	8.205	8.197	8.222	8.136	8.137	8.142	8.202
Average	8.283	8.250	8.248	8.256	8.250	8.259	8.262	8.215	8.201	8.212	8.231
Max	8.319	8.300	8.273	8.309	8.297	8.341	8.297	8.288	8.283	8.282	8.270
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



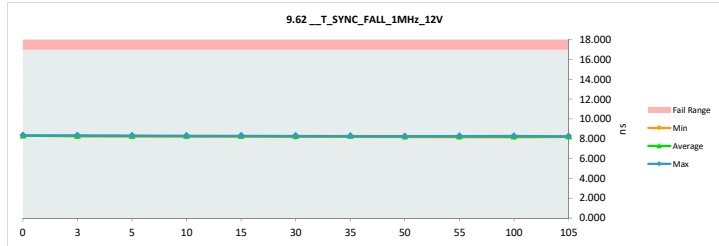
LDR TID Report  
TPS7H5001-SP

9.62_T_SYNC_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.281	8.308	0.027
0	118	8.306	8.341	0.035
0	122	8.276	8.348	0.072
3	61	8.296	8.280	-0.016
3	62	8.280	8.259	-0.021
3	63	8.262	8.251	-0.011
3	64	8.266	8.270	0.004
3	65	8.351	8.348	-0.003
3	66	8.335	8.256	-0.079
5	67	8.255	8.258	0.003
5	68	8.306	8.302	-0.004
5	69	8.291	8.312	0.021
5	70	8.216	8.266	0.050
5	71	8.294	8.292	-0.002
5	72	8.308	8.285	-0.023
10	73	8.296	8.303	0.007
10	74	8.280	8.266	-0.014
10	75	8.262	8.213	-0.049
10	76	8.266	8.286	0.020
10	77	8.351	8.321	-0.030
10	78	8.335	8.258	-0.077
15	79	8.255	8.243	-0.012
15	80	8.306	8.303	-0.003
15	81	8.291	8.277	-0.014
15	82	8.216	8.279	0.063
15	83	8.294	8.242	-0.052
15	84	8.308	8.308	0.000
30	86	8.296	8.281	-0.015
30	89	8.280	8.253	-0.027
30	91	8.262	8.235	-0.027
30	92	8.266	8.207	-0.059
30	94	8.351	8.306	-0.045
30	95	8.335	8.279	-0.056
35	100	8.255	8.280	0.025
35	101	8.306	8.304	-0.002
35	102	8.291	8.284	-0.007
35	103	8.216	8.243	0.027
35	104	8.294	8.290	-0.004
35	105	8.308	8.241	-0.067
50	106	8.295	8.230	-0.065
50	107	8.312	8.233	-0.079
50	108	8.331	8.253	-0.078
50	109	8.222	8.187	-0.035
50	110	8.368	8.267	-0.101
50	111	8.282	8.220	-0.062
55	113	8.298	8.296	-0.002
55	114	8.247	8.195	-0.052
55	115	8.277	8.250	-0.027
55	116	8.259	8.237	-0.022
55	126	8.318	8.232	-0.086
55	128	8.279	8.170	-0.109
100	129	8.371	8.281	-0.090
100	131	8.305	8.221	-0.084
100	132	8.312	8.243	-0.069
100	133	8.389	8.301	-0.088
100	134	8.330	8.237	-0.093
100	135	8.244	8.160	-0.084
100	138	8.292	8.193	-0.099
100	139	8.299	8.239	-0.060
100	140	8.330	8.237	-0.093
100	141	8.298	8.208	-0.090
100	142	8.265	8.244	-0.021
100	143	8.263	8.236	-0.027
100	144	8.296	8.184	-0.112
100	220	8.303	8.202	-0.101
100	221	8.278	8.170	-0.108
100	224	8.329	8.231	-0.098
100	225	8.380	8.286	-0.094
100	226	8.313	8.270	-0.043
100	228	8.294	8.257	-0.037
100	232	8.302	8.201	-0.101
100	233	8.330	8.235	-0.095
100	236	8.288	8.202	-0.086
100	238	8.244	8.212	-0.032
100	243	8.311	8.239	-0.072
105	244	8.317	8.247	-0.070
105	246	8.274	8.198	-0.076
105	248	8.307	8.267	-0.040
105	374	8.300	8.241	-0.059
105	376	8.332	8.194	-0.138
	Max	8.389	8.348	0.072
	Average	8.295	8.254	-0.042
	Min	8.216	8.160	-0.138
	Std Dev	0.036	0.042	0.046



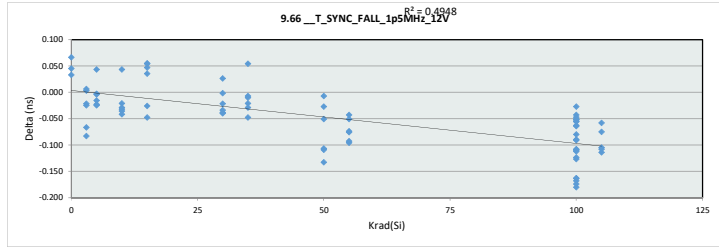
9.62_T_SYNC_FALL_1MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.308	8.251	8.258	8.213	8.242	8.207	8.241	8.187	8.170	8.160	8.194
Average	8.332	8.277	8.286	8.275	8.275	8.260	8.274	8.232	8.230	8.229	8.229
Max	8.348	8.348	8.312	8.321	8.308	8.306	8.304	8.267	8.296	8.301	8.267
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



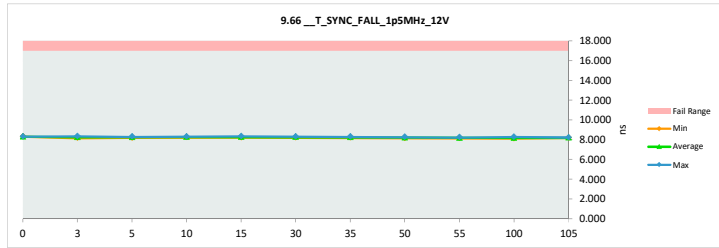
LDR TID Report  
TPS7H5001-SP

9.66_T_SYNC_FALL_1p5MHz_12V		
Test Site		
Tester		
Test Number		
Unit	ns	ns
Max Limit	17	17
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.251	8.296	0.045
0	118	8.260	8.293	0.033
0	122	8.252	8.318	0.066
3	61	8.302	8.305	0.003
3	62	8.311	8.228	-0.083
3	63	8.187	8.162	-0.025
3	64	8.277	8.210	-0.067
3	65	8.339	8.345	0.006
3	66	8.285	8.263	-0.022
5	67	8.201	8.185	-0.016
5	68	8.319	8.296	-0.023
5	69	8.219	8.262	0.043
5	70	8.268	8.265	-0.003
5	71	8.252	8.248	-0.004
5	72	8.297	8.272	-0.025
10	73	8.302	8.273	-0.029
10	74	8.311	8.269	-0.042
10	75	8.187	8.230	0.043
10	76	8.277	8.241	-0.036
10	77	8.339	8.318	-0.021
10	78	8.285	8.253	-0.032
15	79	8.201	8.256	0.055
15	80	8.319	8.293	-0.026
15	81	8.219	8.266	0.047
15	82	8.268	8.322	0.054
15	83	8.252	8.204	-0.048
15	84	8.297	8.332	0.035
30	86	8.302	8.268	-0.034
30	89	8.311	8.309	-0.002
30	91	8.187	8.213	0.026
30	92	8.277	8.238	-0.039
30	94	8.339	8.299	-0.040
30	95	8.285	8.263	-0.022
35	100	8.201	8.255	0.054
35	101	8.319	8.271	-0.048
35	102	8.219	8.190	-0.029
35	103	8.268	8.258	-0.010
35	104	8.252	8.231	-0.021
35	105	8.297	8.290	-0.007
50	106	8.299	8.248	-0.051
50	107	8.283	8.256	-0.027
50	108	8.309	8.200	-0.109
50	109	8.231	8.224	-0.007
50	110	8.357	8.250	-0.107
50	111	8.286	8.153	-0.133
55	113	8.281	8.230	-0.051
55	114	8.222	8.126	-0.096
55	115	8.213	8.170	-0.043
55	116	8.245	8.169	-0.076
55	126	8.321	8.228	-0.093
55	128	8.275	8.201	-0.074
100	129	8.336	8.212	-0.124
100	131	8.307	8.195	-0.112
100	132	8.304	8.248	-0.056
100	133	8.319	8.272	-0.047
100	134	8.374	8.211	-0.163
100	135	8.206	8.143	-0.063
100	138	8.265	8.213	-0.052
100	139	8.251	8.187	-0.064
100	140	8.297	8.189	-0.108
100	141	8.278	8.098	-0.180
100	142	8.303	8.176	-0.127
100	143	8.212	8.169	-0.043
100	144	8.219	8.139	-0.080
100	220	8.228	8.173	-0.055
100	221	8.225	8.115	-0.110
100	224	8.338	8.175	-0.163
100	225	8.415	8.241	-0.174
100	226	8.299	8.248	-0.051
100	228	8.363	8.195	-0.168
100	232	8.263	8.154	-0.109
100	233	8.238	8.186	-0.052
100	236	8.245	8.154	-0.091
100	238	8.245	8.218	-0.027
100	243	8.290	8.200	-0.090
105	244	8.284	8.176	-0.108
105	246	8.232	8.174	-0.058
105	248	8.297	8.222	-0.075
105	374	8.280	8.175	-0.105
105	376	8.319	8.205	-0.114
	Max	8.415	8.345	0.066
	Average	8.276	8.228	-0.049
	Min	8.187	8.098	-0.180
	Std Dev	0.047	0.054	0.058



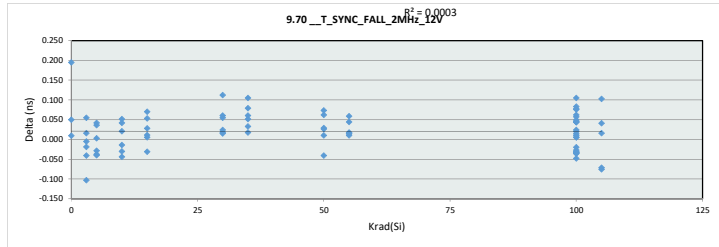
9.66_T_SYNC_Fa											
Test Site											
Tester											
Test Number											
Max Limit	17	ns									
Min Limit		ns									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.293	8.162	8.185	8.230	8.204	8.213	8.190	8.153	8.126	8.098	8.174
Average	8.302	8.252	8.255	8.264	8.279	8.265	8.249	8.222	8.187	8.188	8.190
Max	8.318	8.345	8.296	8.318	8.332	8.309	8.290	8.256	8.230	8.272	8.222
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



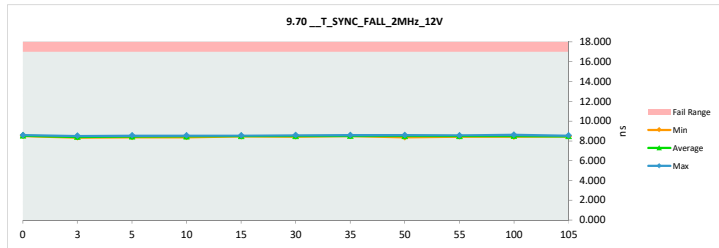
LDR TID Report  
TPS7H5001-SP

9.70_T_SYNC_FALL_2MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.359	8.553	0.194
0	118	8.487	8.495	0.008
0	122	8.419	8.468	0.049
3	61	8.492	8.472	-0.020
3	62	8.424	8.439	0.015
3	63	8.397	8.391	-0.006
3	64	8.370	8.328	-0.042
3	65	8.467	8.363	-0.104
3	66	8.362	8.416	0.054
5	67	8.411	8.446	0.035
5	68	8.501	8.503	0.002
5	69	8.418	8.388	-0.030
5	70	8.388	8.429	0.041
5	71	8.381	8.340	-0.041
5	72	8.521	8.482	-0.039
10	73	8.492	8.512	0.020
10	74	8.424	8.409	-0.015
10	75	8.397	8.352	-0.045
10	76	8.370	8.420	0.050
10	77	8.467	8.436	-0.031
10	78	8.362	8.403	0.041
15	79	8.411	8.438	0.027
15	80	8.501	8.511	0.010
15	81	8.418	8.422	0.004
15	82	8.388	8.440	0.052
15	83	8.381	8.450	0.069
15	84	8.521	8.489	-0.032
30	86	8.492	8.546	0.054
30	89	8.424	8.438	0.014
30	91	8.397	8.414	0.017
30	92	8.370	8.429	0.059
30	94	8.467	8.490	0.023
30	95	8.362	8.473	0.111
35	100	8.411	8.461	0.050
35	101	8.501	8.517	0.016
35	102	8.418	8.477	0.059
35	103	8.388	8.492	0.104
35	104	8.381	8.459	0.078
35	105	8.521	8.553	0.032
50	106	8.467	8.476	0.009
50	107	8.452	8.524	0.072
50	108	8.432	8.457	0.025
50	109	8.414	8.442	0.028
50	110	8.496	8.557	0.061
50	111	8.373	8.331	-0.042
55	113	8.482	8.525	0.043
55	114	8.355	8.413	0.058
55	115	8.419	8.434	0.015
55	116	8.458	8.471	0.013
55	126	8.496	8.505	0.009
55	128	8.425	8.441	0.016
100	129	8.531	8.535	0.004
100	131	8.431	8.473	0.042
100	132	8.413	8.495	0.082
100	133	8.554	8.518	-0.036
100	134	8.480	8.523	0.043
100	135	8.365	8.421	0.056
100	138	8.461	8.509	0.048
100	139	8.454	8.420	-0.034
100	140	8.488	8.532	0.044
100	141	8.385	8.408	0.023
100	142	8.477	8.440	-0.037
100	143	8.474	8.425	-0.049
100	144	8.474	8.453	-0.021
100	220	8.467	8.486	0.019
100	221	8.382	8.391	0.009
100	224	8.463	8.429	-0.034
100	225	8.460	8.521	0.061
100	226	8.455	8.530	0.075
100	228	8.516	8.592	0.076
100	232	8.481	8.494	0.013
100	233	8.436	8.440	0.004
100	236	8.421	8.389	-0.032
100	238	8.386	8.490	0.104
100	243	8.483	8.455	-0.028
105	244	8.500	8.427	-0.073
105	246	8.500	8.423	-0.077
105	248	8.403	8.504	0.101
105	374	8.396	8.436	0.040
105	376	8.424	8.439	0.015
105	Max	8.554	8.592	0.194
105	Average	8.439	8.459	0.020
105	Min	8.355	8.328	-0.104
105	Std Dev	0.051	0.055	0.049



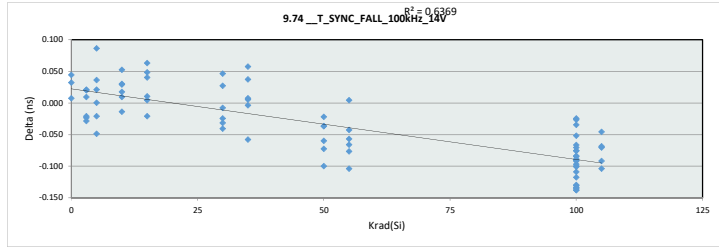
9.70_T_SYNC_FALL_2MHz_12V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.468	8.328	8.340	8.352	8.422	8.414	8.459	8.331	8.413	8.389	8.423
Average	8.505	8.402	8.431	8.422	8.458	8.465	8.493	8.465	8.474	8.446	
Max	8.553	8.472	8.503	8.512	8.511	8.546	8.553	8.557	8.525	8.592	8.504
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



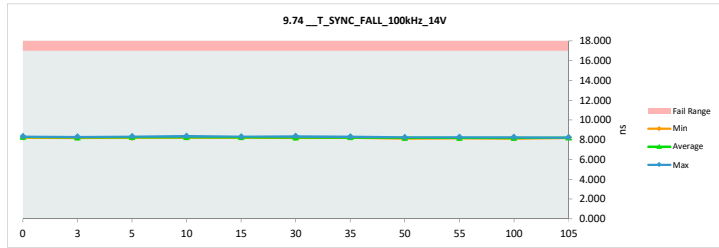
LDR TID Report  
TPS7H5001-SP

9.74_T_SYNC_FALL_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.269	8.301	0.032
0	118	8.251	8.295	0.044
0	122	8.215	8.222	0.007
3	61	8.221	8.242	0.021
3	62	8.216	8.187	-0.029
3	63	8.186	8.162	-0.024
3	64	8.203	8.212	0.009
3	65	8.298	8.277	-0.021
3	66	8.218	8.238	0.020
5	67	8.165	8.201	0.036
5	68	8.279	8.300	0.021
5	69	8.201	8.287	0.086
5	70	8.204	8.204	0.000
5	71	8.236	8.187	-0.049
5	72	8.257	8.236	-0.021
10	73	8.221	8.238	0.017
10	74	8.216	8.245	0.029
10	75	8.186	8.216	0.030
10	76	8.203	8.212	0.009
10	77	8.298	8.350	0.052
10	78	8.218	8.204	-0.014
15	79	8.165	8.213	0.048
15	80	8.279	8.283	0.004
15	81	8.201	8.241	0.040
15	82	8.204	8.267	0.063
15	83	8.236	8.215	-0.021
15	84	8.257	8.267	0.010
30	86	8.221	8.267	0.046
30	89	8.216	8.208	-0.008
30	91	8.186	8.161	-0.025
30	92	8.203	8.162	-0.041
30	94	8.298	8.325	0.027
30	95	8.218	8.186	-0.032
35	100	8.165	8.202	0.037
35	101	8.279	8.284	0.005
35	102	8.201	8.258	0.057
35	103	8.204	8.211	0.007
35	104	8.236	8.232	-0.004
35	105	8.257	8.199	-0.058
50	106	8.254	8.154	-0.100
50	107	8.235	8.213	-0.022
50	108	8.266	8.193	-0.073
50	109	8.170	8.110	-0.060
50	110	8.258	8.221	-0.037
50	111	8.205	8.168	-0.037
55	113	8.292	8.235	-0.057
55	114	8.186	8.143	-0.043
55	115	8.198	8.121	-0.077
55	116	8.187	8.191	0.004
55	126	8.264	8.160	-0.104
55	128	8.222	8.156	-0.066
100	129	8.306	8.222	-0.084
100	131	8.268	8.167	-0.101
100	132	8.246	8.170	-0.076
100	133	8.342	8.204	-0.138
100	134	8.325	8.195	-0.130
100	135	8.296	8.161	-0.135
100	138	8.206	8.181	-0.025
100	139	8.267	8.136	-0.131
100	140	8.286	8.215	-0.071
100	141	8.268	8.134	-0.134
100	142	8.259	8.183	-0.076
100	143	8.266	8.214	-0.052
100	144	8.205	8.113	-0.092
100	220	8.270	8.152	-0.118
100	221	8.201	8.110	-0.091
100	224	8.280	8.191	-0.089
100	225	8.345	8.236	-0.109
100	226	8.326	8.229	-0.097
100	228	8.266	8.239	-0.027
100	232	8.244	8.177	-0.067
100	233	8.269	8.131	-0.138
100	236	8.276	8.191	-0.085
100	238	8.219	8.184	-0.035
100	243	8.279	8.179	-0.100
105	244	8.269	8.198	-0.071
105	246	8.267	8.163	-0.104
105	248	8.271	8.202	-0.069
105	374	8.254	8.208	-0.046
105	376	8.307	8.215	-0.092
105	Max	8.345	8.350	0.006
105	Average	8.243	8.207	-0.036
105	Min	8.165	8.110	-0.138
105	Std Dev	0.043	0.050	0.057



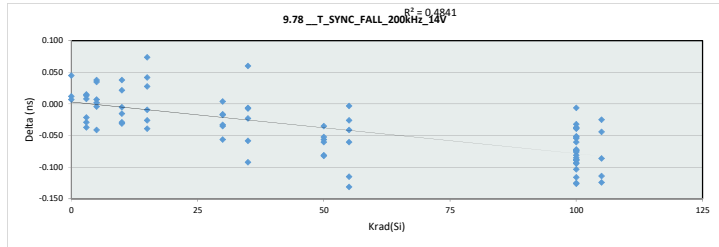
9.74_T_SYNC_FALL_100kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.222	8.162	8.187	8.204	8.213	8.161	8.199	8.110	8.121	8.110	8.163
Average	8.273	8.220	8.236	8.244	8.248	8.218	8.231	8.177	8.168	8.180	8.197
Max	8.301	8.277	8.300	8.350	8.283	8.325	8.284	8.221	8.235	8.239	8.215
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



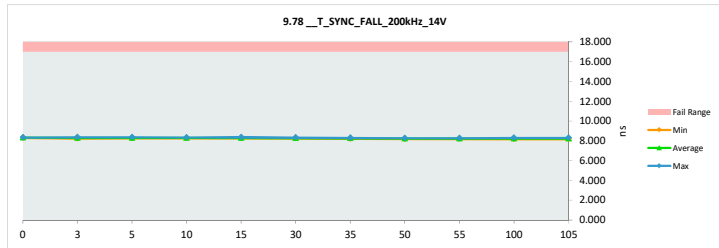
LDR TID Report  
TPS7H5001-SP

9.78_T_SYNC_FALL_200kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.296	8.307	0.011
0	118	8.284	8.328	0.044
0	122	8.284	8.290	0.006
3	61	8.315	8.322	0.007
3	62	8.286	8.256	-0.030
3	63	8.256	8.234	-0.022
3	64	8.271	8.285	0.014
3	65	8.328	8.340	0.012
3	66	8.314	8.276	-0.038
5	67	8.216	8.253	0.037
5	68	8.294	8.300	0.006
5	69	8.286	8.287	0.001
5	70	8.294	8.252	-0.042
5	71	8.299	8.294	-0.005
5	72	8.320	8.354	0.034
10	73	8.315	8.285	-0.030
10	74	8.286	8.254	-0.032
10	75	8.256	8.240	-0.016
10	76	8.271	8.308	0.037
10	77	8.328	8.322	-0.006
10	78	8.314	8.335	0.021
15	79	8.216	8.257	0.041
15	80	8.294	8.367	0.073
15	81	8.286	8.259	-0.027
15	82	8.294	8.284	-0.010
15	83	8.299	8.326	0.027
15	84	8.320	8.280	-0.040
30	86	8.315	8.279	-0.036
30	89	8.286	8.252	-0.034
30	91	8.256	8.259	0.003
30	92	8.271	8.254	-0.017
30	94	8.328	8.310	-0.018
30	95	8.314	8.257	-0.057
35	100	8.216	8.275	0.059
35	101	8.294	8.270	-0.024
35	102	8.286	8.279	-0.007
35	103	8.294	8.201	-0.093
35	104	8.299	8.291	-0.008
35	105	8.320	8.261	-0.059
50	106	8.289	8.232	-0.057
50	107	8.306	8.223	-0.083
50	108	8.312	8.230	-0.082
50	109	8.234	8.170	-0.061
50	110	8.305	8.252	-0.053
50	111	8.279	8.243	-0.036
55	113	8.300	8.239	-0.061
55	114	8.261	8.219	-0.042
55	115	8.262	8.235	-0.027
55	116	8.258	8.254	-0.004
55	126	8.364	8.232	-0.132
55	128	8.302	8.186	-0.116
100	129	8.336	8.303	-0.033
100	131	8.322	8.240	-0.082
100	132	8.284	8.244	-0.040
100	133	8.342	8.267	-0.075
100	134	8.330	8.269	-0.061
100	135	8.277	8.270	-0.007
100	138	8.314	8.225	-0.089
100	139	8.288	8.233	-0.055
100	140	8.314	8.261	-0.053
100	141	8.307	8.180	-0.127
100	142	8.331	8.241	-0.090
100	143	8.288	8.236	-0.052
100	144	8.320	8.203	-0.117
100	220	8.291	8.202	-0.089
100	221	8.282	8.156	-0.126
100	224	8.323	8.250	-0.073
100	225	8.405	8.301	-0.104
100	226	8.340	8.265	-0.075
100	228	8.291	8.252	-0.039
100	232	8.304	8.218	-0.086
100	233	8.316	8.222	-0.094
100	236	8.304	8.227	-0.077
100	238	8.289	8.251	-0.038
100	243	8.333	8.238	-0.095
105	244	8.345	8.220	-0.125
105	246	8.277	8.162	-0.115
105	248	8.323	8.236	-0.087
105	374	8.325	8.280	-0.045
105	375	8.300	8.274	-0.026
105	Max	8.405	8.367	0.038
105	Average	8.298	8.259	-0.039
105	Min	8.216	8.156	-0.132
105	Std Dev	0.031	0.042	0.047



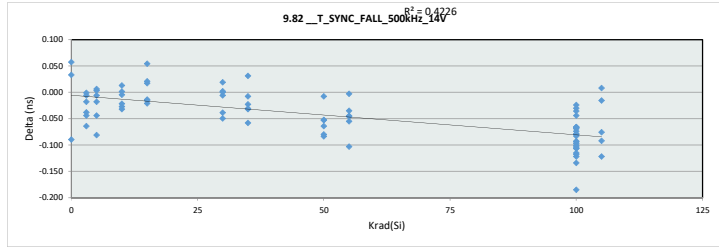
9.78_T_SYNC_FALL_200kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.290	8.234	8.252	8.240	8.257	8.252	8.201	8.170	8.186	8.156	8.162
Average	8.308	8.286	8.290	8.291	8.296	8.269	8.263	8.225	8.228	8.240	8.234
Max	8.328	8.340	8.354	8.335	8.367	8.310	8.291	8.252	8.254	8.303	8.280
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



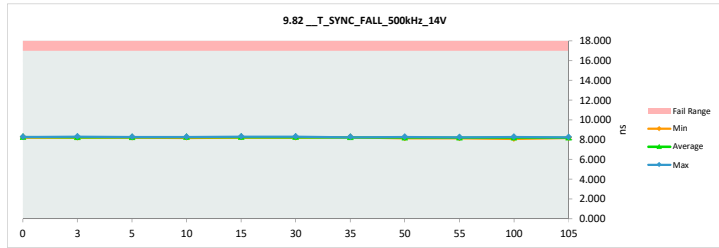
LDR TID Report  
TPS7H5001-SP

9.82_T_SYNC_FALL_500kHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.320	8.230	-0.090
0	118	8.244	8.277	0.033
0	122	8.228	8.285	0.057
3	61	8.265	8.227	-0.038
3	62	8.275	8.211	-0.064
3	63	8.218	8.200	-0.018
3	64	8.252	8.246	-0.006
3	65	8.310	8.309	-0.001
3	66	8.283	8.239	-0.044
5	67	8.240	8.222	-0.018
5	68	8.264	8.270	0.006
5	69	8.260	8.254	-0.006
5	70	8.220	8.223	0.003
5	71	8.304	8.223	-0.081
5	72	8.291	8.247	-0.044
10	73	8.265	8.266	0.001
10	74	8.275	8.248	-0.027
10	75	8.218	8.196	-0.022
10	76	8.252	8.265	0.013
10	77	8.310	8.278	-0.032
10	78	8.283	8.278	-0.005
15	79	8.240	8.226	-0.014
15	80	8.264	8.285	0.021
15	81	8.260	8.314	0.054
15	82	8.220	8.237	0.017
15	83	8.304	8.283	-0.021
15	84	8.291	8.275	-0.016
30	86	8.265	8.284	0.019
30	89	8.275	8.236	-0.039
30	91	8.218	8.218	0.000
30	92	8.252	8.246	-0.006
30	94	8.310	8.312	0.002
30	95	8.283	8.233	-0.050
35	100	8.240	8.232	-0.008
35	101	8.264	8.233	-0.031
35	102	8.260	8.237	-0.023
35	103	8.220	8.251	0.031
35	104	8.304	8.246	-0.058
35	105	8.291	8.259	-0.032
50	106	8.294	8.210	-0.084
50	107	8.248	8.240	-0.008
50	108	8.354	8.273	-0.080
50	109	8.205	8.152	-0.053
50	110	8.296	8.243	-0.053
50	111	8.243	8.179	-0.064
55	113	8.291	8.236	-0.055
55	114	8.245	8.142	-0.103
55	115	8.216	8.171	-0.045
55	116	8.247	8.212	-0.035
55	126	8.248	8.245	-0.003
55	128	8.231	8.185	-0.046
100	129	8.343	8.237	-0.106
100	131	8.306	8.213	-0.093
100	132	8.300	8.231	-0.069
100	133	8.354	8.253	-0.101
100	134	8.352	8.218	-0.134
100	135	8.252	8.145	-0.107
100	138	8.247	8.173	-0.074
100	139	8.253	8.149	-0.104
100	140	8.301	8.277	-0.024
100	141	8.264	8.079	-0.185
100	142	8.225	8.189	-0.036
100	143	8.223	8.179	-0.044
100	144	8.227	8.197	-0.030
100	220	8.261	8.182	-0.079
100	221	8.269	8.147	-0.122
100	224	8.290	8.210	-0.080
100	225	8.351	8.257	-0.094
100	226	8.296	8.178	-0.118
100	228	8.294	8.228	-0.066
100	232	8.315	8.218	-0.097
100	233	8.289	8.222	-0.067
100	236	8.293	8.210	-0.083
100	238	8.274	8.208	-0.066
100	243	8.323	8.208	-0.115
105	244	8.276	8.200	-0.076
105	246	8.297	8.175	-0.122
105	248	8.261	8.245	-0.016
105	374	8.245	8.253	0.008
105	376	8.291	8.199	-0.092
	Max	8.354	8.314	0.057
	Average	8.272	8.227	-0.044
	Min	8.205	8.079	-0.185
	Std Dev	0.036	0.043	0.047



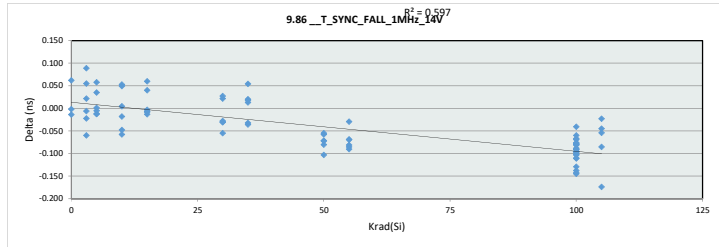
9.82_T_SYNC_FALL_500kHz_14V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.230	8.200	8.222	8.196	8.226	8.218	8.232	8.152	8.142	8.079	8.175
Average	8.264	8.239	8.240	8.255	8.270	8.255	8.243	8.216	8.199	8.200	8.214
Max	8.285	8.309	8.270	8.278	8.314	8.312	8.259	8.273	8.245	8.277	8.253
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



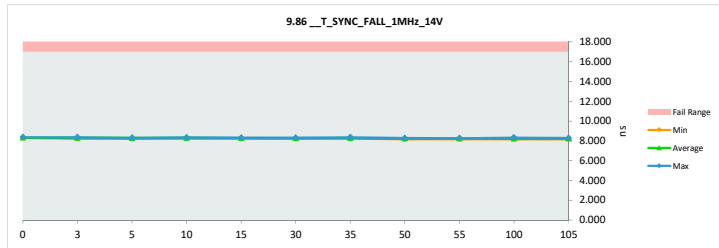
LDR TID Report  
TPS7H5001-SP

9.86_T_SYNC_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.288	8.285	-0.003
0	118	8.302	8.363	0.061
0	122	8.306	8.291	-0.015
3	61	8.256	8.310	0.054
3	62	8.215	8.303	0.088
3	63	8.246	8.223	-0.023
3	64	8.318	8.257	-0.061
3	65	8.318	8.339	0.021
3	66	8.314	8.307	-0.007
5	67	8.242	8.242	0.000
5	68	8.294	8.288	-0.006
5	69	8.274	8.261	-0.013
5	70	8.266	8.253	-0.013
5	71	8.252	8.286	0.034
5	72	8.224	8.281	0.057
10	73	8.256	8.304	0.048
10	74	8.215	8.267	0.052
10	75	8.246	8.227	-0.019
10	76	8.318	8.259	-0.059
10	77	8.318	8.322	0.004
10	78	8.314	8.265	-0.049
15	79	8.242	8.233	-0.009
15	80	8.294	8.286	-0.008
15	81	8.274	8.270	-0.004
15	82	8.266	8.252	-0.014
15	83	8.252	8.291	0.039
15	84	8.224	8.283	0.059
30	86	8.256	8.282	0.026
30	89	8.215	8.236	0.021
30	91	8.246	8.214	-0.032
30	92	8.318	8.289	-0.029
30	94	8.318	8.287	-0.031
30	95	8.314	8.258	-0.056
35	100	8.242	8.254	0.012
35	101	8.294	8.347	0.053
35	102	8.274	8.241	-0.033
35	103	8.266	8.229	-0.037
35	104	8.252	8.269	0.017
35	105	8.224	8.243	0.019
50	106	8.288	8.229	-0.059
50	107	8.305	8.232	-0.073
50	108	8.287	8.231	-0.056
50	109	8.246	8.157	-0.081
50	110	8.364	8.260	-0.104
50	111	8.253	8.180	-0.073
55	113	8.311	8.241	-0.070
55	114	8.295	8.213	-0.082
55	115	8.263	8.177	-0.086
55	116	8.303	8.212	-0.091
55	126	8.288	8.218	-0.070
55	128	8.236	8.206	-0.030
100	129	8.332	8.250	-0.082
100	131	8.332	8.228	-0.104
100	132	8.333	8.194	-0.139
100	133	8.361	8.281	-0.080
100	134	8.321	8.210	-0.111
100	135	8.260	8.183	-0.077
100	138	8.271	8.174	-0.097
100	139	8.296	8.228	-0.068
100	140	8.336	8.255	-0.081
100	141	8.310	8.219	-0.091
100	142	8.253	8.183	-0.070
100	143	8.262	8.165	-0.097
100	144	8.289	8.177	-0.112
100	220	8.280	8.177	-0.103
100	221	8.252	8.163	-0.089
100	224	8.324	8.228	-0.096
100	225	8.366	8.305	-0.061
100	226	8.323	8.231	-0.092
100	228	8.280	8.238	-0.042
100	232	8.323	8.223	-0.100
100	233	8.361	8.215	-0.146
100	236	8.328	8.198	-0.130
100	238	8.349	8.205	-0.144
100	243	8.287	8.208	-0.079
105	244	8.289	8.234	-0.055
105	246	8.272	8.226	-0.046
105	248	8.334	8.159	-0.175
105	374	8.286	8.262	-0.024
105	376	8.321	8.235	-0.086
105	375	8.366	8.363	0.003
105	Max	8.286	8.244	-0.043
105	Min	8.215	8.157	-0.058
105	Std Dev	0.038	0.045	0.057



9.86_T_SYNC_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	8.285 8.223 8.242 8.227 8.233 8.214 8.229 8.157 8.177 8.163 8.159
Average	8.313 8.290 8.269 8.274 8.269 8.261 8.264 8.215 8.211 8.214 8.223
Max	8.363 8.339 8.288 8.322 8.291 8.289 8.347 8.260 8.241 8.305 8.262
UL	17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000

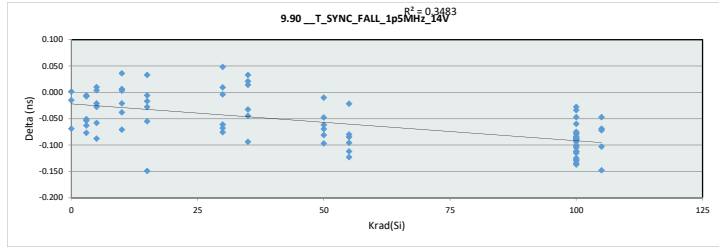




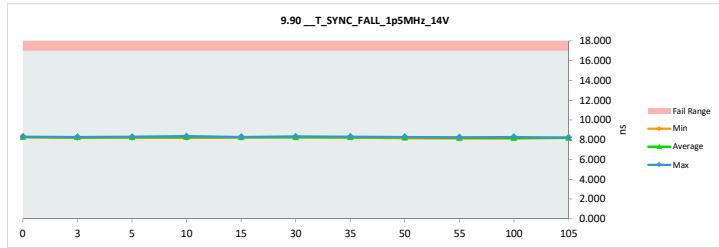
LDR TID Report  
TPS7H5001-SP

9.90_T_SYNC_FALL_1p5MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.243	8.244	0.001
0	118	8.306	8.291	-0.015
0	122	8.330	8.261	-0.069
3	61	8.316	8.262	-0.054
3	62	8.314	8.251	-0.063
3	63	8.166	8.160	-0.006
3	64	8.230	8.222	-0.008
3	65	8.315	8.264	-0.051
3	66	8.316	8.239	-0.077
5	67	8.260	8.202	-0.058
5	68	8.274	8.284	0.010
5	69	8.217	8.196	-0.021
5	70	8.354	8.266	-0.088
5	71	8.235	8.239	0.004
5	72	8.284	8.256	-0.028
10	73	8.316	8.245	-0.071
10	74	8.314	8.293	-0.021
10	75	8.166	8.172	0.006
10	76	8.230	8.233	0.003
10	77	8.315	8.351	0.036
10	78	8.316	8.278	-0.038
15	79	8.260	8.205	-0.055
15	80	8.274	8.268	-0.006
15	81	8.217	8.200	-0.017
15	82	8.354	8.205	-0.149
15	83	8.235	8.268	0.033
15	84	8.284	8.256	-0.028
30	86	8.316	8.240	-0.076
30	89	8.314	8.253	-0.061
30	91	8.166	8.214	0.048
30	92	8.230	8.239	0.009
30	94	8.315	8.311	-0.004
30	95	8.316	8.248	-0.068
35	100	8.260	8.215	-0.045
35	101	8.274	8.307	0.033
35	102	8.217	8.231	0.014
35	103	8.354	8.260	-0.094
35	104	8.235	8.202	-0.033
35	105	8.284	8.305	0.021
50	106	8.281	8.200	-0.081
50	107	8.300	8.252	-0.048
50	108	8.304	8.207	-0.097
50	109	8.217	8.207	-0.010
50	110	8.326	8.264	-0.062
50	111	8.215	8.145	-0.070
55	113	8.232	8.210	-0.022
55	114	8.211	8.099	-0.112
55	115	8.269	8.173	-0.096
55	116	8.256	8.176	-0.080
55	126	8.361	8.238	-0.123
55	128	8.285	8.200	-0.085
100	129	8.343	8.213	-0.130
100	131	8.301	8.207	-0.094
100	132	8.322	8.197	-0.125
100	133	8.350	8.238	-0.112
100	134	8.320	8.234	-0.086
100	135	8.224	8.134	-0.090
100	138	8.330	8.193	-0.137
100	139	8.205	8.130	-0.075
100	140	8.266	8.187	-0.079
100	141	8.216	8.104	-0.112
100	142	8.281	8.197	-0.084
100	143	8.218	8.184	-0.034
100	144	8.264	8.129	-0.135
100	220	8.283	8.155	-0.128
100	221	8.204	8.099	-0.105
100	224	8.260	8.172	-0.088
100	225	8.314	8.267	-0.047
100	226	8.289	8.174	-0.115
100	228	8.266	8.238	-0.028
100	232	8.254	8.152	-0.102
100	233	8.250	8.190	-0.060
100	236	8.245	8.155	-0.090
100	238	8.243	8.142	-0.101
100	243	8.247	8.169	-0.078
105	244	8.258	8.189	-0.069
105	246	8.223	8.176	-0.047
105	248	8.338	8.190	-0.148
105	374	8.287	8.215	-0.072
105	376	8.275	8.172	-0.103
	Max	8.361	8.351	0.048
	Average	8.273	8.215	-0.058
	Min	8.166	8.099	-0.149
	Std Dev	0.047	0.051	0.048



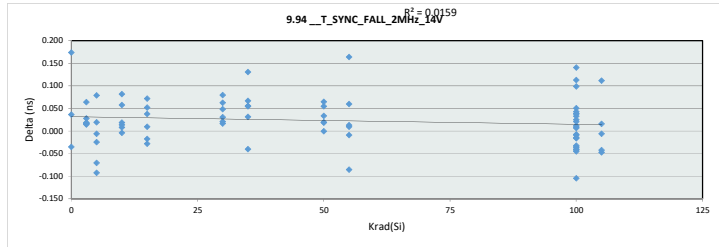
9.90_T_SYNC FA											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.244	8.160	8.196	8.172	8.200	8.214	8.202	8.145	8.099	8.099	8.172
Average	8.265	8.233	8.241	8.262	8.234	8.251	8.253	8.213	8.183	8.178	8.188
Max	8.291	8.264	8.284	8.351	8.268	8.311	8.307	8.264	8.238	8.267	8.215
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



LDR TID Report  
TPS7H5001-SP

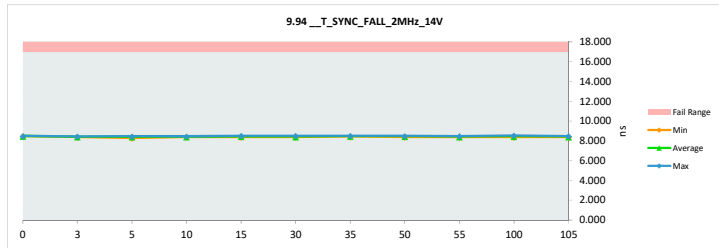
9.94_T_SYNC_FALL_2MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.330	8.505	0.175
0	118	8.535	8.501	-0.034
0	122	8.444	8.482	0.038
3	61	8.448	8.464	0.016
3	62	8.432	8.461	0.029
3	63	8.373	8.391	0.018
3	64	8.366	8.386	0.020
3	65	8.376	8.441	0.065
3	66	8.397	8.415	0.018
5	67	8.448	8.425	-0.023
5	68	8.539	8.470	-0.069
5	69	8.410	8.405	-0.005
5	70	8.357	8.437	0.080
5	71	8.395	8.304	-0.091
5	72	8.446	8.467	0.021
10	73	8.448	8.463	0.015
10	74	8.432	8.429	-0.003
10	75	8.373	8.393	0.020
10	76	8.366	8.425	0.059
10	77	8.376	8.459	0.083
10	78	8.397	8.406	0.009
15	79	8.448	8.459	0.011
15	80	8.539	8.523	-0.016
15	81	8.410	8.383	-0.027
15	82	8.357	8.430	0.073
15	83	8.395	8.434	0.039
15	84	8.446	8.499	0.053
30	86	8.448	8.529	0.081
30	89	8.432	8.450	0.018
30	91	8.373	8.437	0.064
30	92	8.366	8.416	0.050
30	94	8.376	8.398	0.022
30	95	8.397	8.429	0.032
35	100	8.448	8.505	0.057
35	101	8.539	8.500	-0.039
35	102	8.410	8.443	0.033
35	103	8.357	8.489	0.132
35	104	8.395	8.451	0.056
35	105	8.446	8.514	0.068
50	106	8.484	8.505	0.021
50	107	8.449	8.505	0.056
50	108	8.395	8.461	0.066
50	109	8.430	8.431	0.001
50	110	8.490	8.510	0.020
50	111	8.351	8.386	0.035
55	113	8.330	8.495	0.165
55	114	8.385	8.396	0.011
55	115	8.394	8.455	0.061
55	116	8.442	8.456	0.014
55	126	8.531	8.447	-0.084
55	128	8.431	8.423	-0.008
100	129	8.552	8.512	-0.040
100	131	8.414	8.448	0.034
100	132	8.429	8.543	0.114
100	133	8.535	8.548	0.013
100	134	8.470	8.492	0.022
100	135	8.375	8.401	0.026
100	138	8.488	8.528	0.040
100	139	8.491	8.451	-0.040
100	140	8.448	8.458	0.010
100	141	8.387	8.395	0.008
100	142	8.559	8.456	-0.103
100	143	8.458	8.422	-0.036
100	144	8.462	8.454	-0.008
100	220	8.483	8.449	-0.034
100	221	8.426	8.413	-0.013
100	224	8.457	8.451	-0.006
100	225	8.515	8.500	-0.015
100	226	8.478	8.522	0.044
100	228	8.510	8.549	0.039
100	232	8.537	8.506	-0.031
100	233	8.348	8.448	0.100
100	236	8.400	8.452	0.052
100	238	8.351	8.493	0.142
100	243	8.453	8.409	-0.044
105	244	8.502	8.461	-0.041
105	246	8.389	8.406	0.017
105	248	8.383	8.496	0.113
105	374	8.451	8.446	-0.005
105	376	8.432	8.386	-0.046
105	375	8.559	8.549	-0.010
	Max	8.559	8.549	0.010
	Average	8.432	8.455	0.023
	Min	8.330	8.304	-0.026
	Std Dev	0.058	0.046	0.053



9.94_T_SYNC_FALL	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

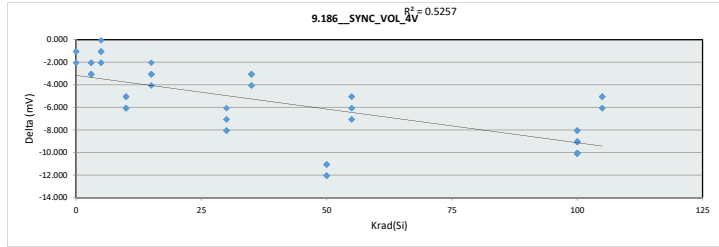
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.482	8.386	8.304	8.393	8.383	8.398	8.443	8.386	8.396	8.395	8.386
Average	8.496	8.426	8.418	8.429	8.455	8.443	8.484	8.466	8.445	8.471	8.439
Max	8.505	8.464	8.470	8.463	8.523	8.529	8.514	8.510	8.495	8.549	8.496
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



LDR TID Report  
TPS7H5001-SP

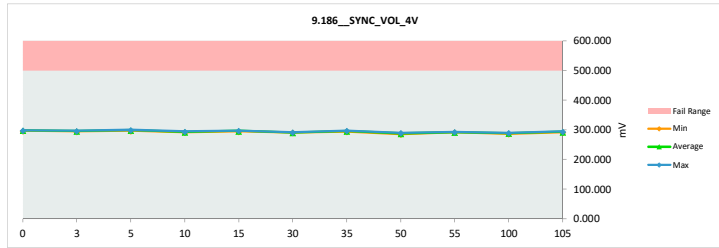
9.186_SYNC_VOL_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	117	300.000	299.000	-1.000
0	118	300.000	299.000	-1.000
0	122	299.000	297.000	-2.000
3	61	299.000	296.000	-3.000
3	62	300.000	297.000	-3.000
3	63	299.000	296.000	-3.000
3	64	299.000	296.000	-3.000
3	65	297.000	295.000	-2.000
3	66	299.000	297.000	-2.000
5	67	300.000	300.000	0.000
5	68	301.000	300.000	-1.000
5	69	299.000	298.000	-1.000
5	70	301.000	299.000	-2.000
5	71	298.000	297.000	-1.000
5	72	302.000	300.000	-2.000
10	73	299.000	293.000	-6.000
10	74	300.000	295.000	-5.000
10	75	299.000	293.000	-6.000
10	76	299.000	294.000	-5.000
10	77	297.000	292.000	-5.000
10	78	299.000	293.000	-6.000
15	79	300.000	298.000	-2.000
15	80	301.000	298.000	-3.000
15	81	299.000	296.000	-3.000
15	82	301.000	298.000	-3.000
15	83	298.000	295.000	-3.000
15	84	302.000	298.000	-4.000
30	86	299.000	291.000	-8.000
30	89	300.000	292.000	-8.000
30	91	299.000	292.000	-7.000
30	92	299.000	292.000	-7.000
30	94	297.000	291.000	-6.000
30	95	299.000	291.000	-8.000
35	100	300.000	297.000	-3.000
35	101	301.000	298.000	-3.000
35	102	299.000	296.000	-3.000
35	103	301.000	298.000	-3.000
35	104	298.000	294.000	-4.000
35	105	302.000	298.000	-4.000
50	106	298.000	286.000	-12.000
50	107	300.000	289.000	-11.000
50	108	301.000	290.000	-11.000
50	109	299.000	288.000	-11.000
50	110	301.000	289.000	-12.000
50	111	299.000	287.000	-12.000
55	113	299.000	293.000	-6.000
55	114	299.000	292.000	-7.000
55	115	298.000	292.000	-6.000
55	116	299.000	292.000	-7.000
55	126	297.000	292.000	-5.000
55	128	298.000	293.000	-5.000
100	129	296.000	287.000	-9.000
100	131	299.000	289.000	-10.000
100	132	299.000	290.000	-9.000
100	133	298.000	288.000	-10.000
100	134	299.000	290.000	-9.000
100	135	297.000	287.000	-10.000
100	138	297.000	288.000	-9.000
100	139	298.000	289.000	-9.000
100	140	299.000	289.000	-10.000
100	141	298.000	288.000	-10.000
100	142	298.000	288.000	-10.000
100	143	297.000	289.000	-8.000
100	144	297.000	289.000	-8.000
100	220	300.000	290.000	-10.000
100	221	298.000	288.000	-10.000
100	224	298.000	290.000	-8.000
100	225	299.000	290.000	-9.000
100	226	298.000	288.000	-10.000
100	228	298.000	288.000	-10.000
100	232	299.000	290.000	-9.000
100	233	300.000	290.000	-10.000
100	236	297.000	287.000	-10.000
100	238	298.000	289.000	-9.000
100	243	298.000	288.000	-10.000
105	244	299.000	293.000	-6.000
105	246	300.000	295.000	-5.000
105	248	299.000	294.000	-5.000
105	374	297.000	292.000	-5.000
105	376	301.000	295.000	-6.000
105	Max	302.000	300.000	0.000
105	Average	299.000	292.750	6.250
105	Min	296.000	286.000	-12.000
105	Std Dev	1.331	3.957	3.320



9.186_SYNC_VOL	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

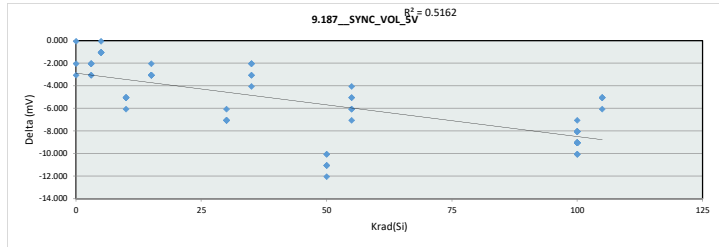
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	297.000	295.000	297.000	292.000	295.000	291.000	294.000	286.000	292.000	287.000	292.000
Min	298.333	296.167	299.000	293.333	297.167	291.500	296.833	288.167	292.333	288.708	293.800
Average	299.000	297.000	300.000	295.000	298.000	292.000	298.000	290.000	293.000	290.000	295.000
Max	299.000	297.000	300.000	295.000	298.000	292.000	298.000	290.000	293.000	290.000	295.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



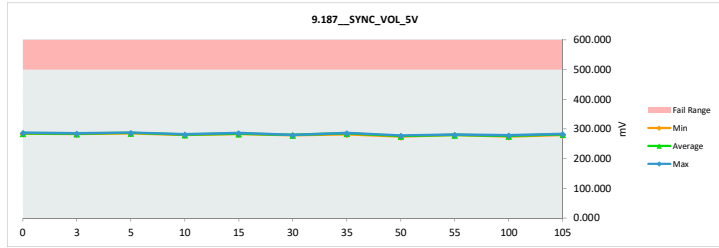
LDR TID Report  
TPS7H5001-SP

9.187_SYNC_VOL_5V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	117	288.000	286.000	-2.000
0	118	287.000	287.000	0.000
0	122	287.000	284.000	-3.000
3	61	286.000	284.000	-2.000
3	62	287.000	285.000	-2.000
3	63	287.000	284.000	-3.000
3	64	287.000	284.000	-3.000
3	65	285.000	283.000	-2.000
3	66	286.000	284.000	-2.000
5	67	288.000	287.000	-1.000
5	68	288.000	287.000	-1.000
5	69	286.000	286.000	0.000
5	70	288.000	287.000	-1.000
5	71	286.000	285.000	-1.000
5	72	289.000	288.000	-1.000
10	73	286.000	281.000	-5.000
10	74	287.000	282.000	-5.000
10	75	287.000	281.000	-6.000
10	76	287.000	282.000	-5.000
10	77	285.000	280.000	-5.000
10	78	286.000	281.000	-5.000
15	79	288.000	285.000	-3.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	286.000	283.000	-3.000
15	84	289.000	286.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	287.000	280.000	-7.000
30	92	287.000	280.000	-7.000
30	94	285.000	279.000	-6.000
30	95	286.000	279.000	-7.000
35	100	288.000	286.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	284.000	-2.000
35	103	288.000	286.000	-2.000
35	104	286.000	282.000	-4.000
35	105	289.000	286.000	-3.000
50	106	286.000	274.000	-12.000
50	107	288.000	278.000	-10.000
50	108	289.000	278.000	-11.000
50	109	287.000	276.000	-11.000
50	110	288.000	277.000	-11.000
50	111	286.000	276.000	-10.000
55	113	286.000	281.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	287.000	280.000	-7.000
55	126	285.000	280.000	-5.000
55	128	285.000	281.000	-4.000
100	129	284.000	276.000	-8.000
100	131	286.000	278.000	-8.000
100	132	286.000	278.000	-8.000
100	133	285.000	276.000	-9.000
100	134	287.000	278.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	278.000	-8.000
100	140	286.000	277.000	-9.000
100	141	286.000	277.000	-9.000
100	142	286.000	276.000	-10.000
100	143	285.000	277.000	-8.000
100	144	285.000	277.000	-8.000
100	220	288.000	279.000	-9.000
100	221	286.000	276.000	-10.000
100	224	286.000	279.000	-7.000
100	225	287.000	278.000	-9.000
100	226	285.000	276.000	-9.000
100	228	286.000	277.000	-9.000
100	232	287.000	278.000	-9.000
100	233	288.000	279.000	-9.000
100	236	285.000	275.000	-10.000
100	238	286.000	277.000	-9.000
100	243	286.000	277.000	-9.000
105	244	287.000	281.000	-6.000
105	246	288.000	283.000	-5.000
105	248	287.000	282.000	-5.000
105	374	285.000	280.000	-5.000
105	375	288.000	283.000	-5.000
105	Max	289.000	288.000	0.000
105	Average	286.563	280.775	-5.788
105	Min	284.000	274.000	-12.000
105	Std Dev	1.210	3.684	3.161



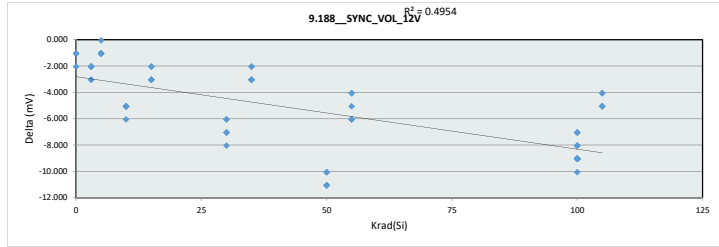
9.187_SYNC_VOL											
Test Site											
Tester											
Test Number											
Max Limit	500										
Min Limit	mV										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	284.000	283.000	285.000	280.000	283.000	279.000	282.000	274.000	280.000	275.000	280.000
Average	285.667	284.000	286.667	281.167	284.667	279.500	284.833	276.500	280.333	277.083	281.800
Max	287.000	285.000	288.000	282.000	286.000	280.000	286.000	278.000	281.000	279.000	283.000
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000



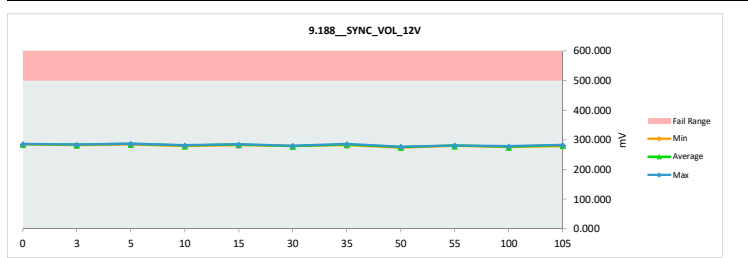
LDR TID Report  
TPS7H5001-SP

9.188_SYNC_VOL_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500

Krad(Si)	Serial #	PRE	POST	Delta
0	117	287.000	286.000	-1.000
0	118	287.000	286.000	-1.000
0	122	286.000	284.000	-2.000
3	61	286.000	284.000	-2.000
3	62	287.000	284.000	-3.000
3	63	286.000	283.000	-3.000
3	64	286.000	284.000	-2.000
3	65	284.000	282.000	-2.000
3	66	286.000	284.000	-2.000
5	67	287.000	287.000	0.000
5	68	288.000	287.000	-1.000
5	69	286.000	285.000	-1.000
5	70	288.000	287.000	-1.000
5	71	285.000	284.000	-1.000
5	72	288.000	287.000	-1.000
10	73	286.000	280.000	-6.000
10	74	287.000	282.000	-5.000
10	75	286.000	281.000	-5.000
10	76	286.000	281.000	-5.000
10	77	284.000	279.000	-5.000
10	78	286.000	281.000	-5.000
15	79	287.000	285.000	-2.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	285.000	282.000	-3.000
15	84	288.000	285.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	286.000	280.000	-6.000
30	92	286.000	279.000	-7.000
30	94	284.000	278.000	-6.000
30	95	286.000	278.000	-8.000
35	100	287.000	285.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	283.000	-3.000
35	103	288.000	286.000	-2.000
35	104	285.000	282.000	-3.000
35	105	288.000	285.000	-3.000
50	106	285.000	274.000	-11.000
50	107	287.000	277.000	-10.000
50	108	288.000	277.000	-11.000
50	109	286.000	276.000	-10.000
50	110	288.000	277.000	-11.000
50	111	286.000	275.000	-11.000
55	113	285.000	280.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	286.000	280.000	-6.000
55	126	284.000	280.000	-4.000
55	128	285.000	281.000	-4.000
100	129	283.000	275.000	-8.000
100	131	286.000	277.000	-9.000
100	132	286.000	277.000	-9.000
100	133	285.000	276.000	-9.000
100	134	286.000	277.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	277.000	-9.000
100	140	286.000	277.000	-9.000
100	141	285.000	276.000	-9.000
100	142	286.000	276.000	-10.000
100	143	284.000	277.000	-7.000
100	144	284.000	277.000	-7.000
100	220	287.000	278.000	-9.000
100	221	285.000	276.000	-9.000
100	224	285.000	278.000	-7.000
100	225	286.000	277.000	-9.000
100	226	285.000	276.000	-9.000
100	228	285.000	276.000	-9.000
100	232	286.000	278.000	-8.000
100	233	287.000	278.000	-9.000
100	236	284.000	275.000	-9.000
100	238	286.000	277.000	-9.000
100	243	285.000	276.000	-9.000
105	244	286.000	281.000	-5.000
105	246	287.000	283.000	-4.000
105	248	286.000	282.000	-4.000
105	374	284.000	279.000	-5.000
105	375	287.000	282.000	-5.000
105	Max	288.000	287.000	0.000
105	Average	285.988	280.325	-5.663
105	Min	283.000	274.000	-11.000
105	Std Dev	1.227	3.697	3.150



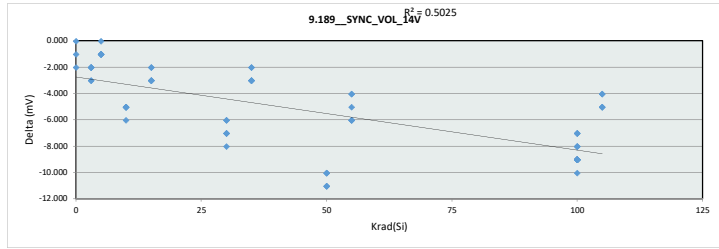
9.188_SYNC_VOL	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	500
Min Limit	500



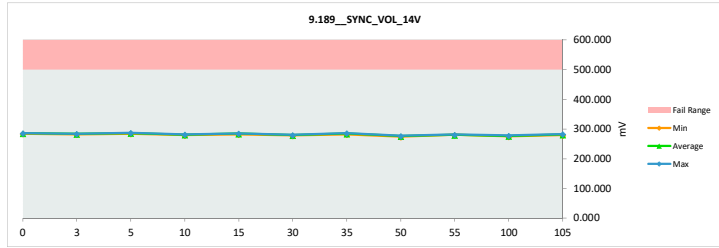
LDR TID Report  
TPS7H5001-SP

9.189_SYNC_VOL_14V		
Test Site		
Tester		
Test Number		
Unit	mV	mV
Max Limit	500	500
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	287.000	286.000	-1.000
0	118	286.000	286.000	0.000
0	122	286.000	284.000	-2.000
3	61	286.000	284.000	-2.000
3	62	287.000	284.000	-3.000
3	63	286.000	283.000	-3.000
3	64	286.000	284.000	-2.000
3	65	284.000	282.000	-2.000
3	66	286.000	284.000	-2.000
5	67	287.000	287.000	0.000
5	68	288.000	287.000	-1.000
5	69	286.000	285.000	-1.000
5	70	288.000	287.000	-1.000
5	71	285.000	284.000	-1.000
5	72	288.000	287.000	-1.000
10	73	286.000	280.000	-6.000
10	74	287.000	282.000	-5.000
10	75	286.000	281.000	-5.000
10	76	286.000	281.000	-5.000
10	77	284.000	279.000	-5.000
10	78	286.000	281.000	-5.000
15	79	287.000	285.000	-2.000
15	80	288.000	285.000	-3.000
15	81	286.000	284.000	-2.000
15	82	288.000	285.000	-3.000
15	83	285.000	282.000	-3.000
15	84	288.000	285.000	-3.000
30	86	286.000	279.000	-7.000
30	89	287.000	280.000	-7.000
30	91	286.000	280.000	-6.000
30	92	286.000	279.000	-7.000
30	94	284.000	278.000	-6.000
30	95	286.000	278.000	-8.000
35	100	287.000	285.000	-2.000
35	101	288.000	285.000	-3.000
35	102	286.000	283.000	-3.000
35	103	288.000	286.000	-2.000
35	104	285.000	282.000	-3.000
35	105	288.000	285.000	-3.000
50	106	285.000	274.000	-11.000
50	107	287.000	277.000	-10.000
50	108	288.000	277.000	-11.000
50	109	286.000	276.000	-10.000
50	110	287.000	277.000	-10.000
50	111	286.000	275.000	-11.000
55	113	285.000	280.000	-5.000
55	114	286.000	280.000	-6.000
55	115	286.000	280.000	-6.000
55	116	286.000	280.000	-6.000
55	126	284.000	280.000	-4.000
55	128	285.000	281.000	-4.000
100	129	283.000	275.000	-8.000
100	131	286.000	277.000	-9.000
100	132	286.000	277.000	-9.000
100	133	285.000	276.000	-9.000
100	134	286.000	277.000	-9.000
100	135	284.000	275.000	-9.000
100	138	284.000	276.000	-8.000
100	139	286.000	277.000	-9.000
100	140	286.000	277.000	-9.000
100	141	285.000	276.000	-9.000
100	142	286.000	276.000	-10.000
100	143	284.000	277.000	-7.000
100	144	284.000	277.000	-7.000
100	220	287.000	278.000	-9.000
100	221	285.000	276.000	-9.000
100	224	285.000	278.000	-7.000
100	225	286.000	277.000	-9.000
100	226	285.000	276.000	-9.000
100	228	285.000	276.000	-9.000
100	232	286.000	278.000	-8.000
100	233	287.000	278.000	-9.000
100	236	284.000	275.000	-9.000
100	238	286.000	277.000	-9.000
100	243	285.000	276.000	-9.000
105	244	286.000	281.000	-5.000
105	246	287.000	283.000	-4.000
105	248	286.000	282.000	-4.000
105	374	284.000	279.000	-5.000
105	375	287.000	282.000	-5.000
105	Max	288.000	287.000	0.000
105	Average	285.963	280.325	-5.638
105	Min	283.000	274.000	-11.000
105	Std Dev	1.206	3.697	3.151



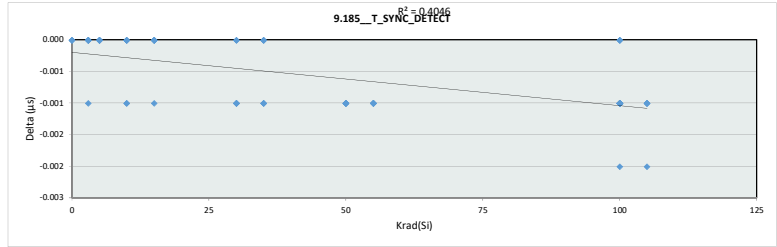
9.189_SYNC_VOL												
Test Site												
Tester												
Test Number												
Max Limit	500											mV
Min Limit												mV
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL												
Min	284.000	282.000	284.000	279.000	282.000	278.000	282.000	274.000	280.000	275.000	279.000	
Average	285.333	283.500	286.167	280.667	284.333	279.000	284.333	276.000	280.167	276.583	281.400	
Max	286.000	284.000	287.000	282.000	285.000	280.000	286.000	277.000	281.000	278.000	283.000	
UL	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	500.000	



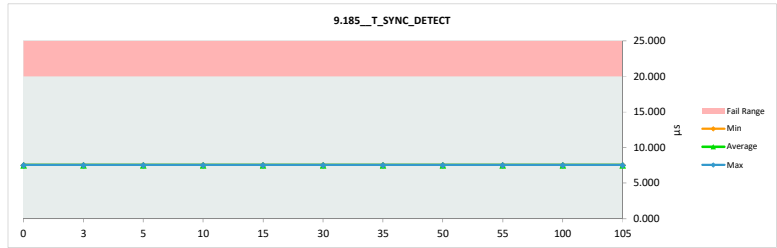
# LDR TID Report TPS7H5001-SP

9.185_T_SYNC_DETECT	
Test Site	
Tester	
Test Number	
Unit	µs
Max Limit	20
Min Limit	20

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.587	7.587	0.000
0	118	7.586	7.586	0.000
0	122	7.587	7.587	0.000
3	61	7.588	7.588	0.000
3	62	7.587	7.587	0.000
3	63	7.587	7.587	0.000
3	64	7.588	7.587	-0.001
3	65	7.587	7.587	0.000
3	66	7.588	7.588	0.000
5	67	7.587	7.587	0.000
5	68	7.586	7.586	0.000
5	69	7.587	7.587	0.000
5	70	7.587	7.587	0.000
5	71	7.588	7.588	0.000
5	72	7.588	7.588	0.000
10	73	7.588	7.588	0.000
10	74	7.587	7.587	0.000
10	75	7.587	7.587	0.000
10	76	7.588	7.587	-0.001
10	77	7.587	7.587	0.000
10	78	7.588	7.587	-0.001
15	79	7.587	7.587	0.000
15	80	7.586	7.586	0.000
15	81	7.587	7.587	0.000
15	82	7.587	7.587	0.000
15	83	7.588	7.588	0.000
15	84	7.588	7.587	-0.001
30	86	7.588	7.587	-0.001
30	89	7.587	7.587	0.000
30	91	7.587	7.586	-0.001
30	92	7.588	7.587	-0.001
30	94	7.587	7.586	-0.001
30	95	7.588	7.588	0.000
35	100	7.587	7.587	0.000
35	101	7.586	7.586	0.000
35	102	7.587	7.586	-0.001
35	103	7.587	7.587	0.000
35	104	7.588	7.587	-0.001
35	105	7.588	7.587	-0.001
50	106	7.588	7.587	-0.001
50	107	7.588	7.587	-0.001
50	108	7.586	7.585	-0.001
50	109	7.587	7.586	-0.001
50	110	7.587	7.586	-0.001
50	111	7.588	7.587	-0.001
55	113	7.588	7.587	-0.001
55	114	7.587	7.586	-0.001
55	115	7.588	7.587	-0.001
55	116	7.588	7.587	-0.001
55	126	7.586	7.585	-0.001
55	128	7.588	7.587	-0.001
100	129	7.587	7.587	0.000
100	131	7.586	7.585	-0.001
100	132	7.587	7.586	-0.001
100	133	7.586	7.585	-0.001
100	134	7.588	7.587	-0.001
100	135	7.587	7.586	-0.001
100	138	7.588	7.587	-0.001
100	139	7.585	7.584	-0.001
100	140	7.586	7.585	-0.001
100	141	7.588	7.587	-0.001
100	142	7.586	7.585	-0.001
100	143	7.587	7.585	-0.002
100	144	7.587	7.586	-0.001
100	220	7.587	7.586	-0.001
100	221	7.587	7.586	-0.001
100	224	7.586	7.585	-0.001
100	225	7.587	7.586	-0.001
100	226	7.588	7.587	-0.001
100	228	7.588	7.587	-0.001
100	232	7.586	7.586	0.000
100	233	7.587	7.586	-0.001
100	236	7.586	7.585	-0.001
100	238	7.588	7.588	0.000
100	243	7.587	7.586	-0.001
105	244	7.587	7.585	-0.002
105	246	7.586	7.585	-0.001
105	248	7.587	7.586	-0.001
105	374	7.586	7.585	-0.001
105	376	7.588	7.587	-0.001
105	Max	7.588	7.588	0.000
105	Average	7.587	7.587	-0.001
105	Min	7.585	7.584	-0.002
105	Std Dev	0.001	0.001	0.001



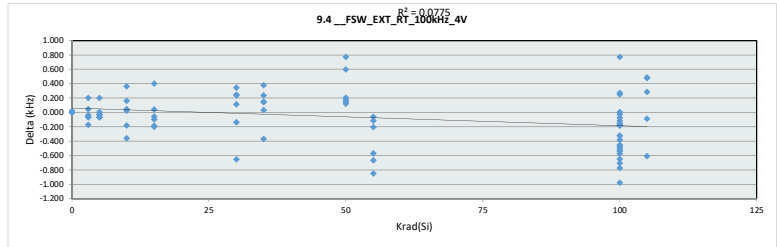
9.185_T_SYNC_D		0	3	5	10	15	30	35	50	55	100	105
LL												
Min		7.586	7.587	7.586	7.587	7.586	7.586	7.586	7.585	7.585	7.584	7.585
Average		7.587	7.587	7.587	7.587	7.587	7.587	7.587	7.586	7.587	7.586	7.586
Max		7.587	7.588	7.588	7.588	7.588	7.588	7.587	7.587	7.587	7.588	7.587
UL		20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000	20.000



LDR TID Report  
TPS7H5001-SP

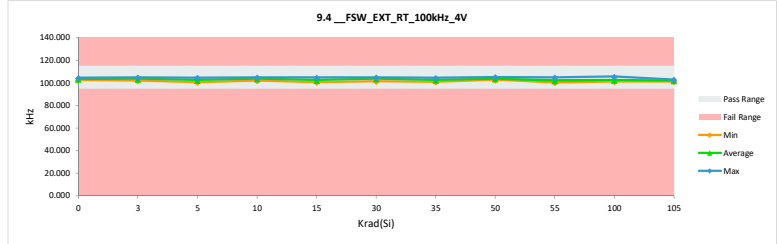
9.4_FSW_EXT_RT_100kHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	117	104.640	104.650	0.010
0	118	103.382	103.390	0.008
0	122	102.764	102.792	0.028
3	61	104.230	104.175	-0.055
3	62	104.811	105.021	0.210
3	63	102.449	102.285	-0.164
3	64	103.076	103.047	-0.029
3	65	104.387	104.331	-0.056
3	66	103.906	103.960	0.054
5	67	102.778	102.792	0.014
5	68	102.320	102.304	-0.016
5	69	102.636	102.574	-0.062
5	70	104.512	104.725	0.213
5	71	102.929	102.904	-0.025
5	72	100.854	100.796	-0.058
10	73	104.230	104.287	0.057
10	74	104.811	105.182	0.371
10	75	102.449	102.277	-0.172
10	76	103.076	102.728	-0.348
10	77	104.387	104.556	0.169
10	78	103.906	103.940	0.034
15	79	102.778	102.831	0.053
15	80	102.320	102.132	-0.188
15	81	102.636	102.550	-0.086
15	82	104.512	104.925	0.413
15	83	102.929	102.752	-0.177
15	84	100.854	100.805	-0.049
30	86	104.230	104.475	0.245
30	89	104.811	105.066	0.255
30	91	102.449	101.804	-0.645
30	92	103.076	102.948	-0.128
30	94	104.387	104.740	0.353
30	95	103.906	104.030	0.124
35	100	102.778	102.931	0.153
35	101	102.320	101.963	-0.357
35	102	102.636	103.027	0.391
35	103	104.512	104.668	0.156
35	104	102.929	102.970	0.041
35	105	100.854	101.101	0.247
50	106	103.512	103.728	0.216
50	107	103.243	103.399	0.156
50	108	105.201	105.387	0.186
50	109	103.760	104.367	0.607
50	110	102.999	103.132	0.133
50	111	102.147	102.930	0.783
55	113	101.779	100.939	-0.840
55	114	102.137	101.576	-0.561
55	115	102.736	102.079	-0.657
55	116	100.590	100.483	-0.107
55	126	105.037	104.985	-0.052
55	128	103.920	103.727	-0.193
100	129	102.274	102.533	0.259
100	131	104.163	103.706	-0.457
100	132	104.663	104.022	-0.641
100	133	102.170	102.154	-0.016
100	134	102.823	102.713	-0.110
100	135	102.850	102.476	-0.374
100	138	101.988	102.768	0.780
100	139	102.710	102.012	-0.698
100	140	102.917	102.743	-0.174
100	141	104.554	103.792	-0.762
100	142	102.335	102.619	0.284
100	143	102.129	101.645	-0.484
100	144	101.800	101.487	-0.313
100	220	103.914	102.945	-0.969
100	221	103.711	103.644	-0.067
100	224	102.665	102.102	-0.563
100	225	104.684	104.170	-0.514
100	226	105.891	105.746	-0.145
100	228	102.169	102.182	0.013
100	232	102.740	102.570	-0.170
100	233	101.931	101.782	-0.149
100	236	102.864	102.551	-0.313
100	238	104.019	103.493	-0.526
100	243	103.319	102.874	-0.445
105	244	102.036	101.436	-0.600
105	246	102.537	102.458	-0.079
105	248	102.469	102.957	0.488
105	374	101.129	101.623	0.494
105	376	102.782	103.077	0.295
105	Max	105.891	105.746	0.783
105	Average	103.134	103.068	-0.066
105	Min	100.590	100.483	-0.969
105	Std Dev	1.122	1.189	0.358



9.4_FSW_EXT R	
Test Site	
Tester	
Test Number	
Max Limit	115 kHz
Min Limit	95 kHz

Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.792	102.285	100.796	102.277	100.805	101.804	101.101	102.930	100.483	101.487
Average	103.611	103.803	102.683	103.828	102.666	103.844	102.777	103.824	102.298	102.864
Max	104.650	105.021	104.725	105.182	104.925	105.066	104.668	105.387	104.985	105.746
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000

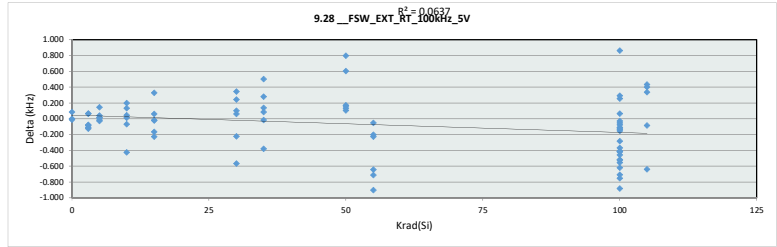




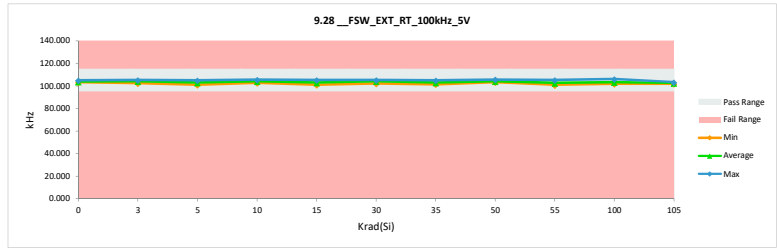
LDR TID Report  
TPS7H5001-SP

9.28_FSW_EXT_RT_100kHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	117	105.035	105.031	-0.004
0	118	103.714	103.720	0.006
0	122	103.134	103.226	0.092
3	61	104.589	104.520	-0.069
3	62	105.327	105.400	0.073
3	63	102.728	102.610	-0.118
3	64	103.503	103.403	-0.100
3	65	104.779	104.702	-0.077
3	66	104.331	104.404	0.073
5	67	103.126	103.145	0.019
5	68	102.663	102.683	0.020
5	69	102.892	102.941	0.049
5	70	104.968	105.120	0.152
5	71	103.389	103.380	-0.009
5	72	101.198	101.178	-0.020
10	73	104.589	104.640	0.051
10	74	105.327	105.534	0.207
10	75	102.728	102.668	-0.060
10	76	103.503	103.086	-0.417
10	77	104.779	104.919	0.140
10	78	104.331	104.349	0.018
15	79	103.126	103.193	0.067
15	80	102.663	102.503	-0.160
15	81	102.892	102.880	-0.012
15	82	104.968	105.303	0.335
15	83	103.389	103.169	-0.220
15	84	101.198	101.184	-0.014
30	86	104.589	104.839	0.250
30	89	105.327	105.437	0.110
30	91	102.728	102.168	-0.560
30	92	103.503	103.289	-0.214
30	94	104.779	105.130	0.351
30	95	104.331	104.399	0.068
35	100	103.126	103.273	0.147
35	101	102.663	102.292	-0.371
35	102	102.892	103.403	0.511
35	103	104.968	105.060	0.092
35	104	103.389	103.380	-0.009
35	105	101.198	101.483	0.285
50	106	104.031	104.143	0.112
50	107	103.647	103.787	0.140
50	108	105.593	105.761	0.168
50	109	104.066	104.675	0.609
50	110	103.360	103.538	0.178
50	111	102.476	103.281	0.805
55	113	102.250	101.353	-0.897
55	114	102.543	101.906	-0.637
55	115	103.130	102.427	-0.703
55	116	101.027	100.834	-0.193
55	126	105.394	105.349	-0.045
55	128	104.320	104.100	-0.220
100	129	102.686	102.949	0.263
100	131	104.487	104.077	-0.410
100	132	104.993	104.383	-0.610
100	133	102.535	102.515	-0.020
100	134	103.186	103.117	-0.069
100	135	103.216	102.815	-0.401
100	138	102.297	103.165	0.868
100	139	103.081	102.382	-0.699
100	140	103.240	103.115	-0.125
100	141	104.932	104.188	-0.744
100	142	102.746	103.043	0.297
100	143	102.472	102.024	-0.448
100	144	102.165	101.890	-0.275
100	220	104.235	103.361	-0.874
100	221	104.091	104.046	-0.045
100	224	102.983	102.468	-0.515
100	225	105.071	104.523	-0.548
100	226	106.325	106.177	-0.148
100	228	102.540	102.612	0.072
100	232	103.117	103.009	-0.108
100	233	102.255	102.152	-0.103
100	236	103.291	102.931	-0.360
100	238	104.399	103.887	-0.512
100	243	103.672	103.264	-0.408
105	244	102.495	101.863	-0.632
105	246	102.852	102.775	-0.077
105	248	102.837	103.244	0.407
105	374	101.500	101.942	0.442
105	376	103.099	103.441	0.342
Max		106.325	106.177	0.868
Average		103.512	103.444	-0.068
Min		101.027	100.834	-0.897
Std Dev		1.143	1.191	0.354



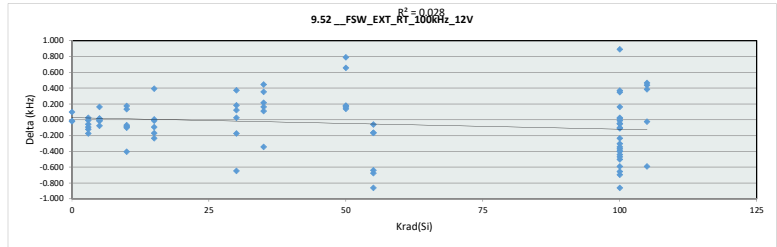
9.28_FSW_EXT												
Test Site												
Tester												
Test Number												
Max Limit	115											
Min Limit	95											
Krad(Si)	0	5	10	15	30	35	50	55	100	105		
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	103.226	102.610	101.178	102.668	101.184	102.168	101.483	103.281	100.834	101.890	101.863	102.653
Average	103.992	104.173	103.075	104.199	103.039	104.210	103.149	104.198	102.662	103.254	102.653	102.653
Max	105.031	105.400	105.120	105.534	105.303	105.437	105.060	105.761	105.349	106.177	103.441	103.441
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000



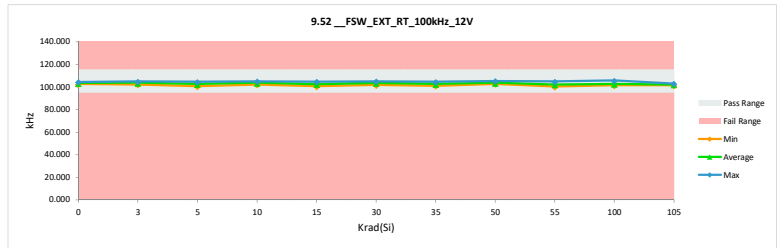
LDR TID Report  
TPS7H5001-SP

		9.52_FSW_EXT_RT_100kHz_12V	
Test Site			
Tester			
Test Number			
Unit	kHz	kHz	
Max Limit	115	115	
Min Limit	95	95	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	104.472	104.461	-0.011
0	118	103.196	103.188	-0.008
0	122	102.578	102.681	0.103
3	61	104.152	104.038	-0.114
3	62	104.842	104.873	0.031
3	63	102.303	102.136	-0.167
3	64	103.005	102.919	-0.086
3	65	104.219	104.169	-0.050
3	66	103.858	103.856	-0.002
5	67	102.620	102.637	0.017
5	68	102.169	102.160	-0.009
5	69	102.449	102.381	-0.068
5	70	104.327	104.495	0.168
5	71	102.790	102.790	0.000
5	72	100.669	100.694	0.025
10	73	104.152	104.089	-0.063
10	74	104.842	105.024	0.182
10	75	102.303	102.211	-0.092
10	76	103.005	102.606	-0.399
10	77	104.219	104.361	0.142
10	78	103.858	103.781	-0.077
15	79	102.620	102.632	0.012
15	80	102.169	101.940	-0.229
15	81	102.449	102.365	-0.084
15	82	104.327	104.726	0.399
15	83	102.790	102.628	-0.162
15	84	100.669	100.663	-0.006
30	86	104.152	104.340	0.188
30	89	104.842	104.969	0.127
30	91	102.303	101.663	-0.640
30	92	103.005	102.840	-0.165
30	94	104.219	104.597	0.378
30	95	103.858	103.889	0.031
35	100	102.620	102.789	0.169
35	101	102.169	101.832	-0.337
35	102	102.449	102.903	0.454
35	103	104.327	104.548	0.221
35	104	102.790	102.906	0.116
35	105	100.669	101.030	0.361
50	106	103.432	103.603	0.171
50	107	103.111	103.298	0.187
50	108	105.073	105.254	0.181
50	109	103.530	104.193	0.663
50	110	102.882	103.029	0.147
50	111	101.992	102.788	0.796
55	113	101.731	100.875	-0.856
55	114	102.079	101.448	-0.631
55	115	102.578	101.908	-0.670
55	116	100.514	100.356	-0.158
55	126	104.874	104.819	-0.055
55	128	103.756	103.598	-0.158
100	129	102.130	102.487	0.357
100	131	103.957	103.595	-0.362
100	132	104.489	103.906	-0.583
100	133	102.005	102.038	0.033
100	134	102.627	102.621	-0.006
100	135	102.717	102.380	-0.337
100	138	101.808	102.708	0.900
100	139	102.561	101.911	-0.650
100	140	102.740	102.698	-0.042
100	141	104.401	103.711	-0.690
100	142	102.166	102.543	0.377
100	143	102.004	101.544	-0.460
100	144	101.686	101.459	-0.227
100	220	103.742	102.886	-0.856
100	221	103.558	103.579	0.021
100	224	102.436	102.002	-0.434
100	225	104.508	104.015	-0.493
100	226	105.824	105.729	-0.095
100	228	101.962	102.131	0.169
100	232	102.611	102.509	-0.102
100	233	101.724	101.679	-0.045
100	236	102.749	102.452	-0.297
100	238	103.803	103.411	-0.392
100	243	103.169	102.779	-0.390
105	244	101.976	101.394	-0.582
105	246	102.336	102.321	-0.015
105	248	102.354	102.826	0.472
105	374	101.013	101.457	0.444
105	376	102.592	102.985	0.393
105	375	105.824	105.729	-0.095
105	377	102.996	102.947	-0.049
105	378	100.514	100.356	-0.158
105	379	1.135	1.177	0.351



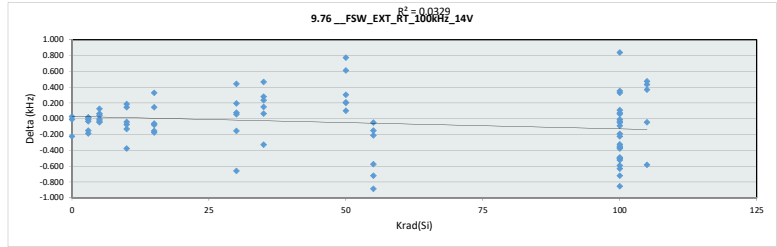
		9.52_FSW_EXT								
Test Site										
Tester										
Test Number										
Max Limit	115	kHz								
Min Limit	95	kHz								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.681	102.136	100.694	102.211	100.663	101.663	101.030	102.788	100.356	101.459
Average	103.443	103.665	102.526	103.679	102.492	103.716	102.668	103.694	102.167	102.782
Max	104.461	104.873	104.495	105.024	104.726	104.969	104.548	105.254	104.819	105.729
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000



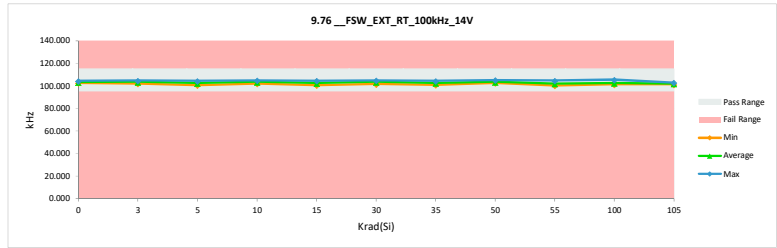
# LDR TID Report TPS7H5001-SP

9.76_FSW_EXT_RT_100kHz_14V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	115
Min Limit	95

Krad(Si)	Serial #	PRE	POST	Delta
0	117	104.456	104.490	0.034
0	118	103.396	103.180	-0.216
0	122	102.660	102.661	0.001
3	61	104.141	103.999	-0.142
3	62	104.859	104.860	0.001
3	63	102.293	102.115	-0.178
3	64	102.961	102.938	-0.023
3	65	104.157	104.182	0.025
3	66	103.812	103.835	0.023
5	67	102.543	102.617	0.074
5	68	102.110	102.112	0.002
5	69	102.412	102.390	-0.022
5	70	104.391	104.526	0.135
5	71	102.808	102.860	0.052
5	72	100.731	100.693	-0.038
10	73	104.141	104.077	-0.064
10	74	104.859	105.014	0.155
10	75	102.293	102.173	-0.120
10	76	102.961	102.594	-0.367
10	77	104.157	104.350	0.193
10	78	103.812	103.779	-0.033
15	79	102.543	102.696	0.153
15	80	102.110	101.965	-0.145
15	81	102.412	102.358	-0.054
15	82	104.391	104.727	0.336
15	83	102.808	102.643	-0.165
15	84	100.731	100.660	-0.071
30	86	104.141	104.343	0.202
30	89	104.859	104.922	0.063
30	91	102.293	101.642	-0.651
30	92	102.961	102.814	-0.147
30	94	104.157	104.605	0.448
30	95	103.812	103.895	0.083
35	100	102.543	102.786	0.243
35	101	102.110	101.788	-0.322
35	102	102.412	102.886	0.474
35	103	104.391	104.548	0.157
35	104	102.808	102.881	0.073
35	105	100.731	101.017	0.286
50	106	103.357	103.571	0.214
50	107	103.102	103.209	0.107
50	108	105.032	105.242	0.210
50	109	103.557	104.174	0.617
50	110	102.724	103.037	0.313
50	111	101.987	102.768	0.781
55	113	101.718	100.841	-0.877
55	114	102.047	101.478	-0.569
55	115	102.611	101.897	-0.714
55	116	100.554	100.351	-0.203
55	126	104.852	104.810	-0.042
55	128	103.748	103.604	-0.144
100	129	102.125	102.462	0.337
100	131	103.949	103.579	-0.370
100	132	104.460	103.878	-0.582
100	133	101.983	102.053	0.070
100	134	102.648	102.647	-0.001
100	135	102.709	102.367	-0.342
100	138	101.842	102.687	0.845
100	139	102.527	101.905	-0.622
100	140	102.708	102.683	-0.025
100	141	104.401	103.690	-0.711
100	142	102.162	102.521	0.359
100	143	102.028	101.515	-0.513
100	144	101.666	101.452	-0.214
100	220	103.735	102.887	-0.848
100	221	103.480	103.562	0.082
100	224	102.447	101.963	-0.484
100	225	104.509	104.012	-0.497
100	226	105.783	105.602	-0.181
100	228	102.016	102.131	0.115
100	232	102.589	102.508	-0.081
100	233	101.740	101.698	-0.042
100	236	102.754	102.438	-0.316
100	238	103.890	103.400	-0.490
100	243	103.141	102.789	-0.352
105	244	101.981	101.405	-0.576
105	246	102.330	102.295	-0.035
105	248	102.316	102.798	0.482
105	374	101.006	101.448	0.442
105	376	102.595	102.970	0.375
Max		105.783	105.602	0.845
Average		102.987	102.937	-0.050
Min		100.554	100.351	-0.877
Std Dev		1.129	1.173	0.352



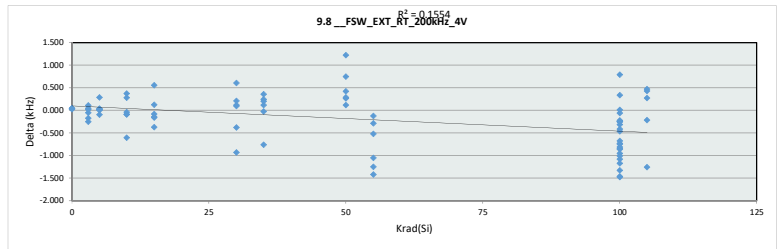
9.76_FSW_EXT										
Test Site										
Tester										
Test Number										
Max Limit	115 kHz									
Min Limit	95 kHz									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000	95.000
Min	102.661	102.115	100.693	102.173	100.660	101.642	101.017	102.768	100.351	101.452
Average	103.444	103.655	102.533	103.665	102.508	103.704	102.651	103.667	102.164	102.768
Max	104.490	104.860	104.526	105.014	104.727	104.922	104.548	105.242	104.810	105.602
UL	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000	115.000



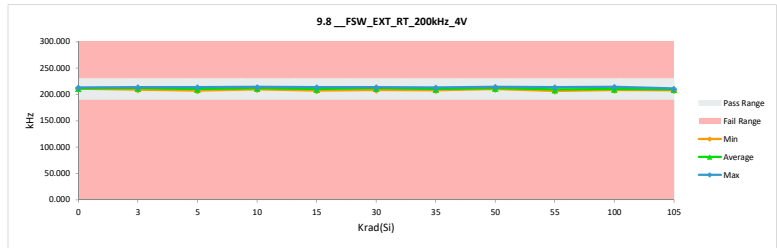
# LDR TID Report TPS7H5001-SP

9.8_FSW_EXT_RT_200kHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz kHz
Max Limit	230 230
Min Limit	190 190

Krad(Si)	Serial #	PRE	POST	Delta
0	117	213.421	213.463	0.042
0	118	211.339	211.402	0.063
0	122	210.901	210.964	0.063
3	61	212.744	212.588	-0.156
3	62	214.187	214.311	0.124
3	63	209.805	209.571	-0.234
3	64	211.436	211.395	-0.041
3	65	212.923	212.956	0.033
3	66	212.524	212.582	0.058
5	67	210.526	210.565	0.039
5	68	210.165	210.221	0.056
5	69	209.898	209.919	0.021
5	70	213.848	214.153	0.305
5	71	210.860	210.894	0.034
5	72	207.661	207.580	-0.081
10	73	212.744	212.718	-0.026
10	74	214.187	214.483	0.296
10	75	209.805	209.730	-0.075
10	76	211.436	210.842	-0.594
10	77	212.923	213.309	0.386
10	78	212.524	212.450	-0.074
15	79	210.526	210.668	0.142
15	80	210.165	209.811	-0.354
15	81	209.898	209.751	-0.147
15	82	213.848	214.416	0.568
15	83	210.860	210.730	-0.130
15	84	207.661	207.597	-0.064
30	86	212.744	212.966	0.222
30	89	214.187	214.315	0.128
30	91	209.805	208.885	-0.920
30	92	211.436	211.071	-0.365
30	94	212.923	213.542	0.619
30	95	212.524	212.631	0.107
35	100	210.526	210.744	0.218
35	101	210.165	209.415	-0.750
35	102	209.898	210.273	0.375
35	103	213.848	213.840	-0.008
35	104	210.860	210.994	0.134
35	105	207.661	207.919	0.258
50	106	211.872	212.307	0.435
50	107	211.244	211.523	0.279
50	108	214.334	214.637	0.303
50	109	212.645	213.410	0.765
50	110	211.133	211.263	0.130
50	111	209.385	210.625	1.240
55	113	209.110	207.702	-1.408
55	114	209.297	208.263	-1.034
55	115	210.432	209.198	-1.234
55	116	207.142	206.871	-0.271
55	126	214.090	213.979	-0.111
55	128	212.584	212.075	-0.509
100	129	209.878	209.901	0.023
100	131	212.831	212.033	-0.798
100	132	213.772	212.611	-1.161
100	133	209.795	209.555	-0.240
100	134	210.997	210.596	-0.401
100	135	211.036	210.320	-0.716
100	138	209.658	210.462	0.804
100	139	210.994	209.683	-1.311
100	140	211.072	210.626	-0.446
100	141	213.150	211.705	-1.445
100	142	210.387	210.738	0.351
100	143	209.516	208.694	-0.822
100	144	209.757	209.025	-0.732
100	220	212.146	210.682	-1.464
100	221	212.042	211.835	-0.207
100	224	210.437	209.497	-0.940
100	225	213.663	212.665	-0.998
100	226	214.902	214.664	-0.238
100	228	209.741	209.697	-0.044
100	232	210.375	210.076	-0.299
100	233	209.668	209.438	-0.230
100	236	210.633	209.973	-0.660
100	238	212.774	211.705	-1.069
100	243	211.106	210.262	-0.844
105	244	209.543	208.298	-1.245
105	246	210.424	210.223	-0.201
105	248	210.848	211.336	0.488
105	374	207.830	208.276	0.446
105	376	211.083	211.368	0.285
105	375	214.902	214.664	-0.238
Max				
Average		211.234	211.043	-0.191
Min		207.142	206.871	-1.464
Std Dev		1.773	1.892	0.568

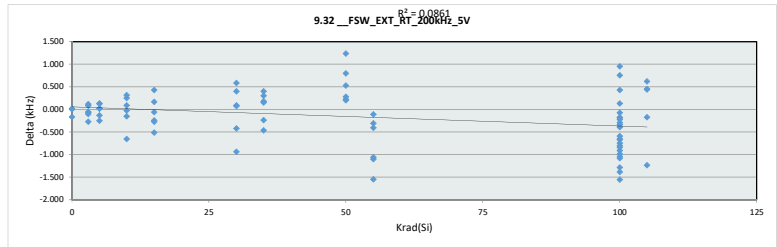


9.8_FSW_EXT_RT											
Test Site											
Tester											
Test Number											
Max Limit	230 kHz										
Min Limit	190 kHz										
Krad(Si)	0	5	10	15	30	35	50	55	100	105	
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	
Min	210.964	209.571	207.580	209.730	207.597	208.885	207.919	210.625	206.871	208.694	
Average	211.943	212.234	210.555	212.255	210.496	212.235	210.531	212.294	209.681	210.685	
Max	213.463	214.311	214.153	214.483	214.416	214.315	213.840	214.637	213.979	214.664	
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	

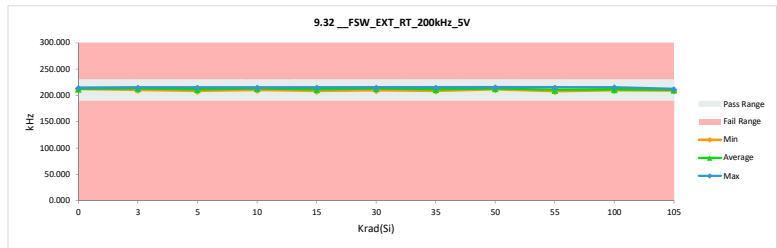


# LDR TID Report TPS7H5001-SP

9.32_FSW_EXT_RT_200kHz_5V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	214.455	214.472	0.017
0	118	212.473	212.321	-0.152
0	122	211.885	211.917	0.032
3	61	213.621	213.574	-0.047
3	62	215.194	215.322	0.128
3	63	210.776	210.516	-0.260
3	64	212.470	212.379	-0.091
3	65	214.025	213.981	-0.044
3	66	213.538	213.631	0.093
5	67	211.528	211.558	0.030
5	68	211.007	211.042	0.035
5	69	210.888	210.770	-0.118
5	70	215.030	215.178	0.148
5	71	212.178	211.944	-0.234
5	72	208.620	208.758	0.138
10	73	213.621	213.723	0.102
10	74	215.194	215.324	0.327
10	75	210.776	210.640	-0.136
10	76	212.470	211.827	-0.643
10	77	214.025	214.291	0.266
10	78	213.538	213.528	-0.010
15	79	211.528	211.707	0.179
15	80	211.007	210.752	-0.255
15	81	210.888	210.656	-0.232
15	82	215.030	215.473	0.443
15	83	212.178	211.679	-0.499
15	84	208.620	208.576	-0.044
30	86	213.621	214.035	0.414
30	89	215.194	215.299	0.105
30	91	210.776	210.854	-0.922
30	92	212.470	212.062	-0.408
30	94	214.025	214.626	0.601
30	95	213.538	213.624	0.086
35	100	211.528	211.722	0.194
35	101	211.007	210.555	-0.452
35	102	210.888	211.303	0.415
35	103	215.030	215.196	0.166
35	104	212.178	211.958	-0.220
35	105	208.620	208.939	0.319
50	106	213.012	213.556	0.544
50	107	212.328	212.546	0.218
50	108	215.417	215.714	0.297
50	109	213.535	214.350	0.815
50	110	212.063	212.304	0.241
50	111	210.406	211.658	1.252
55	113	210.256	208.722	-1.534
55	114	210.324	209.234	-1.090
55	115	211.427	210.380	-1.047
55	116	208.126	207.835	-0.291
55	126	215.130	215.032	-0.098
55	128	213.633	213.237	-0.396
100	129	210.654	211.099	0.445
100	131	213.770	213.038	-0.732
100	132	214.703	213.636	-1.067
100	133	210.810	210.602	-0.208
100	134	211.994	211.671	-0.323
100	135	212.002	211.346	-0.656
100	138	210.517	211.483	0.966
100	139	211.997	210.726	-1.271
100	140	212.060	211.681	-0.379
100	141	214.148	212.780	-1.368
100	142	211.376	212.144	0.768
100	143	210.435	209.609	-0.826
100	144	210.774	210.130	-0.644
100	220	213.166	211.628	-1.538
100	221	213.062	212.999	-0.063
100	224	211.411	210.516	-0.895
100	225	214.699	213.723	-0.976
100	226	215.937	215.770	-0.167
100	228	210.677	210.824	0.147
100	232	211.451	211.174	-0.277
100	233	210.604	210.437	-0.167
100	236	211.639	211.063	-0.576
100	238	213.791	212.764	-1.027
100	243	212.101	211.314	-0.787
105	244	210.578	209.358	-1.220
105	246	211.300	211.138	-0.162
105	248	211.711	212.342	0.631
105	374	208.731	209.198	0.467
105	376	211.964	212.420	0.456
105	Max	215.937	215.770	1.252
105	Average	212.239	212.076	-0.163
105	Min	208.126	207.835	-1.538
105	Std Dev	1.802	1.910	0.578



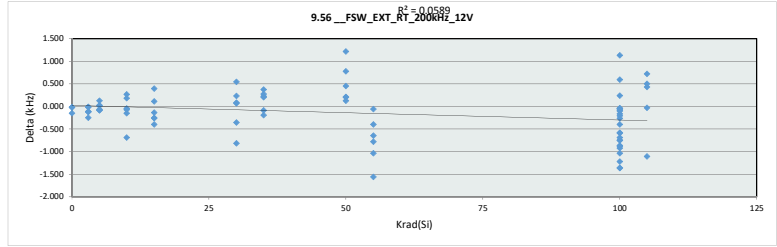
9.32_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	211.917	210.516	208.758	210.640	208.576	209.854	208.939	211.658	207.835	209.609	209.198
Average	212.903	213.234	211.542	213.255	211.474	213.250	211.612	213.355	210.740	211.757	210.891
Max	214.472	215.322	215.178	215.521	215.473	215.299	215.196	215.714	215.032	215.770	212.420
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000



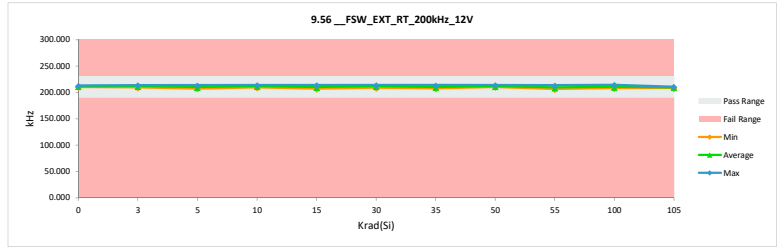
LDR TID Report  
TPS7H5001-SP

		9.56_FSW_EXT_RT_200kHz_12V	
Test Site			
Tester			
Test Number			
Unit	kHz	kHz	
Max Limit	230	230	
Min Limit	190	190	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	213.135	213.133	-0.002
0	118	211.168	211.155	-0.013
0	122	210.703	210.568	-0.135
3	61	212.508	212.400	-0.108
3	62	214.067	214.067	0.000
3	63	209.594	209.358	-0.236
3	64	211.123	211.019	-0.104
3	65	212.731	212.629	-0.102
3	66	212.295	212.300	0.005
5	67	210.260	210.301	0.041
5	68	209.634	209.776	0.142
5	69	209.701	209.634	-0.067
5	70	213.677	213.699	0.022
5	71	210.805	210.729	-0.076
5	72	207.495	207.441	-0.054
10	73	212.508	212.475	-0.033
10	74	214.067	214.263	0.196
10	75	209.594	209.458	-0.136
10	76	211.123	210.446	-0.677
10	77	212.731	213.008	0.277
10	78	212.295	212.236	-0.059
15	79	210.260	210.385	0.125
15	80	209.634	209.513	-0.121
15	81	209.701	209.459	-0.242
15	82	213.677	214.084	0.407
15	83	210.805	210.419	-0.386
15	84	207.495	207.251	-0.244
30	86	212.508	212.752	0.244
30	89	214.067	214.164	0.097
30	91	209.594	208.788	-0.806
30	92	211.123	210.781	-0.342
30	94	212.731	213.287	0.556
30	95	212.295	212.374	0.079
35	100	210.260	210.544	0.284
35	101	209.634	209.557	-0.077
35	102	209.701	210.089	0.388
35	103	213.677	213.896	0.219
35	104	210.805	210.624	-0.181
35	105	207.495	207.734	0.239
50	106	211.830	212.296	0.466
50	107	210.955	211.177	0.222
50	108	214.121	214.334	0.213
50	109	212.191	212.981	0.790
50	110	210.867	211.005	0.138
50	111	209.152	210.384	1.232
55	113	209.039	207.495	-1.544
55	114	209.122	208.102	-1.020
55	115	210.132	209.361	-0.771
55	116	206.964	206.579	-0.385
55	126	213.795	213.748	-0.047
55	128	212.721	212.086	-0.635
100	129	209.553	209.802	0.249
100	131	212.484	211.811	-0.673
100	132	213.423	212.397	-1.026
100	133	209.509	209.444	-0.065
100	134	210.695	210.545	-0.150
100	135	210.750	210.182	-0.568
100	138	209.251	210.398	1.147
100	139	210.741	209.533	-1.208
100	140	210.751	210.499	-0.252
100	141	212.897	211.551	-1.346
100	142	210.145	210.752	0.607
100	143	209.218	208.305	-0.913
100	144	209.435	209.049	-0.386
100	220	211.892	210.540	-1.352
100	221	211.778	211.751	-0.027
100	224	210.099	209.365	-0.734
100	225	213.321	212.471	-0.850
100	226	214.766	214.675	-0.091
100	228	209.464	209.426	-0.038
100	232	210.168	209.971	-0.197
100	233	209.312	209.272	-0.040
100	236	210.419	209.844	-0.575
100	238	212.556	211.691	-0.865
100	243	210.885	210.147	-0.738
105	244	209.382	208.289	-1.093
105	246	210.011	209.997	-0.014
105	248	210.485	211.222	0.737
105	374	207.704	208.216	0.512
105	376	210.740	211.183	0.443
105	375	214.766	214.675	-0.091
105	Max	210.992	210.846	-0.147
105	Average	206.964	206.579	-1.544
105	Min	206.964	206.579	-1.544
105	Std Dev	1.786	1.873	0.539

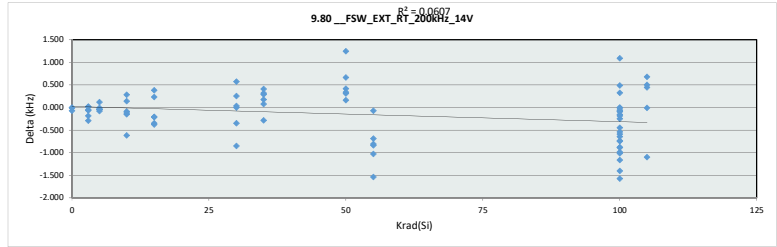


		9.56_FSW_EXT								
Test Site										
Tester										
Test Number										
Max Limit	230	kHz								
Min Limit	190	kHz								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	211.568	209.358	207.441	209.458	207.251	208.788	207.734	210.384	206.579	208.305
Average	211.619	211.962	210.263	211.981	210.185	212.024	210.407	212.030	209.562	210.559
Max	213.133	214.067	213.699	214.263	214.084	214.164	213.896	214.334	213.748	214.675
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000

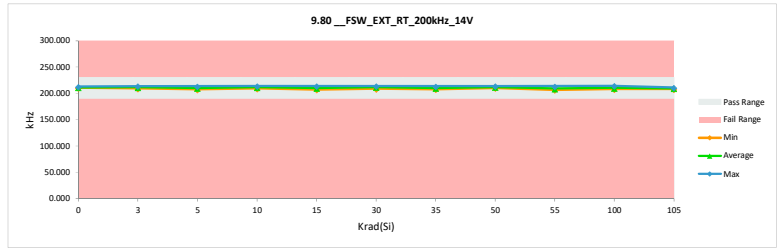


LDR TID Report  
TPS7H5001-SP

9.80_FSW_EXT_RT_200kHz_14V				
Test Site				
Tester				
Test Number				
Unit	kHz	kHz		
Max Limit	230	230		
Min Limit	190	190		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	213.132	213.134	0.002
0	118	211.047	211.059	0.012
0	122	210.668	210.607	-0.061
3	61	212.505	212.333	-0.172
3	62	214.048	214.071	0.023
3	63	209.588	209.307	-0.281
3	64	211.074	211.021	-0.053
3	65	212.690	212.649	-0.041
3	66	212.308	212.339	0.031
5	67	210.179	210.311	0.132
5	68	209.794	209.765	-0.029
5	69	209.651	209.628	-0.023
5	70	213.681	213.614	-0.067
5	71	210.755	210.721	-0.034
5	72	207.408	207.412	0.004
10	73	212.505	212.434	-0.071
10	74	214.048	214.202	0.154
10	75	209.588	209.449	-0.139
10	76	211.074	210.472	-0.602
10	77	212.690	212.986	0.296
10	78	212.308	212.196	-0.112
15	79	210.179	210.424	0.245
15	80	209.794	209.460	-0.334
15	81	209.651	209.448	-0.203
15	82	213.681	214.078	0.397
15	83	210.755	210.390	-0.365
15	84	207.408	207.217	-0.191
30	86	212.505	212.768	0.263
30	89	214.048	214.098	0.050
30	91	209.588	208.751	-0.837
30	92	211.074	210.737	-0.337
30	94	212.690	213.274	0.584
30	95	212.308	212.321	0.013
35	100	210.179	210.478	0.299
35	101	209.794	209.524	-0.270
35	102	209.651	210.072	0.421
35	103	213.681	213.770	0.089
35	104	210.755	210.945	0.190
35	105	207.408	207.735	0.327
50	106	211.825	212.252	0.427
50	107	210.826	211.175	0.349
50	108	214.049	214.371	0.322
50	109	212.311	212.987	0.676
50	110	210.836	211.008	0.172
50	111	209.136	210.396	1.260
55	113	209.001	207.477	-1.524
55	114	209.122	208.104	-1.018
55	115	210.026	209.230	-0.796
55	116	207.314	206.637	-0.677
55	126	213.766	213.706	-0.060
55	128	212.869	212.046	-0.823
100	129	209.320	209.660	0.340
100	131	212.448	211.818	-0.630
100	132	213.388	212.414	-0.974
100	133	209.508	209.425	-0.083
100	134	210.663	210.488	-0.175
100	135	210.722	210.145	-0.577
100	138	209.224	210.323	1.099
100	139	210.688	209.541	-1.147
100	140	210.696	210.456	-0.240
100	141	212.900	211.511	-1.389
100	142	210.133	210.632	0.499
100	143	209.236	208.237	-0.999
100	144	209.477	209.041	-0.436
100	220	211.880	210.318	-1.562
100	221	211.750	211.758	0.008
100	224	210.073	209.343	-0.730
100	225	213.306	212.442	-0.864
100	226	214.730	214.660	-0.070
100	228	209.436	209.437	0.001
100	232	210.120	209.978	-0.142
100	233	209.299	209.250	-0.049
100	236	210.416	209.892	-0.524
100	238	212.556	211.684	-0.872
100	243	210.897	210.174	-0.723
105	244	209.386	208.302	-1.084
105	246	209.997	210.001	0.004
105	248	210.497	211.189	0.692
105	374	207.640	208.156	0.516
105	376	210.678	211.137	0.459
105	Max	214.730	214.660	1.260
105	Average	210.975	210.825	-0.150
105	Min	207.314	206.637	-1.562
105	Std Dev	1.784	1.871	0.550



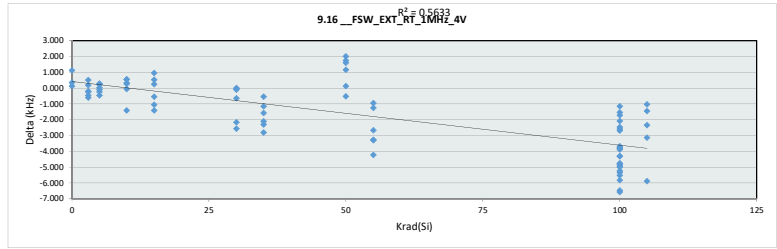
9.80_FSW_EXT											
Test Site											
Tester											
Test Number											
Max Limit	230	kHz									
Min Limit	190	kHz									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000	190.000
Min	210.607	209.307	207.412	209.449	207.217	208.751	207.735	210.396	206.637	208.237	208.156
Average	211.600	211.953	210.242	211.957	210.170	211.992	210.421	212.032	209.533	210.526	209.757
Max	213.134	214.071	213.614	214.202	214.078	214.098	213.770	214.371	213.706	214.660	211.189
UL	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000	230.000



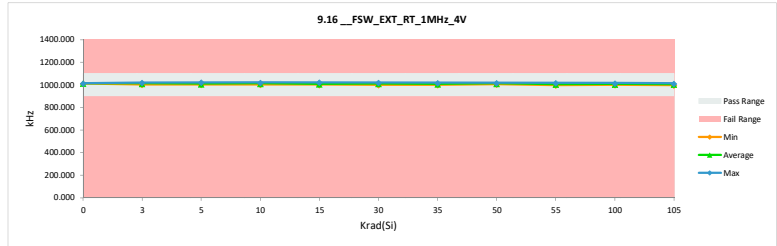
# LDR TID Report TPS7H5001-SP

9.16_FSW_EXT_RT_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz kHz
Max Limit	1100 1100
Min Limit	900 900

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1016.625	1017.001	0.376
0	118	1013.508	1013.677	0.169
0	122	1014.885	1016.047	1.162
3	61	1014.566	1014.006	-0.560
3	62	1021.207	1021.760	0.553
3	63	1005.657	1005.223	-0.434
3	64	1016.414	1016.219	-0.195
3	65	1014.372	1014.213	-0.159
3	66	1016.652	1016.883	0.231
5	67	1010.625	1010.194	-0.431
5	68	1013.291	1013.554	0.263
5	69	1005.295	1005.100	-0.195
5	70	1023.880	1024.197	0.317
5	71	1010.413	1010.344	-0.069
5	72	1007.923	1007.986	0.063
10	73	1014.566	1014.933	0.367
10	74	1021.207	1021.790	0.583
10	75	1005.657	1005.962	0.305
10	76	1016.414	1015.032	-1.382
10	77	1014.372	1014.959	0.587
10	78	1016.652	1016.634	-0.018
15	79	1010.625	1011.188	0.563
15	80	1013.291	1012.789	-0.502
15	81	1005.295	1003.927	-1.368
15	82	1023.880	1024.882	1.002
15	83	1010.413	1009.396	-1.017
15	84	1007.923	1008.198	0.275
30	86	1014.566	1014.497	-0.069
30	89	1021.207	1021.203	-0.004
30	91	1005.657	1003.123	-2.534
30	92	1016.414	1014.298	-2.116
30	94	1014.372	1014.408	0.036
30	95	1016.652	1016.052	-0.600
35	100	1010.625	1010.111	-0.514
35	101	1013.291	1011.221	-2.070
35	102	1005.295	1003.035	-2.260
35	103	1023.880	1021.109	-2.771
35	104	1010.413	1008.871	-1.542
35	105	1007.923	1006.807	-1.116
50	106	1015.360	1016.560	1.200
50	107	1013.268	1013.429	0.161
50	108	1017.042	1018.829	1.787
50	109	1018.953	1020.584	1.631
50	110	1014.793	1014.317	-0.476
50	111	1005.771	1007.808	2.037
55	113	1007.197	1002.999	-4.198
55	114	1002.511	999.264	-3.247
55	115	1009.986	1007.351	-2.635
55	116	1000.700	999.798	-0.902
55	126	1019.883	1018.665	-1.218
55	128	1017.985	1014.734	-3.251
100	129	1010.195	1006.548	-3.647
100	131	1018.490	1013.585	-4.905
100	132	1022.562	1017.759	-4.803
100	133	1012.650	1008.866	-3.784
100	134	1016.399	1012.134	-4.265
100	135	1011.502	1006.778	-4.724
100	138	1011.230	1009.551	-1.679
100	139	1015.098	1009.319	-5.779
100	140	1014.474	1010.639	-3.835
100	141	1013.734	1007.206	-6.528
100	142	1015.802	1014.301	-1.501
100	143	1007.293	1001.808	-5.485
100	144	1015.989	1010.685	-5.304
100	220	1014.570	1008.139	-6.431
100	221	1013.342	1011.303	-2.039
100	224	1011.951	1007.201	-4.750
100	225	1018.230	1013.269	-4.961
100	226	1015.057	1012.508	-2.549
100	228	1010.427	1009.322	-1.105
100	232	1011.534	1009.099	-2.435
100	233	1013.168	1010.525	-2.643
100	236	1005.189	1001.439	-3.750
100	238	1016.712	1011.511	-5.201
100	243	1007.856	1003.596	-4.260
105	244	1007.450	1001.602	-5.848
105	246	1012.746	1011.321	-1.425
105	248	1016.178	1015.196	-0.982
105	374	1002.257	999.959	-2.298
105	376	1016.223	1013.118	-3.105
	Max	1023.880	1024.882	2.037
	Average	1013.096	1011.418	-1.678
	Min	1000.700	999.264	-6.528
	Std Dev	5.180	5.796	2.167



9.16_FSW_EXT										
Test Site										
Tester										
Test Number										
Max Limit	1100 kHz									
Min Limit	900 kHz									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1013.677	1005.223	1005.100	1005.962	1003.927	1003.123	1003.035	1007.808	999.264	1001.439
Average	1015.575	1014.717	1011.896	1014.885	1011.730	1013.930	1010.192	1015.255	1007.135	1009.462
Max	1017.001	1021.760	1024.197	1021.790	1024.882	1021.203	1021.109	1020.584	1018.665	1017.759
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000

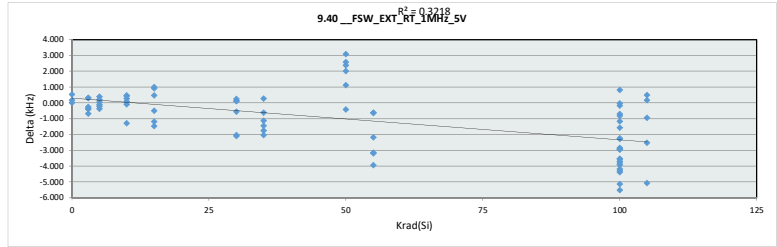




LDR TID Report  
TPS7H5001-SP

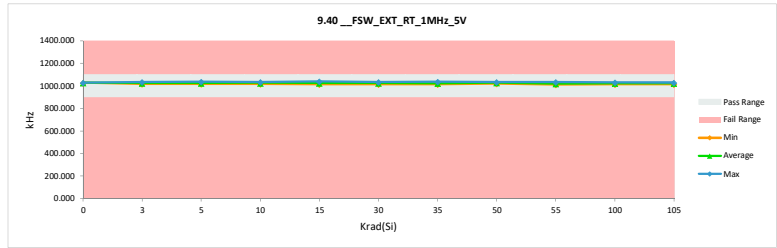
9.40_FSW_EXT_RT_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	1100
Min Limit	900

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1029.916	1030.121	0.205
0	118	1028.590	1028.630	0.040
0	122	1030.407	1030.987	0.580
3	61	1029.874	1029.227	-0.647
3	62	1036.045	1036.408	0.363
3	63	1018.789	1018.433	-0.356
3	64	1030.200	1029.993	-0.207
3	65	1028.048	1027.739	-0.309
3	66	1030.224	1030.579	0.355
5	67	1025.484	1025.145	-0.339
5	68	1027.840	1028.276	0.436
5	69	1018.230	1018.157	-0.073
5	70	1039.545	1039.790	0.245
5	71	1025.306	1025.141	-0.165
5	72	1022.378	1022.464	0.086
10	73	1029.874	1030.005	0.131
10	74	1036.045	1036.367	0.322
10	75	1018.789	1019.254	0.465
10	76	1030.200	1028.959	-1.241
10	77	1028.048	1028.557	0.509
10	78	1030.224	1030.177	-0.047
15	79	1025.484	1026.447	0.963
15	80	1027.840	1027.388	-0.452
15	81	1018.230	1016.804	-1.426
15	82	1039.545	1040.592	1.047
15	83	1025.306	1024.161	-1.145
15	84	1022.378	1022.884	0.506
30	86	1029.874	1030.044	0.170
30	89	1036.045	1036.335	0.290
30	91	1018.789	1016.729	-2.060
30	92	1030.200	1028.210	-1.990
30	94	1028.048	1028.205	0.157
30	95	1030.224	1029.715	-0.509
35	100	1025.484	1025.800	0.316
35	101	1027.840	1026.429	-1.411
35	102	1018.230	1016.513	-1.717
35	103	1039.545	1037.550	-1.995
35	104	1025.306	1024.215	-1.091
35	105	1022.378	1021.798	-0.580
50	106	1030.758	1032.802	2.044
50	107	1028.423	1029.585	1.162
50	108	1028.567	1032.691	3.124
50	109	1034.527	1036.949	2.422
50	110	1030.313	1029.933	-0.380
50	111	1019.126	1021.745	2.619
55	113	1021.368	1017.473	-3.895
55	114	1016.476	1013.368	-3.108
55	115	1024.572	1022.441	-2.131
55	116	1016.054	1015.451	-0.603
55	126	1035.414	1034.827	-0.587
55	128	1032.844	1029.697	-3.147
100	129	1025.279	1023.077	-2.202
100	131	1032.519	1028.261	-4.258
100	132	1037.206	1033.543	-3.663
100	133	1027.861	1025.065	-2.796
100	134	1031.544	1028.629	-2.915
100	135	1023.547	1019.701	-3.846
100	138	1025.798	1025.674	-0.124
100	139	1028.561	1024.225	-4.336
100	140	1028.938	1026.076	-2.862
100	141	1027.002	1021.904	-5.098
100	142	1030.534	1030.542	0.008
100	143	1021.890	1017.752	-4.138
100	144	1030.712	1027.019	-3.693
100	220	1028.749	1023.284	-5.465
100	221	1027.657	1027.005	-0.652
100	224	1026.219	1022.732	-3.487
100	225	1031.446	1027.924	-3.522
100	226	1029.761	1028.639	-1.122
100	228	1024.312	1025.174	0.862
100	232	1026.460	1025.677	-0.783
100	233	1026.759	1025.224	-1.535
100	236	1018.095	1015.850	-2.245
100	238	1031.033	1027.154	-3.879
100	243	1021.927	1019.030	-2.897
105	244	1022.117	1017.082	-5.035
105	246	1026.084	1026.298	0.214
105	248	1029.882	1030.424	0.542
105	374	1016.573	1015.670	-0.903
105	376	1029.804	1027.317	-2.487
105	Max	1039.545	1040.592	3.124
105	Average	1027.406	1026.339	-1.067
105	Min	1016.054	1013.368	-5.465
105	Std Dev	5.510	5.998	1.876



9.40_FSW_EXT	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	1100
Min Limit	900

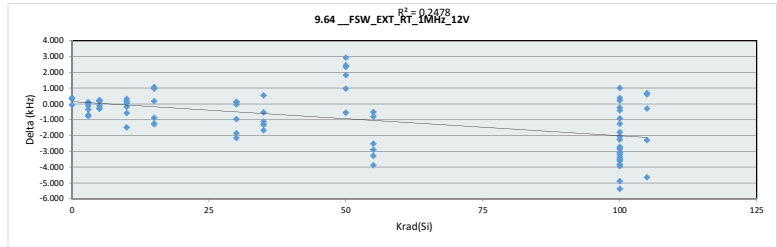
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1028.630	1018.433	1018.157	1019.254	1016.804	1016.729	1016.513	1021.745	1013.368	1015.850	1015.670
Average	1029.913	1028.730	1026.496	1028.887	1026.379	1028.206	1025.384	1030.618	1022.210	1024.965	1023.358
Max	1030.987	1036.408	1039.790	1036.367	1040.592	1036.335	1037.550	1036.949	1034.827	1033.543	1030.424
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



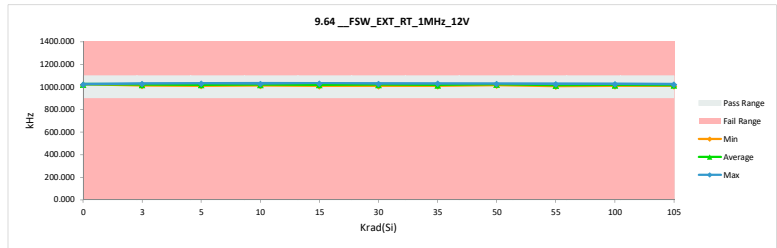
LDR TID Report  
TPS7H5001-SP

		9.64_FSW_EXT_RT_1MHz_12V	
Test Site			
Tester			
Test Number			
Unit	kHz	kHz	
Max Limit	1100	1100	
Min Limit	900	900	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1024.335	1024.688	0.353
0	118	1023.558	1023.517	-0.041
0	122	1025.143	1025.587	0.444
3	61	1024.929	1024.194	-0.735
3	62	1031.106	1031.201	0.095
3	63	1013.840	1013.187	-0.653
3	64	1024.646	1024.558	-0.088
3	65	1022.912	1022.607	-0.305
3	66	1025.168	1025.310	0.142
5	67	1020.286	1020.206	-0.080
5	68	1022.943	1023.222	0.279
5	69	1012.705	1012.985	0.280
5	70	1034.288	1034.499	0.211
5	71	1020.404	1020.135	-0.269
5	72	1017.515	1017.279	-0.236
10	73	1024.929	1024.767	-0.162
10	74	1031.106	1031.196	0.090
10	75	1013.840	1014.048	0.208
10	76	1024.646	1023.203	-1.443
10	77	1022.912	1023.261	0.349
10	78	1025.168	1024.637	-0.531
15	79	1020.286	1021.392	1.106
15	80	1022.943	1022.108	-0.835
15	81	1012.705	1011.493	-1.212
15	82	1034.288	1035.285	0.997
15	83	1020.404	1019.154	-1.250
15	84	1017.515	1017.717	0.202
30	86	1024.929	1024.947	0.018
30	89	1031.106	1031.236	0.130
30	91	1013.840	1011.719	-2.121
30	92	1024.646	1022.841	-1.805
30	94	1022.912	1023.087	0.175
30	95	1025.168	1024.250	-0.918
35	100	1020.286	1020.852	0.566
35	101	1022.943	1021.668	-1.275
35	102	1012.705	1011.447	-1.258
35	103	1034.288	1032.661	-1.627
35	104	1020.404	1019.322	-1.082
35	105	1017.515	1017.017	-0.498
50	106	1025.569	1027.413	1.844
50	107	1022.929	1023.939	1.010
50	108	1024.224	1027.180	2.956
50	109	1028.888	1031.358	2.470
50	110	1025.526	1025.001	-0.525
50	111	1013.970	1016.355	2.385
55	113	1016.250	1012.416	-3.834
55	114	1011.376	1008.131	-3.245
55	115	1019.639	1017.160	-2.479
55	116	1011.238	1010.470	-0.768
55	126	1030.121	1029.650	-0.471
55	128	1027.253	1024.388	-2.865
100	129	1020.295	1018.541	-1.754
100	131	1026.917	1023.131	-3.786
100	132	1031.566	1028.528	-3.038
100	133	1022.548	1020.338	-2.210
100	134	1026.367	1023.587	-2.780
100	135	1018.068	1014.571	-3.497
100	138	1020.583	1020.827	0.244
100	139	1023.121	1019.229	-3.892
100	140	1023.679	1020.902	-2.777
100	141	1021.707	1016.867	-4.840
100	142	1025.232	1025.636	0.404
100	143	1016.769	1013.196	-3.573
100	144	1025.703	1022.269	-3.434
100	220	1023.530	1018.201	-5.329
100	221	1022.242	1021.868	-0.374
100	224	1020.964	1017.954	-3.010
100	225	1026.029	1022.712	-3.317
100	226	1024.782	1023.904	-0.878
100	228	1019.163	1020.211	1.048
100	232	1020.997	1020.806	-0.191
100	233	1021.469	1020.247	-1.222
100	236	1013.068	1011.059	-2.009
100	238	1025.352	1022.213	-3.139
100	243	1017.043	1014.355	-2.688
105	244	1017.237	1012.622	-4.615
105	246	1020.770	1021.416	0.646
105	248	1024.936	1025.645	0.709
105	374	1011.764	1011.506	-0.258
105	376	1024.516	1022.258	-2.258
105	375	1034.288	1035.285	0.996
105	Max	1022.233	1021.257	-0.976
105	Average	1011.238	1008.131	-5.329
105	Min	1011.238	1008.131	-5.329
105	Std Dev	5.447	5.895	1.739



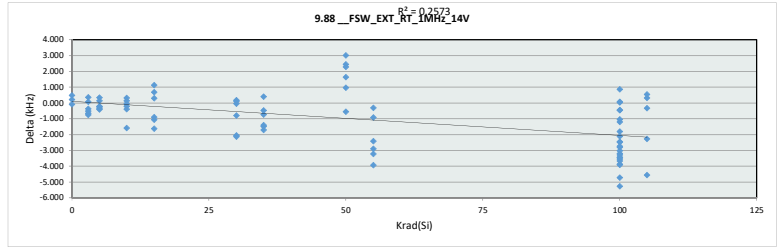
		9.64_FSW_EXT										
Test Site												
Tester												
Test Number												
Max Limit	1100	kHz										
Min Limit	900	kHz										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	
Min	1023.517	1013.187	1012.985	1014.048	1011.493	1011.719	1011.447	1016.355	1008.131	1011.059	1011.506	
Average	1024.597	1023.510	1021.388	1023.519	1021.192	1023.013	1020.495	1025.208	1017.036	1020.048	1018.689	
Max	1025.587	1031.201	1034.499	1031.196	1035.285	1031.236	1032.661	1031.358	1029.650	1028.528	1025.645	
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	



LDR TID Report  
TPS7H5001-SP

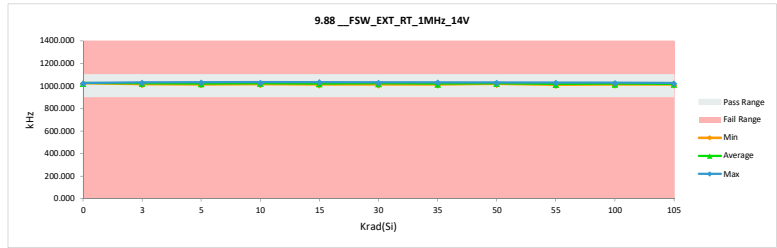
9.88_FSW_EXT_RT_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	1100
Min Limit	900

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1024.104	1024.354	0.250
0	118	1023.471	1023.429	-0.042
0	122	1025.067	1025.586	0.519
3	61	1024.771	1024.049	-0.722
3	62	1031.050	1031.164	0.114
3	63	1013.696	1013.085	-0.611
3	64	1024.752	1024.422	-0.330
3	65	1022.833	1022.363	-0.470
3	66	1024.908	1025.297	0.389
5	67	1020.381	1020.002	-0.379
5	68	1022.923	1023.096	0.173
5	69	1013.022	1012.725	-0.297
5	70	1033.903	1034.276	0.373
5	71	1020.063	1019.890	-0.173
5	72	1017.332	1017.117	-0.215
10	73	1024.771	1024.591	-0.180
10	74	1031.050	1031.032	-0.018
10	75	1013.696	1013.858	0.162
10	76	1024.752	1023.209	-1.543
10	77	1022.833	1023.181	0.348
10	78	1024.908	1024.544	-0.364
15	79	1020.381	1021.090	0.709
15	80	1022.923	1022.065	-0.858
15	81	1013.022	1011.425	-1.597
15	82	1033.903	1035.074	1.171
15	83	1020.063	1019.049	-1.014
15	84	1017.332	1017.674	0.342
30	86	1024.771	1024.916	0.145
30	89	1031.050	1031.041	-0.009
30	91	1013.696	1011.597	-2.099
30	92	1024.752	1022.736	-2.016
30	94	1022.833	1023.040	0.207
30	95	1024.908	1024.158	-0.750
35	100	1020.381	1020.807	0.426
35	101	1022.923	1021.485	-1.438
35	102	1013.022	1011.352	-1.670
35	103	1033.903	1032.531	-1.372
35	104	1020.063	1019.367	-0.696
35	105	1017.332	1016.890	-0.442
50	106	1025.488	1027.166	1.678
50	107	1023.029	1024.034	1.005
50	108	1024.036	1027.076	3.040
50	109	1028.759	1031.230	2.471
50	110	1025.228	1024.710	-0.518
50	111	1013.966	1016.292	2.326
55	113	1016.182	1012.290	-3.892
55	114	1011.301	1008.118	-3.183
55	115	1019.525	1017.140	-2.385
55	116	1011.108	1010.226	-0.882
55	126	1029.869	1029.597	-0.272
55	128	1027.167	1024.299	-2.868
100	129	1020.155	1018.383	-1.772
100	131	1026.874	1023.006	-3.868
100	132	1031.613	1028.450	-3.163
100	133	1022.526	1020.095	-2.431
100	134	1026.188	1023.490	-2.698
100	135	1017.974	1014.342	-3.632
100	138	1020.473	1020.537	0.064
100	139	1023.022	1019.178	-3.844
100	140	1023.600	1020.842	-2.758
100	141	1021.522	1016.841	-4.681
100	142	1025.215	1025.331	0.116
100	143	1016.637	1013.129	-3.508
100	144	1025.610	1022.180	-3.430
100	220	1023.406	1018.168	-5.238
100	221	1022.230	1021.823	-0.407
100	224	1020.772	1017.786	-2.986
100	225	1025.859	1022.604	-3.255
100	226	1024.774	1023.767	-1.007
100	228	1019.152	1020.061	0.909
100	232	1020.994	1020.583	-0.411
100	233	1021.294	1020.130	-1.164
100	236	1012.926	1010.841	-2.085
100	238	1025.336	1021.940	-3.396
100	243	1016.798	1014.376	-2.422
105	244	1017.013	1012.488	-4.525
105	246	1020.772	1021.134	0.362
105	248	1024.827	1025.404	0.577
105	374	1011.664	1011.362	-0.302
105	376	1024.416	1022.174	-2.242
105	Max	1033.903	1035.074	3.040
105	Average	1022.136	1021.127	-1.008
105	Min	1011.108	1008.118	-5.238
105	Std Dev	5.406	5.888	1.716



9.88_FSW_EXT	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	1100
Min Limit	900

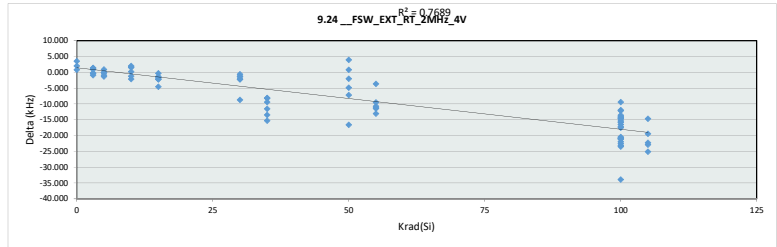
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000	900.000
Min	1023.429	1013.085	1012.725	1013.858	1011.425	1011.597	1011.352	1016.292	1008.118	1010.841	1011.362
Average	1024.456	1023.397	1021.184	1023.403	1021.063	1022.915	1020.405	1025.085	1016.945	1019.912	1018.512
Max	1025.586	1031.164	1034.276	1031.032	1035.074	1031.041	1032.531	1031.230	1029.597	1028.450	1025.404
UL	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000	1100.000



LDR TID Report  
TPS7H5001-SP

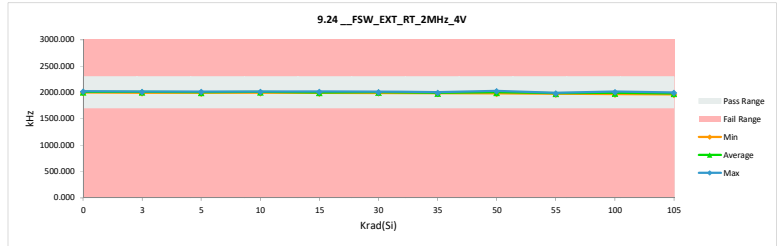
9.24_FSW_EXT_RT_2MHz_4V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	2300
Min Limit	1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2023.508	2025.746	2.238
0	118	2001.757	2002.727	0.970
0	122	2005.692	2009.369	3.677
3	61	1997.022	1996.747	-0.275
3	62	2013.080	2014.639	1.559
3	63	2010.961	2010.901	-0.060
3	64	2016.920	2018.351	1.431
3	65	2019.328	2020.417	1.089
3	66	2016.633	2015.897	-0.736
5	67	1995.582	1995.654	0.072
5	68	2004.039	2002.902	-1.137
5	69	2012.047	2011.293	-0.754
5	70	2019.587	2019.647	0.060
5	71	2003.940	2004.122	0.182
5	72	1991.611	1992.695	1.084
10	73	1997.022	1998.701	1.679
10	74	2013.080	2014.936	1.856
10	75	2010.961	2009.877	-1.084
10	76	2016.920	2014.992	-1.928
10	77	2019.328	2019.735	0.407
10	78	2016.633	2018.816	2.183
15	79	1995.582	1993.568	-2.014
15	80	2004.039	2003.892	-0.147
15	81	2012.047	2007.637	-4.410
15	82	2019.587	2018.466	-1.121
15	83	2003.940	2002.380	-1.560
15	84	1991.611	1989.642	-1.969
30	86	1997.022	1995.971	-1.051
30	89	2013.080	2011.052	-2.028
30	91	2010.961	2009.385	-1.576
30	92	2016.920	2014.966	-1.954
30	94	2019.328	2018.092	-1.236
30	95	2016.633	2016.217	-0.416
35	100	1995.582	1986.376	-9.206
35	101	2004.039	1992.634	-11.405
35	102	2012.047	1998.761	-13.286
35	103	2019.587	2004.438	-15.149
35	104	2003.940	1995.936	-8.004
35	105	1991.611	1983.649	-7.962
50	106	2002.471	2000.658	-1.813
50	107	1993.519	1986.541	-6.978
50	108	2048.079	2031.658	-16.421
50	109	2004.684	1999.961	-4.723
50	110	1996.240	1997.218	0.978
50	111	2012.861	2016.961	4.100
55	113	2005.858	1992.934	-12.924
55	114	1996.174	1986.794	-9.380
55	115	1992.387	1981.278	-11.109
55	116	1977.691	1974.263	-3.428
55	126	2002.051	1991.503	-10.548
55	128	2005.270	1993.957	-11.313
100	129	1992.369	1978.430	-13.939
100	131	2019.744	2007.774	-11.970
100	132	2031.054	2009.250	-21.804
100	133	1995.935	1981.008	-14.927
100	134	2008.763	1995.391	-13.372
100	135	2042.157	2008.398	-33.759
100	138	1993.894	1976.597	-17.297
100	139	2032.074	2008.725	-23.349
100	140	2013.620	2004.424	-9.196
100	141	2009.016	1988.297	-20.719
100	142	2005.849	1990.283	-15.566
100	143	1993.605	1973.296	-20.309
100	144	2012.828	1991.854	-20.974
100	220	2011.347	1994.343	-17.004
100	221	2005.736	1991.468	-14.268
100	224	2010.572	1994.287	-16.285
100	225	2037.975	2017.328	-20.647
100	226	2001.795	1987.793	-14.002
100	228	2000.855	1986.439	-14.416
100	232	2009.142	1993.478	-15.664
100	233	2019.858	2008.059	-11.799
100	236	2017.787	1995.362	-22.425
100	238	2022.068	1999.018	-23.050
100	243	1993.908	1973.359	-20.549
105	244	1992.496	1969.833	-22.663
105	246	2013.902	1991.842	-22.060
105	248	2018.358	1999.112	-19.246
105	374	1991.605	1966.720	-24.885
105	376	2015.153	2000.609	-14.544
105	Max	2048.079	2031.658	4.100
105	Average	2008.549	1999.884	-8.665
105	Min	1977.691	1966.720	-33.759
105	Std Dev	12.518	13.926	8.952



9.24_FSW_EXT	
Test Site	
Tester	
Test Number	
Max Limit	2300 kHz
Min Limit	1700 kHz

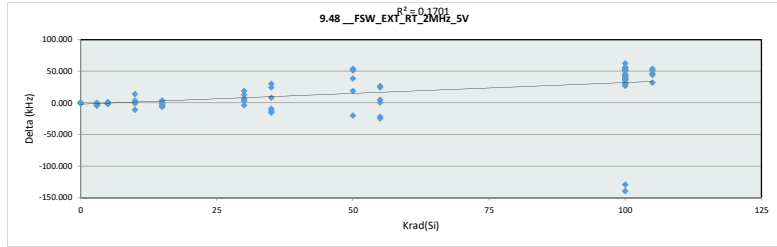
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2002.727	1996.747	1992.695	1998.701	1989.642	1995.971	1983.649	1986.541	1974.263	1973.296
Average	2012.614	2012.825	2004.386	2012.843	2002.598	2009.781	1993.632	2005.500	1986.788	1993.944
Max	2025.746	2020.417	2019.647	2019.735	2018.466	2018.092	2004.438	2031.658	1993.957	2017.328
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



LDR TID Report  
TPS7H5001-SP

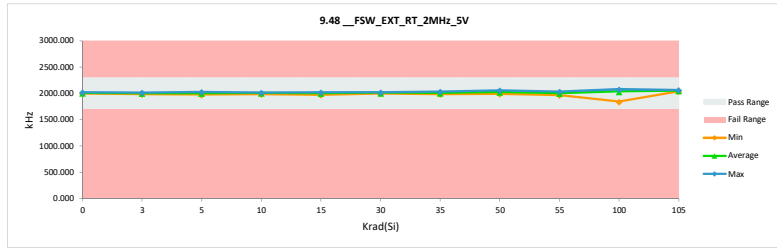
9.48_FSW_EXT_RT_2MHz_5V	
Test Site	
Tester	
Test Number	
Unit	kHz kHz
Max Limit	2300 2300
Min Limit	1700 1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2001.204	2002.346	1.142
0	118	2007.489	2007.550	0.061
0	122	2020.877	2022.769	1.892
3	61	2010.382	2007.273	-3.109
3	62	2015.456	2012.991	-2.465
3	63	1988.663	1987.751	-0.912
3	64	2017.967	2018.290	0.323
3	65	2003.998	2002.594	-1.404
3	66	2012.483	2013.544	1.061
5	67	2006.492	2008.817	2.325
5	68	2007.270	2006.816	-0.454
5	69	1979.653	1979.388	-0.265
5	70	2030.783	2031.872	1.089
5	71	2009.942	2009.148	-0.794
5	72	2002.110	2002.872	0.762
10	73	2010.382	2000.543	-9.839
10	74	2015.456	2017.434	1.978
10	75	1988.663	1989.950	1.287
10	76	2017.967	2018.155	0.188
10	77	2003.998	2019.107	15.109
10	78	2012.483	2017.217	4.734
15	79	2006.492	2011.449	4.957
15	80	2007.270	2007.280	0.010
15	81	1979.653	1976.148	-3.505
15	82	2030.783	2025.672	-5.111
15	83	2009.942	2012.020	2.078
15	84	2002.110	2000.011	-2.099
30	86	2010.382	2007.488	-2.894
30	89	2015.456	2021.360	5.904
30	91	1988.663	2022.414	13.751
30	92	2017.967	2022.343	4.376
30	94	2003.998	2023.847	19.849
30	95	2012.483	2019.848	7.365
35	100	2006.492	2037.831	31.339
35	101	2007.270	1998.648	-8.622
35	102	1979.653	2005.197	25.544
35	103	2030.783	2018.958	-11.825
35	104	2009.942	2019.213	9.271
35	105	2002.110	1987.463	-14.647
50	106	2008.332	2060.823	52.491
50	107	1990.873	2010.659	19.786
50	108	2007.606	2052.268	54.662
50	109	2015.657	1996.780	-18.877
50	110	2005.850	2059.907	54.057
50	111	1981.006	2020.625	39.619
55	113	2009.724	2035.006	25.282
55	114	1998.642	2026.052	27.410
55	115	2001.947	2003.939	1.992
55	116	1989.818	1968.767	-21.051
55	126	2007.673	1984.087	-23.586
55	128	2008.646	2014.259	5.613
100	129	2008.291	2060.763	52.472
100	131	2026.050	2065.102	39.052
100	132	2040.527	2080.941	40.414
100	133	2008.754	2066.027	57.273
100	134	2022.495	2074.495	52.000
100	135	1982.894	1844.614	-138.280
100	138	2002.880	2059.730	56.850
100	139	2011.298	2047.868	36.570
100	140	2017.494	2061.103	43.609
100	141	2001.669	2033.442	31.773
100	142	2018.753	1890.593	-128.160
100	143	1996.672	2043.197	46.525
100	144	2015.989	2067.735	51.746
100	220	2023.464	2053.778	30.314
100	221	2010.524	2048.897	38.373
100	224	2007.539	2050.753	43.214
100	225	2007.875	2047.913	40.038
100	226	2004.278	2057.932	53.654
100	228	2008.405	2071.860	63.455
100	232	2018.803	2074.394	55.591
100	233	2021.978	2053.737	31.759
100	236	1978.872	2007.011	28.139
100	238	2024.573	2065.979	41.406
100	243	2001.873	2039.521	37.648
105	244	1993.588	2041.423	47.835
105	246	2009.189	2063.965	54.776
105	248	2020.812	2066.145	45.333
105	374	1992.688	2045.139	52.451
105	376	2016.309	2049.182	32.873
105	Max	2040.527	2080.941	63.455
105	Average	2007.443	2023.000	15.557
105	Min	1978.872	1844.614	-138.280
105	Std Dev	12.705	36.868	33.580



9.48_FSW_EXT	
Test Site	
Tester	
Test Number	
Max Limit	2300 kHz
Min Limit	1700 kHz

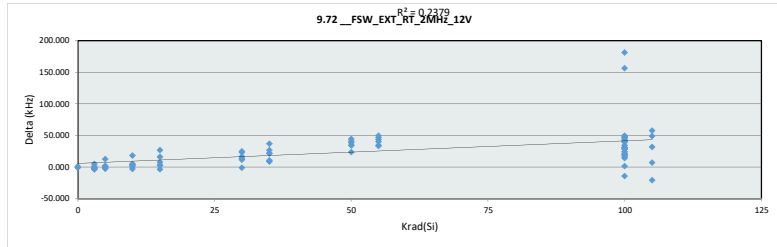
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	2002.346	1987.751	1979.388	1989.950	1976.148	2002.414	1987.463	1996.780	1968.767	1844.614
Average	2010.888	2007.074	2006.486	2010.401	2005.430	2016.217	2011.218	2033.510	2005.352	2040.308
Max	2022.769	2018.290	2031.872	2019.107	2025.672	2023.847	2037.831	2060.823	2035.006	2080.941
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



LDR TID Report  
TPS7H5001-SP

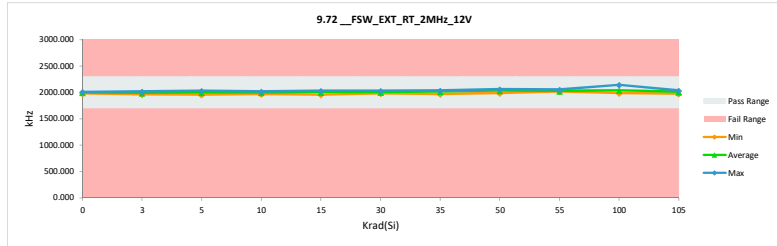
9.72_FSW_EXT_RT_2MHz_12V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	2300
Min Limit	1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1982.972	1983.936	0.964
0	118	2012.898	2013.201	0.303
0	122	2009.199	2011.075	1.876
3	61	2016.957	2017.047	0.090
3	62	2018.609	2020.975	2.366
3	63	1969.194	1968.078	-1.116
3	64	1993.494	1991.170	-2.324
3	65	1988.109	1986.109	-2.000
3	66	1984.093	1990.191	6.098
5	67	2016.338	2017.737	1.399
5	68	2005.131	2004.018	-1.113
5	69	1961.563	1962.149	0.586
5	70	2017.945	2031.567	13.622
5	71	1994.290	1992.924	-1.366
5	72	2007.119	2010.362	3.243
10	73	2016.957	2021.209	4.252
10	74	2018.609	2023.868	5.259
10	75	1969.194	1970.733	1.539
10	76	1993.494	2012.656	19.162
10	77	1988.109	1986.278	-1.831
10	78	1984.093	1990.308	6.215
15	79	2016.338	2025.148	8.810
15	80	2005.131	2009.045	3.914
15	81	1961.563	1958.882	-2.681
15	82	2017.945	2035.169	17.224
15	83	1994.290	2021.934	27.644
15	84	2007.119	2010.504	3.385
30	86	2016.957	2031.595	14.638
30	89	2018.609	2031.243	12.634
30	91	1969.194	1987.003	18.009
30	92	1993.494	2017.649	24.155
30	94	1988.109	1988.056	-0.053
30	95	1984.093	2009.972	25.879
35	100	2016.338	2038.931	22.593
35	101	2005.131	2014.623	9.492
35	102	1961.563	1973.280	11.717
35	103	2017.945	2040.540	22.595
35	104	1994.290	2032.140	37.850
35	105	2007.119	2034.998	27.879
50	106	2018.758	2054.307	35.549
50	107	2002.646	2041.934	39.288
50	108	1980.379	2023.524	42.145
50	109	2017.433	2062.966	45.533
50	110	2017.314	2052.605	35.291
50	111	1963.476	1988.531	25.055
55	113	1984.658	2032.242	47.584
55	114	1970.891	2021.534	50.643
55	115	2000.666	2035.190	34.524
55	116	1995.892	2036.713	40.821
55	126	2011.177	2055.867	44.690
55	128	2000.420	2035.838	35.418
100	129	2013.590	2045.331	31.741
100	131	2008.828	2040.530	31.702
100	132	2023.213	2070.709	47.496
100	133	2012.165	2047.537	35.372
100	134	2017.629	2058.166	40.537
100	135	1966.018	2123.195	157.177
100	138	1999.090	2044.697	45.607
100	139	1992.131	2022.298	30.167
100	140	2001.269	2043.002	41.733
100	141	1973.350	1993.066	19.716
100	142	2014.042	2063.237	49.195
100	143	2004.535	2021.567	17.032
100	144	2021.518	2042.424	20.906
100	220	2033.885	2020.960	-12.925
100	221	2013.798	2016.355	2.557
100	224	1991.006	2034.479	43.473
100	225	1990.429	2020.017	29.588
100	226	2008.332	2031.310	22.978
100	228	2018.925	2050.271	31.346
100	232	2031.257	2056.421	25.164
100	233	1990.509	2021.887	31.378
100	236	1964.552	2146.612	182.060
100	238	1997.122	2047.768	50.646
100	243	1996.059	2011.087	15.028
105	244	2000.347	2008.708	8.361
105	246	1983.611	2042.111	58.500
105	248	1994.498	2027.238	32.740
105	374	1977.431	2027.464	50.033
105	376	2001.693	1981.810	-19.883
105	Max	2033.885	2146.612	182.060
105	Average	1999.102	2023.338	24.236
105	Min	1961.563	1958.882	-19.883
105	Std Dev	18.120	30.893	29.635



9.72_FSW_EXT	
Test Site	
Tester	
Test Number	
Max Limit	2300 kHz
Min Limit	1700 kHz

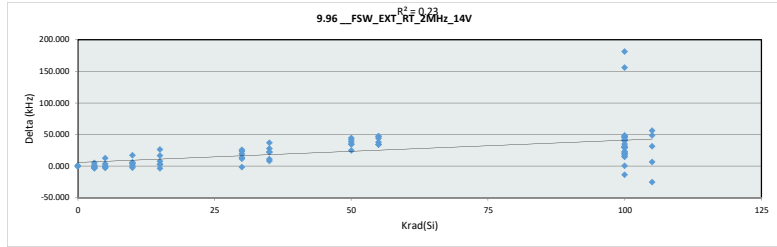
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	1983.936	1968.078	1962.149	1970.733	1958.882	1987.203	1973.280	1988.531	2021.534	1993.066	1981.810
Average	2002.737	1995.595	2003.126	2000.842	2010.114	2010.953	2022.419	2037.145	2036.231	2044.705	2017.466
Max	2013.201	2020.975	2031.567	2023.868	2035.169	2031.595	2040.540	2062.966	2055.867	2146.612	2042.111
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



LDR TID Report  
TPS7H5001-SP

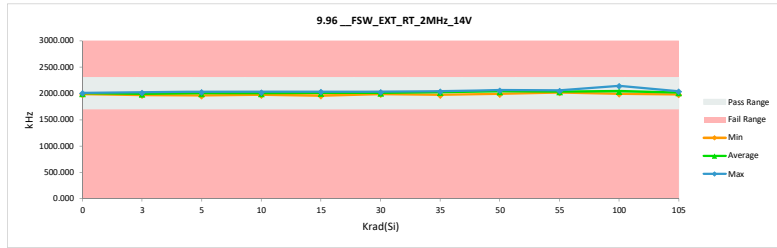
9.96_FSW_EXT_RT_2MHz_14V	
Test Site	
Tester	
Test Number	
Unit	kHz
Max Limit	2300
Min Limit	1700

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1982.915	1983.758	0.843
0	118	2012.703	2013.065	0.362
0	122	2008.769	2010.867	2.098
3	61	2016.400	2016.911	0.511
3	62	2018.336	2020.942	2.606
3	63	1968.601	1967.894	-0.707
3	64	1993.417	1990.784	-2.633
3	65	1987.840	1986.178	-1.662
3	66	1983.859	1990.128	6.269
5	67	2016.495	2017.640	1.145
5	68	2004.941	2003.702	-1.239
5	69	1961.210	1961.465	0.255
5	70	2017.511	2031.112	13.601
5	71	1994.259	1992.658	-1.601
5	72	2005.896	2010.342	4.446
10	73	2016.400	2020.934	4.534
10	74	2018.336	2024.191	5.855
10	75	1968.601	1969.966	1.365
10	76	1993.417	2011.901	18.484
10	77	1987.840	1986.262	-1.578
10	78	1983.859	1990.940	7.081
15	79	2016.495	2025.165	8.670
15	80	2004.941	2008.771	3.830
15	81	1961.210	1958.626	-2.584
15	82	2017.511	2035.030	17.519
15	83	1994.259	2021.622	27.363
15	84	2005.896	2010.244	4.348
30	86	2016.400	2031.131	14.731
30	89	2018.336	2031.018	12.682
30	91	1968.601	1988.839	20.238
30	92	1993.417	2017.909	24.492
30	94	1987.840	1987.523	-0.317
30	95	1983.859	2010.561	26.702
35	100	2016.495	2039.613	23.118
35	101	2004.941	2014.262	9.321
35	102	1961.210	1973.660	12.450
35	103	2017.511	2040.823	23.312
35	104	1994.259	2032.102	37.843
35	105	2005.896	2034.843	28.947
50	106	2018.246	2053.996	35.750
50	107	2002.005	2041.689	39.684
50	108	1980.000	2022.495	42.495
50	109	2017.093	2062.471	45.378
50	110	2016.963	2052.620	35.657
50	111	1963.190	1988.857	25.667
55	113	1984.471	2032.220	47.749
55	114	1970.548	2019.123	48.575
55	115	2000.165	2034.944	34.779
55	116	1997.635	2036.696	39.061
55	126	2010.903	2055.873	44.970
55	128	2000.000	2035.353	35.353
100	129	2013.097	2044.789	31.692
100	131	2008.324	2039.623	31.299
100	132	2023.025	2070.243	47.218
100	133	2011.535	2047.279	35.744
100	134	2016.881	2058.132	41.251
100	135	1965.645	2122.393	156.748
100	138	1998.038	2044.642	46.604
100	139	1991.795	2022.204	30.409
100	140	2000.955	2042.900	41.945
100	141	1973.312	1992.532	19.220
100	142	2013.510	2063.287	49.777
100	143	2004.422	2020.986	16.564
100	144	2021.420	2041.882	20.462
100	220	2033.181	2020.658	-12.523
100	221	2014.090	2015.660	1.570
100	224	1990.482	2033.903	43.421
100	225	1990.083	2019.742	29.659
100	226	2009.138	2031.444	22.306
100	228	2018.378	2050.173	31.795
100	232	2031.208	2055.711	24.503
100	233	1990.180	2022.159	31.979
100	236	1964.300	2146.471	182.171
100	238	1996.626	2044.182	47.556
100	243	1996.188	2011.719	15.531
105	244	2000.241	2007.634	7.393
105	246	1983.747	2040.881	57.134
105	248	1994.573	2026.946	32.373
105	374	1977.505	2027.338	49.833
105	376	2004.640	1980.654	-23.986
105	Max	2033.181	2146.471	182.171
105	Average	1998.855	2023.074	24.218
105	Min	1961.210	1958.626	-23.986
105	Std Dev	18.107	30.817	29.615



9.96_FSW_EXT	
Test Site	
Tester	
Test Number	
Max Limit	2300 kHz
Min Limit	1700 kHz

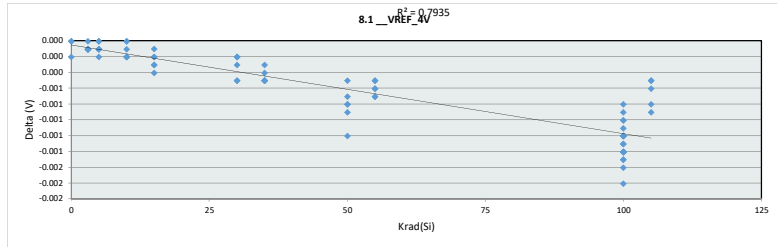
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000	1700.000
Min	1983.758	1967.894	1961.465	1969.966	1958.626	1987.523	1973.660	1988.857	2019.123	1992.532	1980.654
Average	2002.563	1995.473	2002.820	2000.699	2009.910	2011.164	2022.551	2037.021	2035.702	2044.280	2016.691
Max	2013.065	2020.942	2031.112	2024.191	2035.030	2031.131	2040.823	2062.471	2055.873	2146.471	2040.881
UL	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000	2300.000



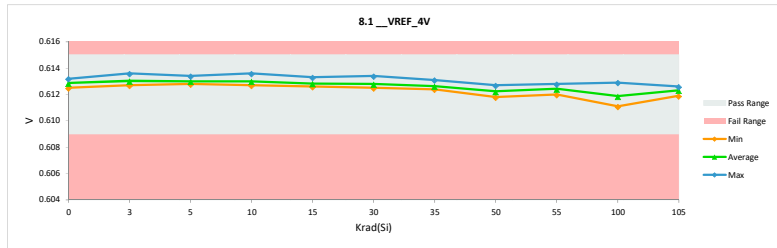
LDR TID Report  
TPS7H5001-SP

		8.1_VREF_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	0.615	0.615	
Min Limit	0.609	0.609	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	0.000
30	89	0.613	0.613	0.000
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	0.000
35	101	0.613	0.612	-0.001
35	102	0.613	0.612	-0.001
35	103	0.613	0.613	0.000
35	104	0.613	0.613	0.000
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	-0.001
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	-0.001
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.001
100	133	0.613	0.612	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.611	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.613	0.612	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	0.000
105	248	0.613	0.613	-0.001
105	374	0.613	0.612	-0.001
105	376	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.612	-0.001
	Min	0.613	0.611	-0.002
	Std Dev	0.000	0.001	0.001



		8.1_VREF_4V	
Test Site			
Tester			
Test Number			
Max Limit	0.615	V	
Min Limit	0.609	V	
Krad(Si)	0	3	5
LL	0.609	0.609	0.609
Min	0.613	0.613	0.613
Average	0.613	0.613	0.613
Max	0.613	0.614	0.614
UL	0.615	0.615	0.615

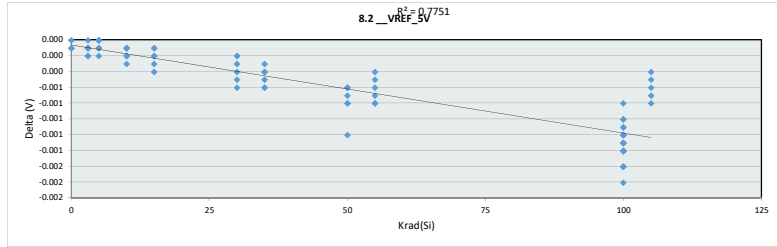




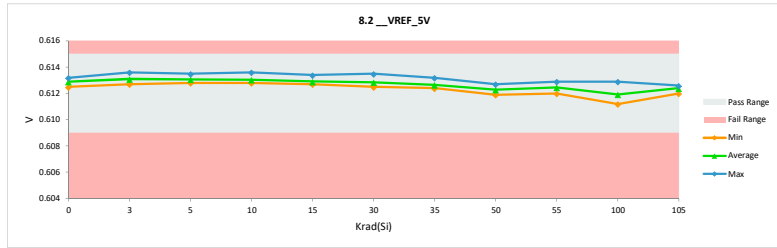
LDR TID Report  
TPS7H5001-SP

8.2_VREF_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.615
Min Limit	0.609

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	0.000
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	-0.001
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	0.000
35	104	0.613	0.613	0.000
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	-0.001
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.612	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.612	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	375	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.613	-0.001
	Min	0.613	0.611	-0.002
	Std Dev	0.000	0.001	0.001



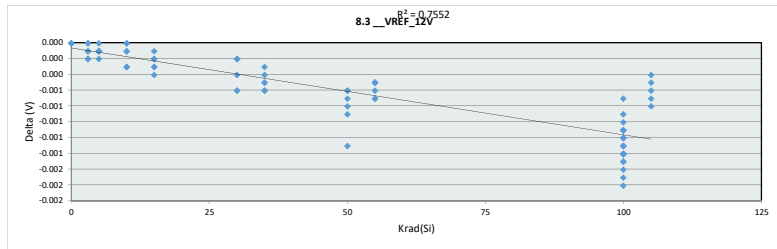
8.2_VREF_5V											
Test Site											
Tester											
Test Number											
Max Limit	0.615 V										
Min Limit	0.609 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.614	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



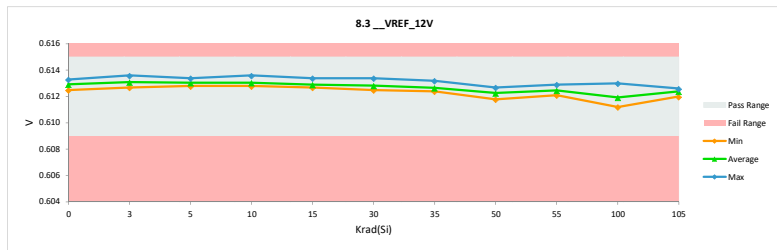
LDR TID Report  
TPS7H5001-SP

8.3_VREF_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.615
Min Limit	0.609

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	-0.001
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	-0.001
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	-0.001
35	104	0.613	0.613	-0.001
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	0.000
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.002
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.613	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	376	0.613	0.612	-0.001
105	Max	0.614	0.614	0.000
105	Average	0.613	0.613	-0.001
105	Min	0.613	0.611	-0.002
105	Std Dev	0.000	0.001	0.001



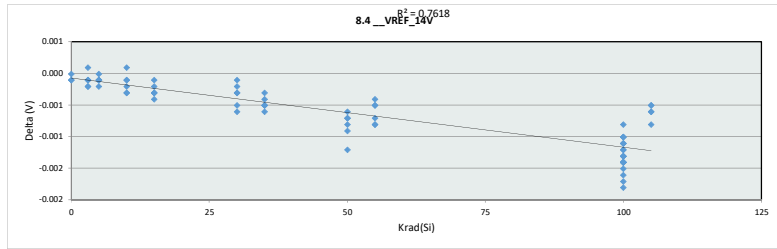
8.3_VREF_12V											
Test Site											
Tester											
Test Number											
Max Limit	0.615 V										
Min Limit	0.609 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



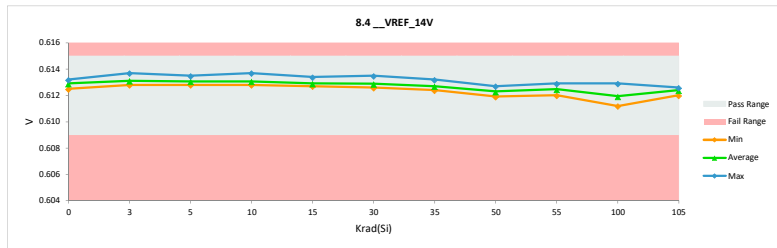
LDR TID Report  
TPS7H5001-SP

8.4_VREF_14V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	0.615	0.615
Min Limit	0.609	0.609

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.613	0.613	0.000
0	118	0.613	0.613	0.000
0	122	0.613	0.613	0.000
3	61	0.613	0.613	0.000
3	62	0.613	0.613	0.000
3	63	0.613	0.613	0.000
3	64	0.613	0.613	0.000
3	65	0.613	0.613	0.000
3	66	0.614	0.614	0.000
5	67	0.613	0.613	0.000
5	68	0.613	0.613	0.000
5	69	0.613	0.613	0.000
5	70	0.613	0.613	0.000
5	71	0.613	0.613	0.000
5	72	0.613	0.613	0.000
10	73	0.613	0.613	0.000
10	74	0.613	0.613	0.000
10	75	0.613	0.613	0.000
10	76	0.613	0.613	0.000
10	77	0.613	0.613	0.000
10	78	0.614	0.614	0.000
15	79	0.613	0.613	0.000
15	80	0.613	0.613	0.000
15	81	0.613	0.613	0.000
15	82	0.613	0.613	0.000
15	83	0.613	0.613	0.000
15	84	0.613	0.613	0.000
30	86	0.613	0.613	-0.001
30	89	0.613	0.613	-0.001
30	91	0.613	0.613	0.000
30	92	0.613	0.613	0.000
30	94	0.613	0.613	0.000
30	95	0.614	0.613	0.000
35	100	0.613	0.613	0.000
35	101	0.613	0.612	-0.001
35	102	0.613	0.613	0.000
35	103	0.613	0.613	-0.001
35	104	0.613	0.613	-0.001
35	105	0.613	0.613	0.000
50	106	0.613	0.613	-0.001
50	107	0.613	0.612	-0.001
50	108	0.613	0.612	-0.001
50	109	0.613	0.612	-0.001
50	110	0.613	0.613	-0.001
50	111	0.613	0.612	-0.001
55	113	0.613	0.613	-0.001
55	114	0.613	0.612	-0.001
55	115	0.613	0.612	-0.001
55	116	0.613	0.613	0.000
55	126	0.613	0.612	-0.001
55	128	0.613	0.613	0.000
100	129	0.613	0.612	-0.001
100	131	0.613	0.612	-0.001
100	132	0.614	0.612	-0.002
100	133	0.614	0.613	-0.001
100	134	0.613	0.612	-0.001
100	135	0.613	0.611	-0.002
100	138	0.613	0.612	-0.001
100	139	0.613	0.612	-0.001
100	140	0.613	0.612	-0.001
100	141	0.613	0.612	-0.002
100	142	0.613	0.612	-0.001
100	143	0.613	0.612	-0.001
100	144	0.613	0.612	-0.001
100	220	0.613	0.611	-0.001
100	221	0.613	0.612	-0.001
100	224	0.613	0.612	-0.002
100	225	0.614	0.613	-0.001
100	226	0.613	0.612	-0.001
100	228	0.613	0.612	-0.001
100	232	0.613	0.611	-0.001
100	233	0.613	0.612	-0.001
100	236	0.614	0.613	-0.001
100	238	0.613	0.612	-0.001
100	243	0.613	0.612	-0.001
105	244	0.613	0.612	-0.001
105	246	0.613	0.613	-0.001
105	248	0.613	0.613	0.000
105	374	0.613	0.613	0.000
105	375	0.613	0.612	-0.001
	Max	0.614	0.614	0.000
	Average	0.613	0.613	-0.001
	Min	0.613	0.611	-0.002
	Std Dev	0.000	0.001	0.001



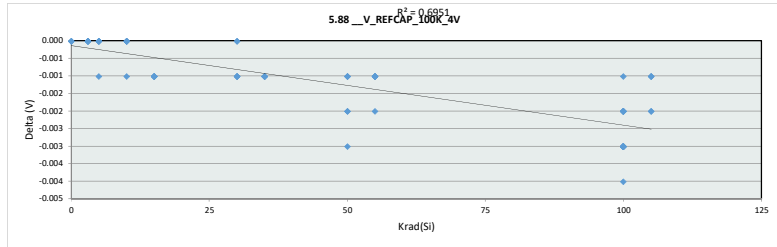
8.4_VREF_14V											
Test Site											
Tester											
Test Number											
Max Limit	0.615	V									
Min Limit	0.609	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609	0.609
Min	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.611	0.612
Average	0.613	0.613	0.613	0.613	0.613	0.613	0.613	0.612	0.612	0.612	0.612
Max	0.613	0.614	0.614	0.614	0.613	0.614	0.613	0.613	0.613	0.613	0.613
UL	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615	0.615



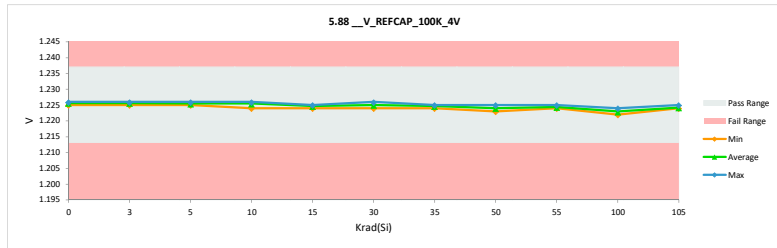
LDR TID Report  
TPS7H5001-SP

5.88_V_REFCAP_100K_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.225	-0.001
10	73	1.225	1.224	-0.001
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.224	-0.001
15	80	1.225	1.224	-0.001
15	81	1.226	1.225	-0.001
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.226	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.225	1.224	-0.001
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.222	-0.003
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



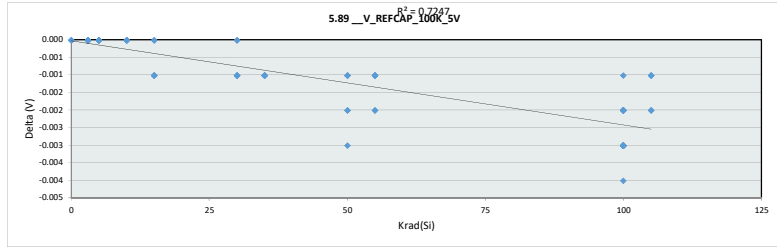
5.88_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.222
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.226	1.225	1.226	1.225	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



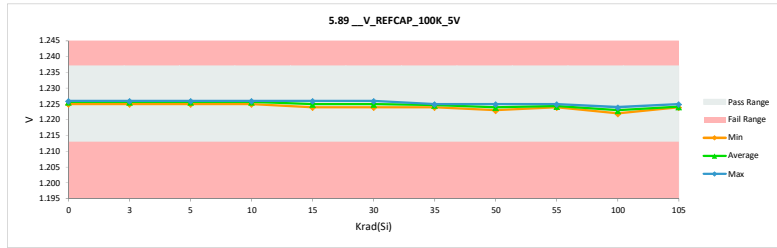
LDR TID Report  
TPS7H5001-SP

5.89_V_REFCAP_100K_5V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



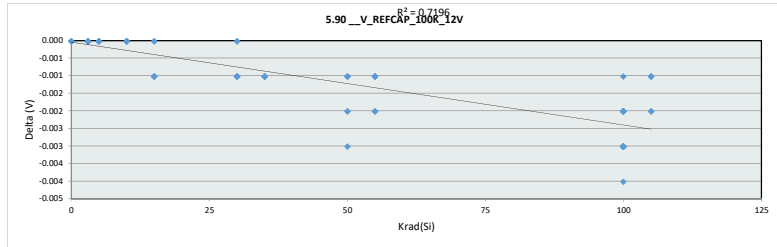
5.89_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237 V										
Min Limit	1.213 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



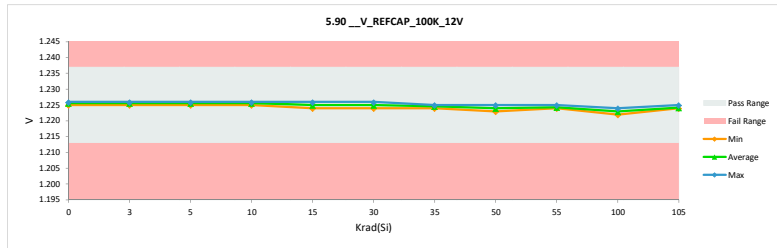
LDR TID Report  
TPS7H5001-SP

5.90_V_REFCAP_100K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001

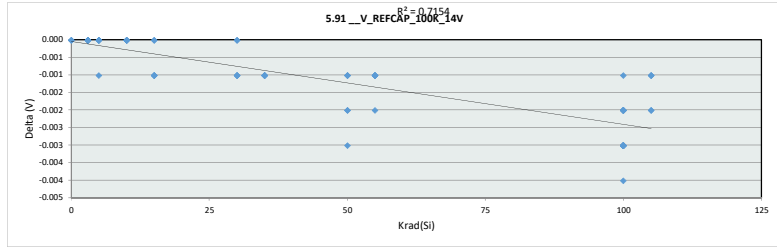


5.90_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

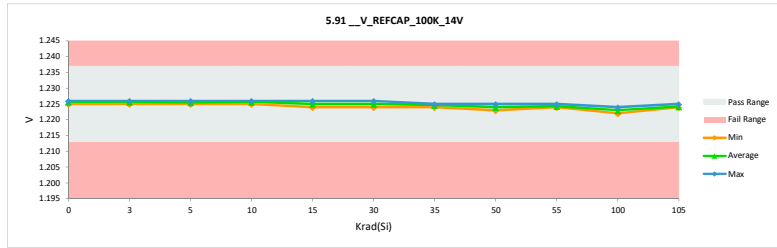


LDR TID Report  
TPS7H5001-SP

5.91 V_REFCAP_100K_14V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	117	1.225	1.225	0.000	
0	118	1.226	1.226	0.000	
0	122	1.226	1.226	0.000	
3	61	1.225	1.225	0.000	
3	62	1.226	1.226	0.000	
3	63	1.226	1.226	0.000	
3	64	1.225	1.225	0.000	
3	65	1.226	1.226	0.000	
3	66	1.226	1.226	0.000	
5	67	1.225	1.225	0.000	
5	68	1.225	1.225	0.000	
5	69	1.226	1.226	0.000	
5	70	1.226	1.226	0.000	
5	71	1.226	1.226	0.000	
5	72	1.226	1.225	-0.001	
10	73	1.225	1.225	0.000	
10	74	1.226	1.225	-0.001	
10	75	1.226	1.226	0.000	
10	76	1.225	1.225	0.000	
10	77	1.226	1.226	0.000	
10	78	1.226	1.226	0.000	
15	79	1.225	1.225	0.000	
15	80	1.225	1.224	-0.001	
15	81	1.226	1.226	0.000	
15	82	1.226	1.225	-0.001	
15	83	1.226	1.225	-0.001	
15	84	1.226	1.225	-0.001	
30	86	1.225	1.224	-0.001	
30	89	1.226	1.225	-0.001	
30	91	1.226	1.225	-0.001	
30	92	1.225	1.225	0.000	
30	94	1.226	1.225	-0.001	
30	95	1.226	1.226	0.000	
35	100	1.225	1.224	-0.001	
35	101	1.225	1.224	-0.001	
35	102	1.226	1.225	-0.001	
35	103	1.226	1.225	-0.001	
35	104	1.226	1.225	-0.001	
35	105	1.226	1.225	-0.001	
50	106	1.225	1.224	-0.001	
50	107	1.226	1.224	-0.002	
50	108	1.226	1.223	-0.003	
50	109	1.226	1.224	-0.002	
50	110	1.226	1.225	-0.001	
50	111	1.225	1.224	-0.001	
55	113	1.225	1.224	-0.001	
55	114	1.225	1.224	-0.001	
55	115	1.225	1.224	-0.001	
55	116	1.226	1.224	-0.002	
55	126	1.226	1.225	-0.001	
55	128	1.226	1.225	-0.001	
100	129	1.225	1.222	-0.003	
100	131	1.226	1.223	-0.003	
100	132	1.226	1.223	-0.003	
100	133	1.226	1.224	-0.002	
100	134	1.226	1.223	-0.003	
100	135	1.226	1.222	-0.004	
100	138	1.226	1.223	-0.003	
100	139	1.226	1.223	-0.003	
100	140	1.226	1.223	-0.003	
100	141	1.225	1.222	-0.003	
100	142	1.225	1.223	-0.002	
100	143	1.225	1.223	-0.002	
100	144	1.225	1.224	-0.001	
100	220	1.225	1.222	-0.003	
100	221	1.226	1.223	-0.003	
100	224	1.225	1.222	-0.003	
100	225	1.226	1.224	-0.002	
100	226	1.226	1.223	-0.003	
100	228	1.226	1.223	-0.003	
100	232	1.226	1.223	-0.003	
100	233	1.226	1.224	-0.002	
100	236	1.226	1.224	-0.002	
100	238	1.225	1.223	-0.002	
100	243	1.225	1.223	-0.002	
105	244	1.226	1.224	-0.002	
105	246	1.226	1.225	-0.001	
105	248	1.225	1.224	-0.001	
105	374	1.225	1.224	-0.001	
105	375	1.226	1.224	-0.002	
105	Max	1.226	1.226	0.000	
105	Average	1.226	1.224	-0.001	
105	Min	1.225	1.222	-0.004	
105	Std Dev	0.000	0.001	0.001	

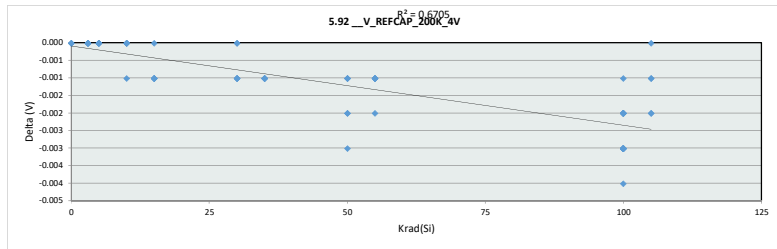


5.91 V_REFCAP													
Test Site													
Tester													
Test Number													
Max Limit	1.237	V											
Min Limit	1.213	V											
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105		
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222	1.224		
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.223		
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224		
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237		

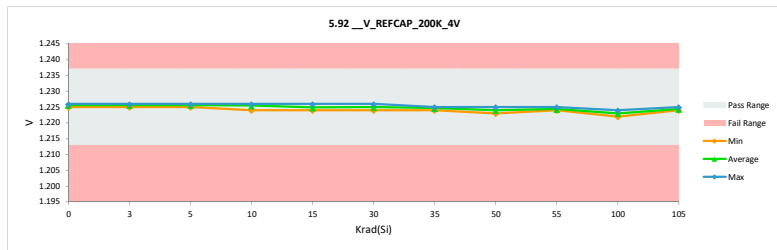


LDR TID Report  
TPS7H5001-SP

5.92_V_REFCAP_200K_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.224	-0.001
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.224	-0.001
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.225	1.224	-0.001
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.225	1.223	-0.002
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.223	-0.003
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.225	1.223	-0.002
100	243	1.225	1.223	-0.002
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.225	0.000
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



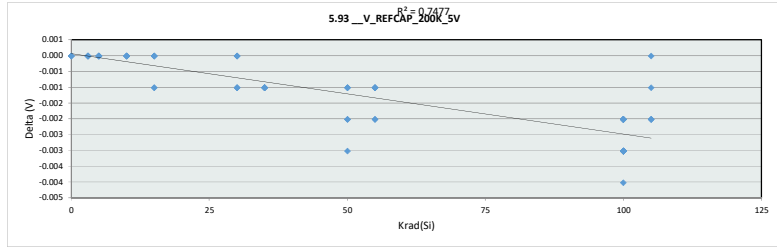
5.92_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



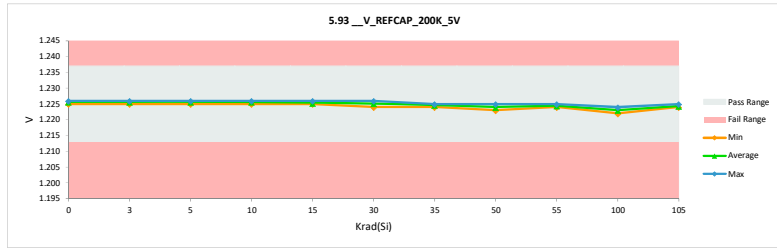


LDR TID Report  
TPS7H5001-SP

5.93_V_REFCAP_200K_5V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	1.237	1.237		
Min Limit	1.213	1.213		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.225	0.000
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.226	0.000
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.226	0.000
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.226	1.224	-0.002
105	374	1.225	1.225	0.000
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



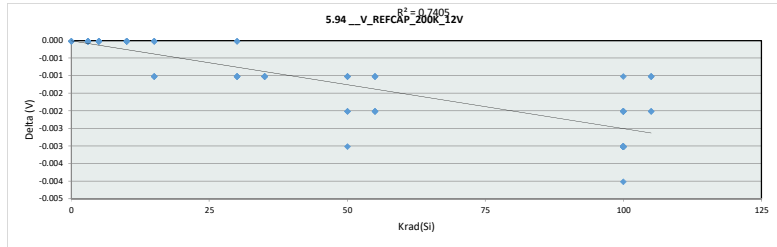
5.93_V_REFCAP												
Test Site												
Tester												
Test Number												
Max Limit	1.237	V										
Min Limit	1.213	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105	
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.222	1.224	
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225	1.224	
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225	
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	



LDR TID Report  
TPS7H5001-SP

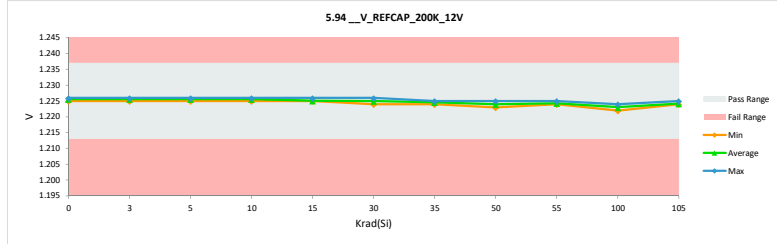
5.94_V_REFCAP_200K_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.225	0.000
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.225	-0.001
15	84	1.226	1.225	-0.001
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.225	-0.001
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.226	1.223	-0.003
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.225	1.224	-0.001
105	374	1.225	1.224	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



5.94_V_REFCAP	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

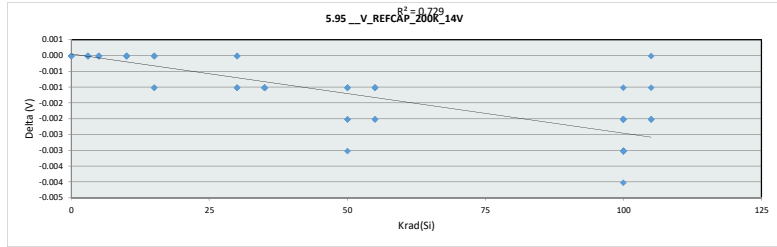
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



LDR TID Report  
TPS7H5001-SP

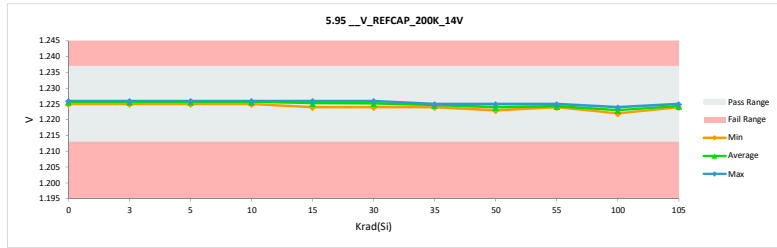
5.95_V_REFCAP_200K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.225	1.225	0.000
0	118	1.226	1.226	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.226	1.226	0.000
3	63	1.226	1.226	0.000
3	64	1.225	1.225	0.000
3	65	1.226	1.226	0.000
3	66	1.226	1.226	0.000
5	67	1.225	1.225	0.000
5	68	1.225	1.225	0.000
5	69	1.226	1.226	0.000
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.226	1.226	0.000
10	75	1.226	1.226	0.000
10	76	1.225	1.225	0.000
10	77	1.226	1.226	0.000
10	78	1.226	1.226	0.000
15	79	1.225	1.225	0.000
15	80	1.225	1.224	-0.001
15	81	1.226	1.226	0.000
15	82	1.226	1.225	-0.001
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.226	1.225	-0.001
30	91	1.226	1.226	0.000
30	92	1.225	1.225	0.000
30	94	1.226	1.225	-0.001
30	95	1.226	1.226	0.000
35	100	1.225	1.224	-0.001
35	101	1.225	1.224	-0.001
35	102	1.226	1.225	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.225	1.224	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.223	-0.003
50	109	1.226	1.224	-0.002
50	110	1.226	1.225	-0.001
50	111	1.225	1.224	-0.001
55	113	1.225	1.224	-0.001
55	114	1.225	1.224	-0.001
55	115	1.226	1.224	-0.002
55	116	1.226	1.224	-0.002
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.225	1.222	-0.003
100	131	1.226	1.223	-0.003
100	132	1.226	1.223	-0.003
100	133	1.226	1.224	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.223	-0.003
100	140	1.226	1.223	-0.003
100	141	1.225	1.222	-0.003
100	142	1.225	1.223	-0.002
100	143	1.225	1.223	-0.002
100	144	1.225	1.224	-0.001
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.225	1.222	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.223	-0.003
100	232	1.226	1.223	-0.003
100	233	1.226	1.224	-0.002
100	236	1.226	1.224	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.224	-0.002
105	246	1.226	1.225	-0.001
105	248	1.226	1.224	-0.002
105	374	1.225	1.225	0.000
105	375	1.226	1.224	-0.002
105	Max	1.226	1.226	0.000
105	Average	1.226	1.224	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.000	0.001	0.001



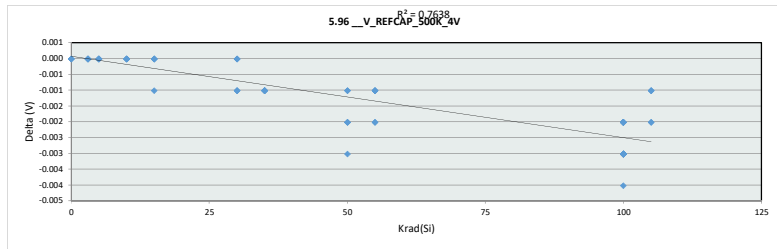
5.95_V_REFCAP	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.224	1.223	1.224
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

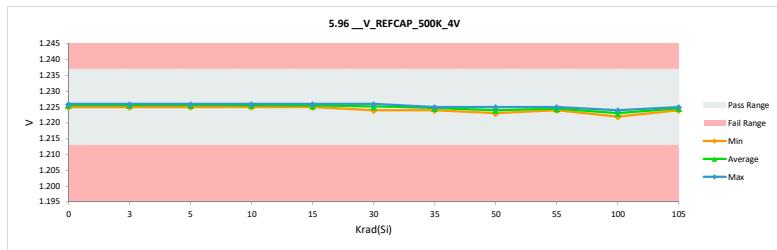


LDR TID Report  
TPS7H5001-SP

5.96_V_REFCAP_500K_4V					
Test Site					
Tester					
Test Number					
Unit	V	V			
Max Limit	1.237	1.237			
Min Limit	1.213	1.213			
Krad(Si)	Serial #	PRE	POST	Delta	
0	117	1.225	1.225	0.000	
0	118	1.226	1.226	0.000	
0	122	1.226	1.226	0.000	
3	61	1.225	1.225	0.000	
3	62	1.226	1.226	0.000	
3	63	1.226	1.226	0.000	
3	64	1.225	1.225	0.000	
3	65	1.226	1.226	0.000	
3	66	1.226	1.226	0.000	
5	67	1.225	1.225	0.000	
5	68	1.225	1.225	0.000	
5	69	1.226	1.226	0.000	
5	70	1.226	1.226	0.000	
5	71	1.226	1.226	0.000	
5	72	1.226	1.226	0.000	
10	73	1.225	1.225	0.000	
10	74	1.226	1.226	0.000	
10	75	1.226	1.226	0.000	
10	76	1.225	1.225	0.000	
10	77	1.226	1.226	0.000	
10	78	1.226	1.226	0.000	
15	79	1.225	1.225	0.000	
15	80	1.225	1.225	0.000	
15	81	1.226	1.226	0.000	
15	82	1.226	1.225	-0.001	
15	83	1.226	1.226	0.000	
15	84	1.226	1.226	0.000	
30	86	1.225	1.224	-0.001	
30	89	1.226	1.225	-0.001	
30	91	1.226	1.226	0.000	
30	92	1.225	1.225	0.000	
30	94	1.226	1.225	-0.001	
30	95	1.226	1.226	0.000	
35	100	1.225	1.224	-0.001	
35	101	1.225	1.224	-0.001	
35	102	1.226	1.225	-0.001	
35	103	1.226	1.225	-0.001	
35	104	1.226	1.225	-0.001	
35	105	1.226	1.225	-0.001	
50	106	1.226	1.224	-0.002	
50	107	1.226	1.224	-0.002	
50	108	1.226	1.223	-0.003	
50	109	1.226	1.224	-0.002	
50	110	1.226	1.225	-0.001	
50	111	1.225	1.224	-0.001	
55	113	1.225	1.224	-0.001	
55	114	1.226	1.224	-0.002	
55	115	1.226	1.224	-0.002	
55	116	1.226	1.225	-0.001	
55	126	1.226	1.225	-0.001	
55	128	1.226	1.225	-0.001	
100	129	1.225	1.222	-0.003	
100	131	1.226	1.223	-0.003	
100	132	1.226	1.223	-0.003	
100	133	1.226	1.224	-0.002	
100	134	1.226	1.223	-0.003	
100	135	1.226	1.222	-0.004	
100	138	1.226	1.223	-0.003	
100	139	1.226	1.224	-0.002	
100	140	1.226	1.223	-0.003	
100	141	1.225	1.222	-0.003	
100	142	1.226	1.223	-0.003	
100	143	1.225	1.223	-0.002	
100	144	1.226	1.224	-0.002	
100	220	1.225	1.222	-0.003	
100	221	1.226	1.223	-0.003	
100	224	1.225	1.222	-0.003	
100	225	1.226	1.224	-0.002	
100	226	1.226	1.224	-0.002	
100	228	1.226	1.223	-0.003	
100	232	1.226	1.223	-0.003	
100	233	1.226	1.224	-0.002	
100	236	1.226	1.224	-0.002	
100	238	1.226	1.223	-0.003	
100	243	1.226	1.223	-0.003	
105	244	1.226	1.225	-0.001	
105	246	1.226	1.225	-0.001	
105	248	1.226	1.224	-0.002	
105	374	1.226	1.225	-0.001	
105	375	1.226	1.224	-0.002	
105	Max	1.226	1.226	0.000	
105	Average	1.226	1.224	-0.001	
105	Min	1.225	1.222	-0.004	
105	Std Dev	0.000	0.001	0.001	



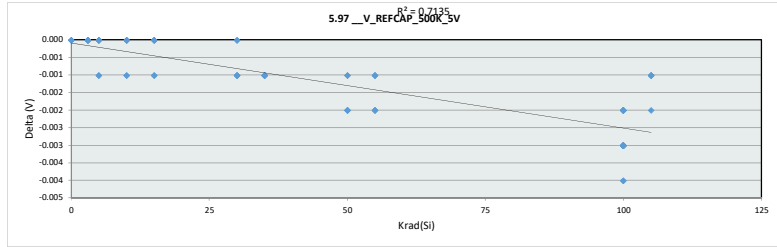
5.96_V_REFCAP											
Test Site											
Tester											
Test Number											
Max Limit	1.237	V									
Min Limit	1.213	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.225	1.225	1.225	1.225	1.225	1.224	1.224	1.223	1.224	1.222	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.223	1.225
Max	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



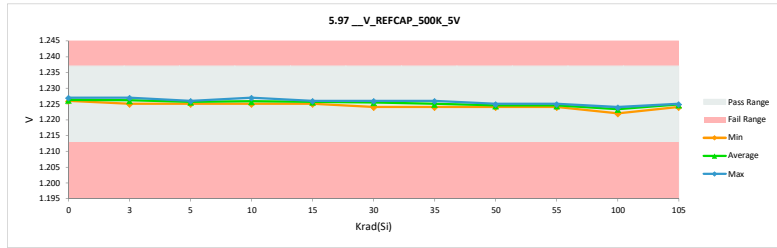
LDR TID Report  
TPS7H5001-SP

5.97_V_REFCAP_500K_5V			
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.226	1.225	-0.001
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.226	1.224	-0.002
100	226	1.226	1.224	-0.002
100	228	1.226	1.224	-0.002
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



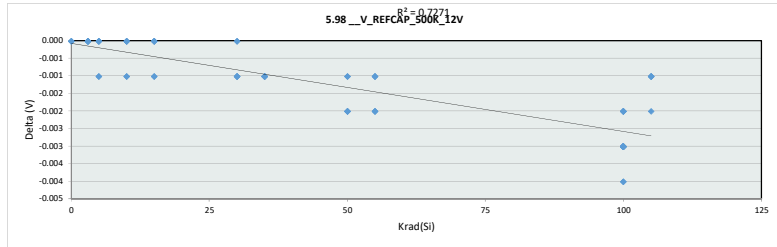
5.97_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



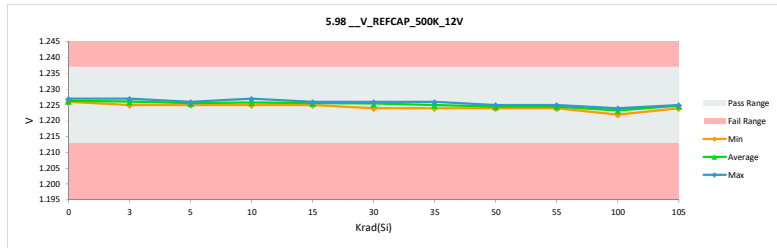
LDR TID Report  
TPS7H5001-SP

5.98_V_REFCAP_500K_12V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.237 1.237
Min Limit	1.213 1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.226	1.225	-0.001
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



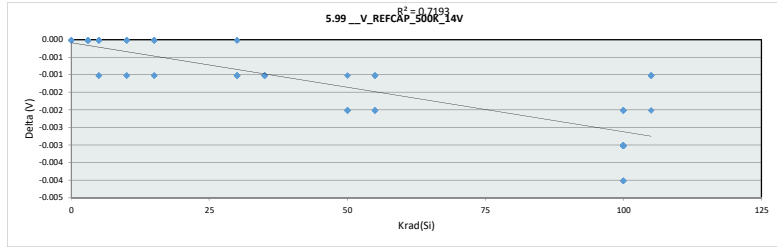
5.98_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.223
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



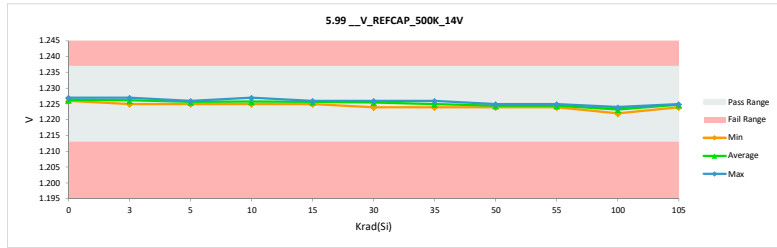
LDR TID Report  
TPS7H5001-SP

5.99_V_REFCAP_500K_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.226	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.222	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.225	1.222	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



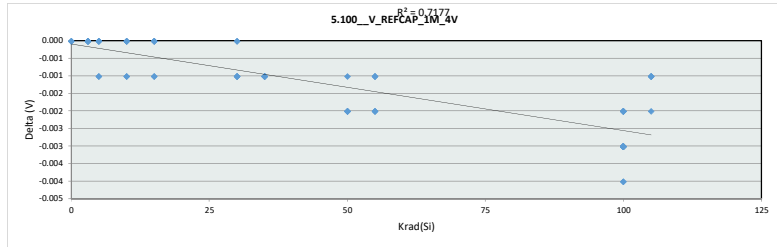
5.99_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.225
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



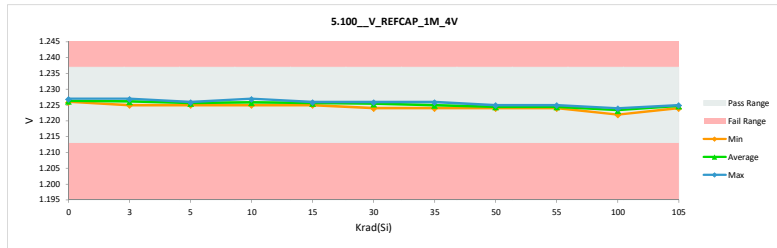
LDR TID Report  
TPS7H5001-SP

		5.100_V_REFCAP_1M_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.225	1.225	0.000
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.226	1.226	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.225	-0.001
5	68	1.225	1.225	0.000
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.226	1.226	0.000
5	72	1.226	1.226	0.000
10	73	1.225	1.225	0.000
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.225	-0.001
10	77	1.226	1.226	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.225	1.225	0.000
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.226	1.226	0.000
15	84	1.226	1.226	0.000
30	86	1.225	1.224	-0.001
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.225	-0.001
30	94	1.226	1.226	0.000
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.225	1.224	-0.001
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.226	1.225	-0.001
35	105	1.226	1.225	-0.001
50	106	1.226	1.225	-0.001
50	107	1.226	1.224	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.224	-0.002
55	114	1.226	1.224	-0.002
55	115	1.226	1.224	-0.002
55	116	1.226	1.225	-0.001
55	126	1.226	1.225	-0.001
55	128	1.226	1.225	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.224	-0.003
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.222	-0.004
100	142	1.226	1.223	-0.003
100	143	1.226	1.223	-0.003
100	144	1.226	1.224	-0.002
100	220	1.226	1.223	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.224	-0.003
100	226	1.226	1.224	-0.002
100	228	1.226	1.224	-0.002
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.226	1.225	-0.001
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	375	1.226	1.224	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.225	1.222	-0.004
105	Std Dev	0.001	0.001	0.001



		5.100_V_REFCAP								
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.224	1.224
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.223
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.225	1.224
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

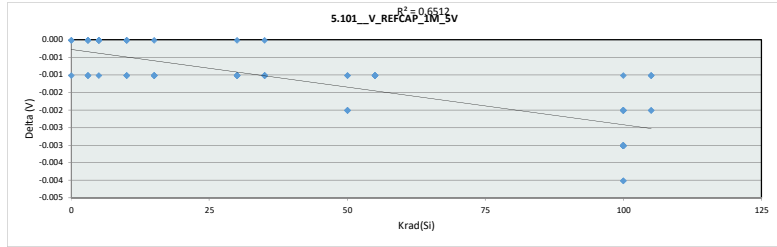




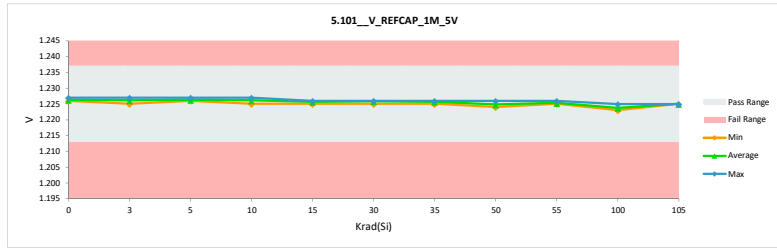
LDR TID Report  
TPS7H5001-SP

		5.101_V_REFCAP_1M_5V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.226	-0.001
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.227	1.226	-0.001
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.226	-0.001
5	72	1.226	1.226	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.226	-0.001
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.226	-0.001
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.226	1.226	0.000
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.226	1.226	0.000
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.226	1.225	-0.001
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.223	-0.004
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.226	1.225	-0.001
100	220	1.226	1.223	-0.003
100	221	1.226	1.224	-0.002
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.226	1.224	-0.002
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.226	1.225	-0.001
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.001	0.001	0.001



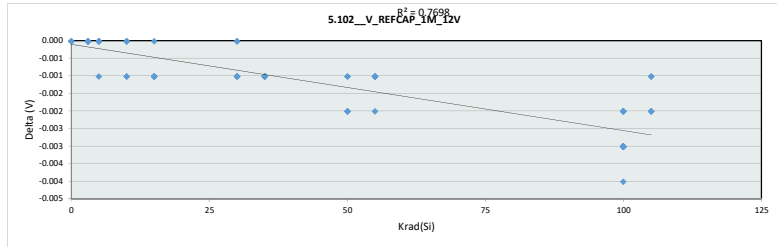
		5.101_V_REFCAP												
Test Site														
Tester														
Test Number		1.237	V											
Min Limit		1.213	V											
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105		
LL		1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min		1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225	1.225	1.225
Average		1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225	1.224	1.225
Max		1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225
UL		1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



LDR TID Report  
TPS7H5001-SP

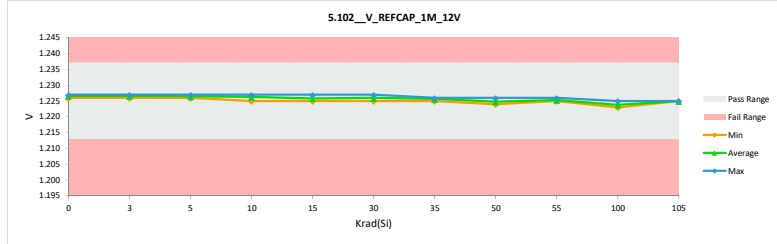
5.102_V_REFCAP_1M_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.226	-0.001
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	375	1.227	1.225	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



5.102_V_REFCAP	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

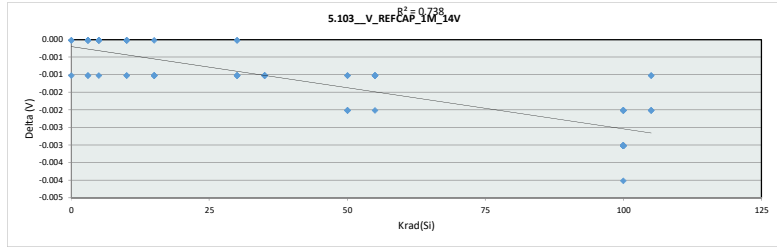
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.226	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



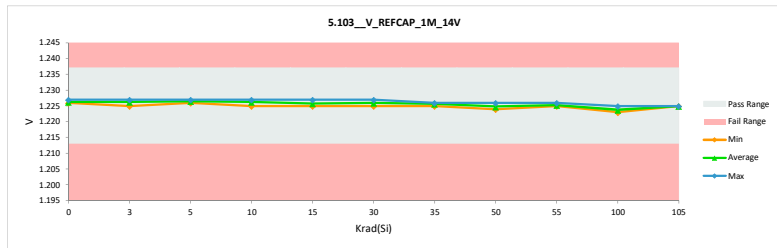
LDR TID Report  
TPS7H5001-SP

5.103_V_REFCAP_1M_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.226	-0.001
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.226	-0.001
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.226	1.225	-0.001
50	107	1.227	1.225	-0.002
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.225	-0.002
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	376	1.227	1.225	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



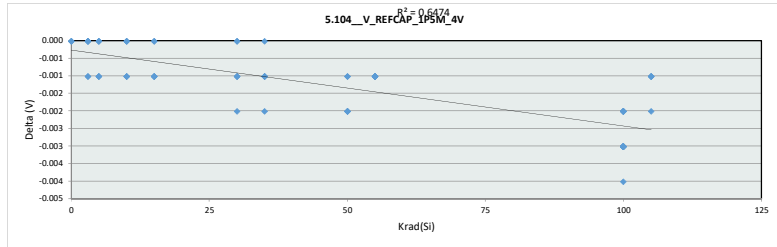
5.103_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223
Average	1.226	1.226	1.227	1.226	1.226	1.226	1.226	1.225	1.225	1.224
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



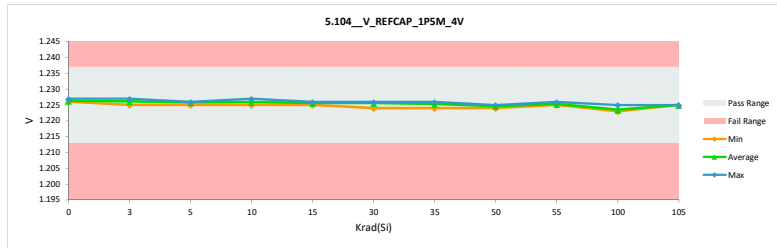
LDR TID Report  
TPS7H5001-SP

5.104_V_REFCAP_1P5M_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.226	1.226	0.000
3	61	1.226	1.225	-0.001
3	62	1.227	1.227	0.000
3	63	1.226	1.226	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.226	-0.001
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.225	-0.001
5	69	1.227	1.226	-0.001
5	70	1.226	1.226	0.000
5	71	1.227	1.226	-0.001
5	72	1.226	1.226	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.226	-0.001
10	75	1.226	1.226	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.226	-0.001
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.226	-0.001
15	82	1.226	1.226	0.000
15	83	1.227	1.226	-0.001
15	84	1.226	1.226	0.000
30	86	1.226	1.224	-0.002
30	89	1.227	1.226	-0.001
30	91	1.226	1.226	0.000
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.226	-0.001
35	100	1.226	1.225	-0.001
35	101	1.226	1.224	-0.002
35	102	1.227	1.226	-0.001
35	103	1.226	1.225	-0.001
35	104	1.227	1.226	-0.001
35	105	1.226	1.226	0.000
50	106	1.226	1.225	-0.001
50	107	1.226	1.225	-0.001
50	108	1.226	1.224	-0.002
50	109	1.227	1.225	-0.002
50	110	1.227	1.225	-0.002
50	111	1.226	1.224	-0.002
55	113	1.226	1.225	-0.001
55	114	1.226	1.225	-0.001
55	115	1.226	1.225	-0.001
55	116	1.226	1.225	-0.001
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.226	1.224	-0.002
100	132	1.227	1.223	-0.004
100	133	1.227	1.225	-0.002
100	134	1.226	1.223	-0.003
100	135	1.226	1.223	-0.003
100	138	1.226	1.223	-0.003
100	139	1.226	1.224	-0.002
100	140	1.226	1.224	-0.002
100	141	1.226	1.223	-0.003
100	142	1.226	1.223	-0.003
100	143	1.226	1.224	-0.002
100	144	1.226	1.224	-0.002
100	220	1.226	1.223	-0.003
100	221	1.226	1.223	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.226	1.223	-0.003
100	233	1.227	1.224	-0.003
100	236	1.227	1.224	-0.003
100	238	1.226	1.223	-0.003
100	243	1.226	1.223	-0.003
105	244	1.226	1.225	-0.001
105	246	1.227	1.225	-0.002
105	248	1.226	1.225	-0.001
105	374	1.226	1.225	-0.001
105	375	1.226	1.225	-0.001
105	Max	1.227	1.227	0.000
105	Average	1.226	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001

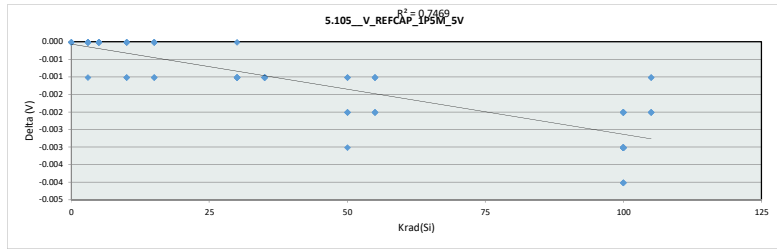


5.104_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.225	1.225	1.225	1.225	1.224	1.224	1.224	1.225	1.223
Average	1.226	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.225	1.224
Max	1.227	1.227	1.226	1.227	1.226	1.226	1.226	1.225	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

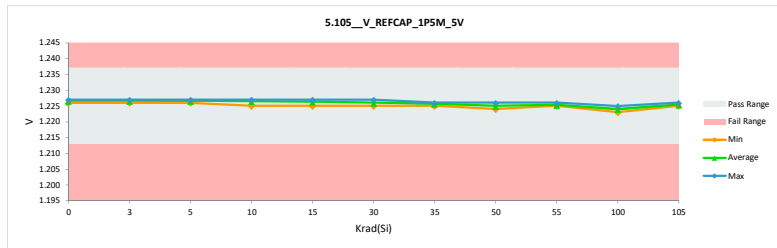


LDR TID Report  
TPS7H5001-SP

		5.105_V_REFCAP_1P5M_5V		
Test Site				
Tester				
Test Number				
Unit		V	V	
Max Limit		1.237	1.237	
Min Limit		1.213	1.213	
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.227	1.226	-0.001
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.227	1.226	-0.001
10	77	1.227	1.227	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.226	0.000
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.227	0.000
15	84	1.227	1.227	0.000
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.227	1.226	-0.001
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.225	-0.002
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.225	-0.001
55	113	1.226	1.225	-0.001
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.224	-0.003
100	140	1.227	1.224	-0.003
100	141	1.227	1.223	-0.004
100	142	1.227	1.224	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.227	1.223	-0.004
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.225	-0.002
100	236	1.227	1.225	-0.002
100	238	1.227	1.224	-0.003
100	243	1.226	1.224	-0.002
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.225	-0.002
105	374	1.227	1.226	-0.001
105	376	1.227	1.225	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



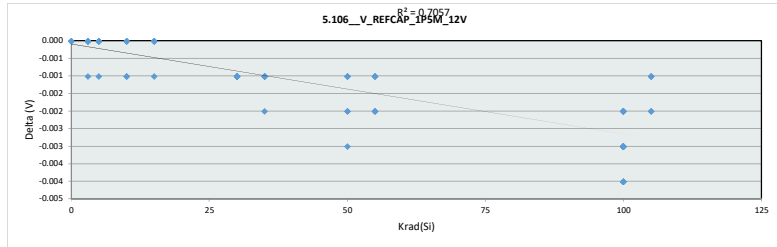
		5.105_V_REFCAP								
Test Site										
Tester										
Test Number										
Max Limit		1.237	V							
Min Limit		1.213	V							
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223
Average	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.225	1.224
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



LDR TID Report  
TPS7H5001-SP

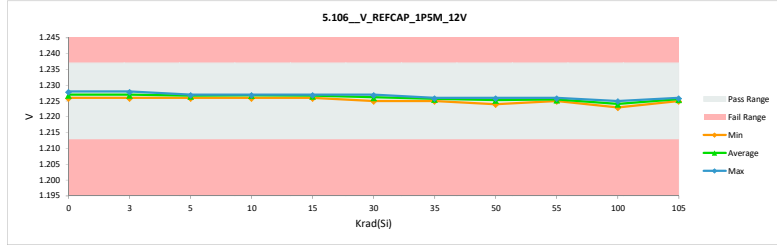
5.106_V_REFCAP_1P5M_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.228	1.228	0.000
3	63	1.227	1.227	0.000
3	64	1.227	1.227	0.000
3	65	1.227	1.227	0.000
3	66	1.228	1.227	-0.001
5	67	1.227	1.226	-0.001
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.226	0.000
10	74	1.228	1.227	-0.001
10	75	1.227	1.227	0.000
10	76	1.227	1.227	0.000
10	77	1.227	1.227	0.000
10	78	1.228	1.227	-0.001
15	79	1.227	1.226	-0.001
15	80	1.226	1.226	0.000
15	81	1.227	1.227	0.000
15	82	1.227	1.227	0.000
15	83	1.227	1.227	0.000
15	84	1.227	1.227	0.000
30	86	1.226	1.225	-0.001
30	89	1.228	1.227	-0.001
30	91	1.227	1.226	-0.001
30	92	1.227	1.226	-0.001
30	94	1.227	1.226	-0.001
30	95	1.228	1.227	-0.001
35	100	1.227	1.225	-0.002
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.226	-0.001
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.226	-0.001
50	110	1.228	1.226	-0.002
50	111	1.226	1.225	-0.001
55	113	1.227	1.225	-0.002
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.226	-0.001
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.227	1.224	-0.003
100	131	1.227	1.224	-0.003
100	132	1.228	1.224	-0.004
100	133	1.228	1.225	-0.003
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.225	-0.002
100	140	1.227	1.224	-0.003
100	141	1.227	1.223	-0.004
100	142	1.227	1.224	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.227	1.223	-0.004
100	221	1.227	1.224	-0.003
100	224	1.227	1.223	-0.004
100	225	1.227	1.225	-0.002
100	226	1.227	1.225	-0.002
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.225	-0.002
100	236	1.227	1.225	-0.002
100	238	1.227	1.224	-0.003
100	243	1.227	1.224	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.226	-0.001
105	374	1.227	1.226	-0.001
105	375	1.227	1.225	-0.002
105	Max	1.228	1.228	0.000
105	Average	1.227	1.226	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



5.106_V_REFCAP	
Test Site	
Tester	
Test Number	
Max Limit	1.237 V
Min Limit	1.213 V

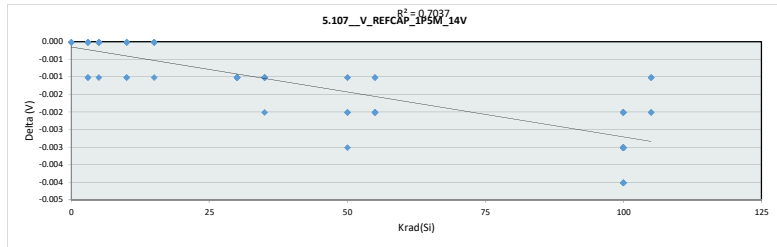
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.223	1.225
Average	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.224	1.225
Max	1.228	1.228	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



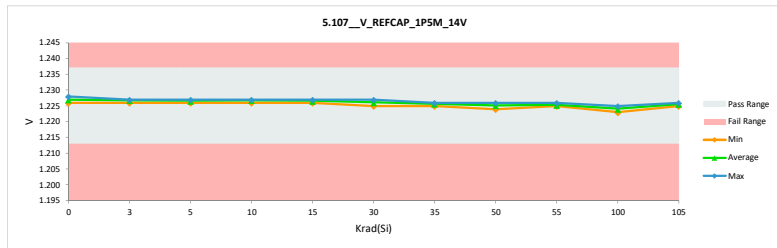
LDR TID Report  
TPS7H5001-SP

		5.107_V_REFCAP_1P5M_14V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.228	1.227	-0.001
3	63	1.227	1.227	0.000
3	64	1.227	1.227	0.000
3	65	1.227	1.227	0.000
3	66	1.228	1.227	-0.001
5	67	1.227	1.226	-0.001
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	0.000
10	73	1.226	1.226	0.000
10	74	1.228	1.227	-0.001
10	75	1.227	1.227	0.000
10	76	1.227	1.227	0.000
10	77	1.227	1.227	0.000
10	78	1.228	1.227	-0.001
15	79	1.227	1.226	-0.001
15	80	1.226	1.226	0.000
15	81	1.227	1.227	0.000
15	82	1.227	1.227	0.000
15	83	1.227	1.227	0.000
15	84	1.227	1.227	0.000
30	86	1.226	1.225	-0.001
30	89	1.228	1.227	-0.001
30	91	1.227	1.226	-0.001
30	92	1.227	1.226	-0.001
30	94	1.227	1.226	-0.001
30	95	1.228	1.227	-0.001
35	100	1.227	1.225	-0.002
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.226	-0.001
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.228	1.226	-0.002
50	111	1.226	1.225	-0.001
55	113	1.227	1.225	-0.002
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.227	1.224	-0.003
100	131	1.227	1.224	-0.003
100	132	1.228	1.224	-0.004
100	133	1.228	1.225	-0.003
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.225	-0.002
100	140	1.227	1.224	-0.003
100	141	1.227	1.223	-0.004
100	142	1.227	1.224	-0.003
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.227	1.223	-0.004
100	221	1.227	1.224	-0.003
100	224	1.227	1.223	-0.004
100	225	1.227	1.225	-0.002
100	226	1.227	1.225	-0.002
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.228	1.225	-0.003
100	236	1.227	1.225	-0.002
100	238	1.227	1.224	-0.003
100	243	1.227	1.224	-0.003
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.226	-0.001
105	374	1.227	1.226	-0.001
105	376	1.227	1.225	-0.002
105	Max	1.228	1.228	0.000
105	Average	1.227	1.226	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.001	0.001	0.001



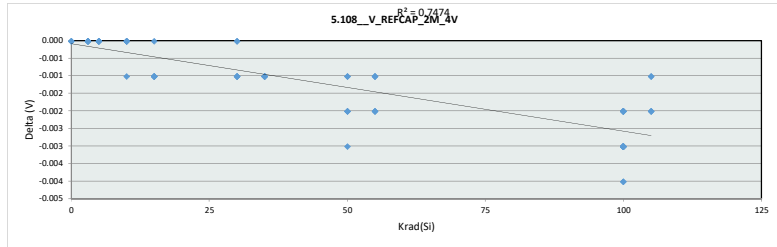
		5.107_V_REFCAP								
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.226	1.226	1.225	1.225	1.224	1.225	1.223
Average	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225	1.225	1.224
Max	1.228	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



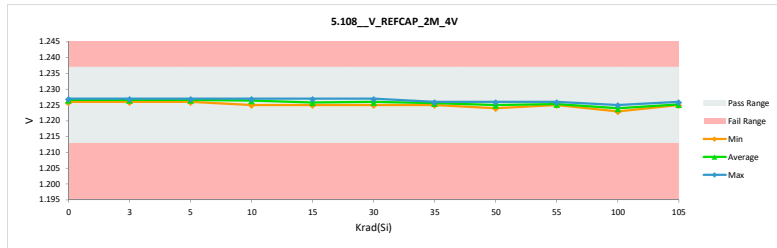
LDR TID Report  
TPS7H5001-SP

		5.108_V_REFCAP_2M_4V	
Test Site			
Tester			
Test Number			
Unit	V	V	
Max Limit	1.237	1.237	
Min Limit	1.213	1.213	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.226	1.226	0.000
0	118	1.227	1.227	0.000
0	122	1.227	1.227	0.000
3	61	1.226	1.226	0.000
3	62	1.227	1.227	0.000
3	63	1.227	1.227	0.000
3	64	1.226	1.226	0.000
3	65	1.227	1.227	0.000
3	66	1.227	1.227	0.000
5	67	1.226	1.226	0.000
5	68	1.226	1.226	0.000
5	69	1.227	1.227	0.000
5	70	1.227	1.227	0.000
5	71	1.227	1.227	0.000
5	72	1.227	1.227	-0.001
10	73	1.226	1.225	-0.001
10	74	1.227	1.227	0.000
10	75	1.227	1.227	0.000
10	76	1.226	1.226	0.000
10	77	1.227	1.227	0.000
10	78	1.227	1.227	0.000
15	79	1.226	1.225	-0.001
15	80	1.226	1.225	-0.001
15	81	1.227	1.227	0.000
15	82	1.227	1.226	-0.001
15	83	1.227	1.226	-0.001
15	84	1.227	1.226	-0.001
30	86	1.226	1.225	-0.001
30	89	1.227	1.226	-0.001
30	91	1.227	1.226	-0.001
30	92	1.226	1.226	0.000
30	94	1.227	1.226	-0.001
30	95	1.227	1.227	0.000
35	100	1.226	1.225	-0.001
35	101	1.226	1.225	-0.001
35	102	1.227	1.226	-0.001
35	103	1.227	1.226	-0.001
35	104	1.227	1.226	-0.001
35	105	1.227	1.226	-0.001
50	106	1.227	1.225	-0.002
50	107	1.227	1.225	-0.002
50	108	1.227	1.224	-0.003
50	109	1.227	1.225	-0.002
50	110	1.227	1.226	-0.001
50	111	1.226	1.225	-0.001
55	113	1.226	1.225	-0.001
55	114	1.227	1.225	-0.002
55	115	1.227	1.225	-0.002
55	116	1.227	1.225	-0.002
55	126	1.227	1.226	-0.001
55	128	1.227	1.226	-0.001
100	129	1.226	1.223	-0.003
100	131	1.227	1.224	-0.003
100	132	1.227	1.224	-0.003
100	133	1.227	1.225	-0.002
100	134	1.227	1.224	-0.003
100	135	1.227	1.223	-0.004
100	138	1.227	1.224	-0.003
100	139	1.227	1.224	-0.003
100	140	1.227	1.224	-0.003
100	141	1.226	1.223	-0.003
100	142	1.226	1.224	-0.002
100	143	1.226	1.224	-0.002
100	144	1.227	1.225	-0.002
100	220	1.226	1.223	-0.003
100	221	1.227	1.224	-0.003
100	224	1.226	1.223	-0.003
100	225	1.227	1.225	-0.002
100	226	1.227	1.224	-0.003
100	228	1.227	1.224	-0.003
100	232	1.227	1.224	-0.003
100	233	1.227	1.225	-0.002
100	236	1.227	1.225	-0.002
100	238	1.227	1.223	-0.004
100	243	1.226	1.224	-0.002
105	244	1.227	1.225	-0.002
105	246	1.227	1.226	-0.001
105	248	1.227	1.225	-0.002
105	374	1.226	1.225	-0.001
105	375	1.227	1.225	-0.002
105	Max	1.227	1.227	0.000
105	Average	1.227	1.225	-0.001
105	Min	1.226	1.223	-0.004
105	Std Dev	0.000	0.001	0.001



		5.108_V_REFCAP								
Test Site										
Tester										
Test Number										
Max Limit	1.237	V								
Min Limit	1.213	V								
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.226	1.226	1.226	1.225	1.225	1.225	1.225	1.224	1.225	1.223
Average	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225	1.224	1.225
Max	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.226	1.225
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

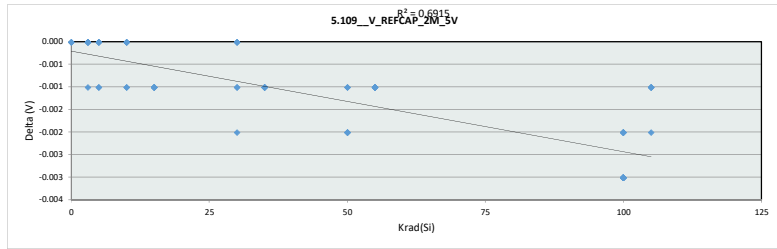




LDR TID Report  
TPS7H5001-SP

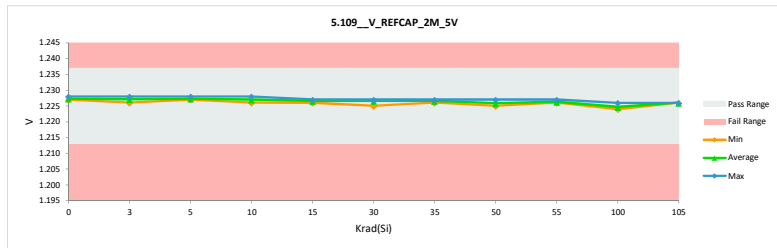
5.109_V_REFCAP_2M_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.227	1.227	0.000
3	61	1.227	1.226	-0.001
3	62	1.228	1.228	0.000
3	63	1.227	1.227	0.000
3	64	1.227	1.227	0.000
3	65	1.227	1.227	0.000
3	66	1.228	1.228	0.000
5	67	1.227	1.227	0.000
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.227	-0.001
5	72	1.228	1.227	-0.001
10	73	1.227	1.226	-0.001
10	74	1.228	1.227	-0.001
10	75	1.227	1.227	0.000
10	76	1.227	1.227	0.000
10	77	1.227	1.227	0.000
10	78	1.228	1.228	0.000
15	79	1.227	1.226	-0.001
15	80	1.227	1.226	-0.001
15	81	1.228	1.227	-0.001
15	82	1.228	1.227	-0.001
15	83	1.228	1.227	-0.001
15	84	1.228	1.227	-0.001
30	86	1.227	1.225	-0.002
30	89	1.228	1.227	-0.001
30	91	1.227	1.227	0.000
30	92	1.227	1.227	0.000
30	94	1.227	1.227	0.000
30	95	1.228	1.227	-0.001
35	100	1.227	1.226	-0.001
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.227	1.226	-0.001
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.228	1.227	-0.001
50	111	1.227	1.225	-0.002
55	113	1.227	1.226	-0.001
55	114	1.227	1.226	-0.001
55	115	1.227	1.226	-0.001
55	116	1.227	1.226	-0.001
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.227	1.224	-0.003
100	131	1.227	1.225	-0.002
100	132	1.228	1.225	-0.003
100	133	1.228	1.226	-0.002
100	134	1.228	1.225	-0.003
100	135	1.227	1.224	-0.003
100	138	1.227	1.224	-0.003
100	139	1.227	1.225	-0.002
100	140	1.228	1.225	-0.003
100	141	1.227	1.224	-0.003
100	142	1.227	1.224	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.227	1.224	-0.003
100	221	1.227	1.225	-0.002
100	224	1.227	1.224	-0.003
100	225	1.228	1.225	-0.003
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.225	-0.003
100	236	1.228	1.225	-0.003
100	238	1.227	1.224	-0.003
100	243	1.227	1.224	-0.003
105	244	1.227	1.226	-0.001
105	246	1.228	1.226	-0.002
105	248	1.227	1.226	-0.001
105	374	1.227	1.226	-0.001
105	376	1.227	1.226	-0.001
105	Max	1.228	1.228	0.000
105	Average	1.227	1.226	-0.001
105	Min	1.227	1.224	-0.003
105	Std Dev	0.001	0.001	0.001



5.109_V_REFCAP	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

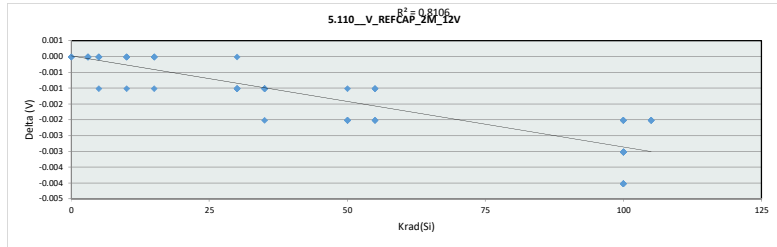
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.226	1.227	1.226	1.226	1.225	1.226	1.225	1.226	1.224	1.226
Average	1.227	1.227	1.227	1.227	1.227	1.227	1.227	1.226	1.226	1.225	1.226
Max	1.228	1.228	1.228	1.228	1.227	1.227	1.227	1.227	1.227	1.226	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



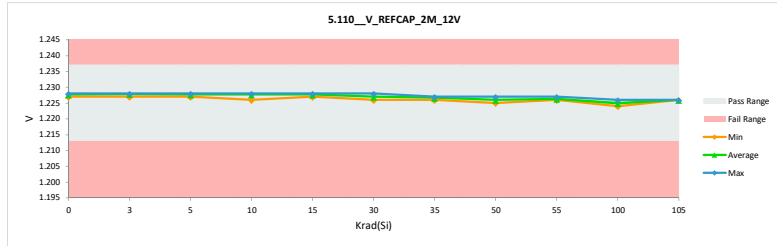
LDR TID Report  
TPS7H5001-SP

5.110_V_REFCAP_2M_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.237
Min Limit	1.213

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.228	1.228	0.000
3	61	1.227	1.227	0.000
3	62	1.228	1.228	0.000
3	63	1.228	1.228	0.000
3	64	1.228	1.228	0.000
3	65	1.228	1.228	0.000
3	66	1.228	1.228	0.000
5	67	1.228	1.227	-0.001
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.228	0.000
5	72	1.228	1.228	0.000
10	73	1.227	1.226	-0.001
10	74	1.228	1.228	0.000
10	75	1.228	1.228	0.000
10	76	1.228	1.228	0.000
10	77	1.228	1.228	0.000
10	78	1.228	1.228	0.000
15	79	1.228	1.227	-0.001
15	80	1.227	1.227	0.000
15	81	1.228	1.228	0.000
15	82	1.228	1.228	0.000
15	83	1.228	1.228	0.000
15	84	1.228	1.228	0.000
30	86	1.227	1.226	-0.001
30	89	1.228	1.227	-0.001
30	91	1.228	1.227	-0.001
30	92	1.228	1.227	-0.001
30	94	1.228	1.227	-0.001
30	95	1.228	1.228	0.000
35	100	1.228	1.226	-0.002
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.228	1.226	-0.002
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.229	1.227	-0.002
50	111	1.227	1.226	-0.001
55	113	1.227	1.226	-0.001
55	114	1.228	1.226	-0.002
55	115	1.228	1.226	-0.002
55	116	1.228	1.226	-0.002
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.228	1.225	-0.003
100	131	1.228	1.225	-0.003
100	132	1.228	1.225	-0.003
100	133	1.229	1.226	-0.003
100	134	1.228	1.225	-0.003
100	135	1.228	1.224	-0.004
100	138	1.228	1.225	-0.003
100	139	1.228	1.225	-0.003
100	140	1.228	1.225	-0.003
100	141	1.228	1.224	-0.004
100	142	1.228	1.225	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.228	1.224	-0.004
100	221	1.228	1.225	-0.003
100	224	1.228	1.224	-0.004
100	225	1.228	1.226	-0.002
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.226	-0.002
100	236	1.228	1.226	-0.002
100	238	1.228	1.224	-0.004
100	243	1.227	1.224	-0.003
105	244	1.228	1.226	-0.002
105	246	1.228	1.226	-0.002
105	248	1.228	1.226	-0.002
105	374	1.228	1.226	-0.002
105	375	1.228	1.226	-0.002
105	Max	1.229	1.228	0.000
105	Average	1.228	1.226	-0.001
105	Min	1.227	1.224	-0.004
105	Std Dev	0.000	0.001	0.001

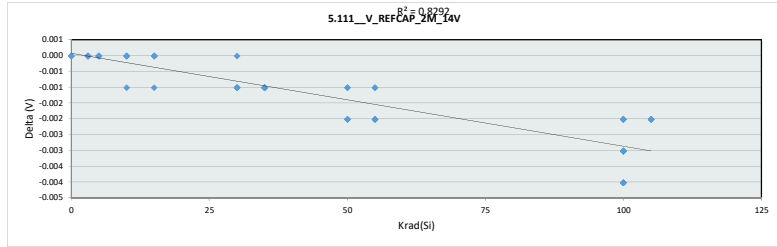


5.110_V_REFCAP										
Test Site										
Tester										
Test Number										
Max Limit	1.237 V									
Min Limit	1.213 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min	1.227	1.227	1.227	1.226	1.227	1.226	1.226	1.225	1.226	1.224
Average	1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.226	1.226	1.225
Max	1.228	1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.227	1.226
UL	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237

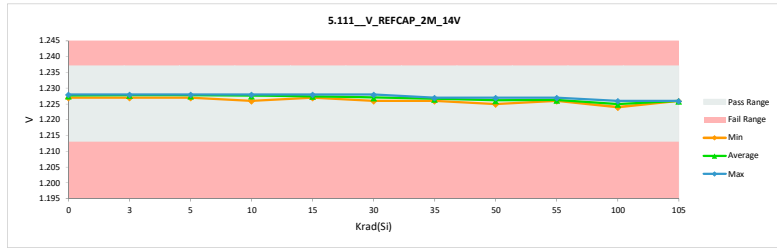


LDR TID Report  
TPS7H5001-SP

5.111_V_REFCAP_2M_14V				
Test Site	Tester	Test Number	Unit	Delta
			V	
Max Limit			1.237	1.237
Min Limit			1.213	1.213
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.227	1.227	0.000
0	118	1.228	1.228	0.000
0	122	1.228	1.228	0.000
3	61	1.227	1.227	0.000
3	62	1.228	1.228	0.000
3	63	1.228	1.228	0.000
3	64	1.228	1.228	0.000
3	65	1.228	1.228	0.000
3	66	1.228	1.228	0.000
5	67	1.227	1.227	0.000
5	68	1.227	1.227	0.000
5	69	1.228	1.228	0.000
5	70	1.228	1.228	0.000
5	71	1.228	1.228	0.000
5	72	1.228	1.228	0.000
10	73	1.227	1.226	-0.001
10	74	1.228	1.228	0.000
10	75	1.228	1.228	0.000
10	76	1.228	1.228	0.000
10	77	1.228	1.228	0.000
10	78	1.228	1.228	0.000
15	79	1.227	1.227	0.000
15	80	1.227	1.227	0.000
15	81	1.228	1.228	0.000
15	82	1.228	1.228	0.000
15	83	1.228	1.227	-0.001
15	84	1.228	1.228	0.000
30	86	1.227	1.226	-0.001
30	89	1.228	1.227	-0.001
30	91	1.228	1.227	-0.001
30	92	1.228	1.227	-0.001
30	94	1.228	1.227	-0.001
30	95	1.228	1.228	0.000
35	100	1.227	1.226	-0.001
35	101	1.227	1.226	-0.001
35	102	1.228	1.227	-0.001
35	103	1.228	1.227	-0.001
35	104	1.228	1.227	-0.001
35	105	1.228	1.227	-0.001
50	106	1.228	1.227	-0.001
50	107	1.228	1.226	-0.002
50	108	1.227	1.225	-0.002
50	109	1.228	1.226	-0.002
50	110	1.229	1.227	-0.002
50	111	1.227	1.226	-0.001
55	113	1.228	1.226	-0.002
55	114	1.228	1.226	-0.002
55	115	1.228	1.226	-0.002
55	116	1.228	1.226	-0.002
55	126	1.228	1.227	-0.001
55	128	1.228	1.227	-0.001
100	129	1.228	1.225	-0.003
100	131	1.228	1.225	-0.003
100	132	1.229	1.225	-0.004
100	133	1.229	1.226	-0.003
100	134	1.228	1.225	-0.003
100	135	1.228	1.224	-0.004
100	138	1.228	1.225	-0.003
100	139	1.228	1.225	-0.003
100	140	1.228	1.225	-0.003
100	141	1.228	1.224	-0.004
100	142	1.228	1.225	-0.003
100	143	1.227	1.225	-0.002
100	144	1.228	1.226	-0.002
100	220	1.228	1.224	-0.004
100	221	1.228	1.225	-0.003
100	224	1.228	1.224	-0.004
100	225	1.228	1.226	-0.002
100	226	1.228	1.225	-0.003
100	228	1.228	1.225	-0.003
100	232	1.228	1.225	-0.003
100	233	1.228	1.226	-0.002
100	236	1.228	1.226	-0.002
100	238	1.228	1.224	-0.004
100	243	1.227	1.224	-0.003
105	244	1.228	1.226	-0.002
105	246	1.228	1.226	-0.002
105	248	1.228	1.226	-0.002
105	374	1.228	1.226	-0.002
105	376	1.228	1.226	-0.002
	Max	1.229	1.228	0.000
	Average	1.228	1.226	-0.001
	Min	1.227	1.224	-0.004
	Std Dev	0.000	0.001	0.001



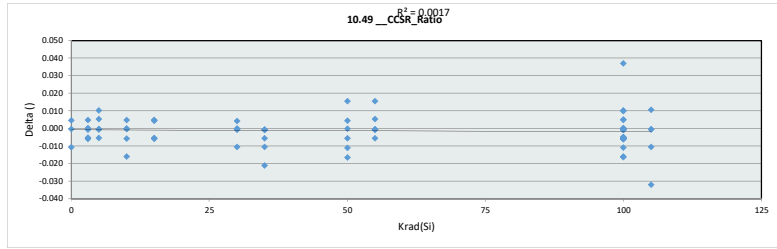
5.111_V_REFCAP														
Test Site	Tester	Test Number	Unit	Max Limit	Min Limit	5	10	15	30	35	50	55	100	105
			V	1.237	1.213									
LL				1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213	1.213
Min				1.227	1.227	1.227	1.226	1.227	1.226	1.226	1.225	1.226	1.224	1.226
Average				1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.226	1.226	1.225	1.226
Max				1.228	1.228	1.228	1.228	1.228	1.228	1.227	1.227	1.227	1.226	1.226
UL				1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237	1.237



LDR TID Report  
TPS7H5001-SP

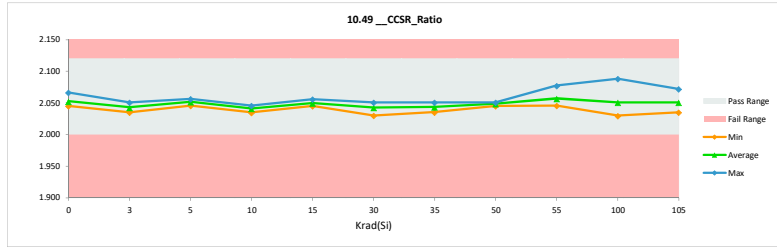
10.49_CCSR_Ratio		
Test Site		
Tester		
Test Number		
Unit		
Max Limit	2.12	2.12
Min Limit	2	2

Krad(Si)	Serial #	PRE	POST	Delta
0	117	2.041	2.046	0.005
0	118	2.067	2.067	0.000
0	122	2.056	2.046	-0.010
3	61	2.041	2.046	0.005
3	62	2.046	2.041	-0.006
3	63	2.046	2.046	0.000
3	64	2.051	2.051	0.000
3	65	2.040	2.036	-0.005
3	66	2.041	2.041	0.000
5	67	2.041	2.046	0.006
5	68	2.056	2.056	0.000
5	69	2.056	2.051	-0.005
5	70	2.046	2.057	0.011
5	71	2.051	2.051	0.000
5	72	2.051	2.051	0.000
10	73	2.041	2.046	0.005
10	74	2.046	2.041	-0.005
10	75	2.046	2.046	0.000
10	76	2.051	2.035	-0.016
10	77	2.040	2.041	0.001
10	78	2.041	2.041	0.000
15	79	2.041	2.046	0.005
15	80	2.056	2.051	-0.005
15	81	2.056	2.051	-0.005
15	82	2.046	2.051	0.005
15	83	2.051	2.046	-0.005
15	84	2.051	2.056	0.005
30	86	2.041	2.041	0.000
30	89	2.046	2.046	0.000
30	91	2.046	2.046	0.000
30	92	2.051	2.051	0.000
30	94	2.040	2.030	-0.010
30	95	2.041	2.046	0.005
35	100	2.041	2.040	-0.001
35	101	2.056	2.036	-0.021
35	102	2.056	2.046	-0.010
35	103	2.046	2.046	0.000
35	104	2.051	2.046	-0.005
35	105	2.051	2.051	0.000
50	106	2.062	2.051	-0.011
50	107	2.041	2.046	0.005
50	108	2.051	2.046	-0.005
50	109	2.035	2.051	0.016
50	110	2.067	2.051	-0.016
50	111	2.051	2.051	0.000
55	113	2.051	2.051	0.000
55	114	2.062	2.062	0.000
55	115	2.051	2.046	-0.005
55	116	2.051	2.051	0.000
55	126	2.062	2.078	0.016
55	128	2.051	2.057	0.006
100	129	2.035	2.046	0.011
100	131	2.062	2.056	-0.006
100	132	2.051	2.046	-0.005
100	133	2.041	2.046	0.005
100	134	2.041	2.041	0.000
100	135	2.046	2.030	-0.016
100	138	2.056	2.057	0.001
100	139	2.062	2.046	-0.016
100	140	2.041	2.040	-0.001
100	141	2.072	2.067	-0.005
100	142	2.067	2.061	-0.006
100	143	2.041	2.041	0.000
100	144	2.088	2.078	-0.010
100	220	2.046	2.046	0.000
100	221	2.051	2.046	-0.005
100	224	2.056	2.057	0.001
100	225	2.035	2.041	0.005
100	226	2.067	2.062	-0.005
100	228	2.051	2.046	-0.004
100	232	2.051	2.089	0.037
100	233	2.046	2.041	-0.005
100	236	2.046	2.041	-0.005
100	238	2.051	2.062	0.011
100	243	2.046	2.046	0.000
105	244	2.072	2.072	0.000
105	246	2.078	2.046	-0.032
105	248	2.046	2.036	-0.010
105	374	2.051	2.062	0.011
105	376	2.041	2.041	0.000
105	Max	2.088	2.089	0.037
105	Average	2.050	2.049	-0.001
105	Min	2.035	2.030	-0.032
105	Std Dev	0.010	0.010	0.009



10.49_CCSR_Ratio		
Test Site		
Tester		
Test Number		
Max Limit	2.12	
Min Limit	2	

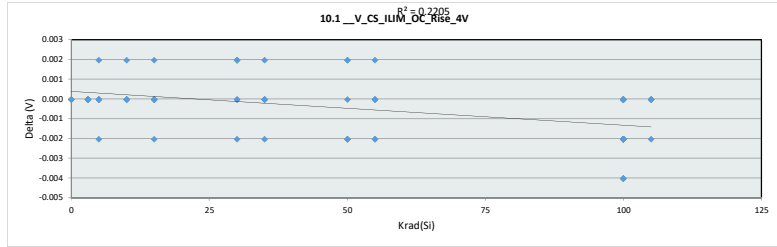
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000	2.000
Min	2.046	2.036	2.046	2.035	2.046	2.030	2.036	2.046	2.046	2.030	2.036
Average	2.053	2.043	2.052	2.042	2.050	2.043	2.044	2.049	2.057	2.051	2.051
Max	2.067	2.051	2.057	2.046	2.056	2.051	2.051	2.051	2.078	2.089	2.072
UL	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120	2.120



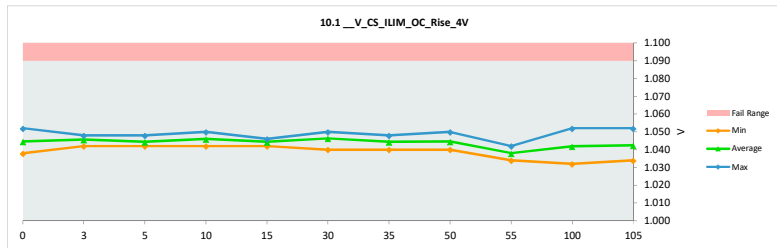
LDR TID Report  
TPS7H5001-SP

10.1 V_CS_ILIM_OC_Rise_4V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	1.09	1.09
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.052	1.052	0.000
0	118	1.038	1.038	0.000
0	122	1.044	1.044	0.000
3	61	1.048	1.048	0.000
3	62	1.046	1.046	0.000
3	63	1.046	1.046	0.000
3	64	1.044	1.044	0.000
3	65	1.042	1.042	0.000
3	66	1.048	1.048	0.000
5	67	1.048	1.048	0.000
5	68	1.046	1.046	0.000
5	69	1.042	1.044	0.002
5	70	1.044	1.044	0.000
5	71	1.042	1.042	0.000
5	72	1.044	1.042	-0.002
10	73	1.048	1.050	0.002
10	74	1.046	1.046	0.000
10	75	1.046	1.046	0.000
10	76	1.044	1.044	0.000
10	77	1.042	1.042	0.000
10	78	1.048	1.048	0.000
15	79	1.048	1.046	-0.002
15	80	1.046	1.046	0.000
15	81	1.042	1.044	0.002
15	82	1.044	1.044	0.000
15	83	1.042	1.042	0.000
15	84	1.044	1.044	0.000
30	86	1.048	1.050	0.002
30	89	1.046	1.046	0.000
30	91	1.046	1.046	0.000
30	92	1.044	1.046	0.002
30	94	1.042	1.040	-0.002
30	95	1.048	1.050	0.002
35	100	1.048	1.048	0.000
35	101	1.046	1.046	0.000
35	102	1.042	1.044	0.002
35	103	1.044	1.044	0.000
35	104	1.042	1.040	-0.002
35	105	1.044	1.044	0.000
50	106	1.050	1.050	0.000
50	107	1.046	1.044	-0.002
50	108	1.046	1.044	-0.002
50	109	1.044	1.042	-0.002
50	110	1.046	1.048	0.002
50	111	1.038	1.040	0.002
55	113	1.036	1.034	-0.002
55	114	1.036	1.036	0.000
55	115	1.038	1.036	-0.002
55	116	1.040	1.040	0.000
55	126	1.042	1.042	0.000
55	128	1.038	1.040	0.002
100	129	1.046	1.044	-0.002
100	131	1.034	1.034	0.000
100	132	1.038	1.036	-0.002
100	133	1.038	1.038	0.000
100	134	1.044	1.044	0.000
100	135	1.046	1.042	-0.004
100	138	1.052	1.050	-0.002
100	139	1.040	1.036	-0.004
100	140	1.040	1.040	0.000
100	141	1.044	1.040	-0.004
100	142	1.034	1.032	-0.002
100	143	1.046	1.044	-0.002
100	144	1.052	1.052	0.000
100	220	1.048	1.046	-0.002
100	221	1.050	1.048	-0.002
100	224	1.042	1.042	0.000
100	225	1.042	1.040	-0.002
100	226	1.042	1.040	-0.002
100	228	1.048	1.044	-0.004
100	232	1.042	1.042	0.000
100	233	1.050	1.048	-0.002
100	236	1.040	1.038	-0.002
100	238	1.042	1.042	0.000
100	243	1.046	1.044	-0.002
105	244	1.038	1.038	0.000
105	246	1.034	1.034	0.000
105	248	1.052	1.052	0.000
105	374	1.040	1.040	0.000
105	376	1.050	1.048	-0.002
105	Max	1.052	1.052	0.002
105	Average	1.044	1.043	-0.001
105	Min	1.034	1.032	-0.004
105	Std Dev	0.004	0.004	0.001



10.1 V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.038	1.042	1.042	1.042	1.042	1.040	1.040	1.040	1.034	1.032	1.034
Average	1.045	1.046	1.044	1.044	1.044	1.046	1.044	1.044	1.045	1.038	1.042
Max	1.052	1.048	1.048	1.050	1.046	1.050	1.048	1.050	1.042	1.052	1.052
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

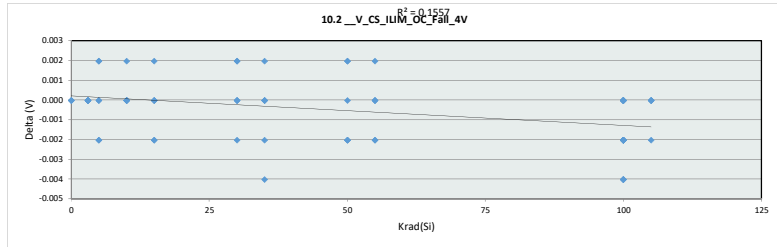


LDR TID Report  
TPS7H5001-SP

10.2 V\_CS\_ILIM\_OC\_Fail\_4V

Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

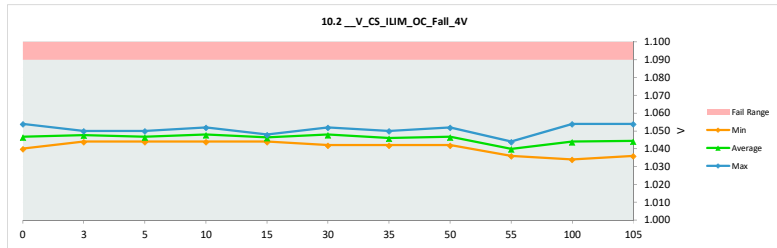
Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.054	1.054	0.000
0	118	1.040	1.040	0.000
0	122	1.046	1.046	0.000
3	61	1.050	1.050	0.000
3	62	1.048	1.048	0.000
3	63	1.048	1.048	0.000
3	64	1.046	1.046	0.000
3	65	1.044	1.044	0.000
3	66	1.050	1.050	0.000
5	67	1.050	1.050	0.000
5	68	1.050	1.048	-0.002
5	69	1.044	1.046	0.002
5	70	1.046	1.048	0.002
5	71	1.044	1.044	0.000
5	72	1.046	1.044	-0.002
10	73	1.050	1.052	0.002
10	74	1.048	1.048	0.000
10	75	1.048	1.048	0.000
10	76	1.046	1.046	0.000
10	77	1.044	1.044	0.000
10	78	1.050	1.050	0.000
15	79	1.050	1.048	-0.002
15	80	1.050	1.048	-0.002
15	81	1.044	1.046	0.002
15	82	1.046	1.046	0.000
15	83	1.044	1.044	0.000
15	84	1.046	1.046	0.000
30	86	1.050	1.052	0.002
30	89	1.048	1.048	0.000
30	91	1.048	1.048	0.000
30	92	1.046	1.048	0.002
30	94	1.044	1.042	-0.002
30	95	1.050	1.050	0.000
35	100	1.050	1.050	0.000
35	101	1.050	1.046	-0.004
35	102	1.044	1.046	0.002
35	103	1.046	1.046	0.000
35	104	1.044	1.042	-0.002
35	105	1.046	1.046	0.000
50	106	1.052	1.052	0.000
50	107	1.048	1.046	-0.002
50	108	1.048	1.046	-0.002
50	109	1.046	1.044	-0.002
50	110	1.048	1.050	0.002
50	111	1.040	1.042	0.002
55	113	1.038	1.036	-0.002
55	114	1.038	1.038	0.000
55	115	1.040	1.038	-0.002
55	116	1.042	1.042	0.000
55	126	1.044	1.044	0.000
55	128	1.040	1.042	0.002
100	129	1.048	1.046	-0.002
100	131	1.036	1.036	0.000
100	132	1.038	1.038	0.000
100	133	1.040	1.040	0.000
100	134	1.046	1.046	0.000
100	135	1.048	1.044	-0.004
100	138	1.054	1.052	-0.002
100	139	1.042	1.040	-0.002
100	140	1.042	1.040	-0.002
100	141	1.046	1.042	-0.004
100	142	1.038	1.034	-0.004
100	143	1.048	1.046	-0.002
100	144	1.054	1.054	0.000
100	220	1.050	1.050	0.000
100	221	1.052	1.050	-0.002
100	224	1.044	1.044	0.000
100	225	1.044	1.042	-0.002
100	226	1.044	1.042	-0.002
100	228	1.050	1.046	-0.004
100	232	1.044	1.044	0.000
100	233	1.052	1.050	-0.002
100	236	1.042	1.040	-0.002
100	238	1.044	1.044	0.000
100	243	1.048	1.046	-0.002
105	244	1.040	1.040	0.000
105	246	1.036	1.036	0.000
105	248	1.054	1.054	0.000
105	374	1.042	1.042	0.000
105	376	1.052	1.050	-0.002
105	Max	1.054	1.054	0.002
105	Average	1.046	1.045	-0.001
105	Min	1.036	1.034	-0.004
105	Std Dev	0.004	0.004	0.002



10.2 V\_CS\_ILIM

Test Site	
Tester	
Test Number	
Max Limit	1.09
Min Limit	V
Min Limit	V

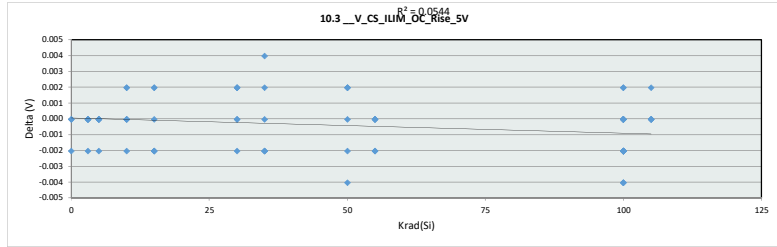
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.040	1.044	1.044	1.044	1.044	1.042	1.042	1.042	1.036	1.034	1.036
Average	1.047	1.048	1.047	1.048	1.046	1.048	1.046	1.047	1.040	1.044	1.044
Max	1.054	1.050	1.050	1.052	1.048	1.052	1.050	1.052	1.044	1.054	1.054
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



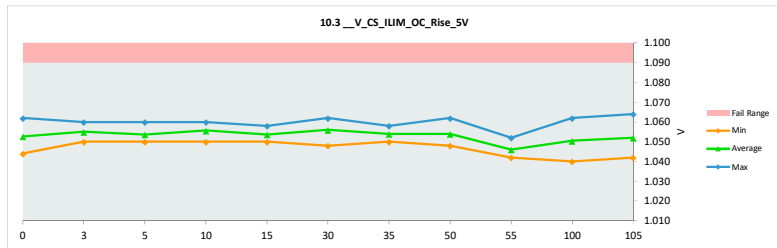
LDR TID Report  
TPS7H5001-SP

10.3 V_CS_ILIM_OC_Rise_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.062	1.062	0.000
0	118	1.046	1.044	-0.002
0	122	1.052	1.052	0.000
3	61	1.058	1.058	0.000
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.054	1.054	0.000
3	65	1.050	1.050	0.000
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.054	1.054	0.000
5	69	1.052	1.052	0.000
5	70	1.056	1.056	0.000
5	71	1.052	1.050	-0.002
5	72	1.050	1.050	0.000
10	73	1.058	1.060	0.002
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.054	1.056	0.002
10	77	1.050	1.050	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.058	-0.002
15	80	1.054	1.052	-0.002
15	81	1.052	1.054	0.002
15	82	1.056	1.058	0.002
15	83	1.052	1.050	-0.002
15	84	1.050	1.050	0.000
30	86	1.058	1.060	0.002
30	89	1.056	1.056	0.000
30	91	1.054	1.054	0.000
30	92	1.054	1.056	0.002
30	94	1.050	1.048	-0.002
30	95	1.060	1.062	0.002
35	100	1.060	1.058	-0.002
35	101	1.054	1.052	-0.002
35	102	1.052	1.054	0.002
35	103	1.056	1.056	0.000
35	104	1.052	1.050	-0.002
35	105	1.050	1.054	0.004
50	106	1.060	1.062	0.002
50	107	1.054	1.054	0.000
50	108	1.054	1.044	-0.010
50	109	1.056	1.052	-0.004
50	110	1.054	1.056	0.002
50	111	1.046	1.048	0.002
55	113	1.044	1.042	-0.002
55	114	1.042	1.042	0.000
55	115	1.048	1.046	-0.002
55	116	1.048	1.048	0.000
55	126	1.052	1.052	0.000
55	128	1.046	1.046	0.000
100	129	1.054	1.052	-0.002
100	131	1.044	1.042	-0.002
100	132	1.044	1.042	-0.002
100	133	1.046	1.046	0.000
100	134	1.050	1.052	0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.050	1.048	-0.002
100	141	1.050	1.048	-0.002
100	142	1.044	1.040	-0.004
100	143	1.054	1.054	0.000
100	144	1.062	1.062	0.000
100	220	1.058	1.058	0.000
100	221	1.062	1.060	-0.002
100	224	1.048	1.048	0.000
100	225	1.050	1.048	-0.002
100	226	1.050	1.048	-0.002
100	228	1.058	1.054	-0.004
100	232	1.050	1.052	0.002
100	233	1.058	1.056	-0.002
100	236	1.046	1.046	0.000
100	238	1.050	1.050	0.000
100	243	1.054	1.052	-0.002
105	244	1.046	1.048	0.002
105	246	1.042	1.042	0.000
105	248	1.064	1.064	0.000
105	374	1.048	1.048	0.000
105	376	1.058	1.058	0.000
105	Max	1.064	1.064	0.004
105	Average	1.053	1.052	0.000
105	Min	1.042	1.040	-0.004
105	Std Dev	0.005	0.006	0.002



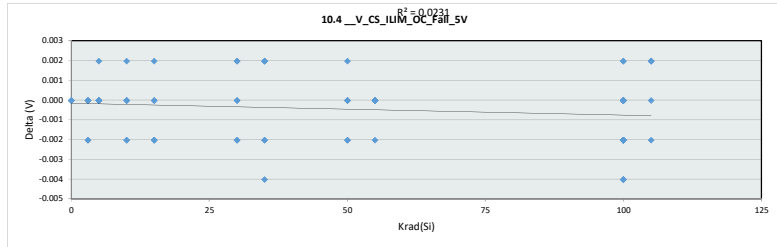
10.3 V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09 V										
Min Limit	V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.050	1.050	1.048	1.050	1.048	1.042	1.040	1.042
Average	1.053	1.055	1.054	1.054	1.054	1.056	1.054	1.054	1.046	1.051	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.062	1.058	1.062	1.052	1.062	1.064
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



LDR TID Report  
TPS7H5001-SP

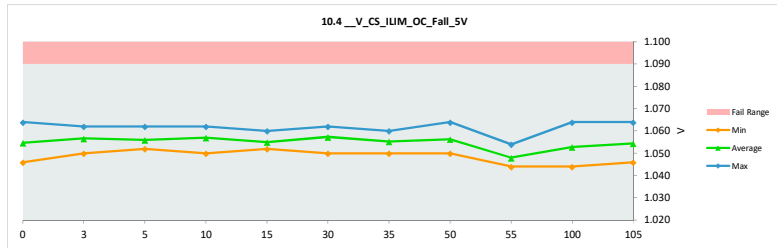
10.4 V_CS_ILIM_OC_Fail_5V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	1.09 1.09
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.064	1.064	0.000
0	118	1.046	1.046	0.000
0	122	1.054	1.054	0.000
3	61	1.060	1.060	0.000
3	62	1.058	1.056	-0.002
3	63	1.056	1.056	0.000
3	64	1.056	1.056	0.000
3	65	1.052	1.050	-0.002
3	66	1.062	1.062	0.000
5	67	1.062	1.062	0.000
5	68	1.056	1.056	0.000
5	69	1.054	1.054	0.000
5	70	1.056	1.058	0.002
5	71	1.054	1.054	0.000
5	72	1.052	1.052	0.000
10	73	1.060	1.060	0.000
10	74	1.058	1.056	-0.002
10	75	1.056	1.056	0.000
10	76	1.056	1.058	0.002
10	77	1.052	1.050	-0.002
10	78	1.062	1.062	0.000
15	79	1.062	1.060	-0.002
15	80	1.056	1.054	-0.002
15	81	1.054	1.054	0.000
15	82	1.056	1.058	0.002
15	83	1.054	1.052	-0.002
15	84	1.052	1.052	0.000
30	86	1.060	1.062	0.002
30	89	1.058	1.056	-0.002
30	91	1.056	1.056	0.000
30	92	1.056	1.058	0.002
30	94	1.052	1.050	-0.002
30	95	1.062	1.062	0.000
35	100	1.062	1.060	-0.002
35	101	1.056	1.054	-0.002
35	102	1.054	1.056	0.002
35	103	1.056	1.058	0.002
35	104	1.054	1.050	-0.004
35	105	1.052	1.054	0.002
50	106	1.064	1.064	0.000
50	107	1.064	1.056	-0.002
50	108	1.056	1.046	-0.000
50	109	1.056	1.054	-0.002
50	110	1.058	1.058	0.000
50	111	1.048	1.050	0.002
55	113	1.046	1.044	-0.002
55	114	1.044	1.044	0.000
55	115	1.048	1.048	0.000
55	116	1.050	1.050	0.000
55	126	1.054	1.054	0.000
55	128	1.048	1.048	0.000
100	129	1.056	1.056	0.000
100	131	1.044	1.044	0.000
100	132	1.046	1.046	0.000
100	133	1.048	1.046	-0.002
100	134	1.054	1.054	0.000
100	135	1.056	1.054	-0.002
100	138	1.064	1.060	-0.004
100	139	1.050	1.046	-0.004
100	140	1.052	1.050	-0.002
100	141	1.052	1.050	-0.002
100	142	1.046	1.044	-0.002
100	143	1.056	1.056	0.000
100	144	1.064	1.064	0.000
100	220	1.058	1.060	0.002
100	221	1.064	1.062	-0.002
100	224	1.052	1.050	-0.002
100	225	1.050	1.052	0.002
100	226	1.052	1.050	-0.002
100	228	1.060	1.056	-0.004
100	232	1.054	1.054	0.000
100	233	1.060	1.058	-0.002
100	236	1.050	1.048	-0.002
100	238	1.052	1.052	0.000
100	243	1.056	1.056	0.000
105	244	1.048	1.050	0.002
105	246	1.044	1.046	0.002
105	248	1.066	1.064	-0.002
105	374	1.050	1.052	0.002
105	376	1.060	1.060	0.000
105	Max	1.066	1.064	0.002
105	Average	1.055	1.054	0.000
105	Min	1.044	1.044	-0.004
105	Std Dev	0.005	0.005	0.002



10.4 V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09 V
Min Limit	V

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.046	1.050	1.052	1.050	1.052	1.050	1.050	1.050	1.044	1.044	1.046
Average	1.055	1.057	1.056	1.057	1.055	1.057	1.055	1.056	1.048	1.053	1.054
Max	1.064	1.062	1.062	1.062	1.060	1.062	1.060	1.064	1.054	1.064	1.064
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

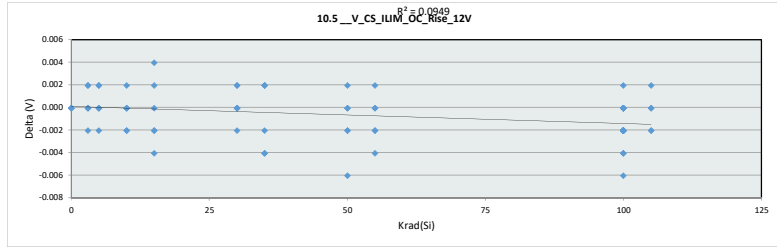




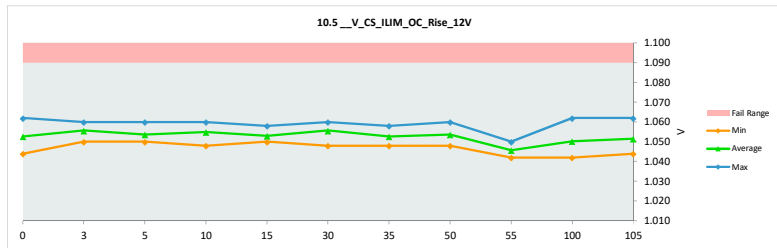
LDR TID Report  
TPS7H5001-SP

10.5 V_CS_ILIM_OC_Rise_12V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	1.09	1.09
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.062	1.062	0.000
0	118	1.044	1.044	0.000
0	122	1.052	1.052	0.000
3	61	1.058	1.060	0.002
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.056	1.056	0.000
3	65	1.048	1.050	0.002
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.054	1.052	-0.002
5	69	1.050	1.052	0.002
5	70	1.054	1.056	0.002
5	71	1.052	1.052	0.000
5	72	1.050	1.050	0.000
10	73	1.058	1.060	0.002
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.056	1.054	-0.002
10	77	1.048	1.048	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.056	-0.004
15	80	1.054	1.052	-0.002
15	81	1.050	1.052	0.002
15	82	1.054	1.058	0.004
15	83	1.052	1.050	-0.002
15	84	1.050	1.050	0.000
30	86	1.058	1.060	0.002
30	89	1.056	1.054	-0.002
30	91	1.054	1.054	0.000
30	92	1.056	1.058	0.002
30	94	1.048	1.048	0.000
30	95	1.060	1.060	0.000
35	100	1.060	1.058	-0.002
35	101	1.054	1.050	-0.004
35	102	1.050	1.052	0.002
35	103	1.054	1.056	0.002
35	104	1.052	1.048	-0.004
35	105	1.050	1.052	0.002
50	106	1.062	1.060	-0.002
50	107	1.056	1.054	-0.002
50	108	1.054	1.054	0.000
50	109	1.056	1.050	-0.006
50	110	1.056	1.056	0.000
50	111	1.046	1.048	0.002
55	113	1.046	1.042	-0.004
55	114	1.040	1.042	0.002
55	115	1.048	1.046	-0.002
55	116	1.048	1.048	0.000
55	126	1.052	1.050	-0.002
55	128	1.046	1.046	0.000
100	129	1.054	1.054	0.000
100	131	1.042	1.042	0.000
100	132	1.044	1.042	-0.002
100	133	1.046	1.044	-0.002
100	134	1.052	1.050	-0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.050	1.048	-0.002
100	141	1.052	1.048	-0.004
100	142	1.042	1.042	0.000
100	143	1.056	1.054	-0.002
100	144	1.060	1.062	0.002
100	220	1.056	1.056	0.000
100	221	1.060	1.058	-0.002
100	224	1.048	1.046	-0.002
100	225	1.050	1.050	0.000
100	226	1.050	1.048	-0.002
100	228	1.058	1.052	-0.006
100	232	1.052	1.052	0.000
100	233	1.058	1.056	-0.002
100	236	1.048	1.046	-0.002
100	238	1.050	1.048	-0.002
100	243	1.054	1.054	0.000
105	244	1.048	1.046	-0.002
105	246	1.042	1.044	0.002
105	248	1.064	1.062	-0.002
105	374	1.048	1.048	0.000
105	376	1.058	1.058	0.000
105	375	1.064	1.062	-0.002
105	Max	1.053	1.052	-0.001
105	Average	1.053	1.052	-0.001
105	Min	1.040	1.042	-0.006
105	Std Dev	0.005	0.005	0.002



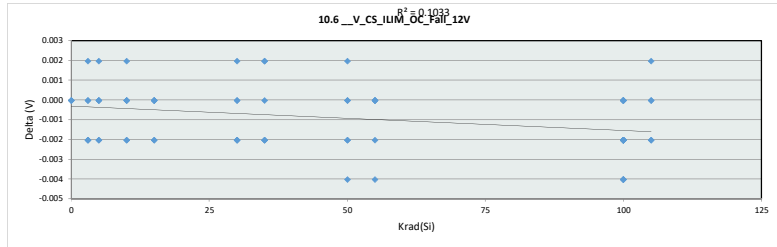
10.5 V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.048	1.050	1.048	1.048	1.048	1.042	1.042	1.044
Average	1.053	1.056	1.054	1.055	1.053	1.056	1.053	1.054	1.046	1.050	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.060	1.058	1.060	1.050	1.062	1.062
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



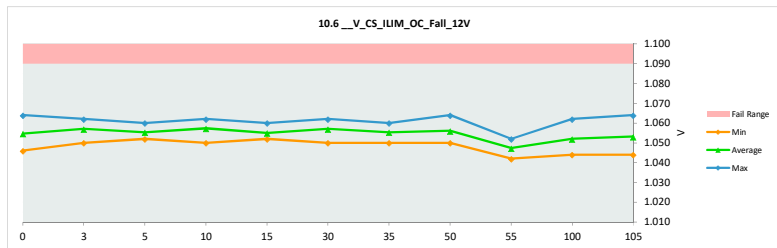
LDR TID Report  
TPS7H5001-SP

10.6 V_CS_ILIM_OC_Fall_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.064	1.064	0.000
0	118	1.046	1.046	0.000
0	122	1.054	1.054	0.000
3	61	1.060	1.062	0.002
3	62	1.058	1.056	-0.002
3	63	1.056	1.056	0.000
3	64	1.058	1.056	-0.002
3	65	1.052	1.050	-0.002
3	66	1.062	1.062	0.000
5	67	1.062	1.060	-0.002
5	68	1.056	1.054	-0.002
5	69	1.054	1.054	0.000
5	70	1.058	1.058	0.000
5	71	1.052	1.054	0.002
5	72	1.052	1.052	0.000
10	73	1.060	1.062	0.002
10	74	1.058	1.056	-0.002
10	75	1.056	1.056	0.000
10	76	1.058	1.058	0.000
10	77	1.052	1.050	-0.002
10	78	1.062	1.062	0.000
15	79	1.062	1.060	-0.002
15	80	1.056	1.054	-0.002
15	81	1.054	1.054	0.000
15	82	1.058	1.058	0.000
15	83	1.052	1.052	0.000
15	84	1.052	1.052	0.000
30	86	1.060	1.062	0.002
30	89	1.058	1.056	-0.002
30	91	1.056	1.054	-0.002
30	92	1.058	1.058	0.000
30	94	1.052	1.050	-0.002
30	95	1.062	1.062	0.000
35	100	1.062	1.060	-0.002
35	101	1.056	1.054	-0.002
35	102	1.054	1.056	0.002
35	103	1.058	1.058	0.000
35	104	1.052	1.050	-0.002
35	105	1.052	1.054	0.002
50	106	1.064	1.064	0.000
50	107	1.058	1.056	-0.002
50	108	1.056	1.054	-0.002
50	109	1.058	1.054	-0.004
50	110	1.058	1.058	0.000
50	111	1.048	1.050	0.002
55	113	1.048	1.044	-0.004
55	114	1.042	1.042	0.000
55	115	1.048	1.048	0.000
55	116	1.050	1.050	0.000
55	126	1.054	1.052	-0.002
55	128	1.048	1.048	0.000
100	129	1.056	1.054	-0.002
100	131	1.044	1.044	0.000
100	132	1.046	1.044	-0.002
100	133	1.048	1.046	-0.002
100	134	1.054	1.054	0.000
100	135	1.056	1.054	-0.002
100	138	1.062	1.060	-0.002
100	139	1.050	1.046	-0.004
100	140	1.052	1.050	-0.002
100	141	1.052	1.048	-0.004
100	142	1.046	1.044	-0.002
100	143	1.056	1.056	0.000
100	144	1.064	1.062	-0.002
100	220	1.058	1.058	0.000
100	221	1.064	1.060	-0.004
100	224	1.050	1.048	-0.002
100	225	1.052	1.050	-0.002
100	226	1.052	1.050	-0.002
100	228	1.060	1.056	-0.004
100	232	1.054	1.054	0.000
100	233	1.060	1.058	-0.002
100	236	1.048	1.046	-0.002
100	238	1.052	1.052	0.000
100	243	1.056	1.054	-0.002
105	244	1.048	1.048	0.000
105	246	1.044	1.044	0.000
105	248	1.066	1.064	-0.002
105	374	1.050	1.052	0.002
105	376	1.066	1.058	-0.002
105	Max	1.066	1.064	0.002
105	Average	1.055	1.054	-0.001
105	Min	1.042	1.042	-0.004
105	Std Dev	0.005	0.005	0.002



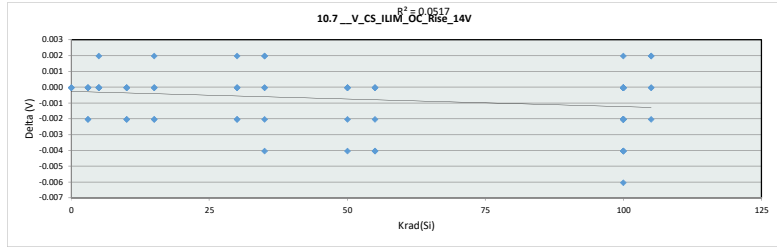
10.6 V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09
Min Limit	V
Krad(Si)	0
LL	0
Min	1.046
Average	1.055
Max	1.064
UL	1.090



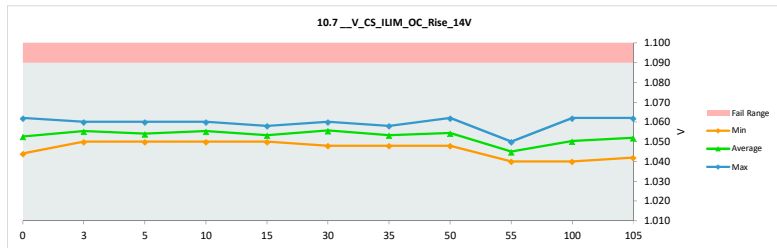
LDR TID Report  
TPS7H5001-SP

10.7_V_CS_ILIM_OC_Rise_14V		
Test Site		
Tester		
Test Number		
Unit	V	V
Max Limit	1.09	1.09
Min Limit		

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.062	1.062	0.000
0	118	1.044	1.044	0.000
0	122	1.052	1.052	0.000
3	61	1.060	1.060	0.000
3	62	1.056	1.054	-0.002
3	63	1.054	1.054	0.000
3	64	1.056	1.054	-0.002
3	65	1.050	1.050	0.000
3	66	1.060	1.060	0.000
5	67	1.060	1.060	0.000
5	68	1.052	1.054	0.002
5	69	1.052	1.052	0.000
5	70	1.056	1.056	0.000
5	71	1.052	1.052	0.000
5	72	1.050	1.050	0.000
10	73	1.060	1.060	0.000
10	74	1.056	1.054	-0.002
10	75	1.054	1.054	0.000
10	76	1.056	1.054	-0.002
10	77	1.050	1.050	0.000
10	78	1.060	1.060	0.000
15	79	1.060	1.058	-0.002
15	80	1.052	1.052	0.000
15	81	1.052	1.052	0.000
15	82	1.056	1.056	0.000
15	83	1.052	1.050	-0.002
15	84	1.050	1.052	0.002
30	86	1.060	1.060	0.000
30	89	1.056	1.054	-0.002
30	91	1.054	1.054	0.000
30	92	1.056	1.058	0.002
30	94	1.050	1.048	-0.002
30	95	1.060	1.060	0.000
35	100	1.060	1.058	-0.002
35	101	1.052	1.052	0.000
35	102	1.052	1.054	0.002
35	103	1.056	1.056	0.000
35	104	1.052	1.048	-0.004
35	105	1.050	1.052	0.002
50	106	1.062	1.062	0.000
50	107	1.056	1.054	-0.002
50	108	1.054	1.054	0.000
50	109	1.056	1.052	-0.004
50	110	1.056	1.056	0.000
50	111	1.048	1.048	0.000
55	113	1.046	1.042	-0.004
55	114	1.040	1.040	0.000
55	115	1.048	1.044	-0.004
55	116	1.048	1.048	0.000
55	126	1.052	1.050	-0.002
55	128	1.046	1.046	0.000
100	129	1.054	1.052	-0.002
100	131	1.042	1.042	0.000
100	132	1.044	1.044	0.000
100	133	1.044	1.044	0.000
100	134	1.052	1.050	-0.002
100	135	1.054	1.052	-0.002
100	138	1.060	1.058	-0.002
100	139	1.048	1.044	-0.004
100	140	1.048	1.048	0.000
100	141	1.052	1.046	-0.006
100	142	1.044	1.040	-0.004
100	143	1.054	1.054	0.000
100	144	1.062	1.062	0.000
100	220	1.056	1.056	0.000
100	221	1.062	1.060	-0.002
100	224	1.048	1.048	0.000
100	225	1.050	1.050	0.000
100	226	1.050	1.048	-0.002
100	228	1.058	1.054	-0.004
100	232	1.052	1.052	0.000
100	233	1.058	1.056	-0.002
100	236	1.048	1.044	-0.004
100	238	1.048	1.050	0.002
100	243	1.056	1.054	-0.002
105	244	1.046	1.048	0.002
105	246	1.042	1.042	0.000
105	248	1.064	1.062	-0.002
105	374	1.048	1.050	0.002
105	376	1.058	1.058	0.000
105	Max	1.064	1.062	0.002
105	Average	1.053	1.052	-0.001
105	Min	1.040	1.040	-0.006
105	Std Dev	0.005	0.006	0.002



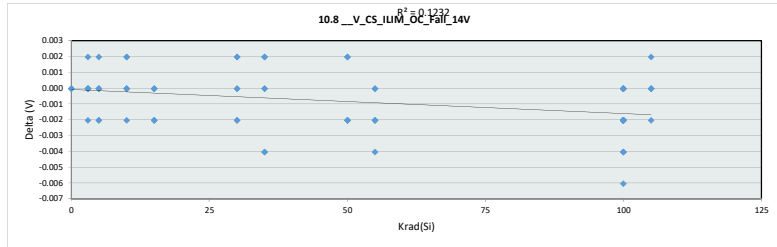
10.7_V_CS_ILIM											
Test Site											
Tester											
Test Number											
Max Limit	1.09	V									
Min Limit		V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	1.044	1.050	1.050	1.050	1.050	1.048	1.048	1.048	1.040	1.040	1.042
Average	1.053	1.055	1.054	1.054	1.053	1.056	1.053	1.054	1.045	1.050	1.052
Max	1.062	1.060	1.060	1.060	1.058	1.060	1.058	1.062	1.050	1.062	1.062
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090



LDR TID Report  
TPS7H5001-SP

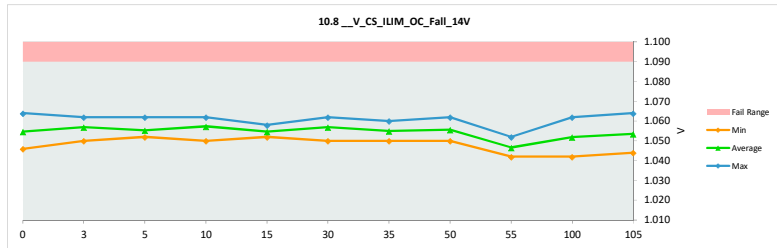
10.8 V_CS_ILIM_OC_Fall_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	1.09
Min Limit	1.09

Krad(Si)	Serial #	PRE	POST	Delta
0	117	1.064	1.064	0.000
0	118	1.046	1.046	0.000
0	122	1.054	1.054	0.000
3	61	1.060	1.062	0.002
3	62	1.056	1.056	0.000
3	63	1.056	1.056	0.000
3	64	1.056	1.056	0.000
3	65	1.052	1.050	-0.002
3	66	1.062	1.062	0.000
5	67	1.060	1.062	0.002
5	68	1.056	1.054	-0.002
5	69	1.054	1.054	0.000
5	70	1.058	1.058	0.000
5	71	1.054	1.052	-0.002
5	72	1.052	1.052	0.000
10	73	1.060	1.062	0.002
10	74	1.056	1.056	0.000
10	75	1.056	1.056	0.000
10	76	1.056	1.058	0.002
10	77	1.052	1.050	-0.002
10	78	1.062	1.062	0.000
15	79	1.060	1.058	-0.002
15	80	1.056	1.054	-0.002
15	81	1.054	1.054	0.000
15	82	1.058	1.058	0.000
15	83	1.054	1.052	-0.002
15	84	1.052	1.052	0.000
30	86	1.060	1.062	0.002
30	89	1.056	1.056	0.000
30	91	1.056	1.054	-0.002
30	92	1.056	1.058	0.002
30	94	1.052	1.050	-0.002
30	95	1.062	1.062	0.000
35	100	1.060	1.060	0.000
35	101	1.056	1.052	-0.004
35	102	1.054	1.056	0.002
35	103	1.058	1.058	0.000
35	104	1.054	1.050	-0.004
35	105	1.052	1.054	0.002
50	106	1.064	1.062	-0.002
50	107	1.058	1.056	-0.002
50	108	1.056	1.054	-0.002
50	109	1.056	1.054	-0.002
50	110	1.056	1.058	0.002
50	111	1.048	1.050	0.002
55	113	1.048	1.044	-0.004
55	114	1.042	1.042	0.000
55	115	1.048	1.046	-0.002
55	116	1.050	1.048	-0.002
55	126	1.054	1.052	-0.002
55	128	1.048	1.048	0.000
100	129	1.056	1.054	-0.002
100	131	1.044	1.044	0.000
100	132	1.046	1.044	-0.002
100	133	1.048	1.046	-0.002
100	134	1.054	1.054	0.000
100	135	1.056	1.054	-0.002
100	138	1.062	1.060	-0.002
100	139	1.050	1.046	-0.004
100	140	1.052	1.050	-0.002
100	141	1.054	1.048	-0.006
100	142	1.044	1.042	-0.002
100	143	1.058	1.056	-0.002
100	144	1.064	1.062	-0.002
100	220	1.058	1.058	0.000
100	221	1.064	1.062	-0.002
100	224	1.050	1.048	-0.002
100	225	1.052	1.050	-0.002
100	226	1.052	1.048	-0.004
100	228	1.060	1.056	-0.004
100	232	1.054	1.054	0.000
100	233	1.060	1.058	-0.002
100	236	1.048	1.046	-0.002
100	238	1.052	1.052	0.000
100	243	1.056	1.056	0.000
105	244	1.048	1.048	0.000
105	246	1.044	1.044	0.000
105	248	1.066	1.064	-0.002
105	374	1.050	1.052	0.002
105	375	1.060	1.060	0.000
105	376	1.066	1.064	-0.002
	Max	1.055	1.054	-0.001
	Average	1.055	1.054	-0.001
	Min	1.042	1.042	-0.006
	Std Dev	0.005	0.006	0.002



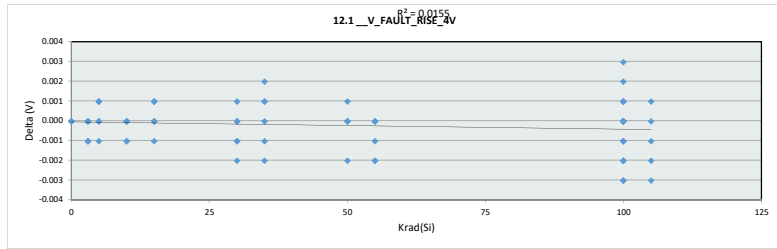
10.8 V_CS_ILIM	
Test Site	
Tester	
Test Number	
Max Limit	1.09
Min Limit	V
Krad(Si)	0
LL	0
Min	1.046
Average	1.055
Max	1.064
UL	1.090

	3	5	10	15	30	35	50	55	100	105
LL										
Min	1.046	1.050	1.052	1.050	1.052	1.050	1.050	1.050	1.042	1.044
Average	1.055	1.057	1.055	1.057	1.055	1.057	1.055	1.056	1.047	1.052
Max	1.064	1.062	1.062	1.062	1.058	1.062	1.060	1.062	1.052	1.062
UL	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090	1.090

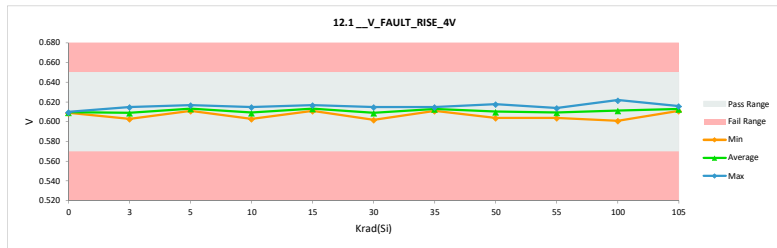


LDR TID Report  
TPS7H5001-SP

12.1_V_FAULT_RISE_4V				
Test Site				
Tester				
Test Number				
Unit	V	V		
Max Limit	0.65	0.65		
Min Limit	0.57	0.57		
Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.610	0.610	0.000
0	122	0.610	0.610	0.000
3	61	0.605	0.604	-0.001
3	62	0.614	0.614	0.000
3	63	0.603	0.603	0.000
3	64	0.615	0.614	-0.001
3	65	0.606	0.605	-0.001
3	66	0.615	0.615	0.000
5	67	0.616	0.615	-0.001
5	68	0.610	0.611	0.001
5	69	0.610	0.611	0.001
5	70	0.616	0.617	0.001
5	71	0.611	0.611	0.000
5	72	0.615	0.615	0.000
10	73	0.605	0.605	0.000
10	74	0.614	0.614	0.000
10	75	0.603	0.603	0.000
10	76	0.615	0.614	-0.001
10	77	0.606	0.605	-0.001
10	78	0.615	0.615	0.000
15	79	0.616	0.615	-0.001
15	80	0.610	0.611	0.001
15	81	0.610	0.611	0.001
15	82	0.616	0.617	0.001
15	83	0.611	0.611	0.000
15	84	0.615	0.615	0.000
30	86	0.605	0.604	-0.001
30	89	0.614	0.615	0.001
30	91	0.603	0.602	-0.001
30	92	0.615	0.613	-0.002
30	94	0.606	0.606	0.000
30	95	0.615	0.615	0.000
35	100	0.616	0.614	-0.002
35	101	0.610	0.612	0.002
35	102	0.610	0.611	0.001
35	103	0.616	0.615	-0.001
35	104	0.611	0.612	0.001
35	105	0.615	0.615	0.000
50	106	0.614	0.614	0.000
50	107	0.612	0.613	0.001
50	108	0.606	0.606	0.000
50	109	0.618	0.618	0.000
50	110	0.606	0.604	-0.002
50	111	0.607	0.607	0.000
55	113	0.606	0.606	0.000
55	114	0.606	0.604	-0.002
55	115	0.612	0.611	-0.001
55	116	0.616	0.614	-0.002
55	126	0.613	0.613	0.000
55	128	0.609	0.609	0.000
100	129	0.610	0.611	0.001
100	131	0.604	0.603	-0.001
100	132	0.606	0.606	0.000
100	133	0.611	0.611	0.000
100	134	0.609	0.606	-0.003
100	135	0.614	0.613	-0.001
100	138	0.611	0.611	0.000
100	139	0.603	0.601	-0.002
100	140	0.607	0.608	0.001
100	141	0.617	0.614	-0.003
100	142	0.616	0.616	0.000
100	143	0.609	0.609	0.000
100	144	0.616	0.614	-0.002
100	220	0.613	0.613	0.000
100	221	0.616	0.616	0.000
100	224	0.602	0.605	0.003
100	225	0.610	0.610	0.000
100	226	0.611	0.610	-0.001
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.614	0.615	0.001
100	238	0.612	0.610	-0.002
100	243	0.617	0.617	0.000
105	244	0.612	0.611	-0.001
105	246	0.610	0.611	0.001
105	248	0.617	0.615	-0.002
105	374	0.613	0.613	0.000
105	376	0.619	0.616	-0.003
105	Max	0.622	0.622	0.003
105	Average	0.611	0.611	0.000
105	Min	0.602	0.601	-0.003
105	Std Dev	0.005	0.005	0.001



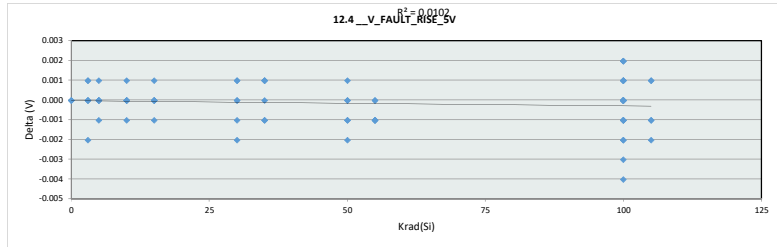
12.1_V_FAULT_RISE_4V											
Test Site											
Tester											
Test Number											
Max Limit	0.65	V									
Min Limit	0.57	V									
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.609	0.603	0.611	0.603	0.611	0.602	0.611	0.604	0.604	0.601	0.611
Average	0.610	0.609	0.613	0.609	0.613	0.609	0.613	0.610	0.610	0.612	0.613
Max	0.610	0.615	0.617	0.615	0.617	0.615	0.615	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



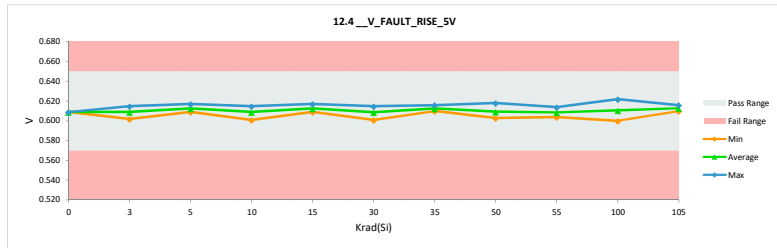
LDR TID Report  
TPS7H5001-SP

12.4_V_FAULT_RISE_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.609	0.609	0.000
0	122	0.609	0.609	0.000
3	61	0.606	0.604	-0.002
3	62	0.613	0.613	0.000
3	63	0.601	0.602	0.001
3	64	0.615	0.615	0.000
3	65	0.605	0.605	0.000
3	66	0.614	0.615	0.001
5	67	0.616	0.615	-0.001
5	68	0.609	0.609	0.000
5	69	0.609	0.610	0.001
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	-0.000
10	73	0.606	0.605	-0.001
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.616	0.000
15	80	0.609	0.609	0.000
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.606	0.604	-0.002
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.609	0.610	0.001
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.604	0.000
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.605	0.000
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.605	-0.003
100	135	0.613	0.612	-0.001
100	138	0.610	0.610	0.000
100	139	0.602	0.600	-0.002
100	140	0.605	0.606	0.001
100	141	0.617	0.613	-0.004
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.613	0.614	0.001
100	221	0.615	0.615	0.000
100	224	0.601	0.603	0.002
100	225	0.610	0.609	-0.001
100	226	0.610	0.610	0.000
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.614	0.002
100	238	0.611	0.610	-0.001
100	243	0.616	0.616	0.000
105	244	0.611	0.610	-0.001
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.613	0.001
105	376	0.618	0.616	-0.002
105	Max	0.622	0.622	0.000
105	Average	0.611	0.611	0.000
105	Min	0.601	0.600	-0.004
105	Std Dev	0.005	0.005	0.001



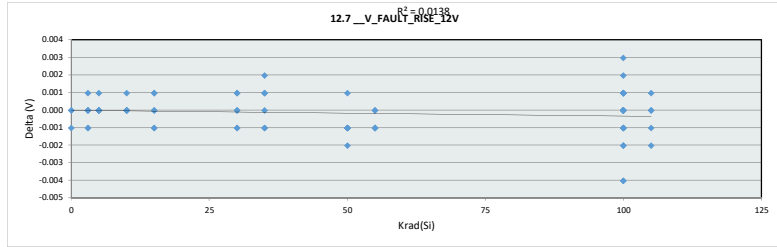
12.4_V_FAULT_R											
Test Site											
Tester											
Test Number											
Max Limit	0.65 V										
Min Limit	0.57 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.609	0.602	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.609	0.609	0.613	0.609	0.613	0.609	0.613	0.610	0.609	0.611	0.613
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



LDR TID Report  
TPS7H5001-SP

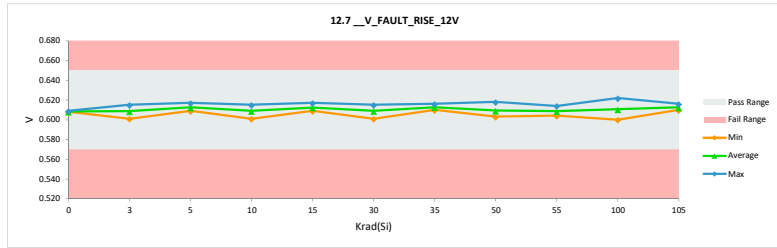
12.7_V_FAULT_RISE_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.609	0.608	-0.001
0	122	0.608	0.608	0.000
3	61	0.605	0.604	-0.001
3	62	0.613	0.613	0.000
3	63	0.601	0.601	0.000
3	64	0.615	0.615	0.000
3	65	0.605	0.604	-0.001
3	66	0.614	0.615	0.001
5	67	0.616	0.616	0.000
5	68	0.608	0.609	0.001
5	69	0.609	0.609	0.000
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	0.000
10	73	0.605	0.605	0.000
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.615	-0.001
15	80	0.608	0.609	0.001
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.605	0.604	-0.001
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.608	0.610	0.002
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.603	-0.001
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.604	-0.001
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.604	-0.004
100	135	0.613	0.612	-0.001
100	138	0.610	0.609	-0.001
100	139	0.601	0.600	-0.001
100	140	0.605	0.606	0.001
100	141	0.617	0.613	-0.004
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.614	0.615	0.001
100	221	0.615	0.615	0.000
100	224	0.600	0.603	0.003
100	225	0.609	0.609	0.000
100	226	0.610	0.610	0.000
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.613	0.001
100	238	0.611	0.609	-0.002
100	243	0.616	0.616	0.000
105	244	0.610	0.610	0.000
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.612	0.000
105	376	0.618	0.616	-0.002
105	375	0.622	0.622	0.000
Max		0.611	0.610	0.000
Average		0.600	0.600	-0.004
Min		0.600	0.600	-0.004
Std Dev		0.005	0.005	0.001



12.7_V_FAULT_R	
Test Site	
Tester	
Test Number	
Max Limit	0.65 V
Min Limit	0.57 V

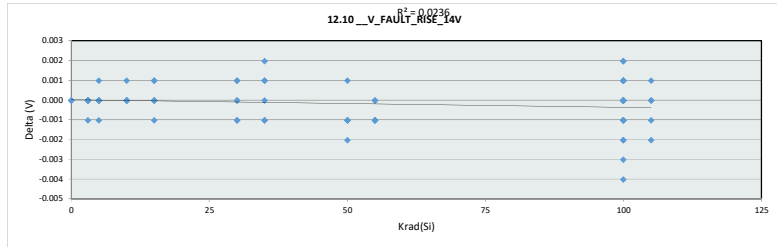
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.608	0.601	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.608	0.609	0.613	0.609	0.612	0.609	0.613	0.609	0.609	0.611	0.613
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650



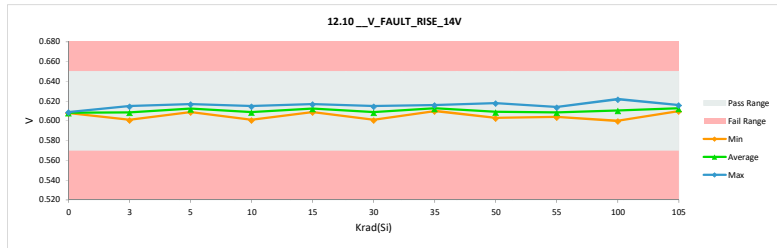
LDR TID Report  
TPS7H5001-SP

12.10_V_FAULT_RISE_14V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.65
Min Limit	0.57

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.609	0.609	0.000
0	118	0.608	0.608	0.000
0	122	0.608	0.608	0.000
3	61	0.605	0.604	-0.001
3	62	0.613	0.613	0.000
3	63	0.601	0.601	0.000
3	64	0.615	0.615	0.000
3	65	0.605	0.605	0.000
3	66	0.614	0.614	0.000
5	67	0.616	0.615	-0.001
5	68	0.608	0.609	0.001
5	69	0.609	0.609	0.000
5	70	0.617	0.617	0.000
5	71	0.610	0.610	0.000
5	72	0.614	0.614	0.000
10	73	0.605	0.605	0.000
10	74	0.613	0.613	0.000
10	75	0.601	0.601	0.000
10	76	0.615	0.615	0.000
10	77	0.605	0.605	0.000
10	78	0.614	0.615	0.001
15	79	0.616	0.616	0.000
15	80	0.608	0.609	0.001
15	81	0.609	0.610	0.001
15	82	0.617	0.617	0.000
15	83	0.610	0.610	0.000
15	84	0.614	0.613	-0.001
30	86	0.605	0.604	-0.001
30	89	0.613	0.614	0.001
30	91	0.601	0.601	0.000
30	92	0.615	0.614	-0.001
30	94	0.605	0.605	0.000
30	95	0.614	0.615	0.001
35	100	0.616	0.615	-0.001
35	101	0.608	0.610	0.002
35	102	0.609	0.610	0.001
35	103	0.617	0.616	-0.001
35	104	0.610	0.611	0.001
35	105	0.614	0.614	0.000
50	106	0.615	0.614	-0.001
50	107	0.612	0.613	0.001
50	108	0.604	0.603	-0.001
50	109	0.619	0.618	-0.001
50	110	0.605	0.603	-0.002
50	111	0.605	0.604	-0.001
55	113	0.605	0.604	-0.001
55	114	0.605	0.604	-0.001
55	115	0.611	0.610	-0.001
55	116	0.615	0.614	-0.001
55	126	0.612	0.612	0.000
55	128	0.608	0.608	0.000
100	129	0.609	0.610	0.001
100	131	0.601	0.600	-0.001
100	132	0.604	0.604	0.000
100	133	0.611	0.611	0.000
100	134	0.608	0.604	-0.004
100	135	0.613	0.612	-0.001
100	138	0.610	0.609	-0.001
100	139	0.602	0.600	-0.002
100	140	0.605	0.606	0.001
100	141	0.617	0.614	-0.003
100	142	0.614	0.614	0.000
100	143	0.608	0.608	0.000
100	144	0.615	0.613	-0.002
100	220	0.614	0.615	0.001
100	221	0.615	0.615	0.000
100	224	0.601	0.603	0.002
100	225	0.609	0.608	-0.001
100	226	0.610	0.609	-0.001
100	228	0.614	0.615	0.001
100	232	0.619	0.621	0.002
100	233	0.622	0.622	0.000
100	236	0.612	0.613	0.001
100	238	0.610	0.610	0.000
100	243	0.616	0.616	0.000
105	244	0.610	0.610	0.000
105	246	0.609	0.610	0.001
105	248	0.616	0.615	-0.001
105	374	0.612	0.612	0.000
105	376	0.618	0.616	-0.002
105	Max	0.622	0.622	0.000
105	Average	0.611	0.610	0.000
105	Min	0.601	0.600	-0.004
105	Std Dev	0.005	0.005	0.001



12.10_V_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	0.65 V										
Min Limit	0.57 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570	0.570
Min	0.608	0.601	0.609	0.601	0.609	0.601	0.610	0.603	0.604	0.600	0.610
Average	0.608	0.609	0.612	0.609	0.613	0.609	0.613	0.609	0.609	0.611	0.613
Max	0.609	0.615	0.617	0.615	0.617	0.615	0.616	0.618	0.614	0.622	0.616
UL	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650	0.650

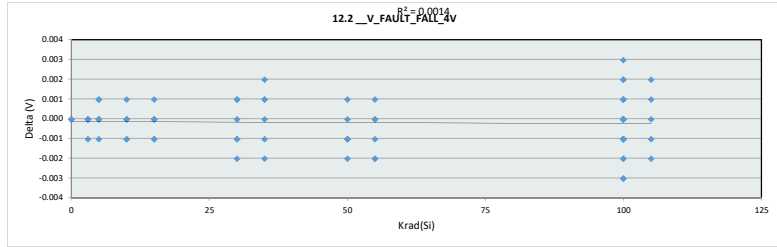




LDR TID Report  
TPS7H5001-SP

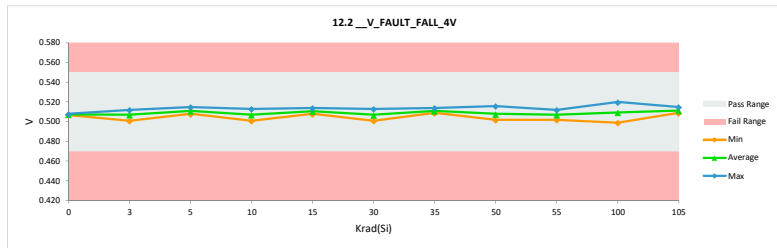
12.2_V_FAULT_FALL_4V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.507	0.507	0.000
0	118	0.508	0.508	0.000
0	122	0.508	0.508	0.000
3	61	0.503	0.503	0.000
3	62	0.512	0.512	0.000
3	63	0.501	0.501	0.000
3	64	0.512	0.512	0.000
3	65	0.504	0.503	-0.001
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.508	0.509	0.001
5	69	0.508	0.508	0.000
5	70	0.514	0.515	0.001
5	71	0.509	0.509	0.000
5	72	0.513	0.513	0.000
10	73	0.503	0.503	0.000
10	74	0.512	0.511	-0.001
10	75	0.501	0.501	0.000
10	76	0.512	0.512	0.000
10	77	0.504	0.503	-0.001
10	78	0.512	0.513	0.001
15	79	0.514	0.513	-0.001
15	80	0.508	0.509	0.001
15	81	0.508	0.508	0.000
15	82	0.514	0.514	0.000
15	83	0.509	0.509	0.000
15	84	0.513	0.512	-0.001
30	86	0.503	0.501	-0.002
30	89	0.512	0.513	0.001
30	91	0.501	0.501	0.000
30	92	0.512	0.511	-0.001
30	94	0.504	0.503	-0.001
30	95	0.512	0.513	0.001
35	100	0.514	0.512	-0.002
35	101	0.508	0.510	0.002
35	102	0.508	0.509	0.001
35	103	0.514	0.514	0.000
35	104	0.509	0.510	0.001
35	105	0.513	0.512	-0.001
50	106	0.512	0.512	0.000
50	107	0.510	0.511	0.001
50	108	0.504	0.503	-0.001
50	109	0.517	0.516	-0.001
50	110	0.504	0.502	-0.002
50	111	0.505	0.504	-0.001
55	113	0.504	0.503	-0.001
55	114	0.504	0.502	-0.002
55	115	0.509	0.509	0.000
55	116	0.514	0.512	-0.002
55	126	0.511	0.511	0.000
55	128	0.506	0.507	0.001
100	129	0.507	0.509	0.002
100	131	0.502	0.501	-0.001
100	132	0.504	0.504	0.000
100	133	0.509	0.509	0.000
100	134	0.507	0.504	-0.003
100	135	0.512	0.511	-0.001
100	138	0.510	0.509	-0.001
100	139	0.501	0.499	-0.002
100	140	0.504	0.505	0.001
100	141	0.515	0.512	-0.003
100	142	0.514	0.513	-0.001
100	143	0.507	0.507	0.000
100	144	0.514	0.512	-0.002
100	220	0.511	0.512	0.001
100	221	0.514	0.514	0.000
100	224	0.500	0.503	0.003
100	225	0.508	0.507	-0.001
100	226	0.508	0.508	0.000
100	228	0.512	0.513	0.001
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.512	0.513	0.001
100	238	0.510	0.509	-0.001
100	243	0.515	0.515	0.000
105	244	0.509	0.509	0.000
105	246	0.507	0.509	0.002
105	248	0.514	0.513	-0.001
105	374	0.510	0.511	0.001
105	376	0.517	0.515	-0.002
105	Max	0.520	0.520	0.003
105	Average	0.509	0.509	0.000
105	Min	0.500	0.499	-0.003
105	Std Dev	0.005	0.005	0.001



12.2_V_FAULT_F	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

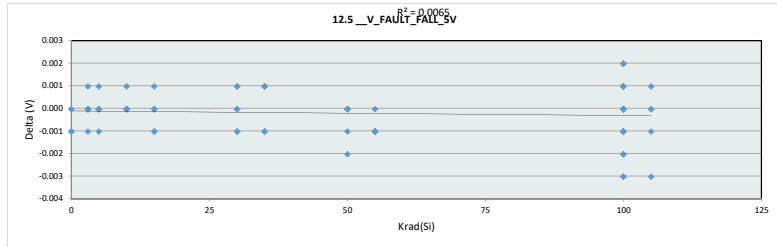
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.507	0.501	0.508	0.501	0.508	0.501	0.509	0.502	0.502	0.499	0.509
Average	0.508	0.507	0.511	0.507	0.511	0.507	0.511	0.508	0.507	0.510	0.511
Max	0.508	0.512	0.515	0.513	0.514	0.513	0.514	0.516	0.512	0.520	0.515
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



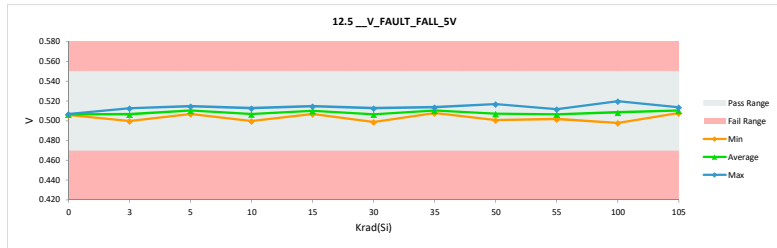
LDR TID Report  
TPS7H5001-SP

12.5_V_FAULT_FALL_5V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.508	0.507	-0.001
0	118	0.507	0.506	-0.001
0	122	0.507	0.507	0.000
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.499	0.500	0.001
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.507	0.507	0.000
5	69	0.507	0.508	0.001
5	70	0.515	0.515	0.000
5	71	0.508	0.508	0.000
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.499	0.500	0.001
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.512	0.000
15	79	0.514	0.513	-0.001
15	80	0.507	0.507	0.000
15	81	0.507	0.507	0.000
15	82	0.515	0.515	0.000
15	83	0.508	0.509	0.001
15	84	0.512	0.511	-0.001
30	86	0.503	0.502	-0.001
30	89	0.511	0.512	0.001
30	91	0.499	0.499	0.000
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.507	0.508	0.001
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.508	0.509	0.001
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.511	0.000
50	108	0.502	0.502	0.000
50	109	0.517	0.517	0.000
50	110	0.503	0.501	-0.002
50	111	0.503	0.502	-0.001
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.511	0.510	-0.001
55	128	0.506	0.506	0.000
100	129	0.508	0.509	0.001
100	131	0.499	0.498	-0.001
100	132	0.502	0.502	0.000
100	133	0.509	0.509	0.000
100	134	0.506	0.503	-0.003
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.512	0.512	0.000
100	143	0.507	0.506	-0.001
100	144	0.513	0.511	-0.002
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.499	0.501	0.002
100	225	0.507	0.507	0.000
100	226	0.508	0.508	0.000
100	228	0.512	0.514	0.002
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.510	0.511	0.001
100	238	0.509	0.508	-0.001
100	243	0.514	0.514	0.000
105	244	0.509	0.508	-0.001
105	246	0.507	0.508	0.001
105	248	0.513	0.513	0.000
105	374	0.510	0.510	0.000
105	376	0.517	0.514	-0.003
105	Max	0.520	0.520	0.002
105	Average	0.509	0.509	0.000
105	Min	0.499	0.498	-0.003
105	Std Dev	0.005	0.005	0.001



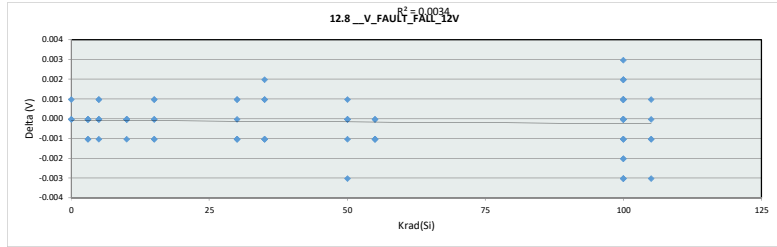
12.5_V_FAULT_F											
Test Site											
Tester											
Test Number											
Max Limit	0.55 V										
Min Limit	0.47 V										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.500	0.507	0.500	0.507	0.499	0.508	0.501	0.502	0.498	0.508
Average	0.507	0.507	0.511	0.507	0.510	0.507	0.511	0.508	0.507	0.509	0.511
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



LDR TID Report  
TPS7H5001-SP

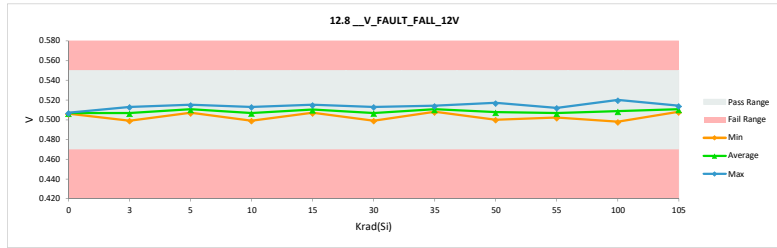
12.8_V_FAULT_FALL_12V	
Test Site	
Tester	
Test Number	
Unit	V
Max Limit	0.55
Min Limit	0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.507	0.507	0.000
0	118	0.506	0.506	0.000
0	122	0.506	0.507	0.001
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.500	0.499	-0.001
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.506	0.507	0.001
5	69	0.507	0.508	0.001
5	70	0.515	0.515	0.000
5	71	0.508	0.508	0.000
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.500	0.499	-0.001
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.512	0.000
15	79	0.514	0.513	-0.001
15	80	0.506	0.507	0.001
15	81	0.507	0.508	0.001
15	82	0.515	0.515	0.000
15	83	0.508	0.508	0.000
15	84	0.512	0.511	-0.001
30	86	0.503	0.502	-0.001
30	89	0.511	0.512	0.001
30	91	0.500	0.499	-0.001
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.506	0.508	0.002
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.508	0.509	0.001
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.512	0.001
50	108	0.502	0.501	-0.001
50	109	0.517	0.517	0.000
50	110	0.503	0.500	-0.003
50	111	0.503	0.503	0.000
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.510	0.510	0.000
55	128	0.506	0.506	0.000
100	129	0.507	0.509	0.002
100	131	0.499	0.498	-0.001
100	132	0.502	0.502	0.000
100	133	0.509	0.510	0.001
100	134	0.506	0.503	-0.003
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.511	0.512	0.001
100	143	0.507	0.506	-0.001
100	144	0.513	0.510	-0.003
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.498	0.501	0.003
100	225	0.508	0.506	-0.002
100	226	0.508	0.508	0.000
100	228	0.512	0.513	0.001
100	232	0.517	0.519	0.002
100	233	0.519	0.520	0.001
100	236	0.509	0.511	0.002
100	238	0.508	0.508	0.000
100	243	0.514	0.514	0.000
105	244	0.508	0.508	0.000
105	246	0.507	0.508	0.001
105	248	0.514	0.513	-0.001
105	374	0.511	0.510	-0.001
105	376	0.517	0.514	-0.003
105	Max	0.519	0.520	0.003
105	Average	0.509	0.508	0.000
105	Min	0.498	0.498	-0.003
105	Std Dev	0.005	0.005	0.001



12.8_V_FAULT_F	
Test Site	
Tester	
Test Number	
Max Limit	0.55 V
Min Limit	0.47 V

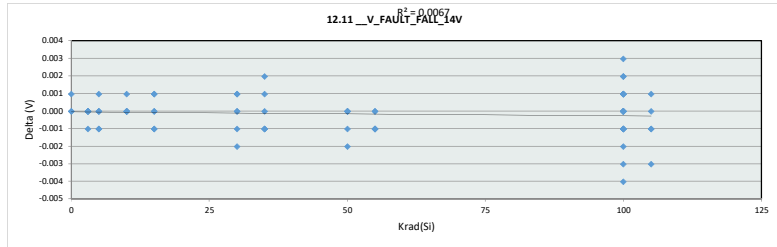
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.499	0.507	0.499	0.507	0.499	0.508	0.500	0.502	0.498	0.508
Average	0.507	0.507	0.511	0.507	0.510	0.507	0.511	0.508	0.507	0.509	0.511
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520	0.514
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



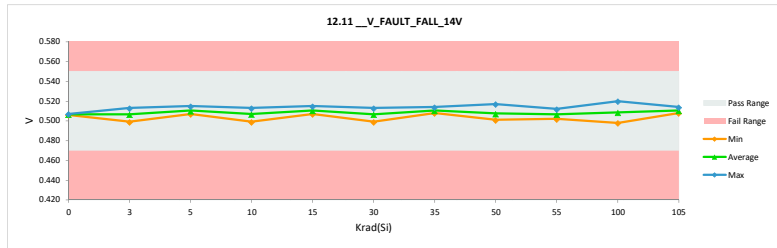
LDR TID Report  
TPS7H5001-SP

12.11_V_FAULT_FALL_14V	
Test Site	
Tester	
Test Number	
Unit	V V
Max Limit	0.55 0.55
Min Limit	0.47 0.47

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.507	0.507	0.000
0	118	0.506	0.507	0.001
0	122	0.506	0.506	0.000
3	61	0.503	0.502	-0.001
3	62	0.511	0.511	0.000
3	63	0.499	0.499	0.000
3	64	0.513	0.513	0.000
3	65	0.503	0.503	0.000
3	66	0.512	0.512	0.000
5	67	0.514	0.513	-0.001
5	68	0.506	0.507	0.001
5	69	0.507	0.507	0.000
5	70	0.515	0.515	0.000
5	71	0.509	0.508	-0.001
5	72	0.512	0.512	0.000
10	73	0.503	0.503	0.000
10	74	0.511	0.511	0.000
10	75	0.499	0.499	0.000
10	76	0.513	0.513	0.000
10	77	0.503	0.503	0.000
10	78	0.512	0.513	0.001
15	79	0.514	0.513	-0.001
15	80	0.506	0.507	0.001
15	81	0.507	0.508	0.001
15	82	0.515	0.515	0.000
15	83	0.509	0.509	0.000
15	84	0.512	0.511	-0.001
30	86	0.503	0.501	-0.002
30	89	0.511	0.512	0.001
30	91	0.499	0.499	0.000
30	92	0.513	0.512	-0.001
30	94	0.503	0.503	0.000
30	95	0.512	0.513	0.001
35	100	0.514	0.513	-0.001
35	101	0.506	0.508	0.002
35	102	0.507	0.508	0.001
35	103	0.515	0.514	-0.001
35	104	0.509	0.509	0.000
35	105	0.512	0.511	-0.001
50	106	0.512	0.512	0.000
50	107	0.511	0.511	0.000
50	108	0.502	0.502	0.000
50	109	0.517	0.517	0.000
50	110	0.503	0.501	-0.002
50	111	0.503	0.502	-0.001
55	113	0.503	0.502	-0.001
55	114	0.503	0.502	-0.001
55	115	0.509	0.508	-0.001
55	116	0.513	0.512	-0.001
55	126	0.510	0.510	0.000
55	128	0.506	0.506	0.000
100	129	0.507	0.508	0.001
100	131	0.499	0.498	-0.001
100	132	0.502	0.501	-0.001
100	133	0.508	0.509	0.001
100	134	0.506	0.502	-0.004
100	135	0.511	0.510	-0.001
100	138	0.508	0.508	0.000
100	139	0.500	0.498	-0.002
100	140	0.503	0.504	0.001
100	141	0.515	0.512	-0.003
100	142	0.512	0.512	0.000
100	143	0.506	0.506	0.000
100	144	0.512	0.511	-0.001
100	220	0.512	0.513	0.001
100	221	0.513	0.513	0.000
100	224	0.498	0.501	0.003
100	225	0.507	0.507	0.000
100	226	0.508	0.508	0.000
100	228	0.512	0.514	0.002
100	232	0.517	0.519	0.002
100	233	0.520	0.520	0.000
100	236	0.510	0.511	0.001
100	238	0.509	0.508	-0.001
100	243	0.514	0.514	0.000
105	244	0.509	0.508	-0.001
105	246	0.507	0.508	0.001
105	248	0.514	0.513	-0.001
105	374	0.510	0.510	0.000
105	376	0.517	0.514	-0.003
105	Max	0.520	0.520	0.003
105	Average	0.509	0.508	0.000
105	Min	0.498	0.498	-0.004
105	Std Dev	0.005	0.005	0.001



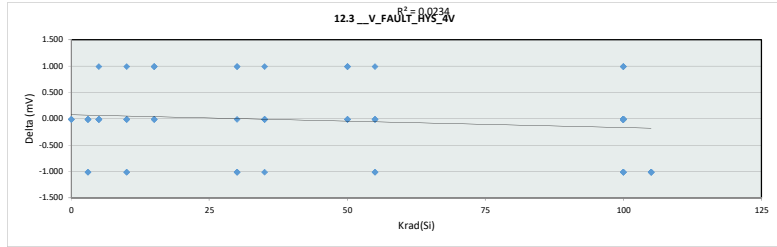
12.11_V_FAULT										
Test Site										
Tester										
Test Number										
Max Limit	0.55 V									
Min Limit	0.47 V									
Krad(Si)	0	5	10	15	30	35	50	55	100	105
LL	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470	0.470
Min	0.506	0.499	0.507	0.499	0.507	0.499	0.508	0.501	0.502	0.498
Average	0.507	0.507	0.510	0.507	0.511	0.507	0.511	0.508	0.507	0.509
Max	0.507	0.513	0.515	0.513	0.515	0.513	0.514	0.517	0.512	0.520
UL	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550	0.550



LDR TID Report  
TPS7H5001-SP

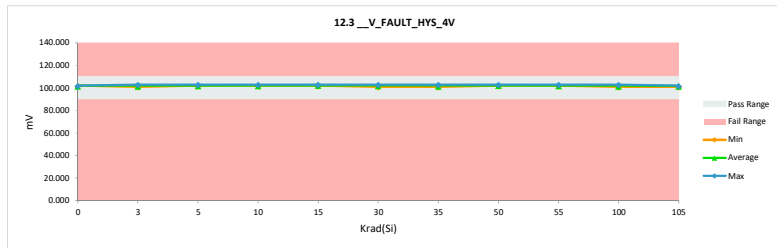
12.3_V_FAULT_HYS_4V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	102.000	102.000	0.000
0	118	102.000	102.000	0.000
0	122	102.000	102.000	0.000
3	61	102.000	101.000	-1.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	103.000	102.000	-1.000
3	65	102.000	102.000	0.000
3	66	103.000	103.000	0.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	103.000	1.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	103.000	1.000
10	75	102.000	102.000	0.000
10	76	103.000	102.000	-1.000
10	77	102.000	102.000	0.000
10	78	103.000	102.000	-1.000
15	79	102.000	102.000	0.000
15	80	102.000	102.000	0.000
15	81	102.000	103.000	1.000
15	82	102.000	103.000	1.000
15	83	102.000	102.000	0.000
15	84	102.000	103.000	1.000
30	86	102.000	103.000	1.000
30	89	102.000	102.000	0.000
30	91	102.000	101.000	-1.000
30	92	103.000	102.000	-1.000
30	94	102.000	103.000	1.000
30	95	103.000	102.000	-1.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	101.000	-1.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	102.000	102.000	0.000
50	107	102.000	102.000	0.000
50	108	102.000	103.000	1.000
50	109	101.000	102.000	1.000
50	110	102.000	102.000	0.000
50	111	102.000	103.000	1.000
55	113	102.000	103.000	1.000
55	114	102.000	102.000	0.000
55	115	103.000	102.000	-1.000
55	116	102.000	102.000	0.000
55	126	102.000	102.000	0.000
55	128	103.000	102.000	-1.000
100	129	103.000	102.000	-1.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	102.000	0.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	101.000	102.000	1.000
100	139	102.000	102.000	0.000
100	140	103.000	103.000	0.000
100	141	102.000	102.000	0.000
100	142	102.000	103.000	1.000
100	143	102.000	102.000	0.000
100	144	102.000	102.000	0.000
100	220	102.000	101.000	-1.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	102.000	103.000	1.000
100	226	103.000	102.000	-1.000
100	228	102.000	102.000	0.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	102.000	0.000
100	238	102.000	101.000	-1.000
100	243	102.000	102.000	0.000
105	244	103.000	102.000	-1.000
105	246	103.000	102.000	-1.000
105	248	103.000	102.000	-1.000
105	374	103.000	102.000	-1.000
105	376	102.000	101.000	-1.000
105	Max	103.000	103.000	1.000
105	Average	102.163	102.113	-0.050
105	Min	101.000	101.000	-1.000
105	Std Dev	0.434	0.503	0.654



12.3_V_FAULT_HYS_4V	
Test Site	
Tester	
Test Number	
Max Limit	110 mV
Min Limit	90 mV

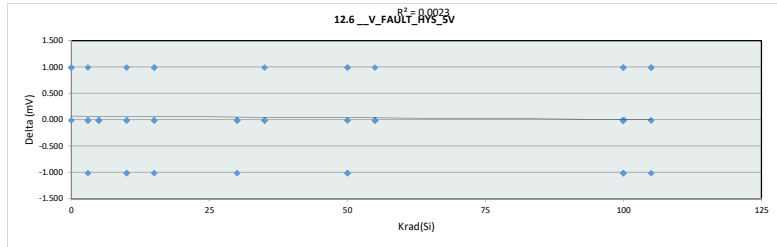
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	101.000	102.000	102.000	102.000	101.000	101.000	102.000	102.000	101.000	101.000
Average	102.000	102.000	102.167	102.167	102.500	102.167	102.000	102.333	102.167	102.042	101.800
Max	102.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



LDR TID Report  
TPS7H5001-SP

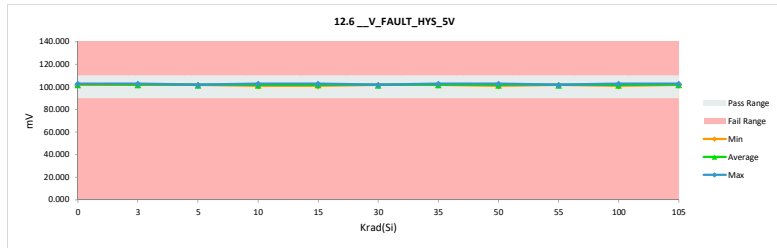
12.6_V_FAULT_HYS_5V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	101.000	102.000	1.000
0	118	102.000	103.000	1.000
0	122	102.000	102.000	0.000
3	61	103.000	102.000	-1.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	102.000	102.000	0.000
3	65	102.000	102.000	0.000
3	66	102.000	103.000	1.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	102.000	0.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	103.000	102.000	-1.000
10	74	102.000	102.000	0.000
10	75	102.000	101.000	-1.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	103.000	1.000
15	79	102.000	103.000	1.000
15	80	102.000	102.000	0.000
15	81	102.000	103.000	1.000
15	82	102.000	102.000	0.000
15	83	102.000	101.000	-1.000
15	84	102.000	102.000	0.000
30	86	103.000	102.000	-1.000
30	89	102.000	102.000	0.000
30	91	102.000	102.000	0.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	102.000	1.000
50	108	102.000	102.000	0.000
50	109	102.000	101.000	-1.000
50	110	102.000	102.000	0.000
50	111	102.000	103.000	1.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	126	101.000	102.000	1.000
55	128	102.000	102.000	0.000
100	129	101.000	101.000	0.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	102.000	0.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	102.000	102.000	0.000
100	139	102.000	102.000	0.000
100	140	102.000	102.000	0.000
100	141	102.000	101.000	-1.000
100	142	102.000	102.000	0.000
100	143	101.000	102.000	1.000
100	144	102.000	102.000	0.000
100	220	101.000	101.000	0.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	103.000	102.000	-1.000
100	226	102.000	102.000	0.000
100	228	102.000	101.000	-1.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	103.000	1.000
100	238	102.000	102.000	0.000
100	243	102.000	102.000	0.000
105	244	102.000	102.000	0.000
105	246	102.000	102.000	0.000
105	248	103.000	102.000	-1.000
105	374	102.000	103.000	1.000
105	375	101.000	102.000	1.000
105	Max	103.000	103.000	1.000
105	Average	101.988	102.025	0.038
105	Min	101.000	101.000	-1.000
105	Std Dev	0.405	0.449	0.561



12.6_V_FAULT_HYS_5V	
Test Site	
Tester	
Test Number	
Max Limit	110 mV
Min Limit	90 mV

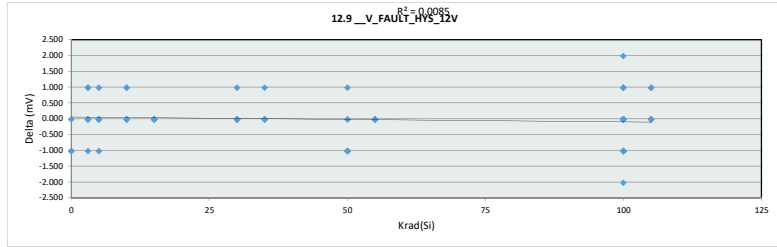
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	102.000	102.000	102.000	101.000	101.000	102.000	102.000	101.000	102.000	101.000	102.000
Average	102.333	102.167	102.000	102.000	102.167	102.000	102.167	102.000	102.000	101.875	102.200
Max	103.000	103.000	102.000	103.000	103.000	102.000	103.000	103.000	102.000	103.000	103.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



LDR TID Report  
TPS7H5001-SP

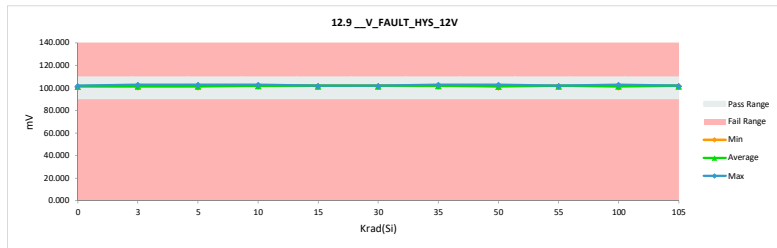
12.9_V_FAULT_HYS_12V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	102.000	102.000	0.000
0	118	103.000	102.000	-1.000
0	122	102.000	101.000	-1.000
3	61	102.000	102.000	0.000
3	62	102.000	102.000	0.000
3	63	101.000	102.000	1.000
3	64	102.000	102.000	0.000
3	65	102.000	101.000	-1.000
3	66	102.000	103.000	1.000
5	67	102.000	103.000	1.000
5	68	102.000	102.000	0.000
5	69	102.000	101.000	-1.000
5	70	102.000	102.000	0.000
5	71	102.000	102.000	0.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	102.000	0.000
10	75	101.000	102.000	1.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	103.000	1.000
15	79	102.000	102.000	0.000
15	80	102.000	102.000	0.000
15	81	102.000	102.000	0.000
15	82	102.000	102.000	0.000
15	83	102.000	102.000	0.000
15	84	102.000	102.000	0.000
30	86	102.000	102.000	0.000
30	89	102.000	102.000	0.000
30	91	101.000	102.000	1.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	102.000	102.000	0.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	101.000	0.000
50	108	102.000	102.000	0.000
50	109	102.000	101.000	-1.000
50	110	102.000	103.000	1.000
50	111	102.000	101.000	-1.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	126	102.000	102.000	0.000
55	128	102.000	102.000	0.000
100	129	102.000	101.000	-1.000
100	131	102.000	102.000	0.000
100	132	102.000	102.000	0.000
100	133	102.000	101.000	-1.000
100	134	102.000	101.000	-1.000
100	135	102.000	102.000	0.000
100	138	102.000	101.000	-1.000
100	139	101.000	102.000	1.000
100	140	102.000	102.000	0.000
100	141	102.000	101.000	-1.000
100	142	103.000	102.000	-1.000
100	143	101.000	102.000	1.000
100	144	102.000	103.000	1.000
100	220	102.000	102.000	0.000
100	221	102.000	102.000	0.000
100	224	102.000	102.000	0.000
100	225	101.000	103.000	2.000
100	226	102.000	102.000	0.000
100	228	102.000	102.000	0.000
100	232	102.000	102.000	0.000
100	233	103.000	102.000	-1.000
100	236	103.000	102.000	-1.000
100	238	103.000	101.000	-2.000
100	243	102.000	102.000	0.000
105	244	102.000	102.000	0.000
105	246	102.000	102.000	0.000
105	248	102.000	102.000	0.000
105	374	101.000	102.000	1.000
105	376	101.000	102.000	1.000
105	Max	103.000	103.000	2.000
105	Average	101.963	101.938	-0.025
105	Min	101.000	101.000	-2.000
105	Std Dev	0.434	0.486	0.675



12.9_V_FAULT_HYS_12V	
Test Site	
Tester	
Test Number	
Max Limit	110 mV
Min Limit	90 mV

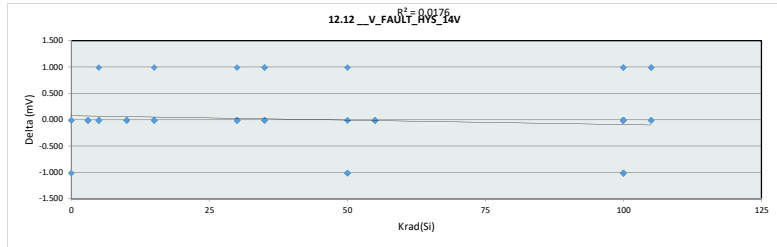
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	101.000	101.000	102.000	102.000	102.000	102.000	101.000	102.000	101.000	102.000
Average	101.667	102.000	102.000	102.167	102.000	102.000	102.167	101.667	102.000	101.833	102.000
Max	102.000	103.000	103.000	103.000	102.000	102.000	103.000	103.000	102.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000



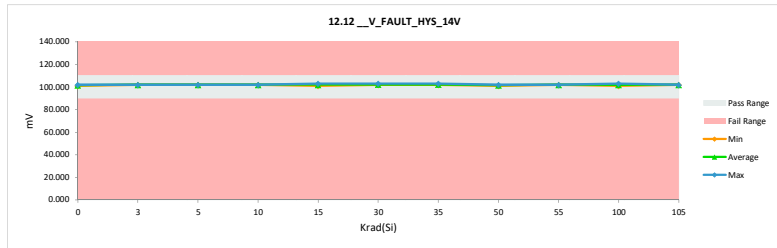
LDR TID Report  
TPS7H5001-SP

12.12 V_FAULT_HYS_14V	
Test Site	
Tester	
Test Number	
Unit	mV
Max Limit	110
Min Limit	90

Krad(Si)	Serial #	PRE	POST	Delta
0	117	102.000	102.000	0.000
0	118	102.000	101.000	-1.000
0	122	102.000	102.000	0.000
3	61	102.000	102.000	0.000
3	62	102.000	102.000	0.000
3	63	102.000	102.000	0.000
3	64	102.000	102.000	0.000
3	65	102.000	102.000	0.000
3	66	102.000	102.000	0.000
5	67	102.000	102.000	0.000
5	68	102.000	102.000	0.000
5	69	102.000	102.000	0.000
5	70	102.000	102.000	0.000
5	71	101.000	102.000	1.000
5	72	102.000	102.000	0.000
10	73	102.000	102.000	0.000
10	74	102.000	102.000	0.000
10	75	102.000	102.000	0.000
10	76	102.000	102.000	0.000
10	77	102.000	102.000	0.000
10	78	102.000	102.000	0.000
15	79	102.000	103.000	1.000
15	80	102.000	102.000	0.000
15	81	102.000	102.000	0.000
15	82	102.000	102.000	0.000
15	83	101.000	101.000	0.000
15	84	102.000	102.000	0.000
30	86	102.000	103.000	1.000
30	89	102.000	102.000	0.000
30	91	102.000	102.000	0.000
30	92	102.000	102.000	0.000
30	94	102.000	102.000	0.000
30	95	102.000	102.000	0.000
35	100	102.000	102.000	0.000
35	101	102.000	102.000	0.000
35	102	102.000	102.000	0.000
35	103	102.000	102.000	0.000
35	104	101.000	102.000	1.000
35	105	102.000	103.000	1.000
50	106	103.000	102.000	-1.000
50	107	101.000	102.000	1.000
50	108	102.000	101.000	-1.000
50	109	102.000	101.000	-1.000
50	110	102.000	102.000	0.000
50	111	102.000	102.000	0.000
55	113	102.000	102.000	0.000
55	114	102.000	102.000	0.000
55	115	102.000	102.000	0.000
55	116	102.000	102.000	0.000
55	126	102.000	102.000	0.000
55	128	102.000	102.000	0.000
100	129	102.000	102.000	0.000
100	131	102.000	102.000	0.000
100	132	102.000	103.000	1.000
100	133	103.000	102.000	-1.000
100	134	102.000	102.000	0.000
100	135	102.000	102.000	0.000
100	138	102.000	101.000	-1.000
100	139	102.000	102.000	0.000
100	140	102.000	102.000	0.000
100	141	102.000	102.000	0.000
100	142	102.000	102.000	0.000
100	143	102.000	102.000	0.000
100	144	103.000	102.000	-1.000
100	220	102.000	102.000	0.000
100	221	102.000	102.000	0.000
100	224	103.000	102.000	-1.000
100	225	102.000	101.000	-1.000
100	226	102.000	101.000	-1.000
100	228	102.000	101.000	-1.000
100	232	102.000	102.000	0.000
100	233	102.000	102.000	0.000
100	236	102.000	102.000	0.000
100	238	101.000	102.000	1.000
100	243	102.000	102.000	0.000
105	244	101.000	102.000	1.000
105	246	102.000	102.000	0.000
105	248	102.000	102.000	0.000
105	374	102.000	102.000	0.000
105	375	101.000	102.000	1.000
105	Max	103.000	103.000	1.000
105	Average	101.963	101.950	-0.013
105	Min	101.000	101.000	-1.000
105	Std Dev	0.371	0.386	0.515



12.12 V_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	110 mV										
Min Limit	90 mV										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000	90.000
Min	101.000	102.000	102.000	102.000	101.000	102.000	102.000	101.000	102.000	101.000	102.000
Average	101.667	102.000	102.000	102.000	102.000	102.167	102.167	101.667	102.000	101.875	102.000
Max	102.000	102.000	102.000	102.000	103.000	103.000	103.000	102.000	102.000	103.000	102.000
UL	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000	110.000

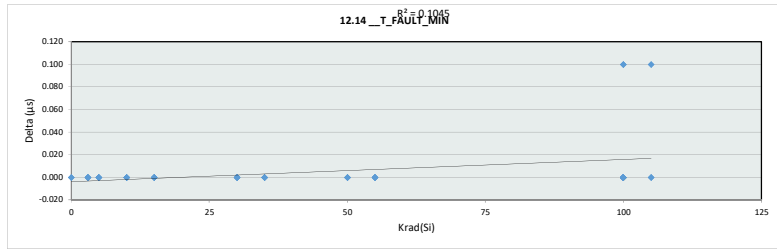




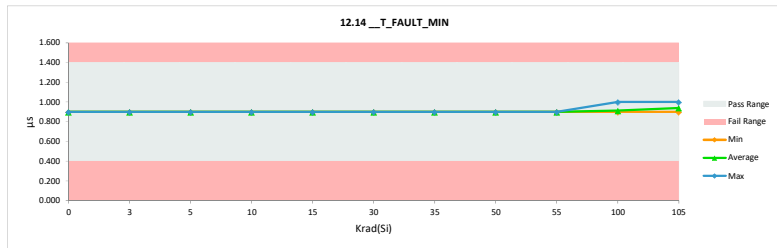
LDR TID Report  
TPS7H5001-SP

12.14 T_FAULT_MIN	
Test Site	
Tester	
Test Number	
Unit	µs
Max Limit	1.4
Min Limit	0.4

Krad(Si)	Serial #	PRE	POST	Delta
0	117	0.900	0.900	0.000
0	118	0.900	0.900	0.000
0	122	0.900	0.900	0.000
3	61	0.900	0.900	0.000
3	62	0.900	0.900	0.000
3	63	0.900	0.900	0.000
3	64	0.900	0.900	0.000
3	65	0.900	0.900	0.000
3	66	0.900	0.900	0.000
5	67	0.900	0.900	0.000
5	68	0.900	0.900	0.000
5	69	0.900	0.900	0.000
5	70	0.900	0.900	0.000
5	71	0.900	0.900	0.000
5	72	0.900	0.900	0.000
10	73	0.900	0.900	0.000
10	74	0.900	0.900	0.000
10	75	0.900	0.900	0.000
10	76	0.900	0.900	0.000
10	77	0.900	0.900	0.000
10	78	0.900	0.900	0.000
15	79	0.900	0.900	0.000
15	80	0.900	0.900	0.000
15	81	0.900	0.900	0.000
15	82	0.900	0.900	0.000
15	83	0.900	0.900	0.000
15	84	0.900	0.900	0.000
30	86	0.900	0.900	0.000
30	89	0.900	0.900	0.000
30	91	0.900	0.900	0.000
30	92	0.900	0.900	0.000
30	94	0.900	0.900	0.000
30	95	0.900	0.900	0.000
35	100	0.900	0.900	0.000
35	101	0.900	0.900	0.000
35	102	0.900	0.900	0.000
35	103	0.900	0.900	0.000
35	104	0.900	0.900	0.000
35	105	0.900	0.900	0.000
50	106	0.900	0.900	0.000
50	107	0.900	0.900	0.000
50	108	0.900	0.900	0.000
50	109	0.900	0.900	0.000
50	110	0.900	0.900	0.000
50	111	0.900	0.900	0.000
55	113	0.900	0.900	0.000
55	114	0.900	0.900	0.000
55	115	0.900	0.900	0.000
55	116	0.900	0.900	0.000
55	126	0.900	0.900	0.000
55	128	0.900	0.900	0.000
100	129	0.900	0.900	0.000
100	131	0.900	0.900	0.000
100	132	0.900	0.900	0.000
100	133	0.900	1.000	0.100
100	134	0.900	0.900	0.000
100	135	0.900	0.900	0.000
100	138	0.900	1.000	0.100
100	139	0.900	0.900	0.000
100	140	0.900	0.900	0.000
100	141	0.900	0.900	0.000
100	142	0.900	0.900	0.000
100	143	0.900	0.900	0.000
100	144	0.900	0.900	0.000
100	220	0.900	0.900	0.000
100	221	0.900	0.900	0.000
100	224	0.900	0.900	0.000
100	225	0.900	1.000	0.100
100	226	0.900	0.900	0.000
100	228	0.900	0.900	0.000
100	232	0.900	0.900	0.000
100	233	0.900	0.900	0.000
100	236	0.900	0.900	0.000
100	238	0.900	0.900	0.000
100	243	0.900	0.900	0.000
105	244	0.900	0.900	0.000
105	246	0.900	1.000	0.100
105	248	0.900	1.000	0.100
105	374	0.900	0.900	0.000
105	376	0.900	0.900	0.000
105	Max	0.900	1.000	0.100
	Average	0.900	0.906	0.006
	Min	0.900	0.900	0.000
	Std Dev	0.000	0.024	0.024



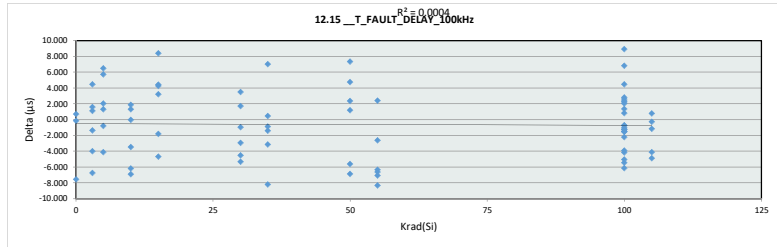
12.14 T_FAULT											
Test Site											
Tester											
Test Number											
Max Limit	1.4 µs										
Min Limit	0.4 µs										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400	0.400
Min	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900
Average	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.913	0.940
Max	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	0.900	1.000	1.000
UL	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400	1.400



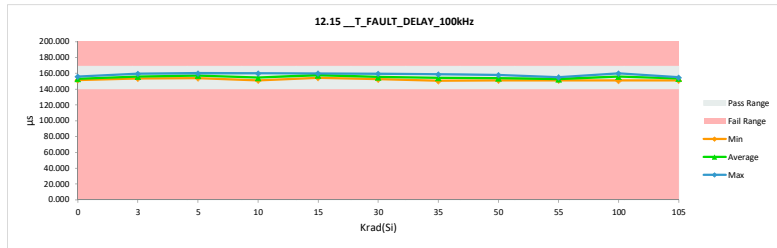
LDR TID Report  
TPS7H5001-SP

		12.15 T_FAULT_DELAY_100kHz	
Test Site			
Tester			
Test Number			
Unit		µs	µs
Max Limit		169	169
Min Limit		140	140

Krad(Si)	Serial #	PRE	POST	Delta
0	117	156.143	156.056	-0.087
0	118	159.074	151.573	-7.501
0	122	151.484	152.229	0.745
3	61	156.932	155.615	-1.317
3	62	158.789	159.947	1.158
3	63	157.986	154.051	-3.935
3	64	151.448	155.983	4.535
3	65	156.182	157.851	1.669
3	66	160.282	153.618	-6.664
5	67	151.792	158.349	6.557
5	68	154.968	154.237	-0.731
5	69	158.714	160.092	1.378
5	70	154.510	160.288	5.778
5	71	159.183	155.113	-4.070
5	72	153.705	155.822	2.117
10	73	156.932	156.955	0.023
10	74	158.789	160.172	1.383
10	75	157.986	151.153	-6.833
10	76	151.448	153.366	1.918
10	77	156.182	152.786	-3.396
10	78	160.282	154.164	-6.118
15	79	151.792	160.258	8.466
15	80	154.968	159.295	4.327
15	81	158.714	156.952	-1.762
15	82	154.510	157.764	3.254
15	83	159.183	154.551	-4.632
15	84	153.705	158.177	4.472
30	86	156.932	154.070	-2.862
30	89	158.789	154.344	-4.445
30	91	157.986	152.738	-5.248
30	92	151.448	153.204	1.756
30	94	156.182	159.721	3.539
30	95	160.282	159.368	-0.914
35	100	151.792	158.873	7.081
35	101	154.968	155.493	0.525
35	102	158.714	157.898	-0.816
35	103	154.510	153.189	-1.321
35	104	159.183	151.038	-8.145
35	105	153.705	150.633	-3.072
50	106	158.671	151.863	-6.808
50	107	150.844	155.662	4.818
50	108	156.731	151.185	-5.546
50	109	151.456	153.882	2.426
50	110	150.688	158.091	7.403
50	111	152.919	154.157	1.238
55	113	157.832	151.572	-6.260
55	114	158.852	151.858	-6.994
55	115	157.847	155.276	-2.571
55	116	152.416	154.863	2.447
55	126	159.618	153.048	-6.570
55	128	159.685	151.431	-8.254
100	129	159.530	158.555	-0.975
100	131	158.720	154.857	-3.863
100	132	156.479	155.007	-1.472
100	133	152.354	151.192	-1.162
100	134	153.883	152.446	-1.437
100	135	153.011	157.528	4.517
100	138	151.345	158.222	6.877
100	139	157.626	156.962	-0.664
100	140	155.256	157.550	2.294
100	141	153.392	154.290	0.898
100	142	151.035	160.028	8.993
100	143	155.673	154.489	-1.184
100	144	153.909	152.443	-1.466
100	220	160.301	154.222	-6.079
100	221	158.395	153.020	-5.375
100	224	157.600	160.016	2.416
100	225	158.006	160.123	2.117
100	226	156.717	159.604	2.887
100	228	159.064	156.919	-2.145
100	232	150.810	153.462	2.652
100	233	158.394	159.793	1.399
100	236	156.087	151.104	-4.983
100	238	158.605	157.912	-0.693
100	243	159.534	155.424	-4.110
105	244	154.325	155.179	0.854
105	246	156.249	151.434	-4.815
105	248	154.956	154.761	-0.195
105	374	159.035	154.997	-4.038
105	375	154.127	153.017	-1.110
105	Max	160.301	160.288	8.993
105	Average	156.027	155.430	-0.597
105	Min	150.688	150.633	-8.254
105	Std Dev	2.934	2.885	4.207



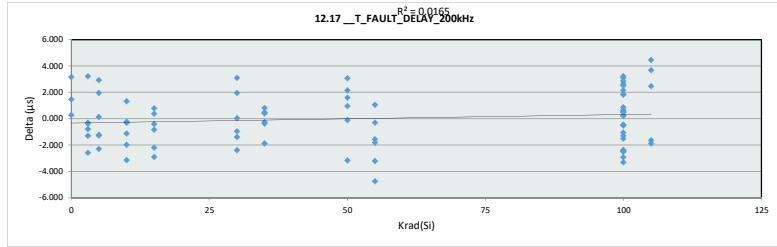
		12.15 T_FAULT										
Test Site												
Tester												
Test Number												
Max Limit		169	µs									
Min Limit		140	µs									
Krad(Si)		0	3	5	10	15	30	35	50	55	100	105
LL		140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000	140.000
Min		151.573	153.618	154.237	151.153	154.551	152.738	150.633	151.185	151.431	151.104	151.434
Average		153.286	156.178	157.317	154.766	157.833	155.574	154.521	154.140	153.008	156.049	153.878
Max		156.056	159.947	160.288	160.172	160.258	159.721	158.873	158.091	155.276	160.123	155.179
UL		169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000	169.000



LDR TID Report  
TPS7H5001-SP

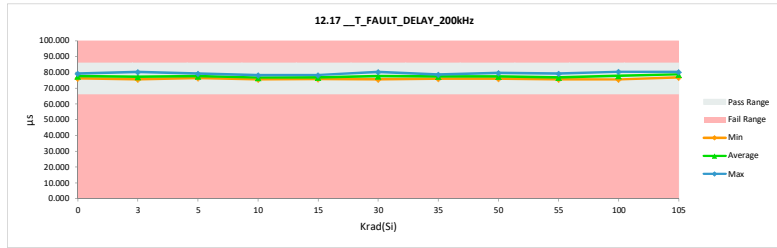
12.17 T_FAULT_DELAY_200kHz	
Test Site	
Tester	
Test Number	
Unit	µs
Max Limit	86
Min Limit	66

Krad(Si)	Serial #	PRE	POST	Delta
0	117	76.024	79.236	3.212
0	118	75.738	77.243	1.505
0	122	75.955	76.263	0.308
3	61	77.634	76.356	-1.278
3	62	78.253	75.702	-2.551
3	63	76.991	80.247	3.256
3	64	76.031	75.684	-0.347
3	65	76.345	75.597	-0.748
3	66	80.256	79.979	-0.277
5	67	76.314	79.272	2.958
5	68	77.920	78.091	0.171
5	69	76.563	78.563	2.000
5	70	79.071	76.803	-2.268
5	71	77.758	76.512	-1.246
5	72	77.981	76.787	-1.194
10	73	77.634	75.681	-1.953
10	74	78.253	77.150	-1.103
10	75	76.991	78.357	1.366
10	76	76.031	75.760	-0.271
10	77	76.345	76.143	-0.202
10	78	80.256	77.139	-3.117
15	79	76.314	77.136	0.822
15	80	77.920	78.333	0.413
15	81	76.563	76.186	-0.377
15	82	79.071	76.213	-2.858
15	83	77.758	76.956	-0.802
15	84	77.981	75.813	-2.168
30	86	77.634	76.696	-0.938
30	89	78.253	80.229	1.976
30	91	76.991	75.628	-1.363
30	92	76.031	79.157	3.126
30	94	76.345	76.422	0.077
30	95	80.256	77.898	-2.358
35	100	76.314	76.103	-0.211
35	101	77.920	76.067	-1.853
35	102	76.563	77.412	0.849
35	103	79.071	78.736	-0.335
35	104	77.758	78.275	0.517
35	105	77.981	78.407	0.426
50	106	76.126	78.320	2.194
50	107	76.178	76.112	-0.066
50	108	75.739	76.721	0.982
50	109	76.659	79.763	3.104
50	110	75.674	77.292	1.618
50	111	79.271	76.130	-3.141
55	113	78.894	75.700	-3.194
55	114	79.548	79.285	-0.263
55	115	78.432	76.903	-1.529
55	116	78.081	76.295	-1.786
55	126	80.422	75.700	-4.722
55	128	76.158	77.250	1.092
100	129	79.056	77.581	-1.475
100	131	76.518	79.780	3.262
100	132	77.120	75.847	-1.273
100	133	75.773	76.469	0.696
100	134	78.090	78.372	0.282
100	135	76.534	78.726	2.192
100	138	78.036	77.535	-0.501
100	139	76.750	76.330	-0.420
100	140	78.159	80.060	1.901
100	141	80.463	77.176	-3.287
100	142	76.838	75.807	-1.031
100	143	77.498	78.078	0.580
100	144	79.988	77.558	-2.430
100	220	78.921	79.423	0.502
100	221	77.287	79.146	1.859
100	224	77.185	79.720	2.535
100	225	79.691	77.212	-2.479
100	226	76.238	77.144	0.906
100	228	77.318	80.453	3.135
100	232	75.636	75.884	0.248
100	233	78.075	75.746	-2.329
100	236	78.451	75.569	-2.882
100	238	76.571	79.237	2.666
100	243	75.494	78.388	2.894
105	244	77.119	79.611	2.492
105	246	78.618	77.010	-1.608
105	248	76.175	79.889	3.714
105	374	75.807	80.286	4.479
105	376	78.693	76.840	-1.853
105	Max	80.463	80.453	4.479
105	Average	77.504	77.507	0.003
105	Min	75.494	75.569	-4.722
105	Std Dev	1.335	1.475	2.020



12.17 T_FAULT	
Test Site	
Tester	
Test Number	
Max Limit	86 µs
Min Limit	66 µs

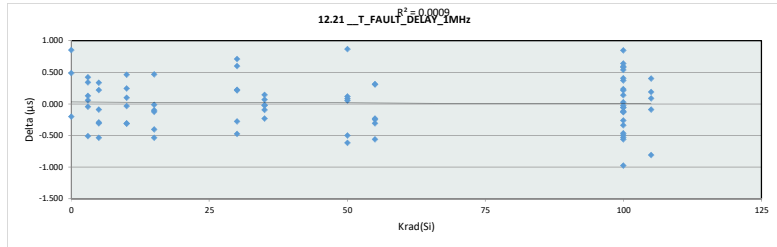
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000	66.000
Min	76.263	75.597	76.512	75.681	75.813	75.628	76.067	76.112	75.700	75.569	76.840
Average	77.581	77.261	77.671	76.705	76.773	77.672	77.500	77.390	76.856	77.802	78.727
Max	79.236	80.247	79.272	78.357	78.333	80.229	78.736	79.763	79.285	80.453	80.286
UL	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000	86.000



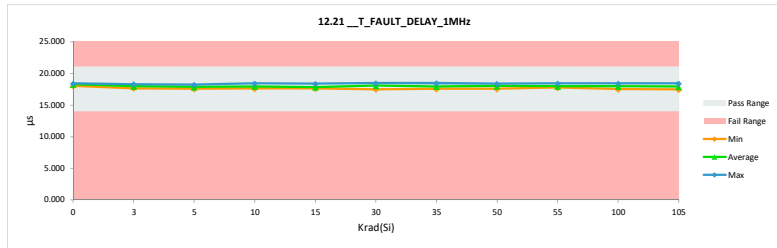
LDR TID Report  
TPS7H5001-SP

		12.21 T_FAULT_DELAY_1MHz	
Test Site			
Tester			
Test Number			
Unit		µs	µs
Max Limit		21	21
Min Limit		14	14

Krad(Si)	Serial #	PRE	POST	Delta
0	117	17.570	18.429	0.859
0	118	17.881	18.375	0.494
0	122	18.212	18.019	-0.193
3	61	17.751	17.713	-0.038
3	62	17.738	18.166	0.428
3	63	17.654	18.002	0.348
3	64	17.996	18.061	0.065
3	65	18.145	17.640	-0.505
3	66	18.164	18.297	0.133
5	67	17.579	17.921	0.342
5	68	17.825	17.739	-0.086
5	69	17.998	18.223	0.225
5	70	18.392	18.106	-0.286
5	71	18.123	17.593	-0.530
5	72	17.856	17.557	-0.299
10	73	17.738	17.857	0.106
10	74	17.738	18.205	0.467
10	75	17.654	17.628	-0.026
10	76	17.996	17.688	-0.308
10	77	18.145	17.843	-0.302
10	78	18.164	18.416	0.252
15	79	17.579	18.054	0.475
15	80	17.825	17.729	-0.096
15	81	17.998	17.599	-0.399
15	82	18.392	18.384	-0.008
15	83	18.123	17.595	-0.528
15	84	17.856	17.739	-0.117
30	86	17.751	17.481	-0.270
30	89	17.738	18.454	0.716
30	91	17.654	18.257	0.603
30	92	17.996	18.217	0.221
30	94	18.145	17.675	-0.470
30	95	18.164	18.393	0.229
35	100	17.579	17.565	-0.014
35	101	17.825	17.734	-0.091
35	102	17.998	18.146	0.148
35	103	18.392	18.463	0.071
35	104	18.123	18.105	-0.018
35	105	17.856	17.629	-0.227
50	106	18.470	17.859	-0.611
50	107	18.134	18.224	0.090
50	108	18.057	18.179	0.122
50	109	17.500	18.370	0.870
50	110	17.508	17.564	0.056
50	111	18.435	17.943	-0.492
55	113	18.317	17.762	-0.555
55	114	18.096	18.411	0.315
55	115	18.127	17.884	-0.243
55	116	18.319	18.092	-0.227
55	126	18.170	17.868	-0.302
55	128	17.556	17.873	0.317
100	129	18.391	18.423	0.032
100	131	17.500	18.351	0.851
100	132	17.567	18.152	0.585
100	133	18.306	17.780	-0.526
100	134	18.439	18.328	-0.111
100	135	18.111	17.988	-0.123
100	138	17.550	17.792	0.242
100	139	18.393	18.062	-0.331
100	140	18.322	17.767	-0.555
100	141	17.554	17.499	-0.055
100	142	18.088	17.831	-0.257
100	143	17.568	17.982	0.414
100	144	17.745	17.726	-0.019
100	220	18.369	18.323	-0.046
100	221	17.642	18.187	0.545
100	224	17.564	17.708	0.144
100	225	17.507	18.154	0.647
100	226	18.195	17.739	-0.456
100	228	17.810	18.408	0.598
100	232	18.209	17.719	-0.490
100	233	17.746	18.125	0.379
100	236	18.464	17.494	-0.970
100	238	17.821	17.703	-0.118
100	243	17.658	17.877	0.219
105	244	17.861	17.776	-0.085
105	246	17.844	18.038	0.194
105	248	18.276	17.475	-0.801
105	374	17.625	17.919	0.094
105	376	18.001	18.409	0.408
105	Max	18.470	18.463	0.870
105	Average	17.954	17.968	0.014
105	Min	17.500	17.475	-0.970
105	Std Dev	0.292	0.293	0.398



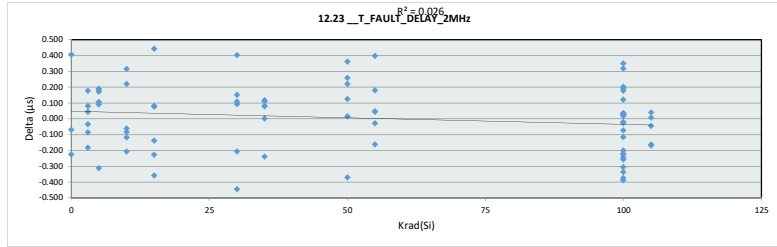
		12.21 T_FAULT									
Test Site											
Tester											
Test Number											
Max Limit		21	µs								
Min Limit		14	µs								
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000
Min	18.019	17.640	17.557	17.628	17.595	17.481	17.565	17.564	17.762	17.494	17.475
Average	18.274	17.980	17.857	17.940	17.850	18.080	17.940	18.023	17.982	17.963	17.923
Max	18.429	18.297	18.223	18.416	18.384	18.454	18.463	18.370	18.411	18.423	18.409
UL	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000	21.000



LDR TID Report  
TPS7H5001-SP

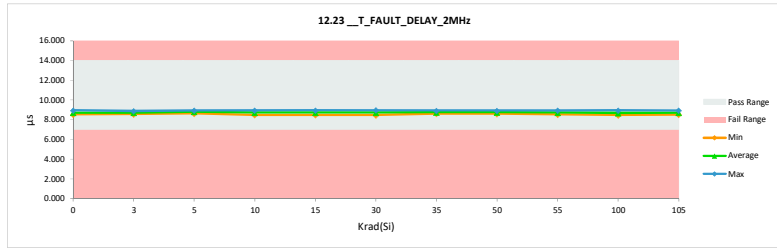
12.23 T_FAULT_DELAY_2MHz	
Test Site	
Tester	
Test Number	
Unit	µs
Max Limit	14
Min Limit	7

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.722	8.654	-0.068
0	118	8.560	8.969	0.409
0	122	8.566	8.543	-0.223
3	61	8.587	8.634	0.047
3	62	8.569	8.652	0.083
3	63	8.923	8.891	-0.032
3	64	8.916	8.832	-0.084
3	65	8.565	8.745	0.180
3	66	8.746	8.566	-0.180
5	67	8.511	8.703	0.192
5	68	8.939	8.630	-0.309
5	69	8.740	8.913	0.173
5	70	8.677	8.863	0.186
5	71	8.853	8.945	0.092
5	72	8.758	8.866	0.108
10	73	8.587	8.528	-0.059
10	74	8.569	8.487	-0.082
10	75	8.923	8.808	-0.115
10	76	8.916	8.711	-0.205
10	77	8.565	8.882	0.317
10	78	8.746	8.969	0.223
15	79	8.511	8.955	0.444
15	80	8.939	8.714	-0.225
15	81	8.740	8.604	-0.136
15	82	8.677	8.762	0.085
15	83	8.853	8.497	-0.356
15	84	8.758	8.837	0.079
30	86	8.587	8.699	0.112
30	89	8.569	8.974	0.405
30	91	8.923	8.718	-0.205
30	92	8.916	8.473	-0.443
30	94	8.565	8.660	0.095
30	95	8.746	8.899	0.153
35	100	8.511	8.631	0.120
35	101	8.939	8.702	-0.237
35	102	8.740	8.854	0.114
35	103	8.677	8.680	0.003
35	104	8.853	8.936	0.083
35	105	8.758	8.840	0.082
50	106	8.529	8.894	0.365
50	107	8.971	8.602	-0.369
50	108	8.701	8.923	0.222
50	109	8.496	8.756	0.260
50	110	8.725	8.742	0.017
50	111	8.513	8.641	0.128
55	113	8.801	8.641	-0.160
55	114	8.561	8.962	0.401
55	115	8.485	8.534	0.049
55	116	8.548	8.596	0.048
55	126	8.779	8.753	-0.026
55	128	8.708	8.890	0.182
100	129	8.872	8.759	-0.113
100	131	8.891	8.693	-0.198
100	132	8.910	8.885	-0.025
100	133	8.619	8.652	0.033
100	134	8.833	8.498	-0.335
100	135	8.902	8.646	-0.256
100	138	8.934	8.631	-0.303
100	139	8.964	8.745	-0.219
100	140	8.737	8.775	0.038
100	141	8.599	8.918	0.319
100	142	8.865	8.621	-0.244
100	143	8.564	8.601	0.037
100	144	8.534	8.560	0.026
100	220	8.577	8.756	0.179
100	221	8.558	8.762	0.204
100	224	8.846	8.474	-0.372
100	225	8.492	8.844	0.352
100	226	8.706	8.634	-0.072
100	228	8.976	8.958	-0.018
100	232	8.480	8.603	0.123
100	233	8.830	8.607	-0.223
100	236	8.687	8.707	0.020
100	238	8.942	8.556	-0.386
100	243	8.674	8.867	0.193
105	244	8.953	8.786	-0.167
105	246	8.681	8.639	-0.042
105	248	8.882	8.722	-0.160
105	374	8.897	8.940	0.043
105	376	8.517	8.527	0.010
	Max	8.976	8.974	0.444
	Average	8.727	8.732	0.005
	Min	8.480	8.473	-0.443
	Std Dev	0.156	0.143	0.210



12.23 T_FAULT	
Test Site	
Tester	
Test Number	
Max Limit	14 µs
Min Limit	7 µs

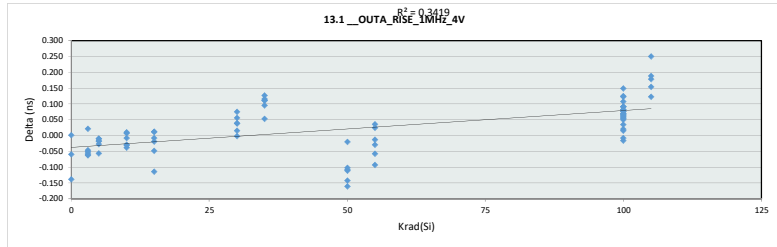
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000	7.000
Min	8.543	8.566	8.630	8.487	8.497	8.473	8.631	8.602	8.534	8.474	8.527
Average	8.722	8.720	8.820	8.731	8.728	8.737	8.774	8.760	8.729	8.698	8.723
Max	8.969	8.891	8.945	8.969	8.955	8.974	8.936	8.923	8.962	8.958	8.940
UL	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000	14.000



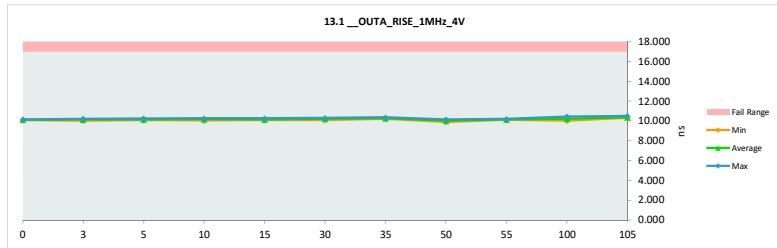
LDR TID Report  
TPS7H5001-SP

13.1 OUTA_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.240	10.103	-0.137
0	118	10.173	10.175	0.002
0	122	10.176	10.118	-0.058
3	61	10.269	10.224	-0.045
3	62	10.279	10.230	-0.049
3	63	10.245	10.184	-0.061
3	64	10.098	10.039	-0.059
3	65	10.220	10.166	-0.054
3	66	10.139	10.162	0.023
5	67	10.162	10.145	-0.017
5	68	10.274	10.248	-0.026
5	69	10.210	10.194	-0.016
5	70	10.183	10.128	-0.055
5	71	10.280	10.271	-0.009
5	72	10.109	10.100	-0.009
10	73	10.269	10.280	0.011
10	74	10.279	10.287	0.008
10	75	10.245	10.216	-0.029
10	76	10.098	10.061	-0.037
10	77	10.220	10.192	-0.028
10	78	10.139	10.132	-0.007
15	79	10.162	10.155	-0.007
15	80	10.274	10.287	0.013
15	81	10.210	10.097	-0.113
15	82	10.183	10.136	-0.047
15	83	10.280	10.262	-0.018
15	84	10.109	10.121	0.012
30	86	10.269	10.309	0.040
30	89	10.279	10.278	-0.001
30	91	10.245	10.321	0.076
30	92	10.098	10.114	0.016
30	94	10.220	10.260	0.040
30	95	10.139	10.196	0.057
35	100	10.162	10.290	0.128
35	101	10.274	10.386	0.112
35	102	10.210	10.323	0.113
35	103	10.183	10.237	0.054
35	104	10.280	10.376	0.096
35	105	10.109	10.226	0.117
50	106	10.152	10.011	-0.141
50	107	10.014	9.904	-0.110
50	108	10.197	10.178	-0.019
50	109	10.195	10.035	-0.160
50	110	10.230	10.129	-0.101
50	111	10.142	10.035	-0.107
55	113	10.188	10.176	-0.012
55	114	10.275	10.219	-0.056
55	115	10.229	10.201	-0.028
55	116	10.233	10.141	-0.092
55	126	10.155	10.181	0.026
55	128	10.140	10.177	0.037
100	129	10.101	10.184	0.083
100	131	10.120	10.197	0.077
100	132	10.098	10.175	0.077
100	133	10.179	10.261	0.082
100	134	10.068	10.053	-0.015
100	135	10.200	10.236	0.036
100	138	10.108	10.175	0.067
100	139	10.087	10.179	0.092
100	140	10.218	10.269	0.051
100	141	10.223	10.216	-0.007
100	142	10.242	10.303	0.061
100	143	10.313	10.463	0.150
100	144	10.115	10.241	0.126
100	220	10.183	10.292	0.109
100	221	10.151	10.221	0.070
100	224	10.207	10.331	0.124
100	225	10.157	10.173	0.016
100	226	10.262	10.318	0.056
100	228	10.227	10.249	0.022
100	232	10.057	10.149	0.092
100	233	10.209	10.273	0.064
100	236	10.262	10.319	0.057
100	238	10.114	10.184	0.070
100	243	10.195	10.287	0.092
105	244	10.297	10.476	0.179
105	246	10.159	10.314	0.155
105	248	10.190	10.380	0.190
105	374	10.270	10.521	0.251
105	376	10.203	10.327	0.124
105	375	10.313	10.521	0.251
Max				
Average		10.192	10.215	0.023
Min		10.014	9.904	-0.160
Std Dev		0.066	0.106	0.081



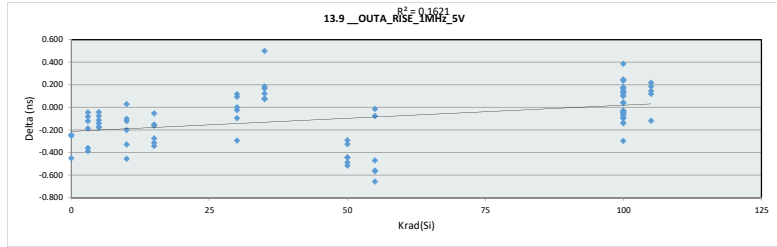
13.1 OUTA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.103	10.039	10.100	10.061	10.097	10.114	10.226	9.904	10.141	10.053	10.314
Average	10.132	10.168	10.181	10.195	10.176	10.246	10.306	10.049	10.183	10.240	10.404
Max	10.175	10.230	10.271	10.287	10.287	10.321	10.386	10.178	10.219	10.463	10.521
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



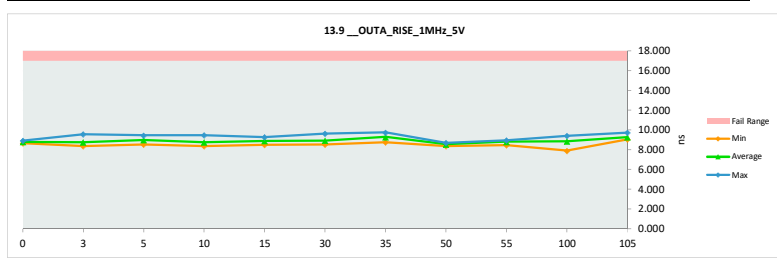
LDR TID Report  
TPS7H5001-SP

13.9_OUTA_RISE_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.283	8.837	-0.446
0	118	8.909	8.660	-0.249
0	122	9.163	8.923	-0.240
3	61	9.934	9.580	-0.354
3	62	8.427	8.385	-0.042
3	63	8.843	8.660	-0.183
3	64	8.801	8.683	-0.118
3	65	8.463	8.386	-0.077
3	66	9.369	8.986	-0.383
5	67	8.772	8.664	-0.108
5	68	9.638	9.465	-0.173
5	69	9.347	9.276	-0.071
5	70	8.585	8.546	-0.039
5	71	9.246	9.109	-0.137
5	72	9.069	8.902	-0.167
10	73	9.934	9.482	-0.452
10	74	8.427	8.459	0.032
10	75	8.843	8.645	-0.198
10	76	8.801	8.685	-0.116
10	77	8.463	8.365	-0.098
10	78	9.369	9.044	-0.325
15	79	8.772	8.501	-0.271
15	80	9.638	9.300	-0.338
15	81	9.347	9.198	-0.149
15	82	8.585	8.537	-0.048
15	83	9.246	8.935	-0.311
15	84	9.069	8.912	-0.157
30	86	9.934	9.645	-0.289
30	89	8.427	8.547	0.120
30	91	8.843	8.821	-0.022
30	92	8.801	8.805	0.004
30	94	8.463	8.560	0.097
30	95	9.369	9.277	-0.092
35	100	8.772	8.853	0.081
35	101	9.638	9.715	0.077
35	102	9.347	9.474	0.127
35	103	8.585	8.758	0.173
35	104	9.246	9.748	0.502
35	105	9.069	9.259	0.190
50	106	9.213	8.703	-0.510
50	107	8.664	8.376	-0.288
50	108	9.095	8.612	-0.483
50	109	8.945	8.624	-0.321
50	110	8.829	8.390	-0.439
50	111	9.037	8.600	-0.437
55	113	9.250	8.784	-0.466
55	114	9.565	8.912	-0.653
55	115	9.386	8.830	-0.556
55	116	9.505	8.945	-0.560
55	126	8.482	8.473	-0.009
55	128	9.036	8.965	-0.071
100	129	8.367	8.617	0.250
100	131	8.990	8.897	-0.093
100	132	8.790	8.765	-0.025
100	133	9.114	8.980	-0.134
100	134	8.798	8.752	-0.046
100	135	9.175	9.127	-0.048
100	138	8.850	8.955	0.105
100	139	8.010	8.249	0.239
100	140	9.030	8.964	-0.066
100	141	9.452	9.158	-0.294
100	142	8.807	8.989	0.182
100	143	9.060	9.228	0.168
100	144	7.856	7.900	0.044
100	220	8.953	9.075	0.122
100	221	8.794	8.705	-0.089
100	224	8.576	8.965	0.389
100	225	9.199	9.156	-0.043
100	226	9.282	9.424	0.142
100	228	9.113	9.252	0.139
100	232	8.589	8.636	0.047
100	233	8.901	8.872	-0.029
100	236	8.882	9.027	0.145
100	238	9.132	8.997	-0.135
100	243	8.216	8.392	0.176
105	244	9.545	9.735	0.190
105	246	9.038	9.187	0.149
105	248	8.833	9.057	0.224
105	374	9.229	9.115	-0.114
105	376	9.249	9.372	0.123
105	Max	9.934	9.748	0.502
105	Average	8.996	8.904	-0.092
105	Min	7.856	7.900	-0.653
105	Std Dev	0.415	0.372	0.234



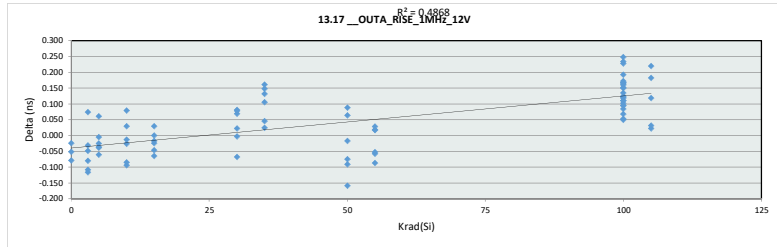
13.9_OUTA_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105



LDR TID Report  
TPS7H5001-SP

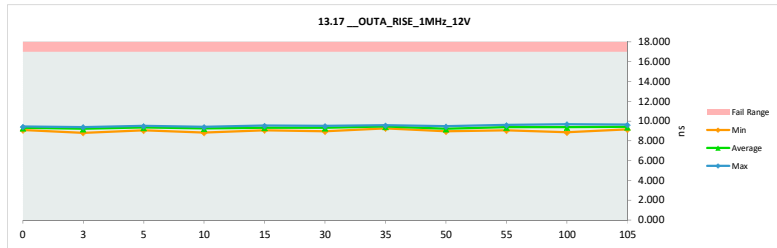
13.17_OUTA_RISE_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns
Max Limit	17
Min Limit	17

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.494	9.444	-0.050
0	118	9.091	9.068	-0.023
0	122	9.464	9.386	-0.078
3	61	9.508	9.393	-0.115
3	62	9.106	9.181	0.075
3	63	9.372	9.342	-0.030
3	64	9.253	9.174	-0.079
3	65	8.858	8.811	-0.047
3	66	9.501	9.394	-0.107
5	67	9.183	9.245	0.062
5	68	9.489	9.454	-0.035
5	69	9.544	9.507	-0.037
5	70	9.109	9.050	-0.059
5	71	9.257	9.233	-0.024
5	72	9.471	9.467	-0.004
10	73	9.508	9.415	-0.093
10	74	9.106	9.186	0.080
10	75	9.372	9.360	-0.012
10	76	9.253	9.284	0.031
10	77	8.858	8.833	-0.025
10	78	9.501	9.417	-0.084
15	79	9.183	9.214	0.031
15	80	9.489	9.473	-0.016
15	81	9.544	9.521	-0.023
15	82	9.109	9.064	-0.045
15	83	9.257	9.194	-0.063
15	84	9.471	9.472	0.001
30	86	9.508	9.442	-0.066
30	89	9.106	9.185	0.079
30	91	9.372	9.396	0.024
30	92	9.253	9.323	0.070
30	94	8.858	8.940	0.082
30	95	9.501	9.499	-0.002
35	100	9.183	9.316	0.133
35	101	9.489	9.536	0.047
35	102	9.544	9.570	0.026
35	103	9.109	9.258	0.149
35	104	9.257	9.419	0.162
35	105	9.471	9.578	0.107
50	106	9.433	9.343	-0.090
50	107	9.175	9.101	-0.074
50	108	9.122	9.187	0.065
50	109	9.375	9.464	0.089
50	110	9.108	8.950	-0.158
50	111	9.300	9.284	-0.016
55	113	9.236	9.185	-0.051
55	114	9.539	9.483	-0.056
55	115	9.487	9.401	-0.086
55	116	9.417	9.435	0.018
55	126	9.032	9.052	0.020
55	128	9.569	9.599	0.030
100	129	8.840	9.089	0.249
100	131	9.486	9.622	0.136
100	132	9.399	9.495	0.096
100	133	9.159	9.263	0.104
100	134	9.336	9.448	0.112
100	135	9.466	9.660	0.194
100	138	9.433	9.487	0.054
100	139	8.827	9.000	0.173
100	140	9.341	9.392	0.051
100	141	9.466	9.630	0.164
100	142	9.197	9.365	0.168
100	143	9.304	9.424	0.120
100	144	8.764	8.859	0.095
100	220	9.322	9.551	0.229
100	221	9.314	9.399	0.085
100	224	9.111	9.279	0.168
100	225	9.165	9.234	0.069
100	226	9.192	9.318	0.126
100	228	9.221	9.374	0.153
100	232	9.148	9.309	0.161
100	233	9.136	9.287	0.151
100	236	9.133	9.368	0.235
100	238	9.556	9.679	0.123
100	243	8.945	9.057	0.112
105	244	9.438	9.462	0.024
105	246	9.436	9.619	0.183
105	248	9.127	9.247	0.120
105	374	9.103	9.136	0.033
105	376	9.396	9.617	0.221
105	Max	9.569	9.679	0.249
105	Average	9.282	9.327	0.046
105	Min	8.764	8.811	-0.158
105	Std Dev	0.204	0.199	0.096



13.17_OUTA_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17
Min Limit	ns

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.068	8.811	9.050	8.833	9.064	8.940	9.258	8.950	9.052	8.859	9.136
Average	9.299	9.216	9.326	9.249	9.323	9.298	9.446	9.222	9.359	9.358	9.416
Max	9.444	9.394	9.507	9.417	9.521	9.499	9.578	9.464	9.599	9.679	9.619
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

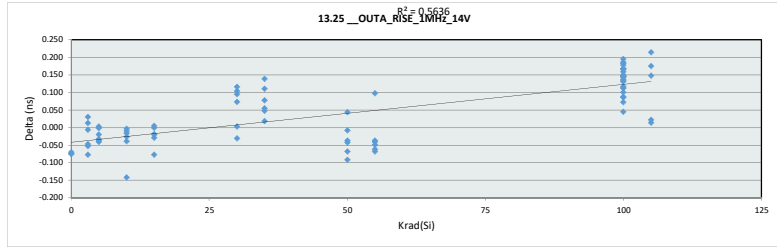




LDR TID Report  
TPS7H5001-SP

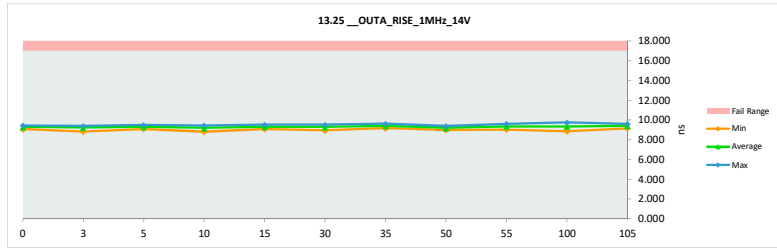
13.25_OUTA_RISE_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.513	9.440	-0.073
0	118	9.146	9.072	-0.074
0	122	9.462	9.393	-0.069
3	61	9.446	9.394	-0.052
3	62	9.160	9.191	0.031
3	63	9.397	9.321	-0.076
3	64	9.203	9.217	0.014
3	65	8.831	8.826	-0.005
3	66	9.417	9.371	-0.046
5	67	9.200	9.199	-0.001
5	68	9.495	9.455	-0.040
5	69	9.516	9.497	-0.019
5	70	9.117	9.085	-0.032
5	71	9.243	9.247	0.004
5	72	9.448	9.411	-0.037
5	73	9.446	9.444	-0.002
10	74	9.160	9.145	-0.015
10	75	9.397	9.256	-0.141
10	76	9.203	9.194	-0.009
10	77	8.831	8.806	-0.025
10	78	9.417	9.379	-0.038
15	79	9.200	9.200	0.000
15	80	9.495	9.419	-0.076
15	81	9.516	9.522	0.006
15	82	9.117	9.089	-0.028
15	83	9.243	9.224	-0.019
15	84	9.448	9.430	-0.018
30	86	9.446	9.416	-0.030
30	89	9.160	9.265	0.105
30	91	9.397	9.401	0.004
30	92	9.203	9.277	0.074
30	94	8.831	8.948	0.117
30	95	9.417	9.513	0.096
35	100	9.200	9.256	0.056
35	101	9.495	9.514	0.019
35	102	9.516	9.627	0.111
35	103	9.117	9.166	0.049
35	104	9.243	9.383	0.140
35	105	9.448	9.527	0.079
50	106	9.368	9.301	-0.067
50	107	9.179	9.088	-0.091
50	108	9.175	9.133	-0.042
50	109	9.345	9.390	0.045
50	110	9.018	8.982	-0.036
50	111	9.292	9.285	-0.007
55	113	9.206	9.139	-0.067
55	114	9.516	9.468	-0.048
55	115	9.446	9.410	-0.036
55	116	9.476	9.436	-0.040
55	126	9.074	9.013	-0.061
55	128	9.499	9.598	0.099
100	129	8.896	9.043	0.147
100	131	9.493	9.566	0.073
100	132	9.464	9.510	0.046
100	133	9.129	9.298	0.169
100	134	9.303	9.416	0.113
100	135	9.532	9.681	0.149
100	138	9.383	9.472	0.089
100	139	8.787	8.934	0.147
100	140	9.262	9.380	0.118
100	141	9.467	9.618	0.151
100	142	9.199	9.365	0.166
100	143	9.217	9.348	0.131
100	144	8.776	8.863	0.087
100	220	9.335	9.518	0.183
100	221	9.290	9.391	0.101
100	224	9.045	9.224	0.179
100	225	9.100	9.243	0.143
100	226	9.201	9.338	0.137
100	228	9.208	9.344	0.136
100	232	9.110	9.297	0.187
100	233	9.105	9.219	0.114
100	236	9.125	9.294	0.169
100	238	9.558	9.754	0.196
100	243	8.881	9.041	0.160
105	244	9.413	9.436	0.023
105	246	9.424	9.600	0.176
105	248	9.089	9.238	0.149
105	374	9.130	9.145	0.015
105	376	9.376	9.591	0.215
105	Max	9.558	9.754	0.215
105	Average	9.268	9.312	0.044
105	Min	8.776	8.806	-0.141
105	Std Dev	0.200	0.200	0.089



13.25_OUTA_RISE	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

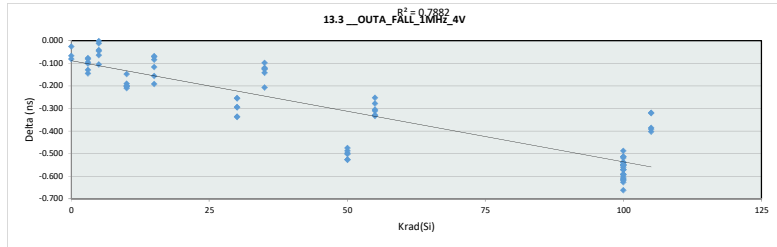
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	9.072	8.826	9.085	8.806	9.089	8.948	9.166	8.982	9.013	8.863	9.145
Average	9.302	9.220	9.316	9.204	9.314	9.303	9.412	9.197	9.344	9.340	9.402
Max	9.440	9.394	9.497	9.444	9.522	9.513	9.627	9.390	9.598	9.754	9.600
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



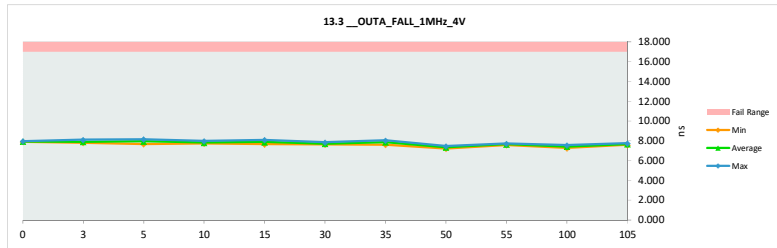
LDR TID Report  
TPS7H5001-SP

13.3 OUTA_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	8.042	7.977	-0.065
0	118	7.930	7.905	-0.025
0	122	8.023	7.943	-0.080
3	61	8.004	7.925	-0.079
3	62	8.194	8.119	-0.075
3	63	7.950	7.850	-0.100
3	64	7.904	7.810	-0.094
3	65	7.934	7.806	-0.128
3	66	8.103	7.960	-0.143
5	67	7.728	7.684	-0.044
5	68	8.174	8.164	-0.010
5	69	7.995	7.932	-0.063
5	70	7.878	7.838	-0.040
5	71	7.968	7.968	0.000
5	72	8.256	8.152	-0.104
10	73	8.004	7.796	-0.208
10	74	8.194	7.993	-0.201
10	75	7.950	7.751	-0.199
10	76	7.904	7.758	-0.146
10	77	7.934	7.746	-0.188
10	78	8.103	7.905	-0.198
15	79	7.728	7.661	-0.067
15	80	8.174	8.105	-0.069
15	81	7.995	7.805	-0.190
15	82	7.878	7.763	-0.115
15	83	7.968	7.886	-0.082
15	84	8.256	8.101	-0.155
30	86	8.004	7.710	-0.294
30	89	8.194	7.860	-0.334
30	91	7.950	7.696	-0.254
30	92	7.904	7.651	-0.253
30	94	7.934	7.642	-0.292
30	95	8.103	7.767	-0.336
35	100	7.728	7.609	-0.119
35	101	8.174	8.049	-0.125
35	102	7.995	7.898	-0.097
35	103	7.878	7.737	-0.141
35	104	7.968	7.847	-0.121
35	105	8.256	8.050	-0.206
50	106	7.888	7.388	-0.500
50	107	7.783	7.287	-0.496
50	108	7.944	7.470	-0.474
50	109	7.723	7.237	-0.486
50	110	7.955	7.429	-0.526
50	111	7.889	7.365	-0.524
55	113	8.039	7.707	-0.332
55	114	7.933	7.624	-0.309
55	115	7.949	7.621	-0.328
55	116	8.032	7.729	-0.303
55	126	7.864	7.588	-0.276
55	128	7.934	7.683	-0.251
100	129	7.963	7.404	-0.559
100	131	8.051	7.515	-0.536
100	132	8.135	7.565	-0.570
100	133	8.110	7.562	-0.548
100	134	8.038	7.429	-0.609
100	135	8.099	7.507	-0.592
100	138	7.927	7.382	-0.545
100	139	7.845	7.295	-0.550
100	140	8.058	7.488	-0.570
100	141	7.934	7.397	-0.537
100	142	8.046	7.457	-0.589
100	143	7.886	7.375	-0.511
100	144	7.792	7.275	-0.517
100	220	8.039	7.414	-0.625
100	221	7.861	7.314	-0.547
100	224	7.985	7.499	-0.486
100	225	7.989	7.420	-0.569
100	226	7.931	7.380	-0.551
100	228	8.170	7.557	-0.613
100	232	7.890	7.378	-0.512
100	233	7.977	7.387	-0.590
100	236	7.920	7.317	-0.603
100	238	8.062	7.445	-0.617
100	243	7.997	7.336	-0.661
105	244	8.084	7.683	-0.401
105	246	8.038	7.721	-0.317
105	248	7.974	7.655	-0.319
105	374	7.983	7.599	-0.384
105	376	8.155	7.765	-0.390
	Max	8.256	8.164	0.000
	Average	7.989	7.668	-0.321
	Min	7.723	7.237	-0.661
	Std Dev	0.123	0.243	0.204



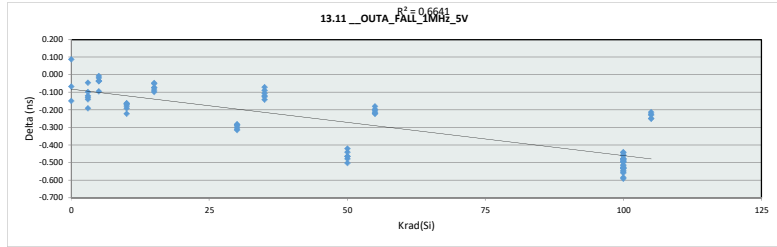
13.3 OUTA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.905	7.806	7.684	7.746	7.661	7.642	7.609	7.237	7.588	7.275	7.599
Average	7.942	7.912	7.956	7.825	7.887	7.721	7.865	7.363	7.659	7.421	7.685
Max	7.977	8.119	8.164	7.993	8.105	7.860	8.050	7.470	7.729	7.565	7.765
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



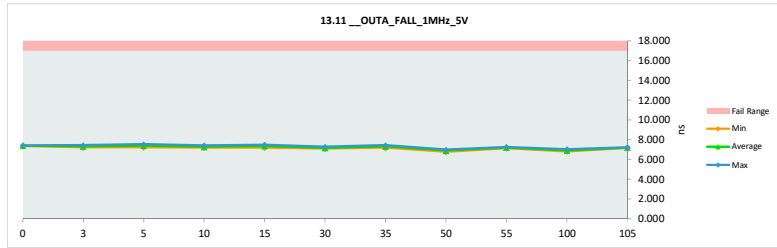
LDR TID Report  
TPS7H5001-SP

13.11_OUTA_FALL_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.498	7.434	-0.064
0	118	7.334	7.425	-0.091
0	122	7.508	7.361	-0.147
3	61	7.468	7.341	-0.127
3	62	7.591	7.453	-0.138
3	63	7.432	7.314	-0.118
3	64	7.417	7.229	-0.188
3	65	7.404	7.361	-0.043
3	66	7.514	7.419	-0.095
5	67	7.285	7.251	-0.034
5	68	7.562	7.471	-0.091
5	69	7.432	7.401	-0.031
5	70	7.343	7.327	-0.016
5	71	7.442	7.409	-0.033
5	72	7.558	7.553	-0.005
10	73	7.468	7.308	-0.160
10	74	7.591	7.428	-0.163
10	75	7.432	7.212	-0.220
10	76	7.417	7.229	-0.188
10	77	7.404	7.240	-0.164
10	78	7.514	7.339	-0.175
15	79	7.285	7.215	-0.070
15	80	7.562	7.466	-0.096
15	81	7.432	7.387	-0.045
15	82	7.343	7.258	-0.085
15	83	7.442	7.371	-0.071
15	84	7.558	7.510	-0.048
30	86	7.468	7.156	-0.312
30	89	7.591	7.295	-0.296
30	91	7.432	7.147	-0.285
30	92	7.417	7.111	-0.306
30	94	7.404	7.122	-0.282
30	95	7.514	7.235	-0.279
35	100	7.285	7.198	-0.087
35	101	7.562	7.445	-0.117
35	102	7.432	7.309	-0.123
35	103	7.343	7.204	-0.139
35	104	7.442	7.374	-0.068
35	105	7.558	7.455	-0.103
50	106	7.379	6.903	-0.476
50	107	7.362	6.862	-0.500
50	108	7.419	7.002	-0.417
50	109	7.555	6.794	-0.461
50	110	7.369	6.931	-0.438
50	111	7.374	6.913	-0.461
55	113	7.426	7.207	-0.219
55	114	7.376	7.199	-0.177
55	115	7.406	7.189	-0.217
55	116	7.438	7.239	-0.199
55	126	7.358	7.148	-0.210
55	128	7.459	7.266	-0.193
100	129	7.419	6.926	-0.493
100	131	7.453	6.999	-0.454
100	132	7.533	7.059	-0.474
100	133	7.539	6.983	-0.556
100	134	7.412	6.954	-0.458
100	135	7.480	6.951	-0.529
100	138	7.402	6.907	-0.495
100	139	7.316	6.874	-0.442
100	140	7.454	6.976	-0.478
100	141	7.477	6.950	-0.527
100	142	7.515	6.926	-0.589
100	143	7.430	6.904	-0.526
100	144	7.348	6.856	-0.492
100	220	7.426	6.910	-0.516
100	221	7.302	6.827	-0.475
100	224	7.420	6.983	-0.437
100	225	7.542	6.961	-0.581
100	226	7.387	6.855	-0.532
100	228	7.528	6.999	-0.529
100	232	7.344	6.861	-0.483
100	233	7.395	6.885	-0.510
100	236	7.387	6.844	-0.543
100	238	7.454	6.973	-0.481
100	243	7.485	6.935	-0.550
105	244	7.461	7.235	-0.226
105	246	7.445	7.223	-0.222
105	248	7.425	7.215	-0.210
105	374	7.424	7.178	-0.246
105	376	7.479	7.232	-0.247
105	Max	7.591	7.553	0.038
105	Average	7.437	7.160	-0.277
105	Min	7.255	6.794	-0.589
105	Std Dev	0.078	0.208	0.187



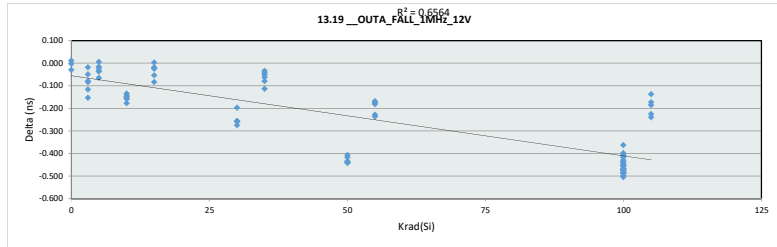
13.11_OUTA_FALL_1MHz_5V											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.361	7.229	7.251	7.212	7.215	7.111	7.198	6.794	7.148	6.827	7.178
Average	7.407	7.353	7.402	7.293	7.368	7.178	7.331	6.901	7.208	6.929	7.217
Max	7.434	7.453	7.553	7.428	7.510	7.295	7.455	7.002	7.266	7.059	7.235
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



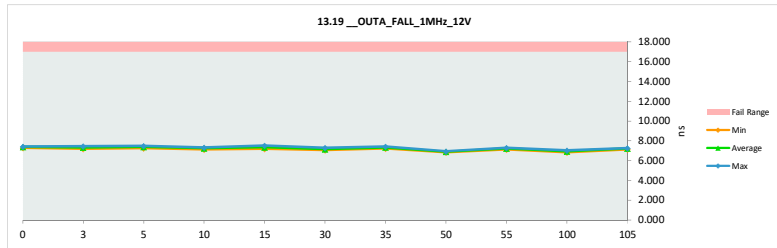
LDR TID Report  
TPS7H5001-SP

13.19_OUTA_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.428	7.427	-0.001
0	118	7.312	7.284	-0.028
0	122	7.420	7.433	0.013
3	61	7.372	7.295	-0.077
3	62	7.486	7.469	-0.017
3	63	7.446	7.365	-0.081
3	64	7.343	7.296	-0.047
3	65	7.325	7.211	-0.114
3	66	7.478	7.327	-0.151
5	67	7.289	7.254	-0.035
5	68	7.502	7.489	-0.013
5	69	7.385	7.365	-0.020
5	70	7.341	7.309	-0.032
5	71	7.393	7.400	0.007
5	72	7.543	7.480	-0.063
10	73	7.372	7.239	-0.133
10	74	7.486	7.334	-0.152
10	75	7.446	7.302	-0.144
10	76	7.343	7.187	-0.156
10	77	7.325	7.150	-0.175
10	78	7.478	7.334	-0.144
15	79	7.289	7.207	-0.082
15	80	7.502	7.450	-0.052
15	81	7.385	7.390	0.005
15	82	7.341	7.319	-0.022
15	83	7.393	7.372	-0.021
15	84	7.543	7.526	-0.017
30	86	7.372	7.116	-0.256
30	89	7.486	7.291	-0.195
30	91	7.446	7.173	-0.273
30	92	7.343	7.088	-0.255
30	94	7.325	7.069	-0.256
30	95	7.478	7.224	-0.254
35	100	7.289	7.240	-0.049
35	101	7.502	7.425	-0.077
35	102	7.385	7.353	-0.032
35	103	7.341	7.281	-0.060
35	104	7.393	7.352	-0.041
35	105	7.543	7.431	-0.112
50	106	7.322	6.918	-0.404
50	107	7.270	6.833	-0.437
50	108	7.390	6.958	-0.432
50	109	7.309	6.894	-0.415
50	110	7.332	6.894	-0.438
50	111	7.376	6.937	-0.439
55	113	7.411	7.185	-0.226
55	114	7.419	7.244	-0.175
55	115	7.415	7.245	-0.170
55	116	7.448	7.214	-0.234
55	126	7.301	7.135	-0.166
55	128	7.476	7.297	-0.179
100	129	7.377	6.912	-0.465
100	131	7.404	6.990	-0.414
100	132	7.453	7.048	-0.405
100	133	7.398	6.932	-0.466
100	134	7.432	6.949	-0.483
100	135	7.384	7.024	-0.360
100	138	7.415	6.930	-0.485
100	139	7.298	6.892	-0.406
100	140	7.383	6.937	-0.446
100	141	7.432	6.951	-0.481
100	142	7.430	6.934	-0.496
100	143	7.322	6.926	-0.396
100	144	7.297	6.860	-0.437
100	220	7.413	6.962	-0.451
100	221	7.344	6.889	-0.455
100	224	7.369	6.924	-0.445
100	225	7.378	6.906	-0.472
100	226	7.367	6.865	-0.502
100	228	7.440	6.966	-0.474
100	232	7.338	6.905	-0.433
100	233	7.341	6.914	-0.427
100	236	7.352	6.870	-0.482
100	238	7.453	6.967	-0.486
100	243	7.404	6.934	-0.470
105	244	7.456	7.219	-0.237
105	246	7.415	7.279	-0.136
105	248	7.396	7.212	-0.184
105	374	7.362	7.139	-0.223
105	376	7.444	7.273	-0.171
	Max	7.543	7.526	0.013
	Average	7.395	7.156	-0.239
	Min	7.270	6.833	-0.502
	Std Dev	0.066	0.200	0.177



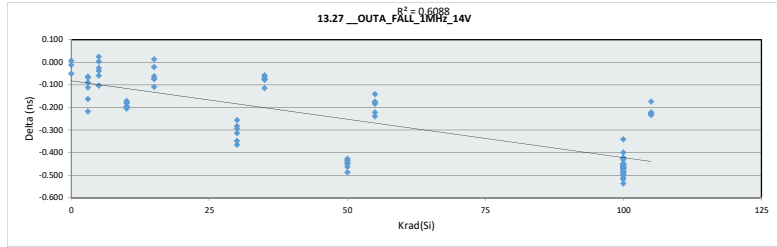
13.19_OUTA_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.284	7.211	7.254	7.150	7.207	7.069	7.240	6.833	7.135	6.860	7.139
Average	7.381	7.327	7.383	7.258	7.377	7.160	7.347	6.906	7.220	6.933	7.224
Max	7.433	7.469	7.489	7.334	7.526	7.291	7.431	6.958	7.297	7.048	7.279
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



LDR TID Report  
TPS7H5001-SP

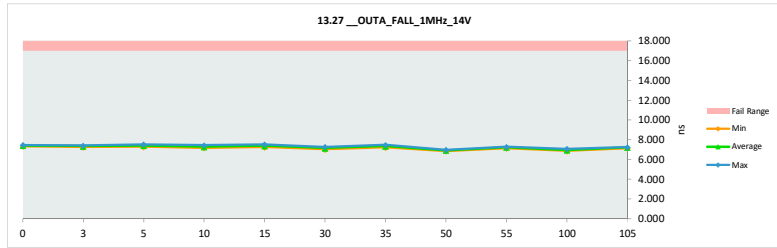
13.27 OUTA_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.454	7.463	0.009
0	118	7.340	7.329	-0.011
0	122	7.436	7.387	-0.049
3	61	7.450	7.388	-0.062
3	62	7.630	7.414	-0.216
3	63	7.447	7.360	-0.087
3	64	7.412	7.251	-0.161
3	65	7.346	7.280	-0.066
3	66	7.508	7.398	-0.110
5	67	7.271	7.276	0.005
5	68	7.533	7.497	-0.036
5	69	7.441	7.340	-0.101
5	70	7.350	7.326	-0.024
5	71	7.448	7.390	-0.058
5	72	7.509	7.535	0.026
10	73	7.450	7.246	-0.204
10	74	7.630	7.438	-0.192
10	75	7.447	7.278	-0.169
10	76	7.412	7.216	-0.196
10	77	7.346	7.171	-0.175
10	78	7.508	7.328	-0.180
15	79	7.271	7.252	-0.019
15	80	7.533	7.473	-0.060
15	81	7.441	7.370	-0.071
15	82	7.350	7.279	-0.071
15	83	7.448	7.341	-0.107
15	84	7.509	7.524	0.015
30	86	7.450	7.158	-0.292
30	89	7.630	7.266	-0.364
30	91	7.447	7.193	-0.254
30	92	7.412	7.131	-0.281
30	94	7.346	7.033	-0.313
30	95	7.508	7.161	-0.347
35	100	7.271	7.215	-0.056
35	101	7.533	7.468	-0.065
35	102	7.441	7.366	-0.075
35	103	7.350	7.275	-0.075
35	104	7.448	7.335	-0.113
35	105	7.509	7.435	-0.074
50	106	7.381	6.920	-0.461
50	107	7.333	6.847	-0.486
50	108	7.406	6.973	-0.433
50	109	7.315	6.867	-0.448
50	110	7.323	6.897	-0.426
50	111	7.390	6.948	-0.442
55	113	7.382	7.208	-0.174
55	114	7.437	7.255	-0.182
55	115	7.404	7.264	-0.140
55	116	7.447	7.273	-0.174
55	126	7.375	7.138	-0.237
55	128	7.498	7.278	-0.220
100	129	7.352	6.905	-0.447
100	131	7.466	7.036	-0.430
100	132	7.464	7.042	-0.422
100	133	7.437	6.962	-0.475
100	134	7.435	6.968	-0.467
100	135	7.411	7.071	-0.340
100	138	7.362	6.941	-0.421
100	139	7.360	6.875	-0.485
100	140	7.435	6.926	-0.509
100	141	7.433	6.978	-0.455
100	142	7.478	6.943	-0.535
100	143	7.414	6.946	-0.468
100	144	7.329	6.871	-0.458
100	220	7.456	6.940	-0.516
100	221	7.376	6.878	-0.498
100	224	7.388	6.922	-0.466
100	225	7.405	6.917	-0.488
100	226	7.390	6.896	-0.494
100	228	7.475	7.013	-0.462
100	232	7.323	6.925	-0.398
100	233	7.334	6.881	-0.453
100	236	7.394	6.929	-0.465
100	238	7.460	7.012	-0.448
100	243	7.400	6.917	-0.483
105	244	7.444	7.216	-0.228
105	246	7.486	7.262	-0.224
105	248	7.411	7.239	-0.172
105	374	7.372	7.140	-0.232
105	376	7.452	7.232	-0.220
105	Max	7.630	7.535	0.026
105	Average	7.424	7.165	-0.258
105	Min	7.271	6.847	-0.535
105	Std Dev	0.074	0.200	0.176



13.27 OUTA_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Max Limit	17 ns
Min Limit	ns

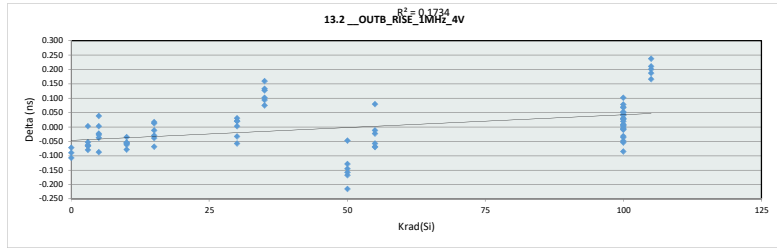
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.329	7.251	7.276	7.171	7.252	7.033	7.215	6.847	7.138	6.871	7.140
Average	7.393	7.349	7.394	7.280	7.373	7.157	7.349	6.909	7.236	6.946	7.218
Max	7.463	7.414	7.535	7.438	7.524	7.266	7.468	6.973	7.278	7.071	7.262
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



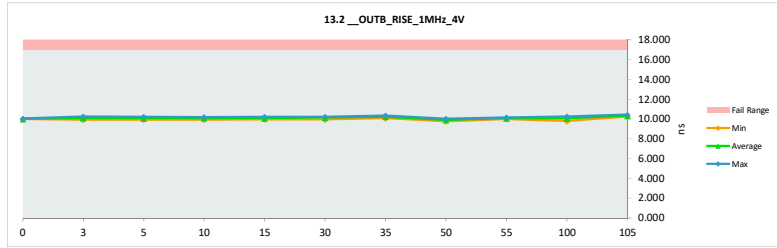
LDR TID Report  
TPS7H5001-SP

13.2 OUTB_RISE_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	10.075	10.004	-0.071
0	118	10.100	10.011	-0.089
0	122	10.156	10.050	-0.106
3	61	10.261	10.265	0.004
3	62	10.197	10.132	-0.065
3	63	10.178	10.126	-0.052
3	64	10.014	9.935	-0.079
3	65	10.144	10.080	-0.064
3	66	10.138	10.076	-0.062
5	67	10.116	10.094	-0.022
5	68	10.188	10.227	0.039
5	69	10.095	10.059	-0.036
5	70	9.962	9.936	-0.026
5	71	10.224	10.228	0.004
5	72	10.080	9.993	-0.087
10	73	10.261	10.205	-0.056
10	74	10.197	10.136	-0.061
10	75	10.178	10.144	-0.034
10	76	10.014	9.936	-0.078
10	77	10.144	10.091	-0.053
10	78	10.138	10.084	-0.054
15	79	10.116	10.087	-0.029
15	80	10.188	10.177	-0.011
15	81	10.095	10.027	-0.068
15	82	9.962	9.976	0.014
15	83	10.224	10.242	0.018
15	84	10.080	10.042	-0.038
30	86	10.261	10.229	-0.032
30	89	10.197	10.201	0.004
30	91	10.178	10.209	0.031
30	92	10.014	9.958	-0.056
30	94	10.144	10.166	0.022
30	95	10.138	10.159	0.021
35	100	10.116	10.211	0.095
35	101	10.188	10.349	0.161
35	102	10.095	10.171	0.076
35	103	9.962	10.090	0.128
35	104	10.224	10.358	0.134
35	105	10.080	10.183	0.103
50	106	10.121	9.954	-0.167
50	107	9.831	9.785	-0.046
50	108	10.186	10.042	-0.144
50	109	10.000	9.872	-0.128
50	110	10.123	9.966	-0.157
50	111	10.059	9.845	-0.214
55	113	10.115	10.059	-0.056
55	114	10.156	10.087	-0.069
55	115	10.194	10.126	-0.068
55	116	10.188	10.177	-0.011
55	126	9.982	10.063	0.081
55	128	10.030	10.008	-0.022
100	129	9.958	10.012	0.054
100	131	10.086	10.113	0.027
100	132	10.049	10.152	0.103
100	133	10.105	10.137	0.032
100	134	9.944	9.896	-0.048
100	135	10.010	10.061	0.051
100	138	10.072	10.074	0.002
100	139	9.968	10.011	0.043
100	140	10.127	10.118	-0.009
100	141	10.078	10.072	-0.006
100	142	10.113	10.123	0.010
100	143	10.197	10.268	0.071
100	144	10.026	10.105	0.079
100	220	10.026	10.033	0.007
100	221	10.015	9.931	-0.084
100	224	10.076	10.144	0.068
100	225	10.001	9.970	-0.031
100	226	10.214	10.222	0.008
100	228	10.135	10.165	0.030
100	232	9.829	9.794	-0.035
100	233	10.131	10.174	0.043
100	236	10.242	10.238	-0.004
100	238	10.088	10.035	-0.053
100	243	10.059	10.079	0.020
105	244	10.241	10.443	0.202
105	246	10.045	10.257	0.212
105	248	10.123	10.312	0.189
105	374	10.259	10.426	0.167
105	376	10.076	10.314	0.238
105	Max	10.261	10.443	0.238
105	Average	10.105	10.104	-0.001
105	Min	9.829	9.785	-0.214
105	Std Dev	0.094	0.131	0.087



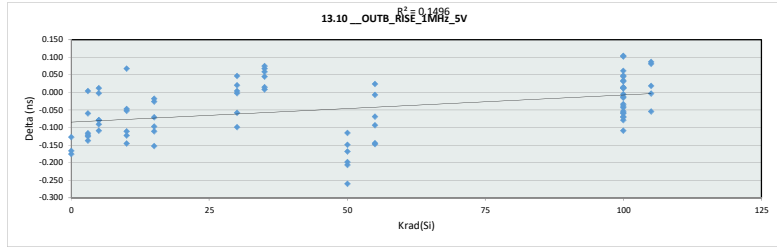
13.2 OUTB_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	10.004	9.935	9.936	9.936	9.976	9.958	10.090	9.785	10.008	9.794	10.257
Average	10.022	10.102	10.090	10.099	10.092	10.154	10.227	9.911	10.087	10.080	10.350
Max	10.050	10.265	10.228	10.205	10.242	10.229	10.358	10.042	10.177	10.268	10.443
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



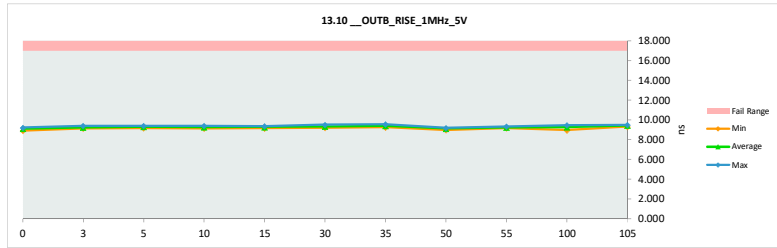
LDR TID Report  
TPS7H5001-SP

13.10_OUTB_RISE_1MHZ_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.404	9.239	-0.165
0	118	9.092	8.918	-0.174
0	122	9.353	9.227	-0.126
3	61	9.532	9.411	-0.121
3	62	9.245	9.186	-0.059
3	63	9.287	9.151	-0.136
3	64	9.312	9.188	-0.124
3	65	9.425	9.310	-0.115
3	66	9.320	9.325	0.005
5	67	9.381	9.394	0.013
5	68	9.387	9.279	-0.108
5	69	9.418	9.340	-0.078
5	70	9.192	9.191	-0.001
5	71	9.508	9.418	-0.090
5	72	9.361	9.283	-0.078
10	73	9.532	9.422	-0.110
10	74	9.245	9.194	-0.051
10	75	9.287	9.143	-0.144
10	76	9.312	9.190	-0.122
10	77	9.425	9.379	-0.046
10	78	9.320	9.389	0.069
15	79	9.381	9.311	-0.070
15	80	9.387	9.370	-0.017
15	81	9.418	9.322	-0.096
15	82	9.192	9.167	-0.025
15	83	9.508	9.356	-0.152
15	84	9.361	9.251	-0.110
30	86	9.532	9.531	-0.001
30	89	9.245	9.250	0.005
30	91	9.287	9.230	-0.057
30	92	9.312	9.214	-0.098
30	94	9.425	9.446	0.021
30	95	9.320	9.368	0.048
35	100	9.381	9.441	0.060
35	101	9.387	9.433	0.046
35	102	9.418	9.428	0.010
35	103	9.192	9.268	0.076
35	104	9.508	9.577	0.069
35	105	9.361	9.377	0.016
50	106	9.388	9.129	-0.259
50	107	9.146	8.998	-0.148
50	108	9.352	9.155	-0.197
50	109	9.280	9.166	-0.114
50	110	9.369	9.202	-0.167
50	111	9.378	9.173	-0.205
55	113	9.356	9.288	-0.068
55	114	9.286	9.194	-0.092
55	115	9.419	9.273	-0.146
55	116	9.498	9.355	-0.143
55	126	9.171	9.196	0.025
55	128	9.263	9.257	-0.006
100	129	9.220	9.323	0.103
100	131	9.266	9.298	0.032
100	132	9.233	9.282	0.049
100	133	9.359	9.302	-0.057
100	134	9.164	9.226	0.062
100	135	9.347	9.239	-0.108
100	138	9.353	9.301	-0.052
100	139	9.193	9.228	0.035
100	140	9.313	9.281	-0.032
100	141	9.408	9.349	-0.059
100	142	9.298	9.313	0.015
100	143	9.501	9.490	-0.011
100	144	9.169	9.215	0.046
100	220	9.315	9.237	-0.078
100	221	9.254	9.250	-0.004
100	224	9.293	9.306	0.013
100	225	9.288	9.274	-0.014
100	226	9.289	9.394	0.105
100	228	9.461	9.391	-0.070
100	232	9.015	8.973	-0.042
100	233	9.418	9.380	-0.038
100	236	9.423	9.355	-0.068
100	238	9.310	9.326	0.016
100	243	9.256	9.269	0.013
105	244	9.469	9.416	-0.053
105	246	9.328	9.326	-0.002
105	248	9.418	9.506	0.088
105	374	9.490	9.510	0.020
105	376	9.346	9.428	0.082
105	Max	9.532	9.577	0.105
105	Average	9.338	9.294	-0.045
105	Min	9.015	8.918	-0.259
105	Std Dev	0.106	0.119	0.080



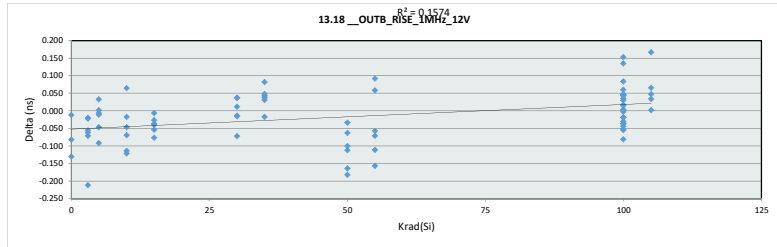
13.10_OUTB_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.918	9.151	9.191	9.143	9.167	9.214	9.268	8.998	9.194	8.973	9.326
Average	9.128	9.262	9.318	9.286	9.296	9.340	9.421	9.137	9.261	9.292	9.437
Max	9.239	9.411	9.418	9.422	9.370	9.531	9.577	9.202	9.355	9.490	9.510
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



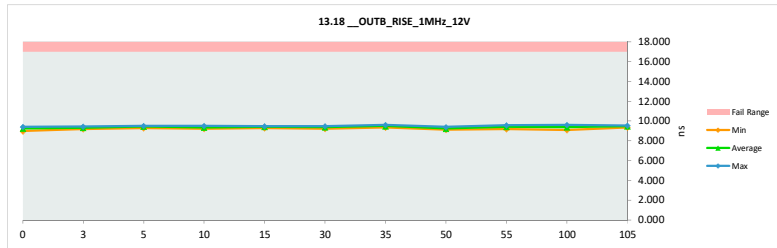
LDR TID Report  
TPS7H5001-SP

13.18_OUTB_RISE_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.520	9.391	-0.129
0	118	9.004	8.993	-0.011
0	122	9.507	9.426	-0.081
3	61	9.522	9.461	-0.061
3	62	9.416	9.206	-0.210
3	63	9.477	9.407	-0.070
3	64	9.346	9.292	-0.054
3	65	9.240	9.219	-0.021
3	66	9.459	9.440	-0.019
5	67	9.481	9.471	-0.010
5	68	9.472	9.506	0.034
5	69	9.536	9.531	-0.005
5	70	9.306	9.309	0.003
5	71	9.529	9.484	-0.045
5	72	9.503	9.412	-0.091
10	73	9.522	9.402	-0.120
10	74	9.416	9.348	-0.068
10	75	9.477	9.364	-0.113
10	76	9.346	9.301	-0.045
10	77	9.240	9.224	-0.016
10	78	9.459	9.525	0.066
15	79	9.481	9.442	-0.039
15	80	9.472	9.420	-0.052
15	81	9.536	9.461	-0.075
15	82	9.306	9.301	-0.005
15	83	9.529	9.494	-0.035
15	84	9.503	9.478	-0.025
30	86	9.522	9.451	-0.071
30	89	9.416	9.455	0.039
30	91	9.477	9.465	-0.012
30	92	9.346	9.359	0.013
30	94	9.240	9.226	-0.014
30	95	9.459	9.498	0.039
35	100	9.481	9.525	0.044
35	101	9.472	9.456	-0.016
35	102	9.536	9.619	0.083
35	103	9.306	9.356	0.050
35	104	9.529	9.568	0.039
35	105	9.503	9.535	0.032
50	106	9.301	9.269	-0.032
50	107	9.227	9.128	-0.099
50	108	9.315	9.152	-0.163
50	109	9.589	9.408	-0.181
50	110	9.260	9.149	-0.111
50	111	9.424	9.362	-0.062
55	113	9.349	9.194	-0.155
55	114	9.528	9.472	-0.056
55	115	9.628	9.558	-0.070
55	116	9.603	9.493	-0.110
55	126	9.281	9.374	0.093
55	128	9.528	9.588	0.060
100	129	9.237	9.274	0.037
100	131	9.435	9.440	0.005
100	132	9.312	9.448	0.136
100	133	9.131	9.133	0.002
100	134	9.312	9.397	0.085
100	135	9.466	9.620	0.154
100	138	9.433	9.478	0.045
100	139	9.265	9.230	-0.035
100	140	9.330	9.279	-0.051
100	141	9.571	9.518	-0.053
100	142	9.463	9.383	-0.080
100	143	9.479	9.495	0.016
100	144	9.318	9.289	-0.029
100	220	9.508	9.491	-0.017
100	221	9.456	9.475	0.019
100	224	9.348	9.379	0.031
100	225	9.211	9.256	0.045
100	226	9.303	9.352	0.049
100	228	9.455	9.503	0.048
100	232	9.151	9.109	-0.042
100	233	9.398	9.459	0.061
100	236	9.444	9.426	-0.018
100	238	9.561	9.560	-0.001
100	243	9.353	9.319	-0.034
105	244	9.437	9.472	0.035
105	246	9.517	9.566	0.049
105	248	9.383	9.551	0.168
105	374	9.357	9.360	0.003
105	376	9.469	9.536	0.067
105	375	9.628	9.620	-0.008
	Max			
	Average	9.413	9.397	-0.016
	Min	9.004	8.993	-0.011
	Std Dev	0.120	0.131	0.072



13.18_OUTB_RISE											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.993	9.206	9.309	9.224	9.301	9.226	9.356	9.128	9.194	9.109	9.360
Average	9.270	9.338	9.452	9.361	9.433	9.409	9.510	9.245	9.447	9.388	9.497
Max	9.426	9.461	9.531	9.525	9.494	9.498	9.619	9.408	9.588	9.620	9.566
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000

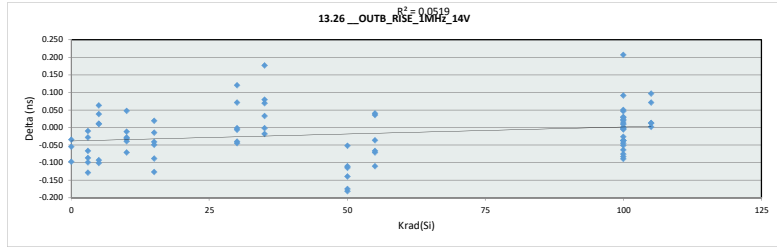




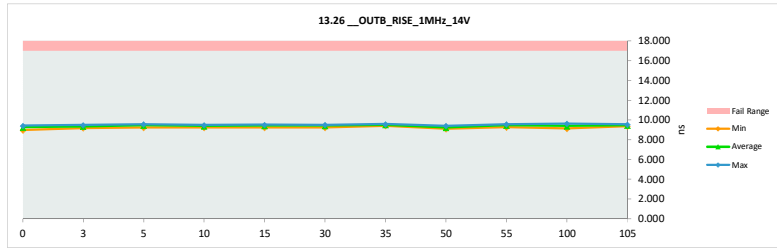
LDR TID Report  
TPS7H5001-SP

13.26_OUTB_RISE_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	9.462	9.366	-0.096
0	118	9.027	8.974	-0.053
0	122	9.459	9.426	-0.033
3	61	9.520	9.422	-0.098
3	62	9.378	9.251	-0.127
3	63	9.466	9.401	-0.065
3	64	9.320	9.311	-0.009
3	65	9.261	9.176	-0.085
3	66	9.505	9.478	-0.027
5	67	9.506	9.518	0.012
5	68	9.453	9.464	0.011
5	69	9.504	9.568	0.064
5	70	9.322	9.230	-0.092
5	71	9.582	9.482	-0.100
5	72	9.374	9.413	0.039
10	73	9.520	9.450	-0.070
10	74	9.378	9.427	0.049
10	75	9.466	9.428	-0.038
10	76	9.320	9.293	-0.027
10	77	9.261	9.230	-0.031
10	78	9.505	9.494	-0.011
15	79	9.506	9.493	-0.013
15	80	9.453	9.405	-0.048
15	81	9.504	9.524	0.020
15	82	9.322	9.235	-0.087
15	83	9.582	9.457	-0.125
15	84	9.374	9.334	-0.040
30	86	9.520	9.481	-0.039
30	89	9.378	9.499	0.121
30	91	9.466	9.461	-0.005
30	92	9.320	9.392	0.072
30	94	9.261	9.218	-0.043
30	95	9.505	9.504	-0.001
35	100	9.506	9.540	0.034
35	101	9.453	9.452	-0.001
35	102	9.504	9.574	0.070
35	103	9.322	9.402	0.080
35	104	9.582	9.565	-0.017
35	105	9.374	9.552	0.178
50	106	9.381	9.267	-0.114
50	107	9.235	9.097	-0.138
50	108	9.227	9.123	-0.104
50	109	9.511	9.402	-0.109
50	110	9.336	9.156	-0.180
50	111	9.422	9.371	-0.051
55	113	9.326	9.256	-0.070
55	114	9.561	9.496	-0.065
55	115	9.577	9.542	-0.035
55	116	9.592	9.483	-0.109
55	126	9.287	9.324	0.037
55	128	9.528	9.569	0.041
100	129	9.256	9.270	0.014
100	131	9.464	9.429	-0.035
100	132	9.456	9.420	-0.036
100	133	9.178	9.128	-0.050
100	134	9.322	9.318	-0.004
100	135	9.530	9.554	0.024
100	138	9.453	9.484	0.031
100	139	9.239	9.239	0.000
100	140	9.335	9.272	-0.063
100	141	9.538	9.567	0.029
100	142	9.460	9.386	-0.074
100	143	9.518	9.475	-0.043
100	144	9.349	9.324	-0.025
100	220	9.513	9.431	-0.082
100	221	9.480	9.531	0.051
100	224	9.372	9.370	-0.002
100	225	9.281	9.193	-0.088
100	226	9.223	9.431	0.208
100	228	9.443	9.453	0.010
100	232	9.164	9.128	-0.036
100	233	9.382	9.430	0.048
100	236	9.397	9.417	0.020
100	238	9.538	9.630	0.092
100	243	9.277	9.279	0.002
105	244	9.364	9.462	0.098
105	246	9.500	9.514	0.014
105	248	9.433	9.447	0.014
105	374	9.352	9.355	0.003
105	376	9.472	9.544	0.072
105	Max	9.592	9.630	0.208
105	Average	9.411	9.393	-0.018
105	Min	9.027	8.974	-0.180
105	Std Dev	0.114	0.134	0.071



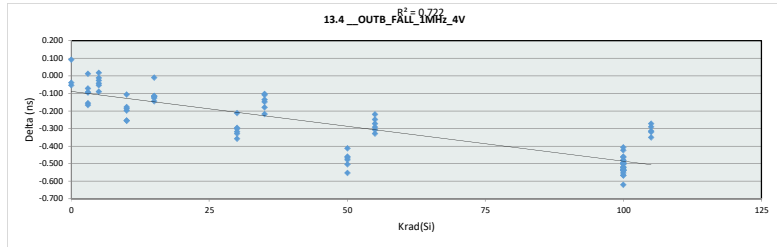
13.26_OUTB_RISE											
Test Site											
Tester											
Test Number											
Min Limit	17 ns										
Max Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	8.974	9.176	9.230	9.230	9.235	9.218	9.402	9.097	9.256	9.128	9.355
Average	9.255	9.340	9.446	9.387	9.408	9.426	9.514	9.236	9.445	9.382	9.464
Max	9.426	9.478	9.568	9.494	9.524	9.504	9.574	9.402	9.569	9.630	9.544
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



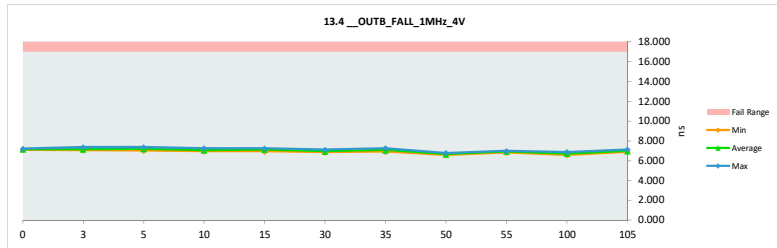
LDR TID Report  
TPS7H5001-SP

13.4_OUTB_FALL_1MHz_4V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	7.123	7.217	0.094
0	118	7.135	7.098	-0.037
0	122	7.212	7.161	-0.051
3	61	7.246	7.154	-0.092
3	62	7.434	7.364	-0.070
3	63	7.173	7.081	-0.092
3	64	7.136	7.149	0.013
3	65	7.216	7.062	-0.154
3	66	7.324	7.159	-0.165
5	67	7.059	7.019	-0.040
5	68	7.345	7.364	0.019
5	69	7.178	7.169	-0.009
5	70	7.194	7.105	-0.089
5	71	7.236	7.185	-0.051
5	72	7.358	7.334	-0.024
10	73	7.246	6.992	-0.254
10	74	7.434	7.258	-0.176
10	75	7.173	6.978	-0.195
10	76	7.136	7.031	-0.105
10	77	7.216	6.964	-0.252
10	78	7.324	7.139	-0.185
15	79	7.059	6.948	-0.111
15	80	7.345	7.220	-0.125
15	81	7.178	7.170	-0.008
15	82	7.194	7.052	-0.142
15	83	7.236	7.119	-0.117
15	84	7.358	7.243	-0.115
30	86	7.246	6.952	-0.294
30	89	7.434	7.118	-0.316
30	91	7.173	6.847	-0.326
30	92	7.136	6.925	-0.211
30	94	7.216	6.916	-0.300
30	95	7.324	6.968	-0.356
35	100	7.059	6.913	-0.146
35	101	7.345	7.239	-0.106
35	102	7.178	7.044	-0.134
35	103	7.194	7.016	-0.178
35	104	7.236	7.021	-0.215
35	105	7.358	7.257	-0.101
50	106	7.245	6.695	-0.550
50	107	7.090	6.588	-0.502
50	108	7.133	6.723	-0.410
50	109	7.038	6.581	-0.457
50	110	7.241	6.776	-0.465
50	111	7.101	6.625	-0.476
55	113	7.231	6.984	-0.247
55	114	7.164	6.838	-0.326
55	115	7.179	6.875	-0.304
55	116	7.268	6.977	-0.291
55	126	7.167	6.949	-0.218
55	128	7.224	6.953	-0.271
100	129	7.198	6.662	-0.536
100	131	7.245	6.729	-0.516
100	132	7.290	6.828	-0.462
100	133	7.349	6.849	-0.500
100	134	7.251	6.701	-0.550
100	135	7.318	6.781	-0.537
100	138	7.163	6.643	-0.520
100	139	7.075	6.581	-0.494
100	140	7.297	6.769	-0.528
100	141	7.160	6.663	-0.497
100	142	7.168	6.711	-0.457
100	143	7.176	6.656	-0.520
100	144	7.053	6.648	-0.405
100	220	7.333	6.795	-0.538
100	221	7.153	6.653	-0.500
100	224	7.197	6.776	-0.421
100	225	7.205	6.727	-0.478
100	226	7.182	6.703	-0.479
100	228	7.435	6.869	-0.566
100	232	7.180	6.687	-0.493
100	233	7.261	6.697	-0.564
100	236	7.111	6.580	-0.531
100	238	7.279	6.743	-0.536
100	243	7.204	6.586	-0.618
105	244	7.293	6.944	-0.349
105	246	7.260	6.943	-0.317
105	248	7.290	6.977	-0.313
105	374	7.184	6.895	-0.289
105	376	7.393	7.122	-0.271
105	Max	7.435	7.364	0.094
105	Average	7.224	6.930	-0.294
105	Min	7.038	6.580	-0.618
105	Std Dev	0.095	0.214	0.189



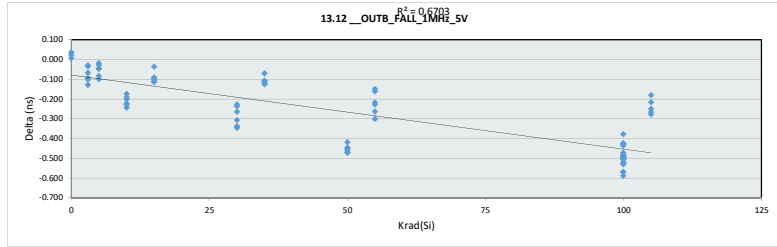
13.4_OUTB_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	7.098	7.062	7.019	6.964	6.948	6.847	6.913	6.581	6.838	6.580	6.895
Average	7.159	7.162	7.196	7.060	7.125	6.954	7.082	6.665	6.929	6.710	6.976
Max	7.217	7.364	7.364	7.258	7.243	7.118	7.257	6.776	6.984	6.869	7.122
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



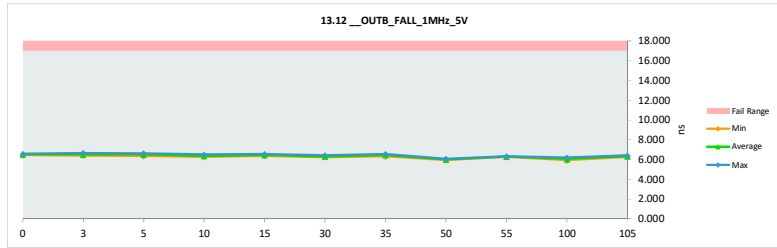
LDR TID Report  
TPS7H5001-SP

13.12_OUTB_FALL_1MHz_5V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.537	6.575	0.038
0	118	6.422	6.447	0.025
0	122	6.532	6.541	0.009
3	61	6.582	6.455	-0.127
3	62	6.696	6.663	-0.033
3	63	6.484	6.393	-0.091
3	64	6.582	6.482	-0.100
3	65	6.488	6.459	-0.029
3	66	6.657	6.591	-0.066
5	67	6.446	6.348	-0.098
5	68	6.651	6.570	-0.081
5	69	6.543	6.526	-0.017
5	70	6.484	6.457	-0.027
5	71	6.560	6.515	-0.045
5	72	6.674	6.629	-0.045
10	73	6.582	6.382	-0.200
10	74	6.696	6.508	-0.188
10	75	6.484	6.312	-0.172
10	76	6.582	6.355	-0.227
10	77	6.488	6.267	-0.221
10	78	6.657	6.415	-0.242
15	79	6.446	6.356	-0.090
15	80	6.651	6.560	-0.091
15	81	6.543	6.509	-0.034
15	82	6.484	6.385	-0.099
15	83	6.560	6.452	-0.108
15	84	6.674	6.562	-0.112
30	86	6.582	6.246	-0.336
30	89	6.696	6.433	-0.263
30	91	6.484	6.248	-0.236
30	92	6.582	6.238	-0.344
30	94	6.488	6.262	-0.226
30	95	6.657	6.352	-0.305
35	100	6.446	6.322	-0.124
35	101	6.651	6.532	-0.119
35	102	6.543	6.435	-0.108
35	103	6.484	6.416	-0.068
35	104	6.560	6.454	-0.106
35	105	6.674	6.567	-0.107
50	106	6.507	6.063	-0.444
50	107	6.441	5.970	-0.471
50	108	6.504	6.025	-0.469
50	109	6.396	5.939	-0.457
50	110	6.482	6.066	-0.416
50	111	6.452	6.005	-0.447
55	113	6.497	6.337	-0.160
55	114	6.465	6.249	-0.216
55	115	6.537	6.311	-0.226
55	116	6.605	6.343	-0.262
55	126	6.448	6.300	-0.148
55	128	6.569	6.270	-0.299
100	129	6.532	6.013	-0.519
100	131	6.546	6.054	-0.492
100	132	6.589	6.167	-0.422
100	133	6.595	6.168	-0.427
100	134	6.527	6.033	-0.494
100	135	6.640	6.072	-0.568
100	138	6.502	6.013	-0.489
100	139	6.470	5.941	-0.529
100	140	6.586	6.098	-0.488
100	141	6.568	6.086	-0.482
100	142	6.590	6.119	-0.471
100	143	6.394	6.018	-0.376
100	144	6.458	5.975	-0.483
100	220	6.622	6.125	-0.497
100	221	6.554	6.063	-0.491
100	224	6.549	6.046	-0.503
100	225	6.644	6.058	-0.586
100	226	6.515	5.992	-0.523
100	228	6.696	6.200	-0.496
100	232	6.434	6.000	-0.434
100	233	6.621	6.116	-0.505
100	236	6.449	5.927	-0.522
100	238	6.612	6.045	-0.567
100	243	6.532	6.036	-0.496
105	244	6.587	6.373	-0.214
105	246	6.525	6.346	-0.179
105	248	6.555	6.306	-0.249
105	374	6.517	6.253	-0.264
105	376	6.694	6.418	-0.276
	Max	6.696	6.663	0.038
	Average	6.550	6.277	-0.273
	Min	6.394	5.927	-0.586
	Std Dev	0.079	0.204	0.185



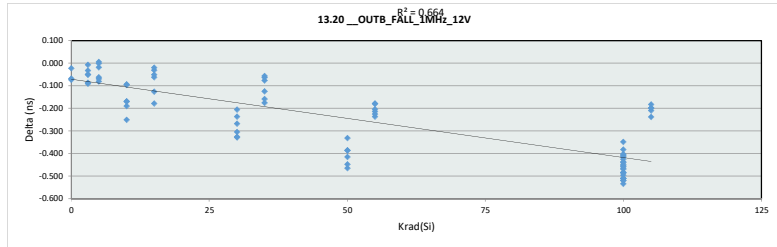
13.12_OUTB_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.447	6.393	6.348	6.267	6.356	6.238	6.322	5.939	6.249	5.927	6.253
Average	6.521	6.507	6.508	6.373	6.471	6.297	6.454	6.013	6.302	6.057	6.339
Max	6.575	6.663	6.629	6.508	6.562	6.433	6.567	6.066	6.343	6.200	6.418
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



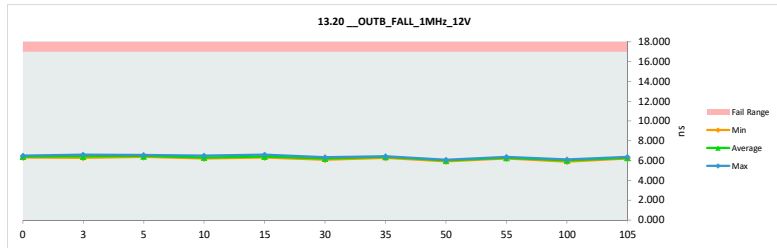
LDR TID Report  
TPS7H5001-SP

13.20 OUTB_FALL_1MHz_12V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.548	6.528	-0.020
0	118	6.408	6.337	-0.071
0	122	6.494	6.428	-0.066
3	61	6.524	6.440	-0.084
3	62	6.621	6.616	-0.005
3	63	6.439	6.408	-0.031
3	64	6.486	6.397	-0.089
3	65	6.330	6.281	-0.049
3	66	6.553	6.505	-0.048
5	67	6.375	6.382	0.007
5	68	6.584	6.585	0.001
5	69	6.494	6.477	-0.017
5	70	6.429	6.369	-0.060
5	71	6.527	6.460	-0.067
5	72	6.624	6.545	-0.079
10	73	6.524	6.275	-0.249
10	74	6.494	6.529	-0.034
10	75	6.439	6.271	-0.168
10	76	6.486	6.320	-0.166
10	77	6.330	6.236	-0.094
10	78	6.553	6.365	-0.188
15	79	6.375	6.326	-0.049
15	80	6.584	6.460	-0.124
15	81	6.494	6.434	-0.060
15	82	6.429	6.400	-0.029
15	83	6.527	6.350	-0.177
15	84	6.624	6.606	-0.018
30	86	6.524	6.198	-0.326
30	89	6.621	6.355	-0.266
30	91	6.439	6.235	-0.204
30	92	6.486	6.162	-0.324
30	94	6.330	6.096	-0.234
30	95	6.553	6.250	-0.303
35	100	6.375	6.301	-0.074
35	101	6.584	6.461	-0.123
35	102	6.494	6.440	-0.054
35	103	6.429	6.367	-0.062
35	104	6.527	6.370	-0.157
35	105	6.624	6.451	-0.173
50	106	6.430	6.101	-0.329
50	107	6.379	5.933	-0.446
50	108	6.402	6.018	-0.384
50	109	6.408	6.023	-0.385
50	110	6.448	6.035	-0.413
50	111	6.439	5.977	-0.462
55	113	6.436	6.259	-0.177
55	114	6.497	6.274	-0.223
55	115	6.542	6.330	-0.212
55	116	6.571	6.336	-0.235
55	126	6.397	6.219	-0.178
55	128	6.603	6.401	-0.202
100	129	6.425	5.972	-0.453
100	131	6.541	6.033	-0.508
100	132	6.429	6.083	-0.346
100	133	6.482	6.036	-0.446
100	134	6.478	6.042	-0.436
100	135	6.525	6.145	-0.380
100	138	6.431	6.022	-0.409
100	139	6.423	5.905	-0.518
100	140	6.435	5.981	-0.454
100	141	6.549	6.033	-0.516
100	142	6.532	6.024	-0.508
100	143	6.497	6.030	-0.467
100	144	6.477	5.994	-0.483
100	220	6.562	6.068	-0.494
100	221	6.469	6.056	-0.413
100	224	6.436	6.035	-0.401
100	225	6.372	5.949	-0.423
100	226	6.445	6.009	-0.436
100	228	6.576	6.092	-0.484
100	232	6.420	6.016	-0.404
100	233	6.501	6.039	-0.462
100	236	6.432	5.951	-0.481
100	238	6.528	6.020	-0.508
100	243	6.508	5.976	-0.532
105	244	6.511	6.275	-0.236
105	246	6.559	6.362	-0.197
105	248	6.441	6.244	-0.197
105	374	6.449	6.242	-0.207
105	376	6.566	6.385	-0.181
105	Max	6.624	6.616	0.007
105	Average	6.487	6.237	-0.250
105	Min	6.330	5.905	-0.532
105	Std Dev	0.075	0.194	0.172



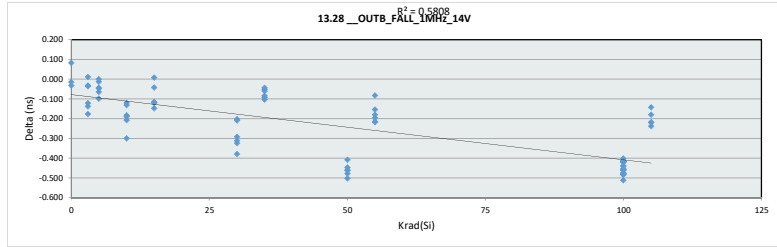
13.20 OUTB_FALL	
Test Site	
Tester	
Test Number	
Min Limit	17 ns
Max Limit	ns
Krad(Si)	0 3 5 10 15 30 35 50 55 100 105
LL	
Min	6.337 6.281 6.369 6.236 6.326 6.096 6.301 5.933 6.219 5.905 6.242
Average	6.431 6.441 6.470 6.333 6.429 6.216 6.398 6.015 6.303 6.021 6.302
Max	6.528 6.616 6.585 6.529 6.606 6.355 6.461 6.101 6.401 6.145 6.385
UL	17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000 17.000



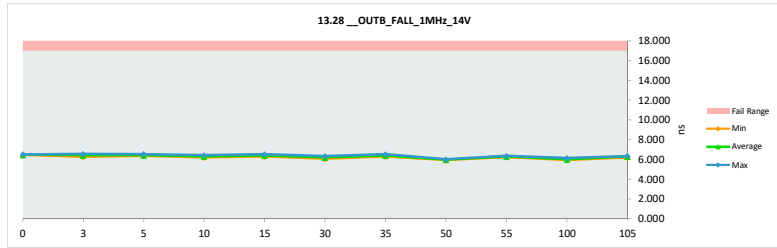
LDR TID Report  
TPS7H5001-SP

13.28_OUTB_FALL_1MHz_14V	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	17 17
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	6.550	6.520	-0.030
0	118	6.366	6.450	0.084
0	122	6.492	6.480	-0.012
3	61	6.490	6.459	-0.031
3	62	6.556	6.569	0.013
3	63	6.541	6.366	-0.175
3	64	6.431	6.397	-0.034
3	65	6.403	6.268	-0.135
3	66	6.627	6.508	-0.119
5	67	6.357	6.359	0.002
5	68	6.563	6.553	-0.010
5	69	6.478	6.416	-0.062
5	70	6.428	6.384	-0.044
5	71	6.488	6.446	-0.042
5	72	6.626	6.529	-0.097
10	73	6.490	6.310	-0.180
10	74	6.556	6.438	-0.118
10	75	6.541	6.354	-0.187
10	76	6.431	6.302	-0.129
10	77	6.403	6.198	-0.205
10	78	6.627	6.329	-0.298
15	79	6.357	6.368	0.011
15	80	6.563	6.523	-0.040
15	81	6.478	6.360	-0.118
15	82	6.428	6.312	-0.116
15	83	6.488	6.375	-0.113
15	84	6.626	6.481	-0.145
30	86	6.490	6.200	-0.290
30	89	6.556	6.350	-0.206
30	91	6.541	6.232	-0.309
30	92	6.431	6.230	-0.201
30	94	6.403	6.081	-0.322
30	95	6.627	6.250	-0.377
35	100	6.357	6.299	-0.058
35	101	6.563	6.514	-0.049
35	102	6.478	6.388	-0.090
35	103	6.428	6.346	-0.082
35	104	6.488	6.447	-0.041
35	105	6.626	6.525	-0.101
50	106	6.446	5.989	-0.457
50	107	6.414	5.955	-0.459
50	108	6.427	6.021	-0.406
50	109	6.433	5.933	-0.500
50	110	6.428	5.983	-0.445
50	111	6.442	5.966	-0.476
55	113	6.420	6.268	-0.152
55	114	6.466	6.253	-0.213
55	115	6.522	6.328	-0.194
55	116	6.536	6.322	-0.214
55	126	6.328	6.247	-0.081
55	128	6.537	6.359	-0.178
100	129	6.396	5.940	-0.456
100	131	6.506	5.997	-0.509
100	132	6.435	6.023	-0.412
100	133	6.500	6.024	-0.476
100	134	6.486	6.004	-0.482
100	135	6.531	6.117	-0.414
100	138	6.427	6.027	-0.400
100	139	6.390	5.972	-0.418
100	140	6.445	6.009	-0.436
100	141	6.512	6.040	-0.472
100	142	6.535	6.058	-0.477
100	143	6.473	6.015	-0.458
100	144	6.465	5.987	-0.478
100	220	6.557	6.143	-0.414
100	221	6.465	6.050	-0.415
100	224	6.454	5.996	-0.458
100	225	6.405	5.966	-0.439
100	226	6.427	5.950	-0.477
100	228	6.586	6.110	-0.476
100	232	6.420	6.003	-0.417
100	233	6.459	6.004	-0.455
100	236	6.422	5.971	-0.451
100	238	6.484	6.071	-0.413
100	243	6.409	5.996	-0.413
105	244	6.544	6.327	-0.217
105	246	6.536	6.358	-0.178
105	248	6.439	6.299	-0.140
105	374	6.449	6.213	-0.236
105	376	6.565	6.348	-0.217
	Max	6.627	6.569	0.058
	Average	6.481	6.232	-0.249
	Min	6.328	5.933	-0.509
	Std Dev	0.072	0.195	0.175



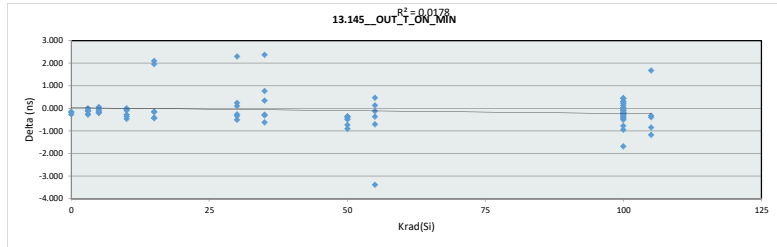
13.28_OUTB_FALL											
Test Site											
Tester											
Test Number											
Max Limit	17 ns										
Min Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	6.450	6.268	6.359	6.198	6.312	6.081	6.299	5.933	6.247	5.940	6.213
Average	6.483	6.428	6.448	6.322	6.403	6.224	6.420	5.975	6.296	6.020	6.309
Max	6.520	6.569	6.553	6.438	6.523	6.350	6.525	6.021	6.359	6.143	6.358
UL	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000	17.000



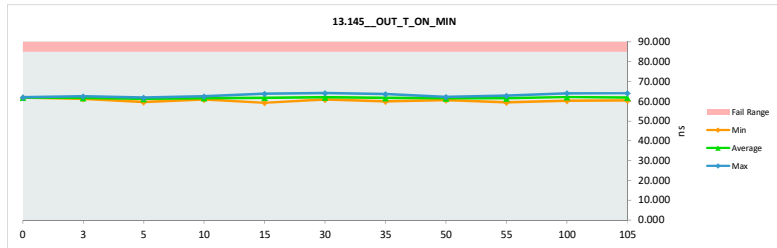
LDR TID Report  
TPS7H5001-SP

13.145_OUT_T_ON_MIN	
Test Site	
Tester	
Test Number	
Unit	ns ns
Max Limit	85 85
Min Limit	

Krad(Si)	Serial #	PRE	POST	Delta
0	117	62.194	62.046	-0.148
0	118	61.936	61.809	-0.127
0	122	62.069	61.827	-0.242
3	61	62.508	62.529	0.021
3	62	61.780	61.681	-0.099
3	63	61.428	61.182	-0.246
3	64	61.685	61.628	-0.057
3	65	61.842	61.745	-0.097
3	66	61.761	61.518	-0.243
5	67	61.840	61.664	-0.176
5	68	61.378	61.311	-0.067
5	69	62.081	61.906	-0.175
5	70	61.278	61.357	0.079
5	71	61.598	61.470	-0.128
5	72	59.635	59.636	0.001
10	73	62.508	62.471	-0.037
10	74	61.780	61.808	0.028
10	75	61.428	60.976	-0.452
10	76	61.685	61.609	-0.076
10	77	61.842	61.582	-0.260
10	78	61.761	61.435	-0.326
15	79	61.840	63.812	1.972
15	80	61.378	61.261	-0.117
15	81	62.081	61.928	-0.153
15	82	61.278	63.394	2.116
15	83	61.598	61.210	-0.388
15	84	59.635	59.219	-0.416
30	86	62.508	62.266	-0.242
30	89	61.780	64.094	2.314
30	91	61.428	60.934	-0.494
30	92	61.685	61.803	0.118
30	94	61.842	62.100	0.258
30	95	61.761	61.448	-0.313
35	100	61.840	61.554	-0.286
35	101	61.378	60.774	-0.604
35	102	62.081	61.814	-0.267
35	103	61.278	63.660	2.382
35	104	61.598	62.384	0.786
35	105	59.635	60.000	0.365
50	106	61.004	60.652	-0.352
50	107	62.561	62.156	-0.405
50	108	62.694	61.975	-0.719
50	109	62.422	61.540	-0.882
50	110	61.625	61.146	-0.479
50	111	61.945	61.610	-0.335
55	113	60.862	61.016	0.154
55	114	63.003	62.902	-0.101
55	115	60.109	59.429	-0.680
55	116	65.227	61.862	-3.365
55	126	62.586	62.236	-0.350
55	128	61.675	62.166	0.491
100	129	60.544	60.286	-0.258
100	131	62.657	63.113	0.456
100	132	60.800	60.752	-0.048
100	133	61.969	61.653	-0.316
100	134	62.743	62.261	-0.482
100	135	63.668	63.525	-0.143
100	138	61.265	60.930	-0.335
100	139	63.211	63.268	0.057
100	140	61.700	60.938	-0.762
100	141	63.559	63.870	0.311
100	142	61.470	61.061	-0.409
100	143	62.422	62.462	0.040
100	144	62.144	61.917	-0.227
100	220	60.954	60.887	-0.067
100	221	63.467	63.614	0.147
100	224	62.128	62.069	-0.059
100	225	63.476	63.709	0.233
100	226	63.103	61.443	-1.660
100	228	61.497	60.573	-0.924
100	232	62.399	61.964	-0.435
100	233	63.745	64.080	0.335
100	236	63.672	63.499	-0.173
100	238	61.054	61.532	0.478
100	243	62.175	61.998	-0.177
105	244	62.614	62.244	-0.370
105	246	62.715	61.565	-1.150
105	248	60.732	60.429	-0.303
105	374	61.925	61.092	-0.833
105	376	62.411	64.104	1.693
105	375	65.227	64.104	2.382
Max		61.932	61.830	-0.102
Average		61.932	61.830	-0.102
Min		59.635	59.219	-3.365
Std Dev		0.941	1.060	0.767



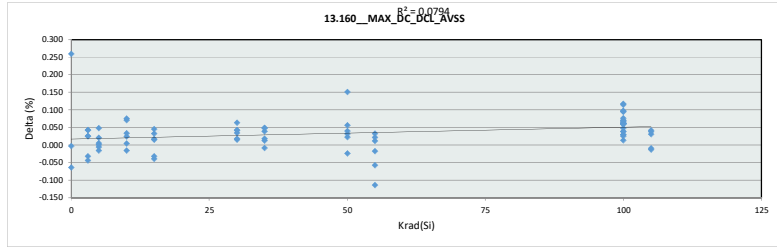
13.145_OUT_T_ON_MIN											
Test Site											
Tester											
Test Number											
Min Limit	85 ns										
Max Limit	ns										
Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL											
Min	61.809	61.182	59.636	60.976	59.219	60.934	60.000	60.652	59.429	60.286	60.429
Average	61.894	61.714	61.224	61.647	61.804	62.108	61.698	61.513	61.602	62.142	61.887
Max	62.046	62.529	61.906	62.471	63.812	64.094	63.660	62.156	62.902	64.080	64.104
UL	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000	85.000



LDR TID Report  
TPS7H5001-SP

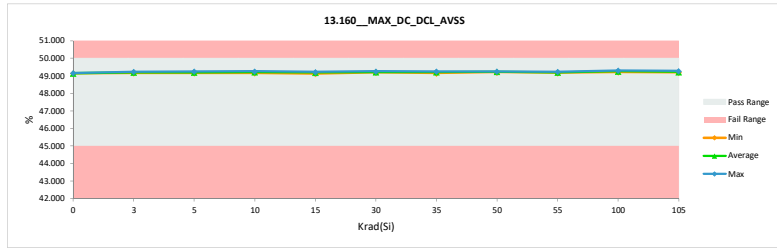
13.160_MAX_DC_DCL_AVSS	
Test Site	
Tester	
Test Number	
Unit	% %
Max Limit	50 50
Min Limit	45 45

Krad(Si)	Serial #	PRE	POST	Delta
0	117	49.165	49.163	-0.002
0	118	48.890	49.151	0.261
0	122	49.215	49.152	-0.063
3	61	49.186	49.213	0.027
3	62	49.168	49.212	0.044
3	63	49.194	49.238	0.044
3	64	49.168	49.195	0.027
3	65	49.232	49.201	-0.031
3	66	49.210	49.167	-0.043
5	67	49.186	49.172	-0.014
5	68	49.161	49.163	0.002
5	69	49.198	49.194	-0.004
5	70	49.216	49.265	0.049
5	71	49.211	49.218	0.007
5	72	49.168	49.190	0.022
10	73	49.186	49.258	0.072
10	74	49.168	49.203	0.035
10	75	49.194	49.271	0.077
10	76	49.168	49.174	0.006
10	77	49.232	49.258	0.026
10	78	49.210	49.196	-0.014
15	79	49.186	49.220	0.034
15	80	49.161	49.208	0.047
15	81	49.198	49.216	0.018
15	82	49.216	49.233	0.017
15	83	49.211	49.173	-0.038
15	84	49.168	49.137	-0.031
30	86	49.186	49.205	0.019
30	89	49.168	49.211	0.043
30	91	49.194	49.238	0.044
30	92	49.168	49.233	0.065
30	94	49.232	49.268	0.036
30	95	49.210	49.227	0.017
35	100	49.186	49.226	0.040
35	101	49.161	49.210	0.049
35	102	49.198	49.217	0.019
35	103	49.216	49.230	0.014
35	104	49.211	49.261	0.050
35	105	49.168	49.161	-0.007
50	106	49.180	49.221	0.041
50	107	49.094	49.246	0.152
50	108	49.202	49.235	0.033
50	109	49.197	49.255	0.058
50	110	49.233	49.257	0.024
50	111	49.239	49.216	-0.023
55	113	49.156	49.190	0.034
55	114	49.215	49.199	-0.016
55	115	49.293	49.180	-0.113
55	116	49.268	49.212	-0.056
55	126	49.215	49.238	0.023
55	128	49.179	49.192	0.013
100	129	49.185	49.281	0.096
100	131	49.170	49.269	0.099
100	132	49.208	49.247	0.039
100	133	49.174	49.236	0.062
100	134	49.161	49.223	0.062
100	135	49.198	49.270	0.072
100	138	49.225	49.287	0.062
100	139	49.160	49.279	0.119
100	140	49.210	49.241	0.031
100	141	49.171	49.241	0.070
100	142	49.216	49.256	0.040
100	143	49.256	49.283	0.027
100	144	49.222	49.290	0.068
100	220	49.194	49.310	0.116
100	221	49.240	49.301	0.061
100	224	49.239	49.288	0.049
100	225	49.184	49.247	0.063
100	226	49.215	49.293	0.078
100	228	49.194	49.254	0.060
100	232	49.264	49.294	0.030
100	233	49.188	49.252	0.064
100	236	49.276	49.291	0.015
100	238	49.172	49.270	0.098
100	243	49.176	49.240	0.064
105	244	49.203	49.246	0.043
105	246	49.184	49.216	0.032
105	248	49.222	49.210	-0.012
105	374	49.295	49.287	-0.008
105	376	49.174	49.213	0.039
105	Max	49.295	49.310	0.015
105	Average	49.195	49.230	0.035
105	Min	48.890	49.137	-0.113
105	Std Dev	0.048	0.040	0.049



13.160_MAX_DC	
Test Site	
Tester	
Test Number	
Max Limit	50 %
Min Limit	45 %

Krad(Si)	0	3	5	10	15	30	35	50	55	100	105
LL	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000	45.000
Min	49.151	49.167	49.163	49.174	49.137	49.205	49.161	49.216	49.180	49.223	49.210
Average	49.155	49.204	49.200	49.227	49.198	49.230	49.218	49.238	49.202	49.268	49.234
Max	49.163	49.238	49.265	49.271	49.233	49.268	49.261	49.257	49.238	49.310	49.287
UL	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000	50.000



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