

User's Guide

MCF8329A Tuning Guide



ABSTRACT

This tuning guide provides step-by-step guidance to setup the MCF8329EVM, connect the MCF8329EVM to Motor Studio, and tune a 3-phase brushless DC motor using the MCF8329A motor driver.

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1 Introduction

The MCF8329A is a 4.5-V to 60-V, three-phase brushless-DC (BLDC) gate driver IC with code-free sensorless field oriented control (FOC) for motor drive applications. The device integrates a single shunt current sense amplifier (CSA) along with an external shunt resistance to sense the motor current. This document helps customers to set up the MCF8329A, enabling them to experience the devices' powerful performance and flexible programmability.

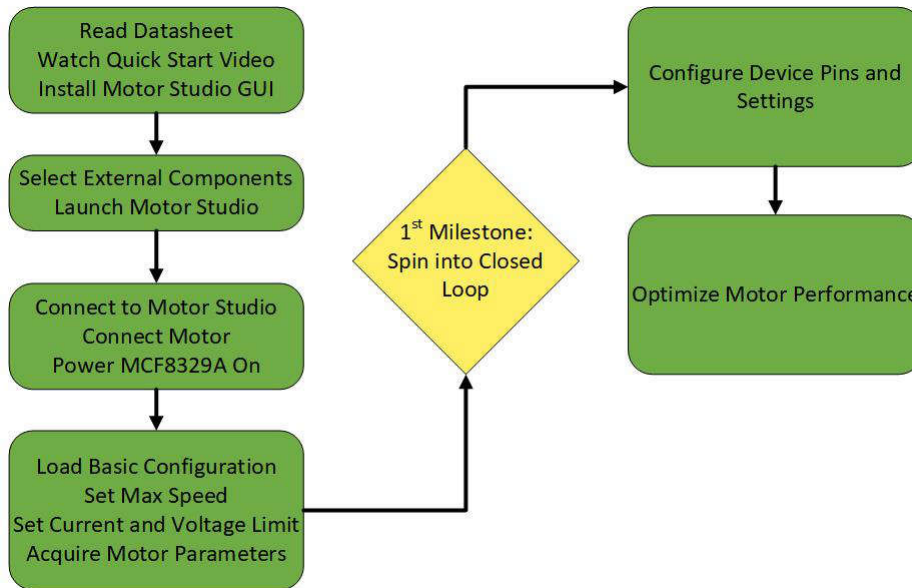


Figure 1-1. Tuning Sequence of Events

Note

Before proceeding through this tuning guide make sure to do the following:

1. Read the [MCF8329A Sensorless Field Oriented Control \(FOC\) Three-phase BLDC Gate Driver Data Sheet](#), [MCF8329EVM User's Guide](#), and watch the quick start video.
2. Get a [MCF8329EVM](#) board.
3. Install the [Motor Studio](#) application.

2 Hardware Design and Setup

The goal of this section is to help users select appropriate components for the external power stage components of the MCF8329A and set up the user configurable settings of the MCF8329EVM.

2.1 Board Design

The following sections provide equations and guidelines for selecting power stage components to achieve desired performance from the motor driver system.

2.1.1 External MOSFET Selection

The MOSFETs for the external half bridge that can be supported by the MCF8329A can be determined by inputting the MOSFET gate charge, output PWM switching frequency, and PVDD voltage into the [Max Qg MOSFET Calculator Tool](#) available on ti.com.

2.1.2 Gate Resistor Selection

Selection of an appropriate gate resistance to limit the gate drive current so that the drain-to-source voltage slew rate (VDS) is set to an appropriate level for the external MOSFETS is essential to achieving good system performance. For more information on the importance of and how to select an appropriate gate resistor value, see the *Gate Drive Current and Gate Resistor Selection* sections in the [MCF8329A Sensorless Field Oriented Control \(FOC\) Three-phase BLDC Gate Driver Data Sheet](#). To simplify the gate resistance selection process, the [Gate Resistor Calculator](#) can be used to estimate the gate resistance required to achieve a desired VDS rise and fall time with an accuracy of $\pm 30\%$.

2.1.3 Bootstrap and GVDD Capacitor Selection

The bootstrap and GVDD capacitors must both be sized appropriately to maintain the bootstrap voltage above the under-voltage lockout threshold during normal operation. For instructions to determine an appropriate capacitance for both the bootstrap capacitors and GVDD capacitor, see the *Bootstrap Capacitor and GVDD Capacitor Selection* sections of the [MCF8329A Sensorless Field Oriented Control \(FOC\) Three-phase BLDC Gate Driver Data Sheet](#).

2.1.4 Current Shunt Resistor Selection

The internal FOC algorithm uses the output of the internal current sense amplifier (CSA) in its computations. It is recommended to set the max measurable current of the internal CSA to 10% above the motors stall current. To determine an appropriate value for the CSA gain and external low-side shunt resistor, see section 7.3.5 of the [MCF8329A Sensorless Field Oriented Control \(FOC\) Three-phase BLDC Gate Driver Data Sheet](#).

2.1.5 VREG MOSFET Selection

The GCTRL pin can be used to drive an external MOSFET that can be used as a voltage regulator to provide power to the VREG pin to reduce power dissipation within the MCF8329A. Instructions on how to select an appropriate MOSFET are provided in section 8.2.1 of the [MCF8329A Sensorless Field Oriented Control \(FOC\) Three-phase BLDC Gate Driver Data Sheet](#).

2.1.6 Additional External Power Stage Components

For additional considerations for external components of the MCF8329A and for high power systems, see the *System Considerations in High Power Designs*, *Capacitor Voltage Ratings*, and *External Power Stage Components* sections of the [MCF8329A Sensorless Field Oriented Control \(FOC\) Three-phase BLDC Gate Driver Data Sheet](#).

3 Connecting to the GUI

Before connecting the MCF8329EVM to the computer, start up the Motor Studio application and select MCF8329A from the drop down. Click on *Proceed* and then click on the *Setup Now* button for instructions on how to connect power, connect a motor, and configure the jumpers and switches on the EVM.

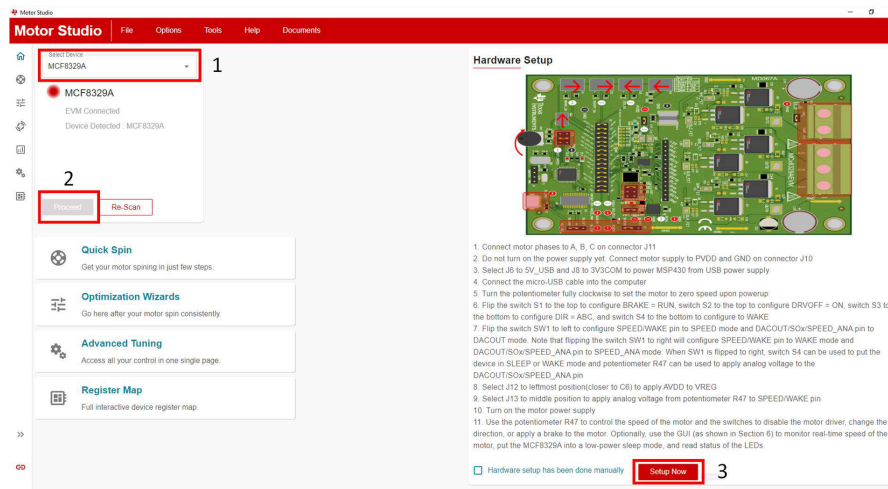


Figure 3-1. EVM Hardware Setup

Once the hardware setup is completed, turn on the power supply connected to the EVM. After the PVDD LED D3 lights up connect a micro-USB to USB cable between the EVM and PC. After a few seconds, Motor Studio should connect to the EVM and the two icons outlined in Figure 3-2 will turn green. If the EVM is not connecting click the *Re-Scan* button.

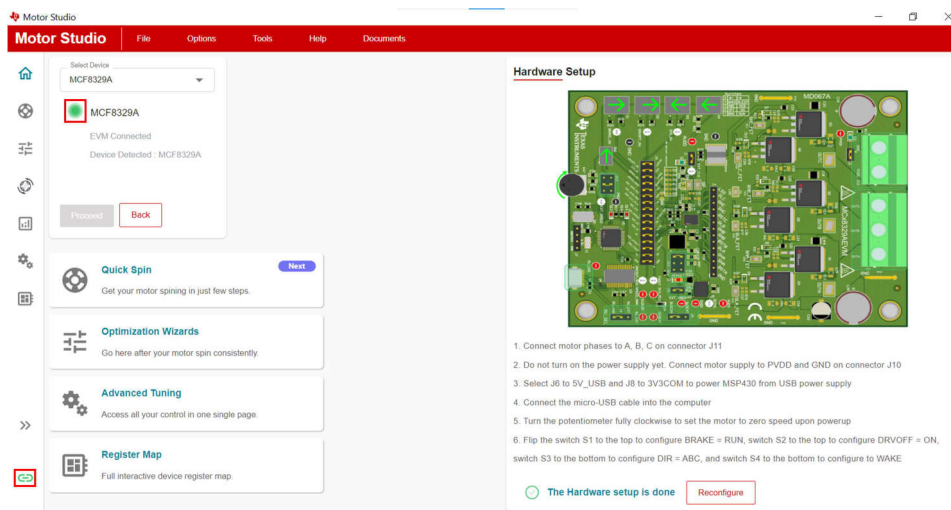


Figure 3-2. EVM Connected Indicators

Note

If the GUI is not able to connect to the GUI after a minute, disconnect the EVM from the PC, restart the Motor Studio GUI. After Motor Studio has started again, reconnect the EVM to the PC.

4 Spinning Into Closed Loop

This section provides standardized steps to tune the MCF8329A's settings so that the motor can successfully spin-up and enter closed loop control.

The general steps to tune the MCF8329A's registers so the motor can spin-up and enter closed loop control are outlined in [Figure 4-1](#).

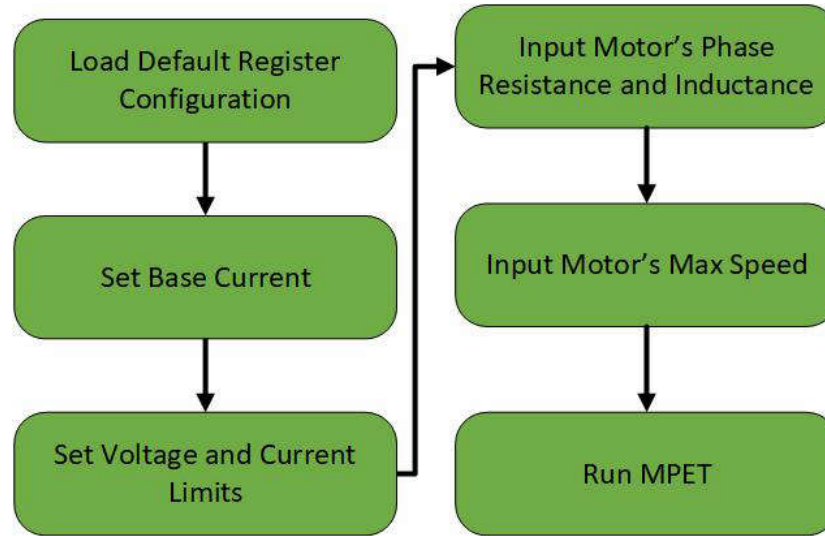


Figure 4-1. Essential Controls Flow Chart

4.1 Essential Configuration

4.1.1 Loading Recommended Default Values

Select the *Quick Spin* option on the Motor Studio Home page or on the menu on the left side of the window. Using the *Select Preset* drop down menu in the *Load Default* section select a register configuration which is most similar to the application use case or the *Default MCF8329A Registers* option. After selecting the desired register configuration, click on the *Load Preset Values* button.

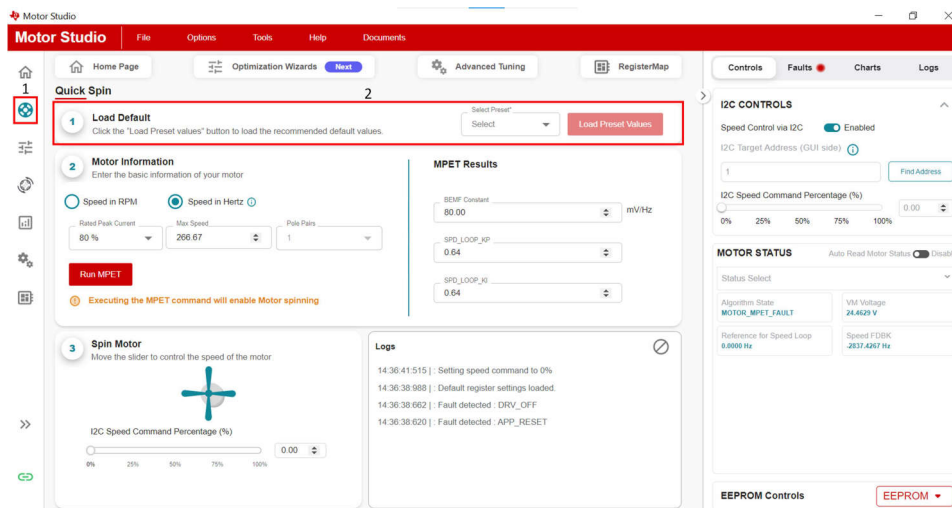


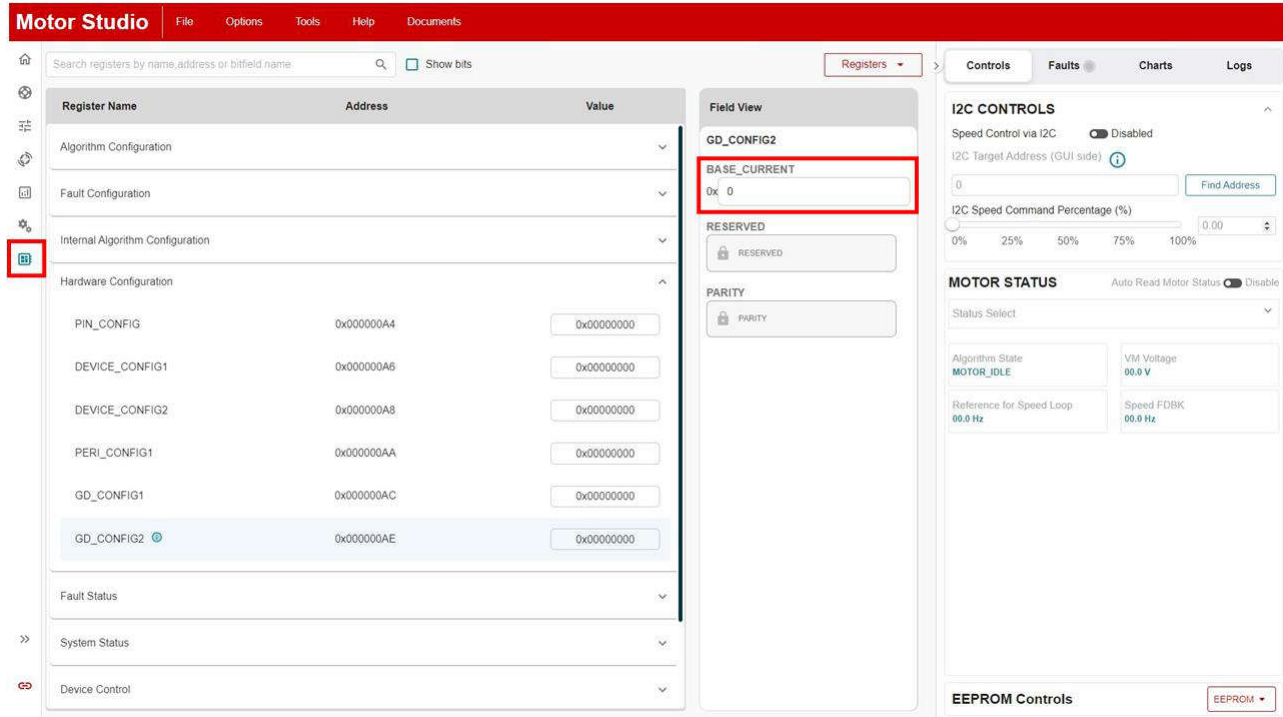
Figure 4-2. Load Default Register Configuration

4.1.2 Setting Base Current

Using the shunt resistor value and CSA gain value determined in [Section 2.1.4](#), use [Equation 1](#) to calculate the max measurable current of the internal CSA.

$$\frac{1.5}{R_{SENSE} \times CSA_{GAIN}} \times \frac{32768}{1200} \quad (1)$$

After converting the result from [Equation 1](#) into a hexadecimal value, input the result into the BASE_CURRENT bit field in register GD_CONFIG2 using the *Register Map* page.



The screenshot shows the Motor Studio software interface. The main window displays a table of registers with columns for Register Name, Address, and Value. The 'GD_CONFIG2' register is selected, and its 'BASE_CURRENT' bit field is highlighted with a red box. The value for this field is '0x 0'. The interface also shows various control panels on the right, including I2C CONTROLS and MOTOR STATUS.

Register Name	Address	Value
Algorithm Configuration		
Fault Configuration		
Internal Algorithm Configuration		
Hardware Configuration		
PIN_CONFIG	0x000000A4	0x00000000
DEVICE_CONFIG1	0x000000A6	0x00000000
DEVICE_CONFIG2	0x000000A8	0x00000000
PERI_CONFIG1	0x000000AA	0x00000000
GD_CONFIG1	0x000000AC	0x00000000
GD_CONFIG2	0x000000AE	0x00000000
Fault Status		
System Status		
Device Control		

Figure 4-3. BASE_CURRENT Bit Field

4.1.3 Setting Current Limits

All the current limits within the MCF8329A are set as a percent of the value programmed into the BASE_CURRENT bit field. For example, if BASE_CURRENT is set to 37.5A and ILIMIT is set to 50%, then the current limit set by ILIMIT will be 18.75A.

HW_LOCK_ILIMIT and LOCK_ILIMIT are configurable current limits intended to protect the system from damage. It is recommended to set these limits to two times higher than the motors rated peak phase current. If the motors rated peak phase current falls between two adjacent limit settings in the configuration, choose the higher of the two settings.

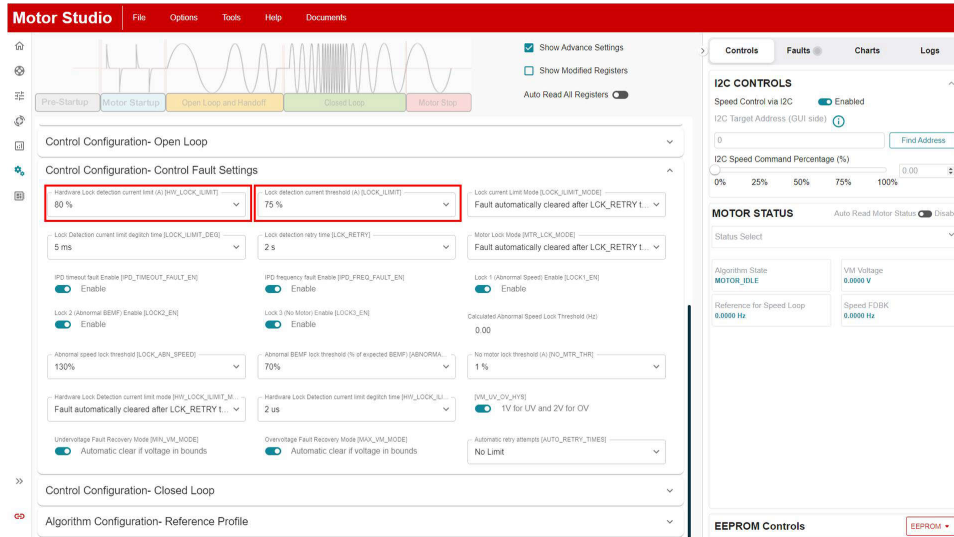


Figure 4-4. Current Protection Limits

ILIMIT, OL_ILIMIT, ALIGN_OR_SLOW_CURRENT_ILIMIT, and IPD_CURR_THR are the max current that are used by the motor driver during the various stages of motor operation. It is recommended to set these values to less than or equal to the rated max phase current of the motor.

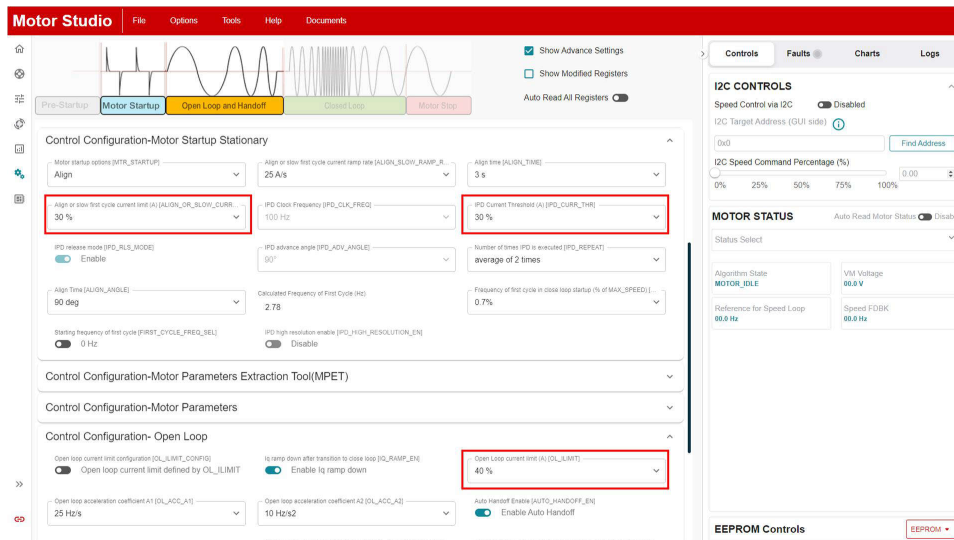


Figure 4-5. OL_ILIMIT, ALIGN_OR_SLOW_CURRENT_ILIMIT, and IPD_CURR_THR Current Limits

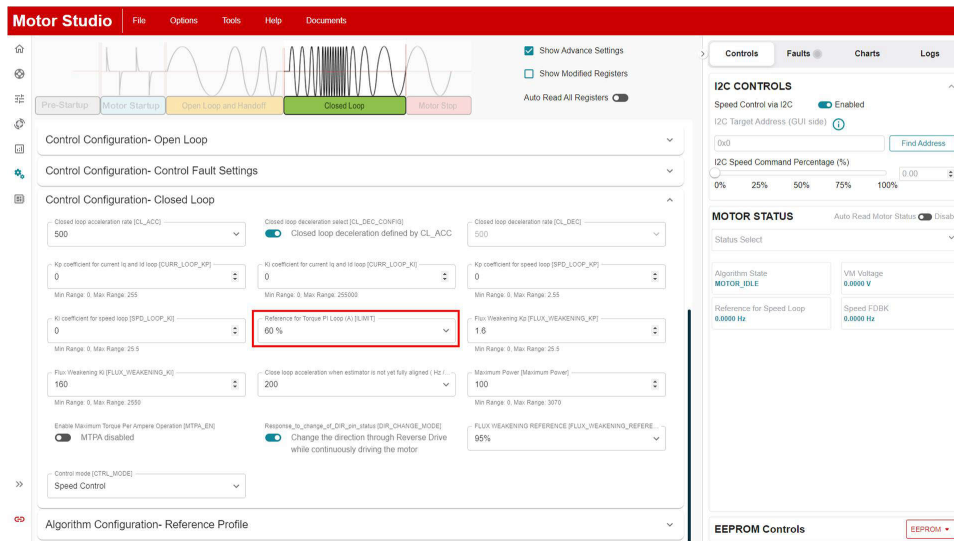


Figure 4-6. I_LIMIT Current Limit

4.1.4 Setting Voltage Limits

BUS_VOLT is used to normalize the modulation algorithm that will be used by the MCF8329A. To improve the resolution of the modulation algorithm at lower motor voltages set the BUS_VOLT to the closest value that is still greater than the expected DC bus voltage or phase voltage. Make sure to account for voltage spikes on the phase node when determining the max expected voltage value.

In applications where the motor voltage cannot go above a certain level, MAX_VM_MOTOR can be used to set the desired voltage limit.

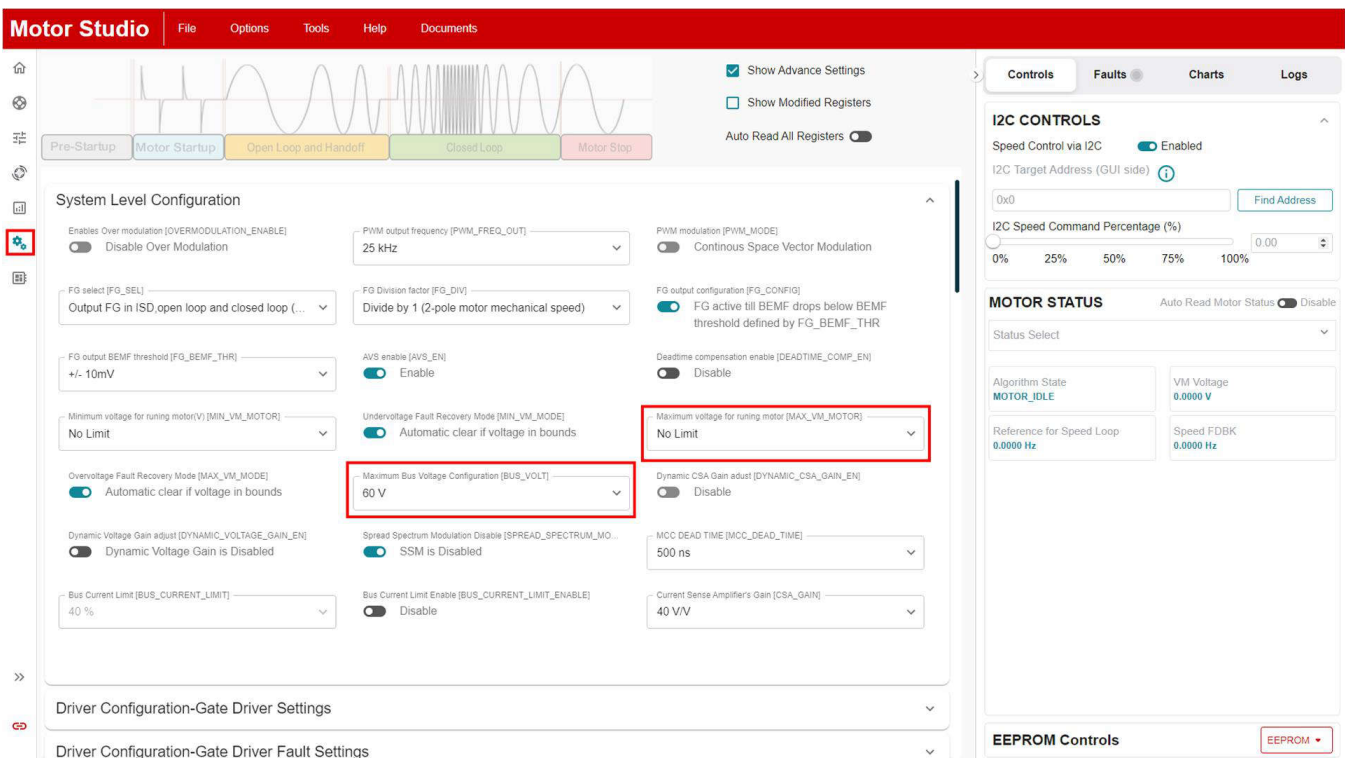


Figure 4-7. Voltage Limits

4.1.5 Input the Motor's Phase Resistance and Inductance

Using the instructions in the [motor parameters FAQ](#), find the motor's phase resistance and inductance. Once these values are found, input the phase resistance into the *Motor Phase Resistance* box and the phase inductance into both the *Lq* and *Ld* boxes in the *Motor Parameters* tab on the *Advanced Tuning* page.

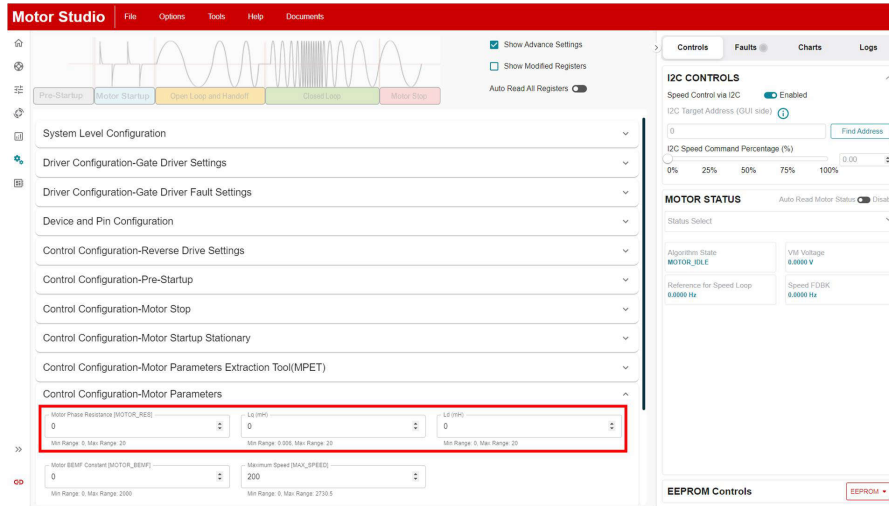


Figure 4-8. Motor Resistance and Inductance

4.1.6 Maximum Electrical Speed (Hz)

Go to the *Motor Information* Section of the GUI on the *Quick Spin* tab and use the following steps to set the motor's max speed:

1. Select *Speed in RPM* or *Speed in Hertz* depending on the unit of speed provided by the motor's data sheet.
2. Input the speed in the Max Speed box. If inputting a speed in RPM, also input the number of pole pairs the motor has using the *Pole Pairs* box.

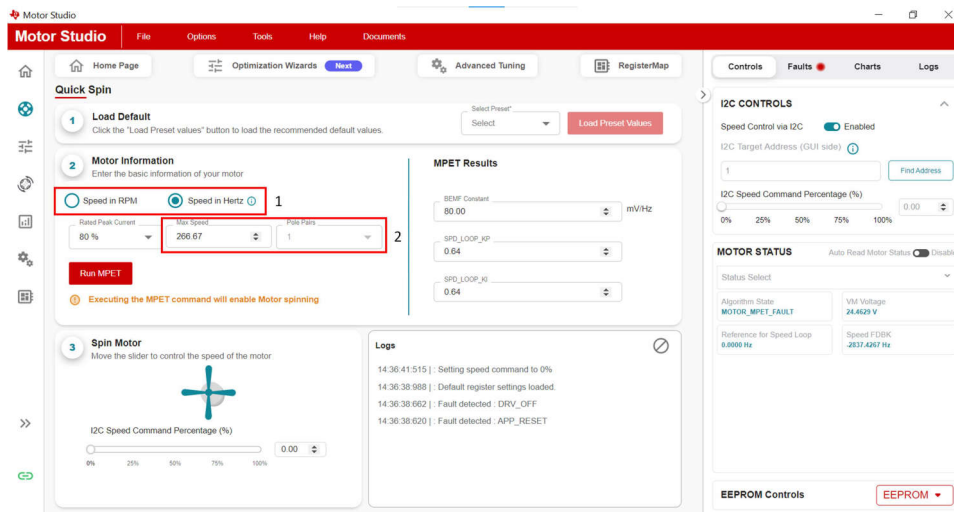


Figure 4-9. Motor Max Speed

Note

Determining number of motor poles without a motor data sheet:

1. Use a lab power supply and make sure it's current limit is set to less than the motor rated current. Do not turn on the supply.
2. Connect V+ of the supply to phase A and V- of the supply to phase B of the motor. Any 2 of the 3 phases can be chosen at random if they are not labeled.
3. Turn on supply. The rotor should have settled at one position with the injecting current.
4. Manually rotate the rotor until rotor snaps to another settle position. It will have several settle-down positions around one mechanical cycle.
5. Count the number of settle-down positions for one fully mechanical cycle, which is the number of pole pairs. Multiplying by two calculates the number of poles.

Be careful of gearing systems within a motor. The gear ratio determines how many rotor revolutions correlate to the shaft's mechanical revolution.

4.1.7 Run MPET to Identify Motor Parameters

Once all the other settings covered in the [Section 4.1](#) section are set, the MPET algorithm within the MCF8329A can be used to measure the BEMF constant and speed loop gains needed to spin the motor in closed loop. Before running MPET, go to the *MPET* page and check that the IPD and open loop current limits are set according to the instructions in [Section 4.1.3](#). Next, enable the *Measure Motor BEMF Constant* and *Measure Motor Mechanical Parameter* switches and click the *Run MPET* button. The motor should begin to spin. After the motor stops spinning, MPET has completed measuring. Click the *Write MPET Results To Shadow Registers* button to use the results measured by MPET.

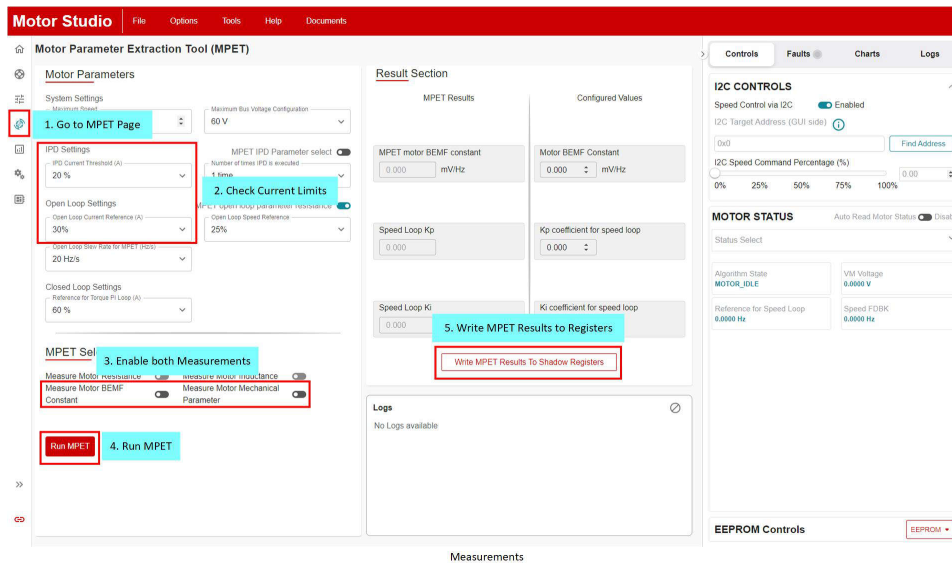


Figure 4-10. How to Run MPET

Note

If a fault is reported or MPET cannot measure the BEMF constant, go to [Section 6](#) for assistance.

4.1.7.1 Skipping MPET Measurements

MPET measurement of the BEMF constant or speed loop gains can be skipped by making sure the register for the parameter is filled with a non-zero value and the measurement switch is disabled.

Use the following steps to disable BEMF constant measurement:

1. Fill in the Motor BEMF constant with a non-zero value, preferably with the value from the motor data sheet or a hand measured value.
2. Disable the Measure Motor BEMF Constant switch.

Use the following steps to disable measurement of the speed loop gains:

1. Fill in the Kp and Ki coefficient for speed loop with non-zero values.
2. Disable the Measure Motor Mechanical Parameter.

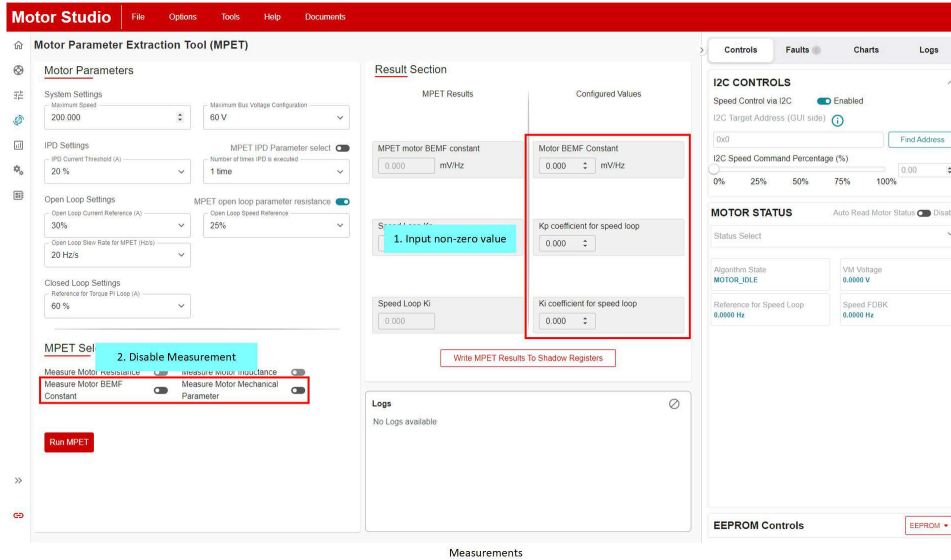
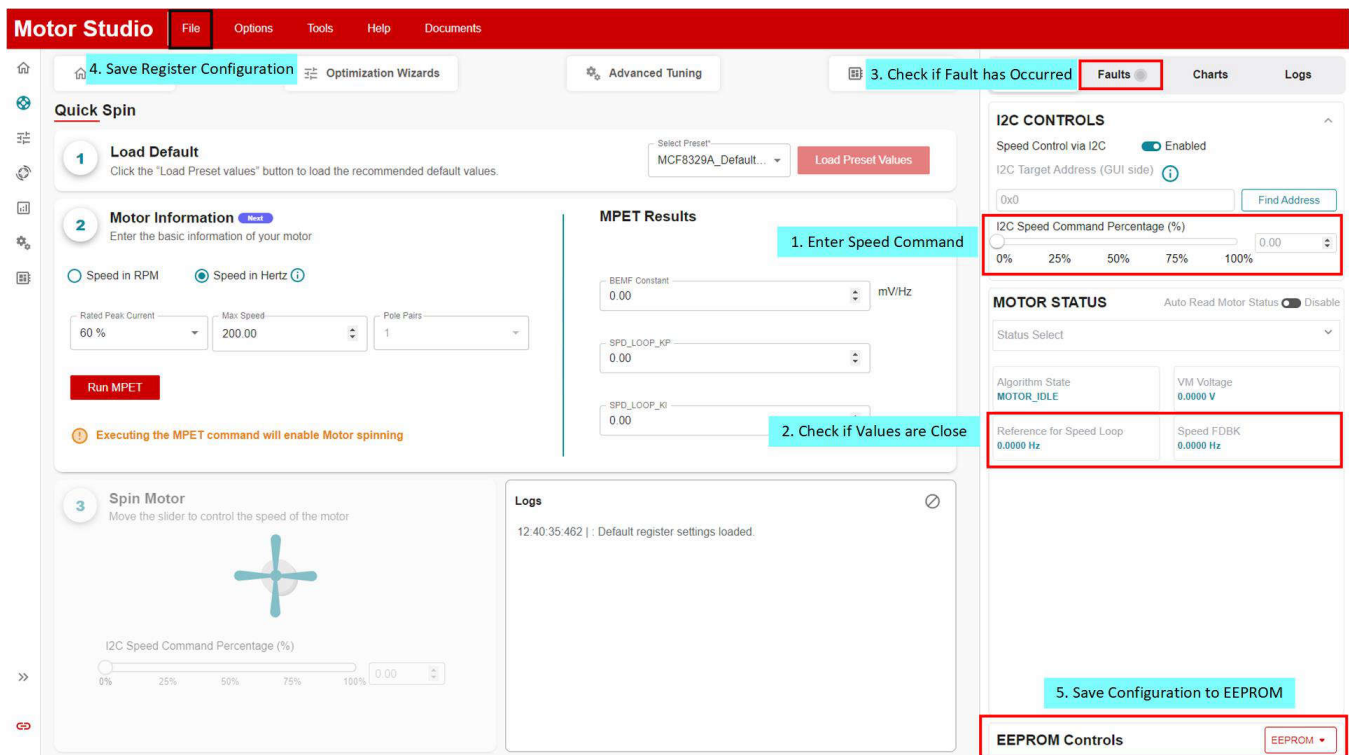


Figure 4-11. How to Skip MPET

4.2 Testing for Successful Startup Into Closed Loop

1. Apply a nonzero speed command using the slider or text box within the *Spin Motor* section of the Quick Spin page. Once a speed command is provided, the motor should begin to spin and accelerate until the motor reaches the target speed.
2. After the motor stops accelerating check that the values in *Reference for Speed Loop* and *Speed FDBK* under the *Motor Status* section are close to the same value.
3. Check for any faults if the *Faults* tab shows a red circle. If a fault has been reported, go to [Section 6](#) and follow the debug steps to correct the fault.
4. Once the motor is able to spin into closed loop and not trigger any faults, stop the motor and save the register configuration to a json file by clicking on *File -> Save Registers*. In the window that pops up, select *Json File* and click on the *Save* Button.
5. To have configuration for the registers covered in [section 7.7 of the MCF8329A data sheet](#) load when the device powers-up these register values can be loaded into EEPROM. To write the configured register values to EEPROM, click the *EEPROM* drop down located at the bottom right of Motor Studio and select the *Write To EEPROM* option. Click the *Yes* button in the window that pops up.



The screenshot shows the Motor Studio interface with several key areas highlighted by red boxes and numbered callouts:

- 1. Enter Speed Command:** Points to the "I2C Speed Command Percentage (%)" slider in the "I2C CONTROLS" panel, which is currently set to 0.00.
- 2. Check if Values are Close:** Points to the "Reference for Speed Loop" and "Speed FDBK" values in the "MOTOR STATUS" panel, both showing 0.0000 Hz.
- 3. Check if Fault has Occurred:** Points to the "Faults" tab in the top navigation bar.
- 4. Save Register Configuration:** Points to the "File" menu in the top navigation bar.
- 5. Save Configuration to EEPROM:** Points to the "EEPROM" dropdown menu in the "EEPROM Controls" section at the bottom right.

Other visible elements include the "MPET Results" section with fields for BEUF Constant, SPD_LOOP_KP, and SPD_LOOP_KI, and the "Spin Motor" section with a speed command slider.

Figure 4-12. Closed Loop Spin Test Steps

5 Basic Controls

This section provides tuning guidance for optimizing the motors performance for many use-case needs.

Note

It s expected to skip the subsection use-cases and scenarios that do not apply to the system or end equipment.

5.1 Speed Input Mode

The MCF8329A offers four options to control the speed of the motor: PWM, frequency, Analog, and I2C. The desired speed mode can be set by changing the value of the SPEED_MODE register on the *Advanced Tuning* page. A description of how to configure these control methods is provided in the *Motor Control Input Options* section of the [MCF8329A Sensorless Field Oriented Control \(FOC\) Three-phase BLDC Gate Driver Data Sheet](#).

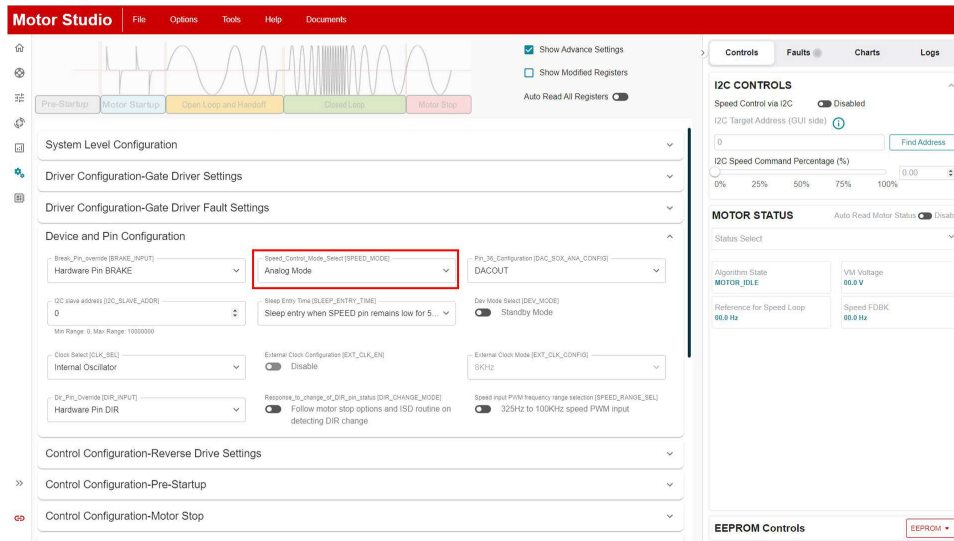


Figure 5-1. Speed Mode Selection

If I2C speed input is chosen, flip SW1 away from the other switches, see [Figure 5-2](#), this will provide a the wake switch signal to the SPEED/WAKE pin to keep the MCF8329A out of sleep/standby mode. If a speed mode other than I2C is being used, flip the switch to the opposite as what is shown in [Figure 5-2](#) to connect the speed pin to J13. For information on how to set J13, see the *Description of User-Selectable Settings on MCF8329EVM (Default in Bold)* table in the [MCF8329EVM User's Guide](#).

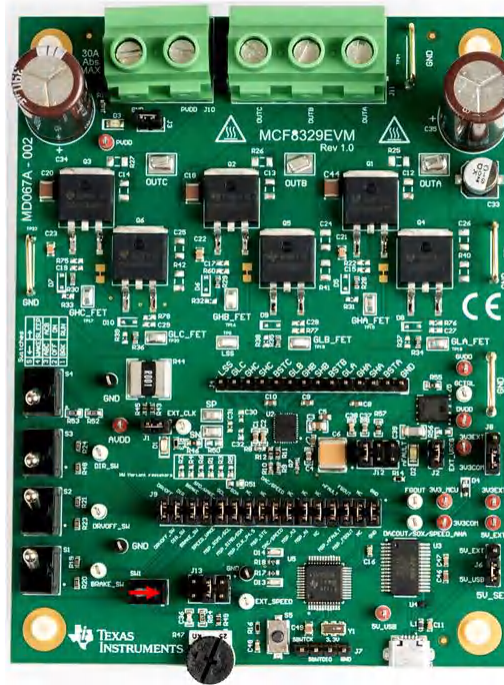


Figure 5-2. SW1 Position for I2C Speed Mode

5.2 Preventing Back Spin of Rotor During Startup

Option 1: Initial Position Detection (IPD):

1. Go to *Optimal Startup* in the *Optimization Wizards* page, select IPD, and click the *Next* button for instructions to set up the IPD start up method.
2. Set the IPD Advance Angle [IPD_ADV_ANGLE] to 90° to get maximum startup torque. If there is sudden jerk observed during startup, then it is recommended to reduce the angle to 60° or 30° for a smoother startup.

Note

Device triggers IPD timeout fault [IPD_T1_FAULT] for motors with very high inductance, or if the motor is not connected. If this fault gets triggered, it is recommended to check if motor is connected to the device.

Device triggers IPD Frequency fault [IPD_FREQ_FAULT] if the IPD clock frequency is set too high. If this fault gets triggered, it is recommended to decrease the IPD Clock value [IPD_CLK_FREQ].

Option 2: Slow First Cycle:

1. Go to *Optimal Startup* in the *Optimization Wizards* page, select slow first cycle, and click the *Next* button for instructions to set up the slow first cycle startup method.

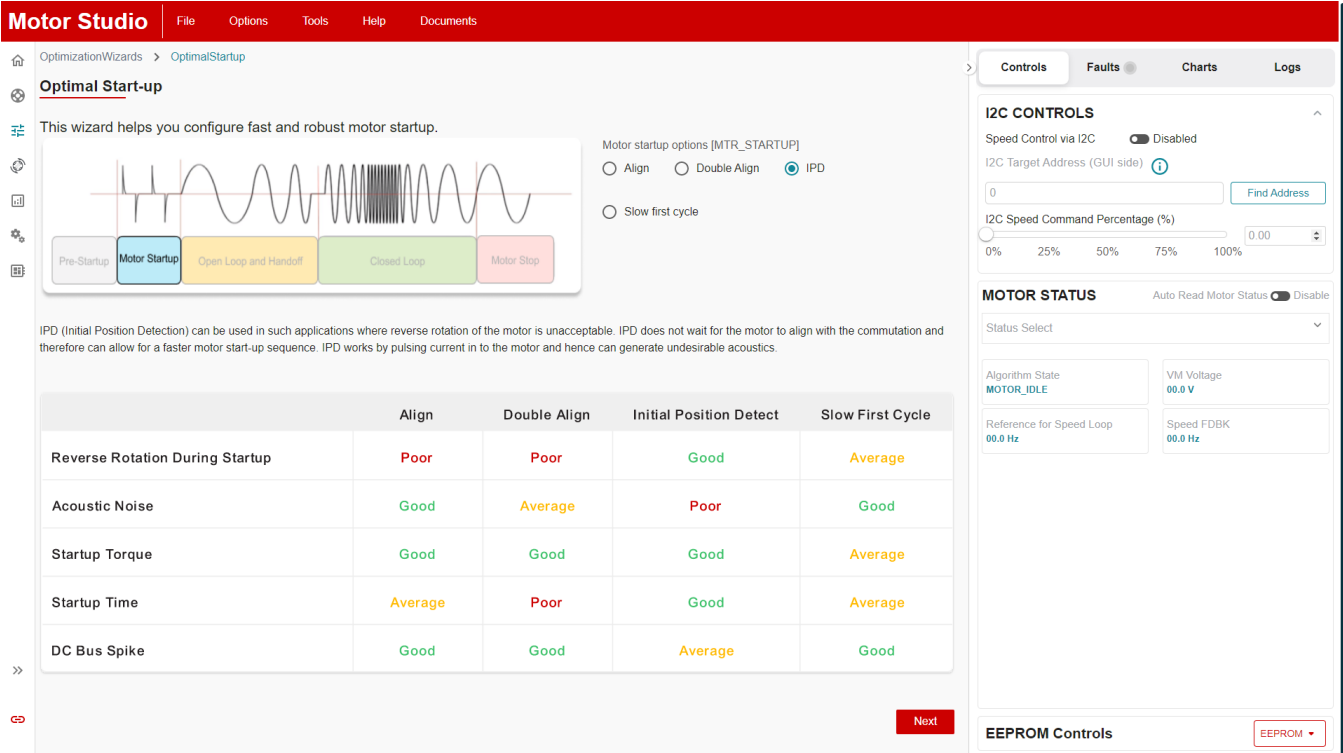


Figure 5-3. Optimal Startup Page

5.3 Faster Startup Timing

Option 1: Initial Position Detection (IPD):

1. Go to *Optimal Startup* in the *Optimization Wizards* page, select IPD, and click the *Next* button.
2. Increase IPD current threshold (A) [IPD_CURR_THR] to the rated current of the motor.
3. Increase IPD clock value [IPD_CLK_FREQ] to a higher frequency up to a value where the device does not trigger IPD frequency fault.
4. Set IPD repeating times [IPD_REPEAT] to 1 time.
5. Set Open loop current limit configuration [OL_ILIMIT_CONFIG] to Open loop current limit defined by ILIMIT.
6. Increase Open loop acceleration coefficient A1 [OL_ACC_A1] and Open loop acceleration coefficient A2 [OL_ACC_A2].

Note

A1 and A2 can be increased until open loop current reaches Lock detection current threshold [LOCK_ILIMIT]. Open loop current can be measured using oscilloscope.

Increasing Open loop acceleration coefficient A1 [OL_ACC_A1] and Open loop acceleration coefficient A2 [OL_ACC_A2] might trigger LOCK_LIMIT fault. If this happens, reduce A1 and A2 until LOCK_LIMIT fault no longer triggers.

7. For ultra-fast startup time (less than 100 ms) it is recommended to follow below steps.
 - a. Disable auto-handoff [AUTO_HANDOFF].
 - b. Configure open to closed loop handoff threshold [OPN_CL_HANDOFF_THR] to a value lesser than or equal to 20 Hz.
8. For startup times above 100ms, it is recommended to follow below steps:
 - a. Enable auto-handoff [AUTO_HANDOFF].

Note

If Abnormal speed fault [ABN_SPEED] gets triggered, it is recommended to decrease open loop acceleration constants [OL_ACC_A1] and [OL_ACC_A2] and also retune IPD by increasing the IPD current threshold [IPD_CURR_THR] and IPD repeat times [IPD_REPEAT].

- Increase Closed loop acceleration rate [CL_ACC].

Note

LOCK_LIMIT fault handling:

Closed loop acceleration rate [CL_ACC] can be increased until closed loop current reaches Lock detection current threshold [LOCK_ILIMIT]. Closed loop current can be measured using oscilloscope. Increasing closed loop acceleration rate [CL_ACC] might trigger LOCK_LIMIT. If this happens, reduce closed loop acceleration rate [CL_ACC] until no longer triggers.

Option 2: Align

- Go to *Optimal Startup* in the *Optimization Wizards* page, select Align, and click the *Next* button for instructions to set up the Align startup method.
- Configure align time [ALIGN_TIME] to 10 ms.
- Follow Step 6 to Step 9 in Option 1.

Figure 5-4 shows FG, phase current and motor electrical speed waveform. Motor takes 50 ms to reach speed from zero speed.

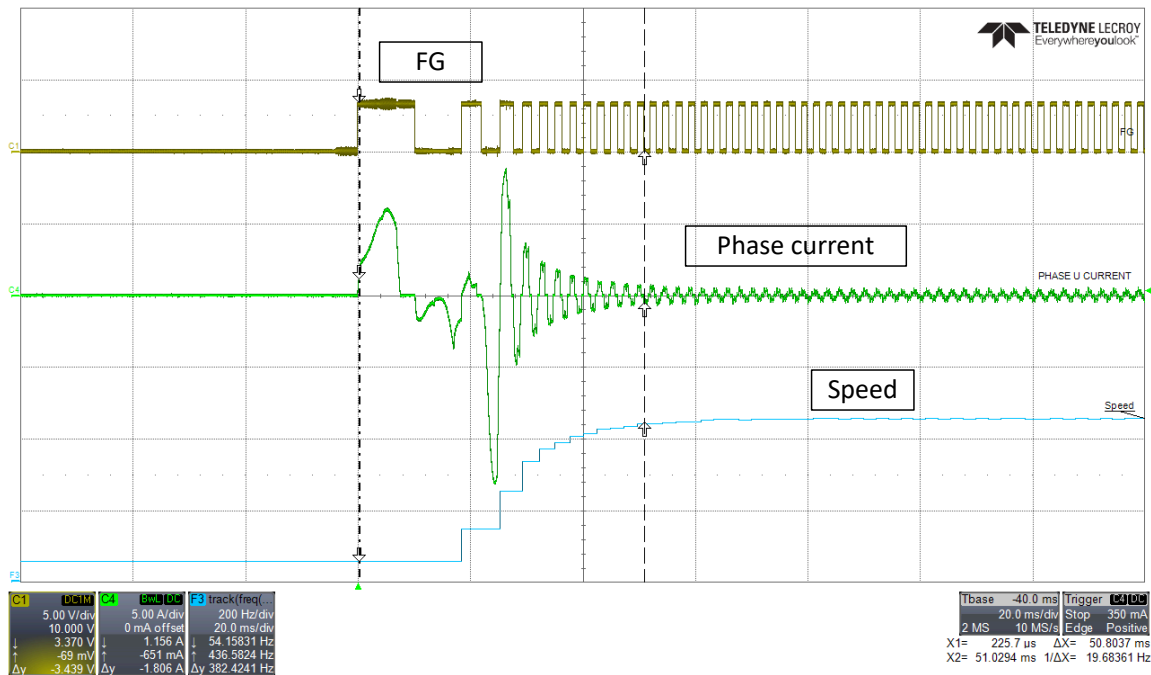


Figure 5-4. Phase Current, FG and Motor Speed - Faster Startup Time

Note

If Abnormal speed fault [ABN_SPEED] or Loss of sync [LOSS_OF_SYNC] fault gets triggered, it is recommended to follow below debug steps:

- Select Double align as the motor startup method in [MTR_STARTUP].
- Increase align time [ALIGN_TIME].
- Configure align current threshold [ALIGN_OR_SLOW_CURRENT_ILIMIT] to 50% of ILIMIT.
- Configure First cycle frequency select [FIRST_CYCLE_FREQ_SEL] to 0.

5.4 Improving Speed Regulation

For applications that require better speed regulation, it is recommended to tune Speed loop PI controllers [SPD_LOOP_KP] and [SPD_LOOP_KI]. Kp coefficient of speed loop [SPD_LOOP_KP] controls the settling time and speed overshoots. Ki coefficient of Speed loop [SPD_LOOP_KI] controls speed overshoot and ensures regulation of speed at set value and drives the error to zero. Speed loop PI controller gains can either be auto-tuned by MCF8329A or tuned manually.

Auto Tuning: MCF8329A auto calculates the Speed loop PI controller gains when [SPD_LOOP_KP] and [SPD_LOOP_KI] are set to zero.

Manual Tuning: Use the following steps to tune Speed loop PI controller gains manually:

1. Set the control mode [CTRL_MODE] to modulation index control (11b).
2. Issue non-zero speed command to start the motor (refer to [Section 4.2](#), step 1 on how to issue non-zero speed command). Motor will spin in open loop.
3. Allow the open loop current to settle down and then measure the peak open loop current.
4. Stop the motor and set the control mode [CTRL_MODE] to current control.
5. Slowly increase the speed command until the motor speed reaches the max speed. Note down the Iq_ref value being reported in the *IQ_REF_CLOSED_LOOP* register.
6. Speed loop Kp [SPD_LOOP_KP] is calculated using [Equation 2](#).

$$\text{Speed loop } K_p = \frac{\text{Iq reference at maximum speed}}{\text{Maximum Electrical Speed in Hz}} \quad (2)$$

7. Speed loop Ki [SPD_LOOP_KI] is calculated using [Equation 3](#).

$$\text{Speed loop } K_i = 0.1 \times \text{Speed loop } K_p \quad (3)$$

8. Stop the motor and set the control mode [CTRL_MODE] to speed control.

Note

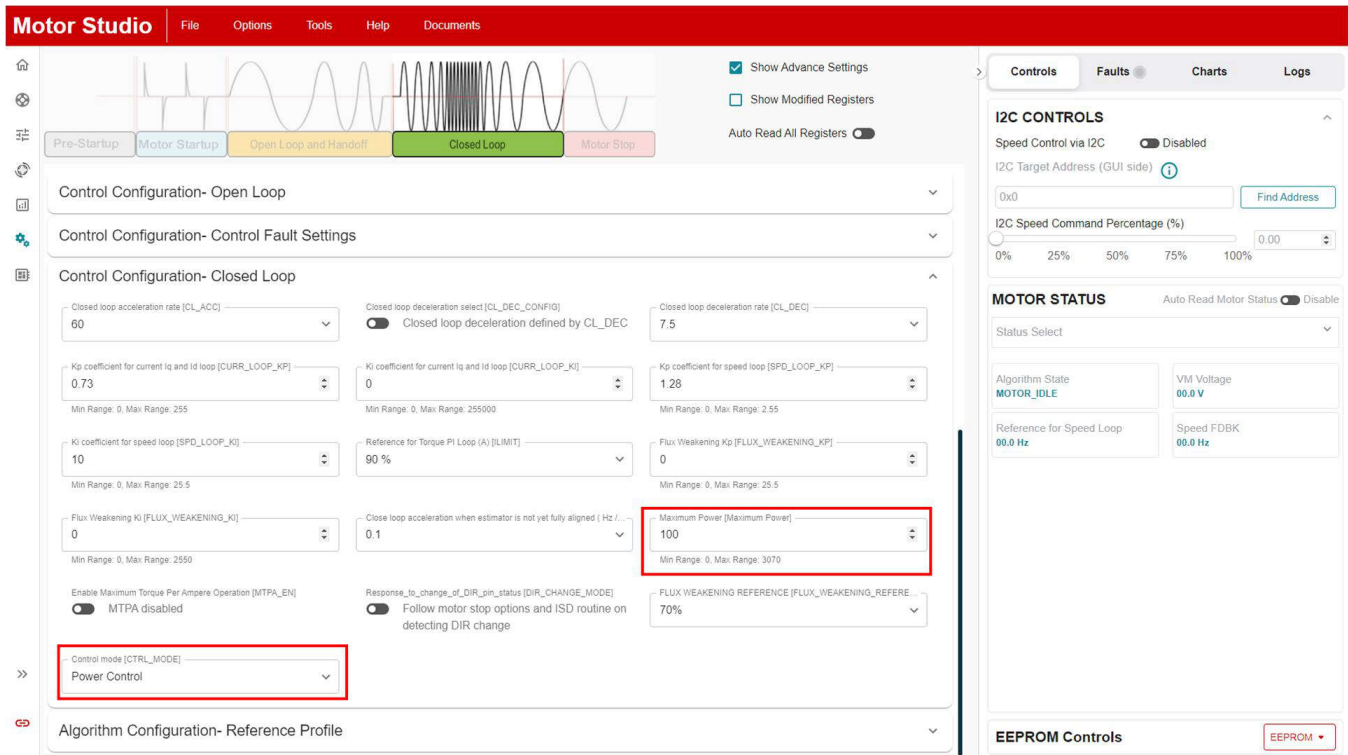
Tuning speed loop Kp and Ki is experimental. If the above recommendation doesn't work, then we recommend to manually tune Speed loop Kp and Ki till the desired results are achieved.

5.5 Limiting and Regulating Supply Power

MCF8329A provides options to limit and regulate supply power. This feature can be utilized in battery powered motor driver applications such as cordless vacuum cleaners, power tools etc.

Use the following steps to limit supply power. In this mode, supply power is only limited to reference power and not actively regulated.

1. Configure CTRL_MODE to power control (1b).
2. Configure MAX_POWER. This sets the maximum power that MCF8329A can draw from the DC input supply at 100% duty command. For example, if MAX_POWER is configured to 25 W, MCF8329A draws 12.5 W from power supply at 50% duty command.
3. The power control loop uses the same PI controller parameters as in the speed loop mode. Kp and Ki coefficients are configured through SPD_POWER_KP and SPD_POWER_KI. Tuning SPD_POWER_KP and SPD_POWER_KI is experimental. The recommendation is to manually tune both parameters until the desired results are achieved.



The screenshot displays the Motor Studio interface for configuring the MCF8329A motor driver. The main window shows the 'Control Configuration- Closed Loop' settings. The 'Maximum Power (Maximum Power)' parameter is set to 100, and the 'Control mode (CTRL_MODE)' is set to 'Power Control'. The right sidebar shows the 'I2C CONTROLS' section with 'Speed Control via I2C' disabled and 'I2C Speed Command Percentage (%)' set to 0.00. The 'MOTOR STATUS' section shows 'Algorithm State' as 'MOTOR_IDLE', 'VM Voltage' as 00.0 V, and 'Speed FDBK' as 00.0 Hz. The 'EEPROM Controls' section is also visible at the bottom right.

Figure 5-5. Power Control Settings

5.6 MTPA Tuning

Maximum torque per ampere (MTPA) is a feature in the MCF8329A to maximize the torque generated per ampere of current for salient motors. To enable MTPA, set MTPA_EN to 1b and set the SALIENCY_PERCENTAGE to a non-zero value by setting the Lq and Ld values if they are provided in the device-specific data sheet.

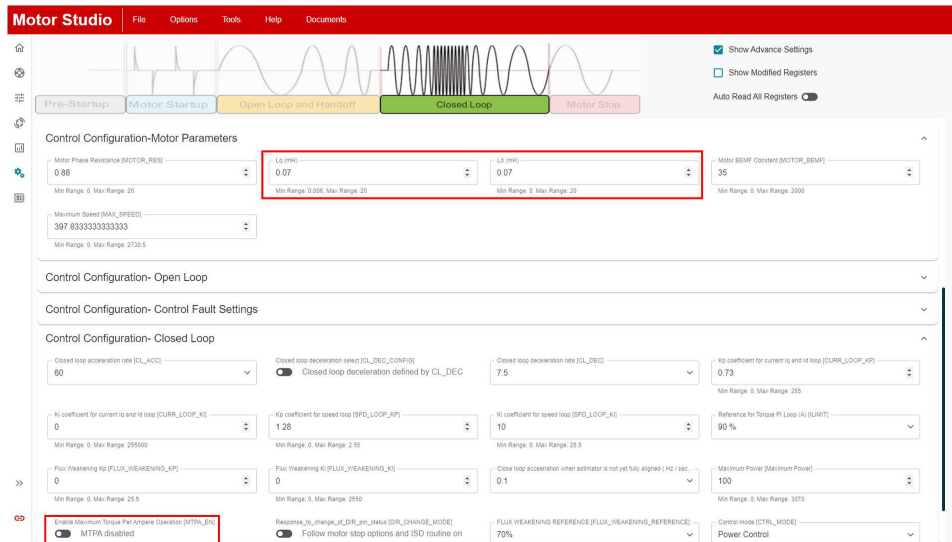


Figure 5-6. MTPA Settings

Note

If the motors Ld or saliency percentage is not known, the approximate SALIENCY_PERCENTAGE can be determined by following the steps below:

1. Set the SALIENCY_PERCENTAGE to 0x1h
2. Set the CTRL_MODE to Current Control mode
3. Provide a speed command.
4. While the motor is spinning, increment the SALIENCY_PERCENTAGE value by 1h until the motors speed begins to decrease.

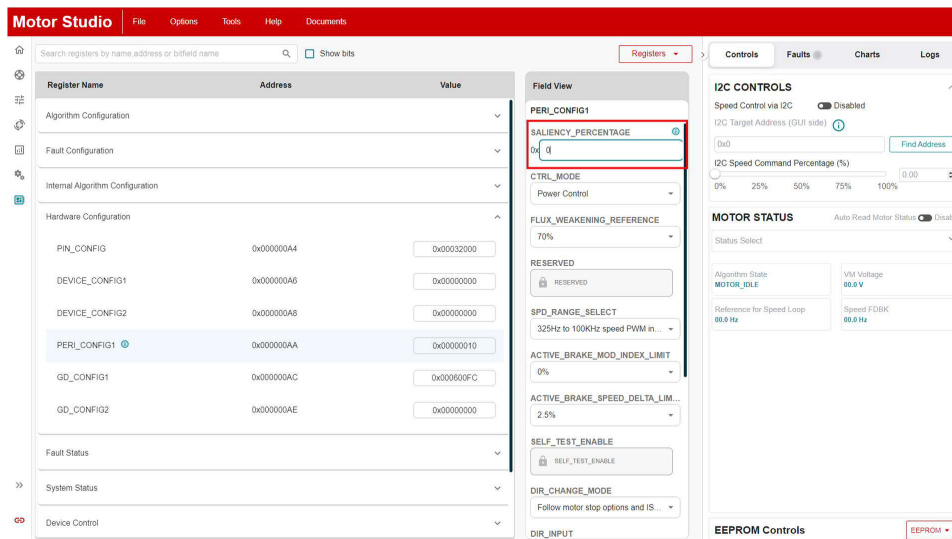


Figure 5-7. Saliency Register

5.7 Motor Studio Optimization Wizards

For step-by-step guidance on configuring the MCF8329A for additional use-cases and optimization features, see the *Optimization Wizards* page on Motor Studio.

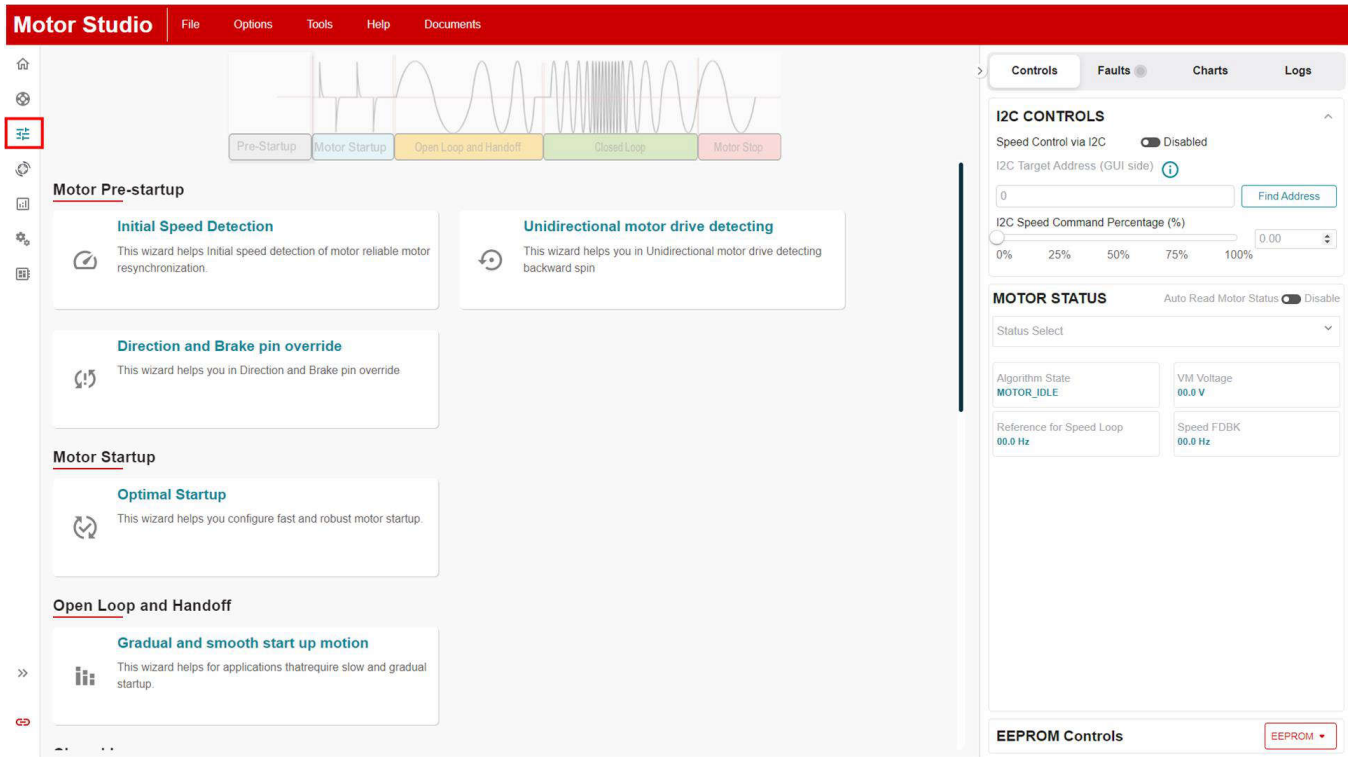


Figure 5-8. Optimization Wizards Page

6 Fault Handling

To see which fault has been reported by the MCF8329A, go to the *Faults* tab and check if any faults with red circles appear. If a fault is shown in this tab, see the section below that has a title similar to the reported fault.

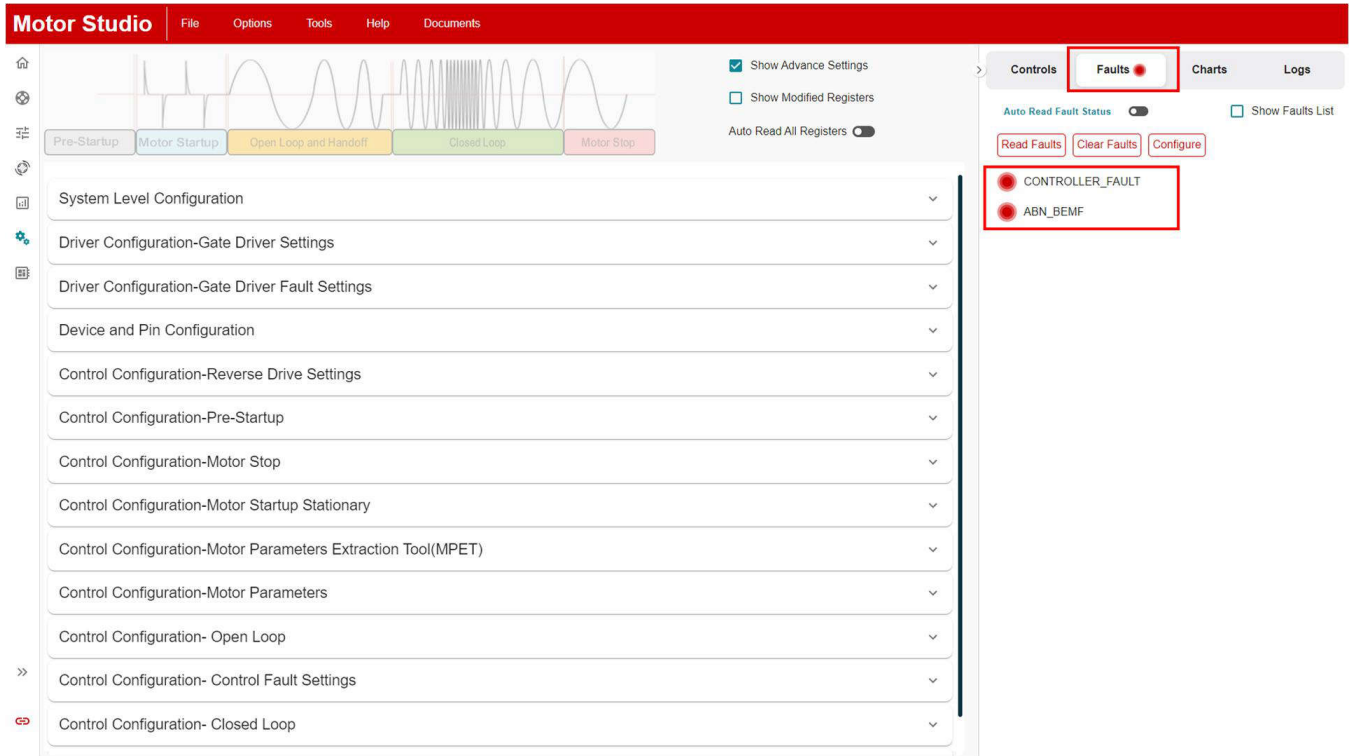


Figure 6-1. Faults Tab

6.1 MPET_BEMF_FAULT [MPET_BEMF_FAULT]

A MPET_BEMF_FAULT gets reported when the measured BEMF is less than the threshold set in STAT_DETECT_THR. If this fault is triggered, go the *MPET* page in Motor Studio and follow the suggestions below:

1. Enable *MPET Open Loop Parameter Resistance*.
2. Increase the *Open Loop Current Reference* value.
3. Decrease the *Open Loop Slew Rate for MPET* value.
4. If the fault still persists, see the [Motor Parameters FAQ](#) for instructions on how to obtain the motor's BEMF constant through the motor's data sheet for through hand measurement. Once the motor's BEMF constant value is found, input the BEMF constant value into the *Motor BEMF Constant* box in the *Configured Values* section on the *MPET* page.

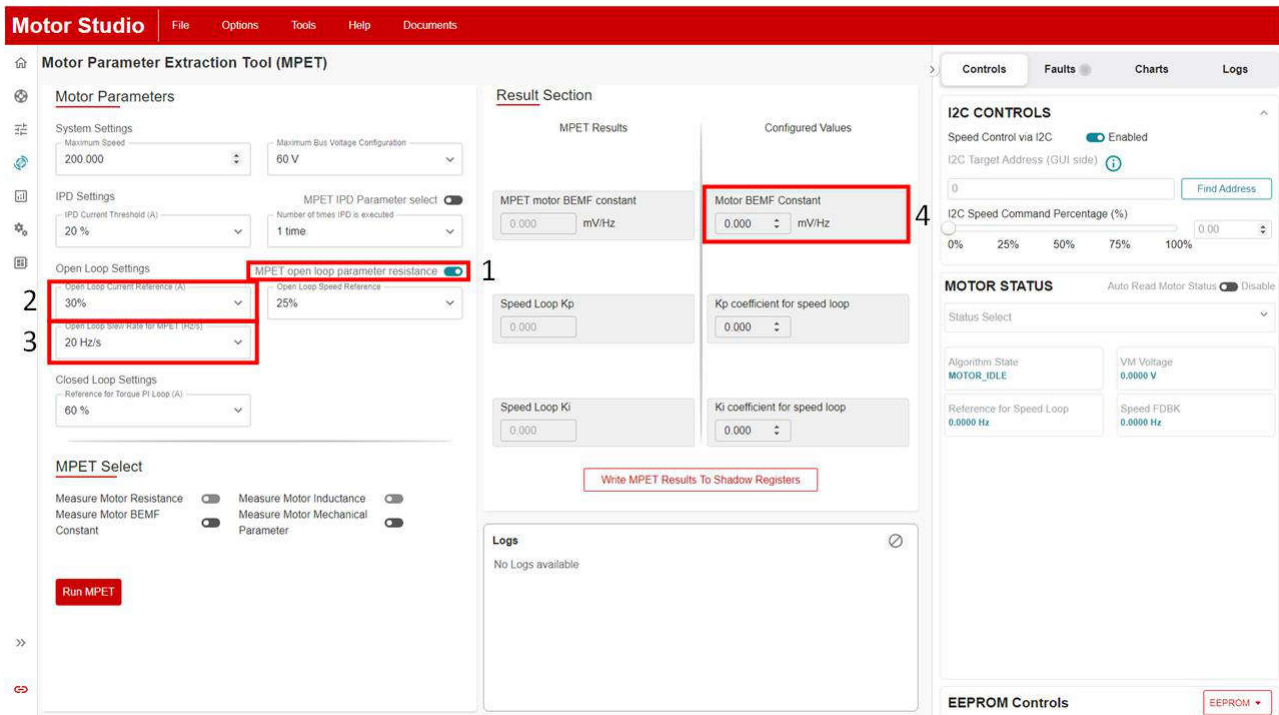


Figure 6-2. MPET_BEMF_FAULT

6.2 Abnormal BEMF Fault [ABN_BEMF]

This fault gets triggered when the difference between the estimated BEMF voltage exceeds the threshold set by ABNORMAL_BEMF_THR. If this fault is triggered, then go the *Control Fault Settings* tab in the *Advanced Tuning* page in Motor Studio and follow the below suggestions:

1. For applications with load dynamics (sudden change in load), it is recommended to set the Abnormal BEMF threshold to 70% to avoid triggering this fault.
2. This fault can get triggered if the programmed BEMF constant is inaccurate. Follow steps recommended in step 4 of [Section 6.1](#) to obtain accurate BEMF constant.

The screenshot displays the Motor Studio interface with the 'Control Configuration- Control Fault Settings' tab selected. The 'Abnormal BEMF lock threshold (% of expected BEMF) [ABNORMA...]' parameter is set to 70% and is highlighted with a red box. Other visible settings include:

- Hardware Lock detection current limit (A) [HW_LOCK_ILIMIT]: 80%
- Lock detection current threshold (A) [LOCK_ILIMIT]: 75%
- Lock current Limit Mode [LOCK_ILIMIT_MODE]: Fault automatically cleared after LCK_RETRY t...
- Lock detection current limit degitch time [LOCK_ILIMIT_DEG]: 5 ms
- Lock detection retry time [LCK_RETRY]: 2 s
- Motor Lock Mode [MTR_LCK_MODE]: Fault automatically cleared after LCK_RETRY t...
- IPD timeout fault Enable [IPD_TIMEOUT_FAULT_EN]: Enable
- IPD frequency fault Enable [IPD_FREQ_FAULT_EN]: Enable
- Lock 1 (Abnormal Speed) Enable [LOCK1_EN]: Enable
- Lock 2 (Abnormal BEMF) Enable [LOCK2_EN]: Enable
- Lock 3 (No Motor) Enable [LOCK3_EN]: Enable
- Calculated Abnormal Speed Lock Threshold (Hz): 0.00
- No motor lock threshold (A) [NO_MTR_THR]: 1%
- Hardware Lock Detection current limit mode [HW_LOCK_ILIMIT_M...]: Fault automatically cleared after LCK_RETRY t...
- Hardware Lock Detection current limit degitch time [HW_LOCK_ILU...]: 2 us
- [VM_UV_OV_HYS]: 1V for UV and 2V for OV
- Undervoltage Fault Recovery Mode [MIN_VM_MODE]: Automatic clear if voltage in bounds
- Ovrvoltage Fault Recovery Mode [MAX_VM_MODE]: Automatic clear if voltage in bounds
- Automatic retry attempts [AUTO_RETRY_TIMES]: No Limit

The right sidebar shows the 'I2C CONTROLS' section with 'Speed Control via I2C' enabled and 'I2C Speed Command Percentage (%)' set to 0.00. The 'MOTOR STATUS' section shows 'Status Select' as 'MOTOR_IDLE', 'VM Voltage' as 0.0000 V, and 'Speed FDBK' as 0.0000 Hz.

Figure 6-3. ABNORMAL_BEMF_THR

6.3 Lock Current Limit [LOCK_LIMIT]

This fault gets triggered when the phase current exceeds the LOCK_ILIMIT threshold. If this fault is triggered, check the motor data sheet for stall torque and load the motor below the stall torque specified in the data sheet. If the load torque is still within the stall torque, go to the *Control Fault Settings* tab in the *Advanced Tuning* page and increase the value of LOCK_ILIMIT.

The screenshot shows the Motor Studio software interface. The main window is titled "Control Configuration- Control Fault Settings". A red box highlights the "Lock detection current threshold (A) [LOCK_ILIMIT]" dropdown menu, which is currently set to "75 %". Other settings visible include:

- Hardware Lock detection current limit (A) [HW_LOCK_ILIMIT]: 80 %
- Lock detection current limit deglitch time [LOCK_ILIMIT_DEG]: 5 ms
- Lock detection retry time [LCK_RETRY]: 2 s
- Lock current Limit Mode [LOCK_ILIMIT_MODE]: Fault automatically cleared after LCK_RETRY t...
- Motor Lock Mode [MTR_LCK_MODE]: Fault automatically cleared after LCK_RETRY t...
- IPD timeout fault Enable [IPD_TIMEOUT_FAULT_EN]: Enable
- IPD frequency fault Enable [IPD_FREQ_FAULT_EN]: Enable
- Lock 1 (Abnormal Speed) Enable [LOCK1_EN]: Enable
- Lock 2 (Abnormal BEMF) Enable [LOCK2_EN]: Enable
- Lock 3 (No Motor) Enable [LOCK3_EN]: Enable
- Calculated Abnormal Speed Lock Threshold (Hz): 0.00
- Abnormal speed lock threshold [LOCK_ABN_SPEED]: 130%
- Abnormal BEMF lock threshold (% of expected BEMF) [ABNORMA...]: 70%
- No motor lock threshold (A) [NO_MTR_THR]: 1 %
- Hardware Lock Detection current limit mode [HW_LOCK_ILIMIT_M...]: Fault automatically cleared after LCK_RETRY t...
- Hardware Lock Detection current limit deglitch time [HW_LOCK_ILU...]: 2 us
- [VM_UV_OV_HYS]: 1V for UV and 2V for OV
- Undervoltage Fault Recovery Mode [MIN_VM_MODE]: Automatic clear if voltage in bounds
- Overvoltage Fault Recovery Mode [MAX_VM_MODE]: Automatic clear if voltage in bounds
- Automatic retry attempts [AUTO_RETRY_TIMES]: No Limit

On the right side, the "I2C CONTROLS" panel shows "Speed Control via I2C" is Enabled. The "MOTOR STATUS" panel shows "Algorithm State" as MOTOR_IDLE, "VM Voltage" as 0.0000 V, and "Speed FDBK" as 0.0000 Hz. The "EEPROM Controls" panel at the bottom right shows "EEPROM" selected.

Figure 6-4. LOCK_ILIMIT

6.4 Hardware Lock Current Limit [HW_LOCK_LIMIT]

This fault gets triggered when the phase current exceeds the HW_LOCK_ILIMIT threshold. If this fault is triggered, use the following recommendations:

- Using the fields circled in [Figure 6-5](#), set SPD_LOOP_KP, SPD_LOOP_KI, CURR_LOOP_KP, and CURR_LOOP_KI to zero. This enables the MCF8329A to automatically calculate the speed loop and current loop PI controller gains.
- If the fault still persists, check the continuity across phase-to-phase, phase-to-GND, and PVDD-to-GND to make sure there is no short across these terminals.

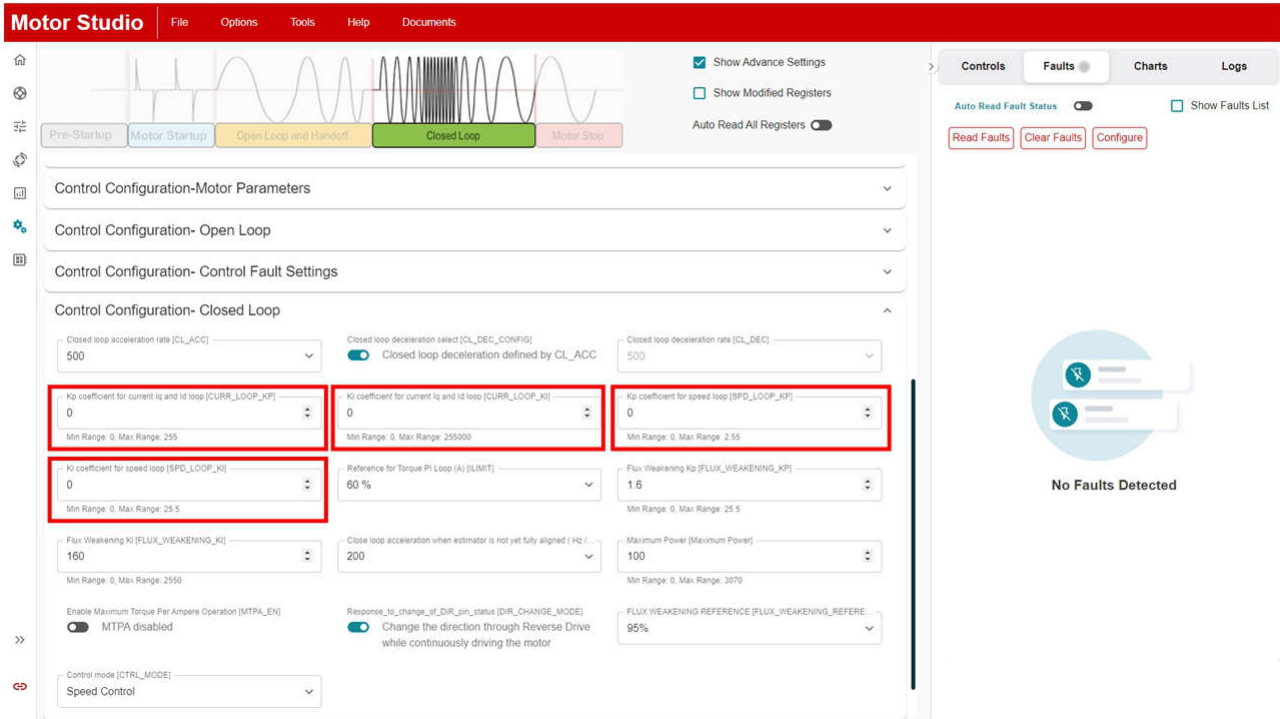


Figure 6-5. HW_LOCK_LIMIT

6.5 No Motor Fault [NO_MTR]

This fault gets triggered when the phase current is below the no motor lock threshold for 500ms during open loop. When this fault gets triggered, use the following recommendations:

1. Make sure the motor phases are securely connected to the OUTA, OUTB, and OUTC test points or the connector block J11.
2. If the fault persists, set the no motor lock current threshold [NO_MTR_THR] to 5%.
3. For low inductance motors, increase PWM switching frequency [PWM_FREQ_OUT].

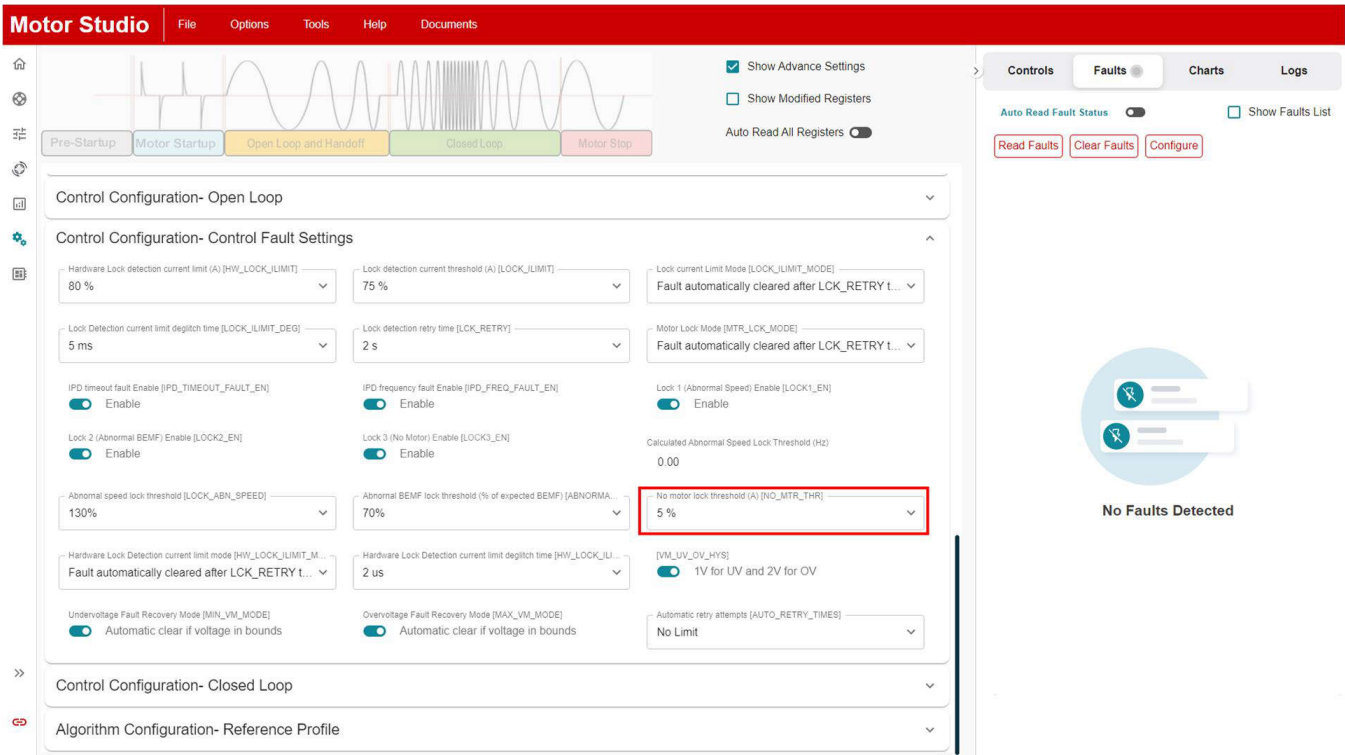


Figure 6-6. NO_MTR

Note

The MCF8329A might trigger loss of sync [LOSS_OF_SYNC] when motor phases are disconnected while the motor is spinning.

6.6 Abnormal Speed [ABN_SPEED]

This fault gets triggered when motor speed exceeds abnormal speed threshold [LOCK_ABN_SPEED]. When this fault gets triggered, use the following recommendations:

1. Increase align time [ALIGN_TIME], decrease slow first cycle frequency [SLOW_FIRST_CYC_FREQ], or increase the IPD current threshold [IPD_CURR_THR] and IPD repeat times [IPD_REPEAT] depending on the start-up mode selected.
2. Decrease open loop acceleration A1 [OL_ACC_A1] and open loop acceleration A2 [OL_ACC_A2].
3. Decrease closed loop acceleration [CL_ACC].

The screenshot displays the Motor Studio software interface. The top menu bar includes 'Motor Studio', 'File', 'Options', 'Tools', 'Help', and 'Documents'. Below the menu is a timeline showing motor operation stages: Pre-Startup, Motor Startup, Open Loop and Handoff, Closed Loop, and Motor Stop. The main configuration area is titled 'Control Configuration-Motor Startup Stationary' and contains several parameter settings:

- Motor startup options (MTR_STARTUP): IPD
- Align or slow first cycle current ramp rate (ALIGN_SLOW_RAMP_R...): 500 A/s
- Align time (ALIGN_TIME): 400 ms (highlighted in red)
- Align or slow first cycle current limit (A) (ALIGN_OR_SLOW_CURR...): 20 %
- IPD Clock Frequency (IPD_CLK_FREQ): 100 Hz
- IPD Current Threshold (A) (IPD_CURR_THR): 20 % (highlighted in red)
- IPD release mode (IPD_RLS_MODE): Disable
- IPD advance angle (IPD_ADV_ANGLE): 90°
- Number of times IPD is executed (IPD_REPEAT): 1 time
- Align Time (ALIGN_ANGLE): 0 deg
- Calculated Frequency of First Cycle (Hz): 0.00
- Frequency of first cycle in close loop startup (% of MAX_SPEED) (...): 0.1% (highlighted in red)
- Starting frequency of first cycle (FIRST_CYCLE_FREQ_SEL): 0 Hz
- IPD high resolution enable (IPD_HIGH_RESOLUTION_EN): Enable

On the right side, the 'Faults' tab is active, showing 'Auto Read Fault Status' as disabled and 'Show Faults List' as checked. Below these are buttons for 'Read Faults', 'Clear Faults', and 'Configure'. A large blue circle with a checkmark and the text 'No Faults Detected' is displayed on the right.

Figure 6-7. ABN_SPEED

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